Lisa S. Foshee General Attorney

BellSouth Telecommunications, Inc. 150 South Monroe Street Room 400 Tallahassee, Florida 32301 (404) 335-0754

January 4, 2002

Mrs. Blanca S. Bayó Director, Division of the Commission Clerk and Administrative Services Florida Public Service Commission 2540 Shumard Oak Boulevard Tallahassee, FL 32399-0850

Re: <u>960786-B-TL (Section 271)</u>

Dear Ms. Bayó:

Enclosed please find the original and six copies of BellSouth Telecommunications, Inc.'s Notice of Filing with attached Affidavit of Alphonso J. Varner which we ask that you file in the captioned docket.

A copy of this letter is enclosed. Please mark it to indicate that the original was filed and return the copy to me. Copies have been served to the parties by Federal Express as shown on the attached Certificate of Service.

Sincerely,

Enclosures

cc: All Parties of Record Marshall M. Criser III Fred J. McCallum

> DOCUMENT NUMBER DATE 00152 JAN-48 FPSC-COMMISSION CLERK

CERTIFICATE OF SERVICE DOCKET NO. 960786-B-TL

I HEREBY CERTIFY that a true and correct copy of the foregoing was served by

Federal Express this 4th day of January, 2002 to the following:

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Lisa S. Foshee (1/16) (+) Signed Protective Agreement

BEFORE THE FLORIDA PUBLIC SERVICE COMMISSION

)

)

In Re: Consideration of BellSouth Telecommunications, Inc.'s entry into interLATA services pursuant to Section 271 of the Federal Telecommunications Act of 1996.

Docket No. 960786-B-TL

Filed: January 4, 2002

BELLSOUTH TELECOMMUNICATIONS, INC.'S NOTICE OF FILING

BellSouth Telecommunications, Inc. ("BellSouth") hereby files the Affidavit of

Alphonso J. Varner that attaches BellSouth's performance data reflecting performance

for the month of October 2001. The Affidavit and the accompanying attachments

describe the performance data and explain the conclusions that can be drawn from it.

Respectfully submitted this 4th day of January 2002.

BELLSOUTH TELECOMMUNICATIONS, INC.

VÁNCY B.ÓWHITE

JAMES MEZA III c/o Nancy Sims 150 South Monroe Street, Suite 400 Tallahassee, FL 32301 (305) 347-5561

LISA FOSHEE

FRED MCCALLUM E. EARL EDENFIELD JR. Suite 4300 675 W. Peachtree St., NE Atlanta, GA 30375 (404) 335-0754

Before the Florida Public Service Commission Tallahassee, Florida

AFFIDAVIT OF ALPHONSO J. VARNER ON BEHALF OF BELLSOUTH TELECOMMUNICATIONS, INC. FILED JANAURY 4, 2002

I, Alphonso J. Varner, being of lawful age and duly sworn upon my oath, depose and state:

 My name is Alphonso J. Varner. I am employed by BellSouth as Senior Director in Interconnection Services. My business address is 675 West Peachtree Street, Atlanta, Georgia 30375.

PROFESSIONAL AND EDUCATIONAL BACKGROUND

- 2. I graduated from Florida State University in 1972 with a Bachelor of Engineering Science degree in systems design engineering. I immediately joined Southern Bell in the division of revenues organization with the responsibility for preparation of all Florida investment separations studies for division of revenues and for reviewing interstate settlements.
- 3. Subsequently, I accepted an assignment in the rates and tariffs organization with responsibilities for administering selected rates and tariffs including preparation of tariff filings. In January 1994, I was appointed Senior Director of Pricing for the nine-state region. I was named Senior Director for Regulatory Policy and Planning in August 1994.

In April 1997, I was named Senior Director of Regulatory for the nine-state BellSouth region, and I accepted my current position in March 2001.

II. PURPOSE OF AFFIDAVIT

-

4. The purpose of my Affidavit is to provide data specific to BellSouth's operations in Florida. This filing reflects performance for the month of October 2001. Exhibit October PM Data and Attachments 1E though 3E that accompany this filing describe the data and explain the conclusions that can be drawn from it.

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1	DISCUSSION OF PERFORMANCE MEASUREMENTS DATA
2 3	I. ANALYSIS OF PERFORMANCE MEASUREMENTS
4	
5	A. Introduction
6	
7	BellSouth is currently producing state level results based on the January 12,
8	2001, Georgia Order from Docket 7892-U. While there are some differences
9	from the interim Service Quality Measurement (SQM) Version 3.0 approved
10	by this Commission on July 3, 2001, they are minor and should not cause any
11	difficulty in determining BellSouth's overall performance level.
12	
13	Attachment 1E is the Monthly State Summary (MSS) for Florida for October
14	2001. The MSS contains 2,337 sub-metrics based on the Georgia Public
15	Service Commission (GPSC) Docket 7892-U. As shown in Attachment 1E,
16	there were 901 sub-metrics for which there was CLEC activity in October
17	2001 and that were compared to either benchmarks or retail analogues.
18	BellSouth met or exceeded the criteria for 733 of these 901 sub-metrics, or
19	81%. The remainder (1,436) of the sub-metrics were either diagnostic (916),
20	had no CLEC activity (480), were parity by design (10), are still under
21	development (2) or are excluded (28) due to data calculation deficiencies.
22	

1 As explained in previous updates to this Exhibit, three of the measures were 2 identified by BellSouth as having deficiencies in their calculations and were 3 investigated and evaluated for appropriate program code corrections. These 4 three measures were Average Jeopardy Notice Interval, FOC & Reject Completeness (including the "Multiple Responses" sub-metrics), and LNP 5 6 Disconnect Timeliness. Program coding modifications have been completed 7 for the FOC and Reject Completeness measures, and the equity indications 8 are now included in the sub-metric counts for October. The Average Interval measurement is still undergoing 9 Jeopardy Notice program 10 modifications, and the LNP Disconnect Timeliness measure is still being 11 evaluated for significant design modifications. Even though these measures 12 are included in the MSS and in the total number of measurements calculation 13 (2,337), the results for these two measures were excluded from the "Met/Total" (733/901) percentage calculations. As the program coding 14 15 corrections are completed, the additional sub-metrics affected by the changes 16 will be included in the Exhibit updates.

17

During the three-month period of August through October 2001, there were a total of 680 sub-metrics that had CLEC activity for all three months and that were compared with either a benchmark or retail analogue. Of those 680 sub-metrics, 595 or 88% satisfied the comparison criteria for a minimum of two of the three months.

23

1 Two general issues can impact the degree to which BellSouth's performance data is meaningful. First, the extreme disaggregation of the data in the 2 3 reports often dilutes the universe size of individual measurements, which in 4 turn reduces the confidence level of each of the individual Z-test results. As a 5 result, there are many performance measurements for which the results are 6 statistically inconclusive due to the small number of observations. Second, in 7 situations in which there are a large number of observations and the 8 difference between the means is very small, the results can be misleading 9 and not indicative of the absolute level of performance that BellSouth 10 provides to CLECs.

11

With respect to the first issue, in many cases, the extensive levels of disaggregation leads to numerous sub-metrics with fewer than 30 observations, which is generally accepted as the smallest number of observations for application of the Z-test. Despite this fact, BellSouth has reported results for all of the measures, even those with statistically inconclusive universe sizes.

18

The second issue arises in situations where BellSouth provides very high quality service to both BellSouth's retail units and the CLECs, where there are very large universe sizes, and the difference between the means is very small. This scenario can cause an apparent missed condition from a quantitative viewpoint. For example, in October 2001, the % Missed

1 Installation Appointments (%MIA), for Residence / Non-Dispatch / < 10 Circuits (A.2.11.1.1.2) showed that BellSouth retail had 0.04% missed 2 3 appointments for the 746,483 scheduled orders. The CLEC %MIA for the 4 same period is 0.15% missed appointments for 54,436 scheduled orders. 5 While there is very little difference in the results, only eleven one hundredths 6 of a percentage point, the universe is so large that the Z-test becomes overly 7 sensitive to any difference. As a result, the statistical test shows that the sub-8 metric missed the standard criteria, but BellSouth's actual performance is at a 9 very high level for both the CLECs and BellSouth retail, in this case, greater 10 than 99.8%. From a practical point of view, the CLECs' ability to compete has 11 not been hindered, even though the statistical result does not technically meet 12 the retail analogue.

13

14 In reviewing the data, the Florida Public Service Commission (Commission) 15 should use the data as a tool in analyzing whether BellSouth has met its 16 commitments. It is not a substitute for the qualitative evaluation of 17 BellSouth's performance. The commission will still need to conduct a 18 qualitative assessment of the data that considers, among other things, 19 universe size, distributional properties of the data, as well as overall 20 performance.

21

Each sub-metric designated as having not satisfied the benchmark or BellSouth retail analogue requirement for August, September and/or October

1	2001 is included in this Exhibit. Each sub-metric discussed is labeled as
2	being missed in any one or more of the months (August/September/October)
3	included in this filing.
4	
5	The following paragraphs will address specific performance measurements
6	associated with each checklist item.
7	
8	B. CHECKLIST ITEM 1 - INTERCONNECTION
9	
10	1. Collocation
11	BellSouth provides three separate collocation reports: 1) Average Response
12	Time; 2) Average Arrangement Time; and 3) Percent of Due Dates Missed.
13	Section E in Attachment 1E, Items E.1.1.1 through E.1.3.2, provides these
14	results. BellSouth met the approved benchmarks for all 8 of the 8 sub-metrics
15	in August and all 10 of the 10 sub-metrics in September and October 2001
16	with CLEC activity.
17	
18	For the three-month period, August through October 2001, there were 6 sub-
19	metrics for which there was CLEC activity in all three months and were
20	compared to retail analogues or benchmarks. All 6 of these sub-metrics met
21	the retail analogue/benchmark comparisons in all three months.
22	
23	2. Local Interconnection Trunking

1 Trunking Reports

Attachment 1E, Section C, Items C.1.1 to C.4.2 of the MSS contains data for
ordering, provisioning, maintenance and repair, and billing associated with
Local Interconnection Trunks.

5

6 In August 2001, BellSouth met 12 of 18 sub-metrics or 67% and in 7 September, met 20 of the 25 sub-metrics or 80% of the applicable 8 benchmarks/analogues for all local interconnection trunking measures having 9 CLEC activity. In October, BellSouth met 19 of the 25 sub-metrics or 76% of 10 the benchmarks/retail analogues having CLEC activity. The sub-metrics that 11 did not meet the benchmarks/retail analogues for August, September and/or 12 October 2001 are as follows:

13

14 Reject Interval / Local Interconnection Trunks (C.1.2) (September/October)

BellSouth met the benchmark interval for 47 of the 57 rejected ASRs for this
sub-metric in September and 57 of the 72 rejected ASRs in October 2001.
The 85% benchmark required that 49 of the 57 September rejects and 62 of
the 72 rejected ASRs in October be returned within the 4-day interval.
BellSouth met the benchmark for this sub-metric in August 2001.

- 20
- 21 <u>FOC & Reject Response Completeness / Local Interconnection Trunks</u>
 22 (C.1.2) (October)

1	BellSouth met the standard criteria for 99 of the 111 responses returned for
2	this sub-metric in October 2001. The 95% benchmark required that 106 of
3	the 111 meet the criteria. BellSouth met the benchmark for this sub-metric in
4	August and September 2001.
5	
6	Order Completion Interval / Local Interconnection Trunks (C.2.1)
7	(August/September/October)
8	Investigation has identified that a significant number of the orders for this sub-
9	metric are for new trunk groups. These orders have a normal installation
10	interval of 30 business days. Trunk group augment orders receive a 20
11	business day completion interval unless the customer requests a longer
12	interval. These intervals are consistent with the 21 to 27-day OCI intervals for
13	CLEC orders for this sub-metric in August, September and October.
14	
15	% Missed Installation Appointments / Local Interconnection Trunks (C.2.5)
16	(August)
17	BellSouth missed 1 of the 34 scheduled appointments for this sub-metric in
18	August 2001. The one missed appointment in August was due to an order
19	being twice rescheduled at the customer's request. BellSouth met the
20	benchmark for this sub-metric in September and October 2001.
21	
22	Provisioning Troubles within 30 Days/ Local Interconnection Trunks (C.2.6)
23	(October)

1 Analysis of the result for this sub-metric revealed that all 72 trouble reports 2 generated were involved with the same event. One CLEC, performing 3 provisioning activities, requested that the trunks be busied out while the work 4 was performed. The trouble ticket should have been entered as "info only" 5 and excluded from this measurement. With the proper coding, this sub-metric 6 would have met the retail analogue comparison for the month. BellSouth met 7 the retail analogue comparison for this sub-metric in August and September 8 2001.

9

Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits / Dispatch (C.2.11.2.1) (August)

12 BellSouth met the standard for 6 of the 7 orders reviewed for August 2001. 13 The 95% benchmark set a requirement of all 7 orders for August based on 14 the quantity of orders for this sub-metric. With a universe size of only 7 orders 15 and a 95% benchmark, a miss on only one order causes a miss for the entire 16 sub-metric. Although BellSouth is within one order of the benchmark for this 17 measure, BellSouth continues to focus on this measurement in order to 18 improve results to meet the benchmark. BellSouth met the benchmark for 19 this sub-metric in September and October 2001.

20

21 Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits / Non 22 Dispatch (C.2.11.2.2) (August/October)

1 BellSouth met the standard for 22 of the 24 orders reviewed for this sub-2 metric in August and 18 of the 19 orders reviewed in October 2001. The 95% 3 benchmark set a requirement of 23 orders in August and all 19 orders in 4 October based on the quantity of orders for this sub-metric. BellSouth 5 continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in 6 7 September 2001.

8

9 Customer Trouble Report Rate / Local Interconnection Trunks / Non-Dispatch

10 (C.3.2.2) (August/September)

11 BellSouth provided over 99.8% trouble free service for both retail and the 12 CLECs for this sub-metric for the months of August and September. When 13 BellSouth provisions high quality service coupled with very large universe 14 sizes, it can cause an apparent out of equity condition from a quantitative 15 viewpoint. In these cases, there is very little variation and the universe size 16 is so large that the Z-test becomes overly sensitive to any difference. In other 17 words, the statistical test shows that the measurement does not meet the 18 fixed critical value when compared with the retail analogue, but BellSouth's 19 actual performance for both CLECs and its own retail operations is at a very 20 high level – in this case over 99%. From a practical point of view, the CLECs' 21 ability to compete has not been hindered even though the statistical results 22 may technically show that BellSouth failed to meet the benchmark/analogue. 23 BellSouth met the retail analogue for this sub-metric in October 2001.

1	
2	Maintenance Average Duration / Local Interconnection Trunks / Dispatch
3	(C.3.3.1) (September)
4	There was only one order for this sub-metric in September 2001. The small
5	universe of orders for the month does not provide a statistically conclusive
6	comparison to the retail analogue. BellSouth met the retail analogue
7	comparison for this sub-metric in October 2001. There was no CLEC activity
8	for this sub-metric in August 2001.
9	
10	% Repeat Troubles within 30 Days / Local Interconnection Trucks (C.3.4.2)
11	(October)
12	The results indicated that there were 72 repeat trouble reports for this sub-
13	metric in October 2001. All 72 of these repeat reports were associated with
14	one group of trunks being busied out multiple times during cooperative testing
15	with a CLEC during their switch modification work. These reports should
16	have been charged as "info only" and not counted against this measurement.
17	With proper coding, this sub-metric would have met the retail analogue
18	comparison for the month. BellSouth met the retail analogue comparison for
19	this sub-metric in August and September 2001.
20	
21	Invoice Accuracy - Interconnection (C.4.1) (August/September)
22	The CLECs experienced Local Interconnection invoice accuracy rates that
23	were less than the invoices BellSouth sends to its customers during August
24	and September 2001 (98.30% accuracy for BellSouth versus 51.41% for the

1 CLEC invoices for August and 98.61% accuracy for BellSouth versus 97.84% 2 for the CLEC invoices in September). The difference in August performance 3 was the result of two CLEC customers being charged incorrect rates. These 4 rates have now been corrected. The difference in September performance 5 was the result of provisioning and system errors that caused the over billing of 6 one CLEC customer. BellSouth met the retail analogue comparison for this 7 sub-metric in October 2001.

8

9 Trunk Blockage

BellSouth has developed a trunk blocking report that compares BellSouth 10 11 retail's trunk blockage rates to those of CLECs. The report, Trunk Group 12 Performance Report (TGP), Attachment 3E, displays trunk blocking in a 13 manner that accurately represents the customer experience. The TGP report 14 tabulates actual call blocking as a percentage of call attempts for all 15 comparable trunk groups administered by BellSouth that handle CLEC and 16 BellSouth traffic, and provides a direct comparison of hour-by-hour blocking 17 between CLEC and BellSouth trunk groups. The analogue/benchmark for the Trunk Group Performance measure is any consecutive two-hour period in 24 18 hours where CLEC blockage exceeds BeilSouth blockage by more than 19 20 0.5%. BellSouth met or exceeded the benchmark for this sub-metric in 21 August, September and October 2001.

22

23 C. CHECKLIST ITEM 2 – UNBUNDLED NETWORK ELEMENTS (UNE)

24

1 This section addresses the measures associated with UNEs under checklist 2 item 2. Attachment 1E, Sections B1 – B3, provides data that is divided into 3 Ordering, Provisioning and Maintenance & Repair operations. In general, the 4 Ordering function is disaggregated into 17 sub-metrics, the Provisioning function has 19 sub-metrics, and there are 12 sub-metrics for the 5 6 Maintenance & Repair function. All Ordering measures will be included in this checklist item because of the overall relationship of the mechanized, partially 7 mechanized and manual processing of Local Service Requests (LSRs). The 8 9 Provisioning and Maintenance & Repair measures for the following products are included in the checklist item as shown below: 10

- 11 <u>Product</u>
- 12 Combo (Loop & Port)
- 13 Combo (Other)
- 14 Other Design
- 15 Other Non-Design
- 16 xDSL Loop
- 17 UNE ISDN Loop
- 18 Line Sharing
- 19 2w Analog Loop Design
- 20 2w Analog Loop Non Design
- 21 2w Analog Loop w/INP Design
- 22 2w Analog Loop w/INP Non Design
- 23 2w Analog Loop w/LNP Design

- Checklist Item:
- #2 -- Unbundled Network Elements
- #2 Unbundled Network Elements
- #2 Unbundled Network Elements
- #2 Unbundled Network Elements
- #4 Unbundled Local Loops
 - #4 Unbundled Local Loops
 - #4 Unbundled Local Loops

1	2w Analog Loop w/LNP Non Design	#4 Unbundled Local Loops
2	Digital Loop < DS1	#4 – Unbundled Local Loops
3	Digital Loop => DS1	#4 – Unbundled Local Loops
4	Local Interoffice Transport	#5 - Unbundled Local Transport
5	Switch Ports	#6 - Unbundled Local Switching
6	INP Standalone	#11 – Local Number Portability
7	LNP Standalone	#11 – Local Number Portability
8		
9	An overall review of the UNE sub-r	netrics for Ordering, Provisioning,
10	Maintenance & Repair and Billing i	ndicates that BellSouth met the
11	benchmark/analogue for 83%, 84% and	81% of the sub-metrics during the
12	months of August, September and Octobe	er 2001, respectively.
13		
14	For the three-month period, August thro	ugh October 2001, there were 370
15	sub-metrics in the UNE measurements f	or which there was CLEC activity in
16	all three months and that were compared	to retail analogues or benchmarks.
17	Of those 370 sub-metrics, 324 su	b-metrics (88%) met the retail
18	analogue/benchmark comparisons in at le	east two of the three months.
19		
20	1. UNE Ordering Measures	
21		
22	Items B.1.1 – B.1.19 in Attachment 1	E show data for Percent Rejected
23	Service Requests, Reject Interval, FO	C Timeliness and FOC & Reject

- Response Completeness. These reports are disaggregated by interface type
 (electronic, partial electronic and manual), as well as product type.
- 3

4 **Reject Interval**

Items B.1.4 - B.1.8 in Attachment 1E examine the Reject Interval for the
month of October 2001. For orders submitted electronically, the benchmark
is 97% within one hour. In August, September and October 2001, 95%, 90%
and 80%, respectively, of all rejected service requests were delivered within
the one-hour benchmark interval. (See the write-up below for Items B.1.4.2 –

10 B.1.4.17 for further discussion concerning electronically submitted orders.)

11

For partially mechanized orders, which are LSRs submitted electronically and requiring service representative intervention, the benchmark is 85% returned within 10 hours. BellSouth exceeded this benchmarks in August, September and October 2001, with 92%, 92% and 90%, respectively, of partially mechanized rejects being returned to the CLECs within the benchmark interval.

18

For manual orders, the current benchmark is 85% within 24 hours. BellSouth also exceeded this requirement, with 95% of the LSRs submitted manually being returned to the CLECs within the 24-hour time period in August, 99% in September and 99% in October 2001.

23

- The following sub-metrics did not meet the established benchmarks in
 August, September and October 2001:
- 3
- 4 Reject Interval / Combo (Loop & Port) / Electronic (B.1.4.3)
- 5 (August/September/October)
- 6 Reject Interval / Line Sharing / Electronic (B.1.4.7) (September/October)
- 7 Reject Interval / 2w Analog Loop Design / Electronic (B.1.4.8)
- 8 (August/September/October)
- 9 Reject Interval / 2w Analog Loop Non-Design / Electronic (B.1.4.9)
- 10 (August/September/October)
- 11 Reject Interval / 2w Analog Loop w/LNP Design / Electronic (B.1.4.12)
- 12 (August/September/October)
- 13 Reject Interval / 2w Analog Loop w/LNP Non-Design / Electronic (B.1.4.13)
- 14 (October)
- 15 Reject Interval / Other Design / Electronic (B.1.4.14)
- 16 (August/September/October)
- 17 Reject Interval / Other Non-Design / Electronic (B.1.4.15)
- 18 (September/October)
- 19 Reject Interval / INP (Standalone) / Electronic (B.1.4.16) (August)
- 20 Reject Interval / LNP (Standalone) / Electronic (B.1.4.17)
- 21 (September/October)
- 22 The current benchmark for these sub-metrics is >= 97% within one hour.
- 23 BellSouth is conducting a detailed root cause analysis of the process for

electronic rejects. This analysis addresses the ordering systems (EDI, TAG,
 and LENS) used by the CLECs and the back-end legacy applications, such
 as SOCS, that are accessed by the ordering systems.

4

5 Thus far, the analysis has determined that many of the LSRs that did not 6 meet the one-hour benchmark in August and September were issued 7 between 11:00 p.m. and 4:30 a.m. Between these hours, the system is 8 unable to process LSRs because certain of the back-end legacy systems are 9 out of service. LSRs submitted during these periods should have been 10 excluded from the measurement. BellSouth implemented a program coding 11 change in September to exclude these LSRs from this measure.

12

13 With the May 2001, data month, BellSouth was directed to change the time 14 stamp identification for the start and complete times of the interval for this 15 measurement from the Local Exchange Ordering (LEO) System to the CLEC 16 ordering interface system (TAG or EDI). However, with this change, 17 BellSouth is currently unable to identify multiple issues of the same version of 18 LSRs that have been rejected (fatal rejects). These rejected LSRs should be excluded from the measurement. If there are multiple issues of the same 19 version, the measure currently calculates the interval from the initial issue to 20 21 the final issue of the LSR returned to the CLEC, Reject or FOC. Consequently, BellSouth's performance level is inappropriately understated. 22 23 BellSouth is currently working to determine a fix for this issue.

1	
2	Reject Interval / UNE ISDN / Partially Electronic (B.1.7.6)
3	(September/October)
4	There were only three LSRs rejected for this sub-metric in September and
5	one rejected LSR in October 2001. The small universe of orders for these
6	months does not provide a conclusive benchmark comparison. There was no
7	CLEC activity for this sub-metric in August 2001.
8	
9	
10	Reject Interval / Line Sharing / Partially Electronic (B.1.7.7) (October)
11	There were only eleven LSRs rejected for this sub-metric in October 2001.
12	The small universe of orders for the month does not provide a conclusive
13	benchmark comparison. BellSouth met the benchmark for this sub-metric in
14	August and September 2001.
15	
16	Reject Interval / 2w Analog Loop Non-Design / Partially Electronic
17	(B.1.6.9/B.1.7.9) (September/October)
18	In September, BellSouth met the 10-hour benchmark interval for 66 of the 78
19	or 84.62% of the rejected LSRs in this sub-metric. Normal rounding
20	convention would indicate that there is no significant difference between the
21	CLEC result and the 85% benchmark. In October 2001, BellSouth met the
22	benchmark interval for 123 of the 146 rejected LSRs – only one LSR short of

1	meeting the benchmark for the sub-metric for the month. BellSouth met the
2	10-hour benchmark for this sub-metric in August 2001.
3	
4	Reject Interval / 2w Analog Loop w/LNP Design / Partially Electronic
5	(B.1.7.12) (September)
6	BellSouth met the benchmark for 172 of the 203 or 84.73% of the LSRs
7	rejected in this sub-metric for September 2001. Normal rounding convention
8	would indicate that there is no significant difference between the CLEC result
9	and the 85% benchmark. BellSouth met the benchmark for this sub-metric in
10	August and October 2001.
11	
12	Reject Interval / 2w Analog Loop w/LNP Non-Design / Partially Electronic
13	(B.1.6.13/B.1.7.13) (August/October)
14	BellSouth met the 10-hour period for 791 (84.5%) of the 936 LSRs rejected
15	for this sub-metric in August 2001. This was only 5 LSRs short (0.5%) of the
16	number required to meet the benchmark for the overall sub-metric for the
17	month. In October 2001, BellSouth met the benchmark for 376 of the 460
18	rejected LSRs for this sub-metric. BellSouth met the 18-hour benchmark for
19	this sub-metric in September 2001.
20	-
21	FOC Timeliness

,

1

For LSRs submitted electronically, the benchmark is 95% of the FOCs returned within 3 hours. BellSouth met the benchmark interval for 98%, 99%

1 and 99% of the electronically submitted LSRs in August. September and 2 October 2001, respectively. For partially mechanized LSRs, the benchmark 3 is 85% of FOCs returned within 10 hours. BellSouth met the benchmark for 4 95%, 95% and 94% of partially electronic FOCs in August, September and October 2001, respectively. For LSRs submitted manually, the benchmark is 5 6 85% returned within 36 hours. BellSouth met the benchmark interval for 99%, 7 98% and 99% of the manual LSRs submitted in August, September and 8 October 2001, respectively. The sub-metrics that did not meet the 9 benchmark in August, September and /or October 2001 are as follows:

10

11 FOC Timeliness / xDSL / Electronic (B.1.9.5) (August/September/October)

12 BellSouth met the benchmark for 644 of the 774 LSRs that received a FOC in 13 August, for 147 of the 160 FOCs for this sub-metric in September and for 211 of the 223 FOCs in October 2001. BellSouth is conducting a detailed root 14 15 cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs 16 17 and the back-end legacy applications, such as SOCS, that are accessed by 18 the ordering systems. For further information, see the explanation included 19 with the electronic reject interval measurement.

20

21 <u>FOC Timeliness / 2w Analog Loop w/LNP Design / Electronic (B.1.9.12)</u> 22 (August)

1 BellSouth met the benchmark for 50 of the 53 LSRs in August that received a 2 FOC for this sub-metric. BellSouth is conducting a detailed root cause 3 analysis of the process for electronic ordering. This analysis addresses the 4 ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-5 end legacy applications, such as SOCS, that are accessed by the ordering 6 systems. For further information, see the explanation included with the 7 electronic reject interval measurement, item B.1.4.x. BellSouth met the 8 benchmark for this sub-metric in September and October 2001.

9

10 FOC Timeliness / xDSL / Partially Electronic (B.1.12.5) (August)

BellSouth met the 10-hour benchmark for 39 of the 47 FOCs returned in
August 2001. BellSouth fell just one order short of satisfying the overall
benchmark for the sub-metric. BellSouth met the benchmark for this submetric in September and October 2001.

15

16 FOC Timeliness / Other Design / Partially Electronic (B.1.12.14) (October)

BellSouth met the 10-hour benchmark interval for 117 of the 146 FOCs
returned for this sub-metric in October 2001. BellSouth met the benchmark
for this sub-metric in August and September 2001.

20

21 FOC Timeliness / 2w Analog Loop w/INP Design / Manual (B.1.13.10) 22 (October)

BellSouth met the benchmark interval for 5 of the 6 FOCs returned for this
 sub-metric in October 2001. The small universe of orders for this sub-metric
 does not provide a conclusive benchmark comparison.

4

5 FOC & Reject Response Completeness and FOC & Reject Response

6 Completeness (Multiple Responses) Measures

7 BellSouth determined that the coding for the FOC & Reject Completeness 8 and FOC & Reject Response Completeness (Multiple Responses) measures 9 failed to include rejections that were classified as "auto clarifications." 10 BellSouth has rewritten the code to correct this problem. Effective with the 11 Exhibit update for September data, the program coding was corrected for all 12 the FOC & Reject Completeness sub-metrics for Checklist Item No. 2, UNE 13 Loop products except for: xDSL, 2w Analog Loop w/INP Design, 2w Analog 14 Loop w/INP Non-Design, 2w Analog Loop w/LNP Design, 2w Analog Loop 15 w/LNP Non-Design, INP (Standalone) and LNP (Standalone). The corrected 16 coding for these measures was implemented and effective with the October 17 The individual sub-metrics with corrected coding that missed the data. 18 required benchmarks in September and/or October 2001 will be addressed 19 separately following the next section. BellSouth did not meet the benchmark 20 in August and/or September 2001 for the FOC and Reject Response 21 Completeness and FOC & Reject Response Completeness (Multiple 22 Responses) metrics listed below:

23

- 1 FOC & Reject Response Completeness / xDSL / Electronic (B.1.14.5)
- 2 (August/September)
- 3 FOC & Reject Response Completeness / ISDN Loop / Electronic (B.1.14.6)
- 4 (August)
- 5 FOC & Reject Response Completeness / 2w Analog Loop Non Design /
- 6 Electronic (B.1.14.9) (August)
- 7 FOC & Reject Response Completeness / xDSL / Partial Electronic (B.1.15.5)
- 8 (August/September)
- 9 FOC & Reject Response Completeness / Switch Ports / Manual (B.1.16.1)
- 10 (August)
- 11 FOC & Reject Response Completeness / Local Interoffice Transport / Manual
- 12 (B.1.16.2) (August)
- 13 FOC & Reject Response Completeness / xDSL / Manual (B.1.16.5)
- 14 (August/September)
- 15 FOC & Reject Response Completeness / Line Sharing / Manual (B.1.16.7)
- 16 (August)
- 17 FOC & Reject Response Completeness / 2w Analog Loop Design / Manual
- 18 (B.1.16.8) (August)
- 19 FOC & Reject Response Completeness / 2w Analog Loop Non-Design /
- 20 Manual (B.1.16.9) (August)
- 21 FOC & Reject Response Completeness / 2w Analog Loop w/INP Design /
- 22 Manual (B.1.16.10) (August)

- 1 FOC & Reject Response Completeness / Other Design / Manual (B.1.16.14)
- 2 <u>(August)</u>
- 3 FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
- 4 <u>Electronic (B.1.17.7) (August)</u>
- 5 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 6 Loop Design / Electronic (B.1.17.8) (August)
- 7 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 8 Loop Non-Design / Electronic (B.1.17.9) (August)
- 9 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 10 / Electronic (B.1.17.14) (August)
- 11 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
- 12 <u>& Port) / Partial Electronic (B.1.18.3) (August)</u>
- 13 FOC & Reject Response Completeness (Multiple Responses) / xDSL / Partial
- 14 Electronic (B.1.18.5) (August)
- 15 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 16 Loop Design / Partial Electronic (B.1.18.8) (August)
- 17 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 18 Loop Non-Design / Partial Electronic (B.1.18.9) (August)
- 19 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 20 / Partial Electronic (B.1.18.14) (August)
- 21 FOC & Reject Response Completeness (Multiple Responses) / Local
- 22 Interoffice Transport / Manual (B.1.19.2) (August)

- 1 FOC & Reject Response Completeness (Multiple Responses) / xDSL /
- 2 <u>Manual (B.1.19.5) (August/September)</u>
- 3 FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop /
- 4 Manual (B.1.19.6) (August)
- 5 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 6 Loop Design / Manual (B.1.19.8) (August)
- 7 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 8 Loop Non-Design / Manual (B.1.19.9) (August)
- 9 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 10 Loop w/INP Design / Manual (B.1.19.10) (August/September)
- 11 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 12 / Manual (B.1.19.14) (August)

BellSouth determined that the coding for the FOC & Reject Completeness and FOC & Reject Response Completeness (Multiple Responses) measures failed to include rejections that were classified as "auto clarifications." BellSouth has rewritten the code to correct this problem. The coding changes were implemented for some products in August and for the remainder of the products in September. The sub-metric "misses" listed above were for operations prior to the implementation of the coding modifications.

20

Effective with October 2001 data, each sub-metric in the Electronic and Partial Electronic sections have been disaggregated between LSRs submitted from the EDI and TAG systems. The following FOC & Reject Response

1	Completeness sub-metrics, for which the program code has been corrected,
2	did not meet the benchmarks for September and/or October 2001:
3	
4	FOC & Reject Response Completeness / xDSL / EDI / Electronic (B.1.14.5.1)
5	(October)
6	There were only 10 orders for this sub-metric in October 2001. The small
7	universe of orders for this sub-metric does not provide a conclusive
8	benchmark comparison.
9	
10	FOC & Reject Response Completeness / xDSL / TAG / Electronic
11	(B.1.14.5.2) (October)
12	BellSouth met the benchmark standard for 325 of the 390 responses for this
13	sub-metric in October 2001. The 95% benchmark required that the criteria be
14	met for 371 of the 390 responses. BellSouth continues to focus on this
15	measurement in order to improve results to meet the benchmark.
16	
17	FOC & Reject Response Completeness / xDSL / TAG / Partial Electronic
18	(B.1.15.5.2) (October)
19	BellSouth met the benchmark standard for 20 of the 43 responses for this
20	sub-metric in October 2001. The 95% benchmark required that the criteria be
21	met for 41 of the 43 responses. BellSouth continues to focus on this
22	measurement in order to improve results to meet the benchmark.
23	

1	FOC & Reject Response Completeness / Local Interoffice Transport / Manual
2	(B.1.16.2) (October)
3	BellSouth met the benchmark standard for 57 of the 62 responses for this
4	sub-metric in October 2001. The 95% benchmark required that the criteria be
5	met for 59 of the 62 responses. BellSouth continues to focus on this
6	measurement in order to improve results to meet the benchmark.
7	
8	FOC & Reject Response Completeness / Combo (Loop & Port) / Manual
9	(B.1.16.3) (October)
10	BellSouth met the benchmark standard for 812 of the 859 responses for this
11	sub-metric in October 2001. The 95% benchmark required that the criteria be
12	met for 817 of the 859 responses. BellSouth continues to focus on this
13	measurement in order to improve results to meet the benchmark.
14	
15	FOC & Reject Response Completeness / Line Sharing / Manual (B.1.16.7)
16	(September/October)
17	BellSouth met the benchmark for 192 of the 203 or 94.56% of the orders for
18	this sub-metric in September 2001. Normal rounding convention would
1 9	indicate that there was not a significant difference between the CLEC result
20	and the 95% benchmark for this sub-metric in September. BellSouth met the
21	benchmark standard for 142 of the 153 responses for this sub-metric in
22	October 2001. The 95% benchmark required that the criteria be met for 146

1	of the 153 responses. BellSouth continues to focus on this measurement in
2	order to improve results to meet the benchmark.
3	
4	FOC & Reject Response Completeness / 2w Analog Loop Design / Manual
5	(B.1.16.8) (September)
6	BellSouth met the benchmark for 122 of the 130 orders for this sub-metric in
7	September 2001. The 95% benchmark set a requirement of 124 orders
8	based on the number of orders for this sub-metric. BellSouth continues to
9	focus on this measurement in order to improve results to meet the
10	benchmark. BellSouth met the benchmark for this sub-metric in October
11	2001.
12	
13	FOC & Reject Response Completeness / 2w Analog Loop Non-Design /
14	Manual (B.1.16.9) (September/October)
15	BellSouth met the benchmark for 861 of the 928 orders for this sub-metric in
16	September and for 1,275 of the 1,378 responses in October 2001. The 95%
17	benchmark set a requirement of 882 orders for September and for 1,310
18	orders in October based on the number of orders for this sub-metric.
19	BellSouth continues to focus on this measurement in order to improve results
20	to meet the benchmark.
21	
22	FOC & Reject Response Completeness / Other Design / Manual (B.1.16.14)
23	(October)

1	BellSouth met the benchmark standard for 410 of the 441 responses for this
2	sub-metric in October 2001. The 95% benchmark required that the criteria be
3	met for 419 of the 441 responses. BellSouth continues to focus on this
4	measurement in order to improve results to meet the benchmark.
5	
6	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
7	<u>& Port) / Electronic (B.1.17.3) (September)</u>
8	BellSouth met the benchmark for 6,459 of the 7,056 orders for this sub-metric
9	in September 2001. The 95% benchmark set a requirement of 6,704 of the
10	7,056 orders based on the number of orders for this sub-metric. BellSouth
11	continues to focus on this measurement in order to improve results to meet
12	the benchmark.
13	
14	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
15	<u>& Port) / EDI / Electronic (B.1.17.3.1) (October)</u>
16	BellSouth met the benchmark for 154 of the 237 orders for this sub-metric in
17	September 2001. The 95% benchmark set a requirement of 226 of the 237
18	orders based on the number of orders for this sub-metric. BellSouth
19	continues to focus on this measurement in order to improve results to meet
20	the benchmark.
21	
22	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
23	<u>& Port) / TAG / Electronic (B.1.17.3.2) (October)</u>

1	BellSouth met the benchmark for 8,765 of the 9,548 orders for this sub-metric
2	in September 2001. The 95% benchmark set a requirement of 9,071 of the
3	9,548 orders based on the number of orders for this sub-metric. BellSouth
4	continues to focus on this measurement in order to improve results to meet
5	the benchmark.
6	
7	FOC & Reject Response Completeness (Multiple Responses) / UNE ISDN /
8	TAG / Electronic (B.1.17.6.2) (October)
9	There were only 10 orders for this sub-metric in October 2001. The small
10	universe of orders for this sub-metric does not provide a conclusive
11	benchmark comparison.
12	
13	FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
14	TAG / Electronic (B.1.17.7.2) (October)
14 15	TAG / Electronic (B.1.17.7.2) (October) BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub-
15	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub-
15 16	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub- metric in September 2001. Normal rounding convention would indicate that
15 16 17	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub- metric in September 2001. Normal rounding convention would indicate that there was not a significant difference between the CLEC result and the 95%
15 16 17 18	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub- metric in September 2001. Normal rounding convention would indicate that there was not a significant difference between the CLEC result and the 95%
15 16 17 18 19	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub- metric in September 2001. Normal rounding convention would indicate that there was not a significant difference between the CLEC result and the 95% benchmark for this sub-metric in October.
15 16 17 18 19 20	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub- metric in September 2001. Normal rounding convention would indicate that there was not a significant difference between the CLEC result and the 95% benchmark for this sub-metric in October.

1	orders based on the number of orders for this sub-metric. BellSouth
2	continues to focus on this measurement in order to improve results to meet
3	the benchmark.
4	
5	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
6	Loop Design / EDI / Electronic (B.1.17.8.1) (October)
7	BellSouth met the benchmark for 180 of the 232 orders for this sub-metric in
8	September 2001. The 95% benchmark set a requirement of 221 of the 232
9	orders based on the number of orders for this sub-metric. BellSouth
10	continues to focus on this measurement in order to improve results to meet
11	the benchmark.
12	
13	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
14	Loop Non-Design / TAG / Electronic (B.1.17.9.2) (October)
15	BellSouth met the benchmark for 495 of the 531 orders for this sub-metric in
16	September 2001. The 95% benchmark set a requirement of 505 of the 531
17	orders based on the number of orders for this sub-metric. BellSouth
18	continues to focus on this measurement in order to improve results to meet
19	the benchmark.
20	-
21	FOC & Reject Response Completeness (Multiple Responses) / Other Design
22	/ Electronic (B.1.17.14) (September)

1	BellSouth met the benchmark for 77 of the 115 orders for this sub-metric in
2	September 2001. The 95% benchmark set a requirement of 110 of the 115
3	orders based on the number of orders for this sub-metric. BellSouth
4	continues to focus on this measurement in order to improve results to meet
5	the benchmark.
6	
7	FOC & Reject Response Completeness (Multiple Responses) / Other Design
8	/ EDI / Electronic (B.1.17.14.1) (October)
9	BellSouth met the benchmark for 10 of the 17 orders for this sub-metric in
10	September 2001. The 95% benchmark set a requirement of all 17 of the 17
11	orders based on the number of orders for this sub-metric. BellSouth
12	continues to focus on this measurement in order to improve results to meet
13	the benchmark.
14	
15	FOC & Reject Response Completeness (Multiple Responses) / Other Design
16	/ TAG / Electronic (B.1.17.14.2) (October)
17	BellSouth met the benchmark for 179 of the 230 orders for this sub-metric in
18	September 2001. The 95% benchmark set a requirement of 219 of the 230
19	orders based on the number of orders for this sub-metric. BellSouth
20	continues to focus on this measurement in order to improve results to meet
21	the benchmark.
22	

•

1	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
2	Design / Electronic (B.1.17.15) (September)
3	BellSouth met the benchmark for 1,513 of the 3,193 orders for this sub-metric
4	in September 2001. The 95% benchmark set a requirement of 3,034 of the
5	3,193 orders based on the number of orders for this sub-metric. BellSouth
6	continues to focus on this measurement in order to improve results to meet
7	the benchmark.
8	
9	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
10	Design / EDI / Electronic (B.1.17.15.1) (October)
11	BellSouth met the benchmark for 3,620 of the 6,900 orders for this sub-metric
12	in September 2001. The 95% benchmark set a requirement of 6,555 of the
13	6,900 orders based on the number of orders for this sub-metric. BellSouth
14	continues to focus on this measurement in order to improve results to meet
15	the benchmark.
16	
17	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
18	Design / TAG / Electronic (B.1.17.15.2) (October)
19	BellSouth met the benchmark for 744 of the 947 orders for this sub-metric in
20	September 2001. The 95% benchmark set a requirement of 900 of the 947
21	orders based on the number of orders for this sub-metric. BellSouth
22	continues to focus on this measurement in order to improve results to meet
23	the benchmark.

1	
2	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
3	& Port) / Partial Electronic (B.1.18.3) (September)
4	BellSouth met the benchmark for 3,692 of the 4,018 orders for this sub-metric
5	in September 2001. The 95% benchmark set a requirement of 3,818 of the
6	4,018 orders based on the number of orders for this sub-metric. BellSouth
7	continues to focus on this measurement in order to improve results to meet
8	the benchmark.
9	
10	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
11	<u>& Port) / EDI / Partial Electronic (B.1.18.3.1) (October)</u>
12	BellSouth met the benchmark for 232 of the 255 orders for this sub-metric in
13	September 2001. The 95% benchmark set a requirement of 243 of the 255
14	orders based on the number of orders for this sub-metric. BellSouth
15	continues to focus on this measurement in order to improve results to meet
16	the benchmark.
17	
18	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
19	& Port) / TAG / Partial Electronic (B.1.18.3.2) (October)
20	BellSouth met the benchmark for 5,610 of the 6,058 orders for this sub-metric
21	in September 2001. The 95% benchmark set a requirement of 5,756 of the
22	6,058 orders based on the number of orders for this sub-metric. BellSouth

1	continues to focus on this measurement in order to improve results to meet
2	the benchmark.
3	
4	FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
5	TAG / Partial Electronic (B.1.18.7.2) (October)
6	BellSouth met the benchmark for 59 of the 63 orders for this sub-metric in
7	September 2001. The 95% benchmark set a requirement of 60 of the 63
8	orders based on the number of orders for this sub-metric. BellSouth
9	continues to focus on this measurement in order to improve results to meet
10	the benchmark.
11	
12	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
13	Loop Design / Partial Electronic (B.1.18.8) (September)
14	BellSouth met the benchmark for 348 of the 403 orders for this sub-metric in
15	September 2001. The 95% benchmark set a requirement of 383 of the 403
16	orders based on the number of orders for this sub-metric. BellSouth
17	continues to focus on this measurement in order to improve results to meet
18	the benchmark.
19	
20	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
21	Loop Design / EDI / Partial Electronic (B.1.18.8.1) (October)
22	BellSouth met the benchmark for 170 of the 184 orders for this sub-metric in
23	September 2001. The 95% benchmark set a requirement of 175 of the 184

1	orders based on the number of orders for this sub-metric. BellSouth
2	continues to focus on this measurement in order to improve results to meet
3	the benchmark.
4	
5	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
6	Loop Design / TAG / Partial Electronic (B.1.18.8.2) (October)
7	BellSouth met the benchmark for 120 of the 133 orders for this sub-metric in
8	September 2001. The 95% benchmark set a requirement of 127 of the 133
9	orders based on the number of orders for this sub-metric. BellSouth
10	continues to focus on this measurement in order to improve results to meet
11	the benchmark.
12	
13	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
14	Loop Non-Design / TAG / Partial Electronic (B.1.18.9.2) (October)
15	BellSouth met the benchmark for 794 of the 847 orders for this sub-metric in
16	September 2001. The 95% benchmark set a requirement of 805 of the 847
17	
	orders based on the number of orders for this sub-metric. BellSouth
18	orders based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet
18 19	
	continues to focus on this measurement in order to improve results to meet
19	continues to focus on this measurement in order to improve results to meet

1	BellSouth met the benchmark for 561 of the 598 orders for this sub-metric in
2	September 2001. The 95% benchmark set a requirement of 569 of the 598
3	orders based on the number of orders for this sub-metric. BellSouth
4	continues to focus on this measurement in order to improve results to meet
5	the benchmark.
6	
7	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
8	Loop w/LNP Design / TAG / Partial Electronic (B.1.18.12.2) (October)
9	BellSouth met the benchmark for 219 of the 231 orders for this sub-metric in
10	September 2001. The 95% benchmark set a requirement of 220 of the 231
11	orders based on the number of orders for this sub-metric. BellSouth
12	continues to focus on this measurement in order to improve results to meet
13	the benchmark.
14	
15	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
16	Loop w/LNP Non-Design / EDI / Partial Electronic (B.1.18.13.1) (October)
17	There were only 3 orders for this sub-metric in October 2001. The small
18	universe of orders for this sub-metric does not provide a conclusive
19	benchmark comparison.
20	-
21	FOC & Reject Response Completeness (Multiple Responses) / Other Design
22	/ Partial Electronic (B.1.18.14) (September)

.

1	BellSouth met the benchmark for 89 of the 119 orders for this sub-metric in
2	September 2001. The 95% benchmark set a requirement of 114 of the 119
3	orders based on the number of orders for this sub-metric. BellSouth
4	continues to focus on this measurement in order to improve results to meet
5	the benchmark.
6	
7	FOC & Reject Response Completeness (Multiple Responses) / Other Design
8	/ EDI / Partial Electronic (B.1.18.14.1) (October)
9	BellSouth met the benchmark for 24 of the 26 orders for this sub-metric in
10	September 2001. The 95% benchmark set a requirement of 25 of the 26
11	orders based on the number of orders for this sub-metric. BellSouth
12	continues to focus on this measurement in order to improve results to meet
13	the benchmark.
14	
15	FOC & Reject Response Completeness (Multiple Responses) / Other Design
16	/ TAG / Partial Electronic (B.1.18.14.2) (October)
17	BellSouth met the benchmark for 148 of the 183 orders for this sub-metric in
18	September 2001. The 95% benchmark set a requirement of 174 of the 183
19	orders based on the number of orders for this sub-metric. BellSouth
20	continues to focus on this measurement in order to improve results to meet
21	the benchmark.
22	

1	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
2	Design / Partial Electronic (B.1.18.15) (September)
3	BellSouth met the benchmark for 1,592 of the 1,688 orders for this sub-metric
4	in September 2001. The 95% benchmark set a requirement of 1,604 of the
5	1,688 orders based on the number of orders for this sub-metric. BellSouth
6	continues to focus on this measurement in order to improve results to meet
7	the benchmark.
8	
9	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
10	Design / EDI / Partial Electronic (B.1.18.15.1) (October)
11	BellSouth met the benchmark for 1,801 of the 1,958 orders for this sub-metric
12	in September 2001. The 95% benchmark set a requirement of 1,851 of the
13	1,958 orders based on the number of orders for this sub-metric. BellSouth
14	continues to focus on this measurement in order to improve results to meet
15	the benchmark.
16	
17	FOC & Reject Response Completeness (Multiple Responses) / Local
18	Interoffice Transport / Manual (B.1.19.2) (September/October)
19	BellSouth met the benchmark for 37 of the 41 orders for this sub-metric in
20	September and for 46 of the 57 orders in October 2001. The 95% benchmark
21	set a requirement of 39 of the 41 orders in September and for 55 of the 57
22	orders in October based on the number of orders for this sub-metric.

1	BellSouth continues to focus on this measurement in order to improve results
2	to meet the benchmark.

4	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
5	<u>& Port) / Manual (B.1.19.3) (September/October)</u>
6	BellSouth met the benchmark for 1,241 of the 1,334 orders for this sub-metric
7	in September and for 757 of the 812 orders in October 2001. The 95%
8	benchmark set a requirement of 1,268 of the 1,334 orders in September and
9	for 772 of the 812 orders in October based on the number of orders for this
10	sub-metric. BellSouth continues to focus on this measurement in order to
11	improve results to meet the benchmark.
12	
13	FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop /
14	Manual (B.1.19.6) (September)
15	BellSouth met the benchmark for 452 of the 485 orders for this sub-metric in
16	September 2001. The 95% benchmark set a requirement of 461 of the 485
17	orders based on the number of orders for this sub-metric. BellSouth
18	continues to focus on this measurement in order to improve results to meet
19	the benchmark. BellSouth met the benchmark for this sub-metric in October
20	2001
21	
22	FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
23	Manual (B.1.19.7) (September)

2	September 2001. The 95% benchmark set a requirement of 183 of the 192
3	orders based on the number of orders for this sub-metric. BellSouth
4	continues to focus on this measurement in order to improve results to meet
5	the benchmark. BellSouth met the benchmark for this sub-metric in October
6	2001.
7	
8	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
9	Loop Design / Manual (B.1.19.8) (September/October)
10	BellSouth met the benchmark for 115 of the 122 orders for this sub-metric in
11	September and for 193 of the 204 orders in October 2001. The 95%
12	benchmark set a requirement of 116 of the 122 orders in September and for
13	194 of the 204 orders in October based on the number of orders for this sub-
14	metric. BellSouth continues to focus on this measurement in order to improve
15	results to meet the benchmark.
16	
17	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
18	Loop Non-Design / Manual (B.1.19.9) (September/October)
19	BellSouth met the benchmark for 783 of the 815 orders for this sub-metric in
20	September and for 1,179 of the 1,275 orders in October 2001. The 95%
21	benchmark set a requirement of 817 of the 815 orders in September and for
22	1,212 of the 1,275 orders in October based on the number of orders for this

BellSouth met the benchmark for 182 of the 192 orders for this sub-metric in

1

1	sub-metric. BellSouth continues to focus on this measurement in order to
2	improve results to meet the benchmark.
3	
4	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
5	Loop w/INP Design / Manual (B.1.19.10) (October)
6	There were only 6 orders for this sub-metric in October 2001. The small
7	universe of orders for this sub-metric does not provide a conclusive
8	benchmark comparison.
9	
10	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
11	Loop w/LNP Design / Manual (B.1.19.12) (October)
12	BellSouth met the benchmark for 265 of the 302 orders for this sub-metric in
13	October 2001. The 95% benchmark set a requirement of 287 of the 302
14	orders based on the number of orders for this sub-metric. BellSouth
15	continues to focus on this measurement in order to improve results to meet
16	the benchmark. BellSouth met the benchmark for this sub-metric in August
17	and September 2001.
18	
19	FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
20	Loop w/LNP Non-Design / Manual (B.1.19.13) (October)
21	BellSouth met the benchmark for 212 of the 244 orders for this sub-metric in
22	October 2001. The 95% benchmark set a requirement of 232 of the 244

23 orders based on the number of orders for this sub-metric. BellSouth

1	continues to focus on this measurement in order to improve results to meet
2	the benchmark. BellSouth met the benchmark for this sub-metric in August
3	and September 2001.
4	
5	FOC & Reject Response Completeness (Multiple Responses) / Other Design
6	/ Manual (B.1.19.14) (September/October)
7	BellSouth met the benchmark for 361 of the 395 orders for this sub-metric in
8	September and for 375 of the 410 orders in October 2001. The 95%
9	benchmark set a requirement of 376 of the 395 orders in September and for
10	390 of the 410 orders in October based on the number of orders for this sub-
11	metric. BellSouth continues to focus on this measurement in order to improve
12	results to meet the benchmark.
12 13	results to meet the benchmark.
	results to meet the benchmark.
13	
13 14	FOC & Reject Response Completeness (Multiple Responses) / LNP
13 14 15	FOC & Reject Response Completeness (Multiple Responses) / LNP (Standalone) / Manual (B.1.19.17) (October)
13 14 15 16	FOC & Reject Response Completeness (Multiple Responses) / LNP (Standalone) / Manual (B.1.19.17) (October) BellSouth met the benchmark for 924 of the 990 orders for this sub-metric in
13 14 15 16 17	FOC & Reject Response Completeness (Multiple Responses) / LNP (Standalone) / Manual (B.1.19.17) (October) BellSouth met the benchmark for 924 of the 990 orders for this sub-metric in October 2001. The 95% benchmark set a requirement of 941 of the 990
13 14 15 16 17 18	FOC & Reject Response Completeness (Multiple Responses) / LNP (Standalone) / Manual (B.1.19.17) (October) BellSouth met the benchmark for 924 of the 990 orders for this sub-metric in October 2001. The 95% benchmark set a requirement of 941 of the 990 orders based on the number of orders for this sub-metric. BellSouth
13 14 15 16 17 18 19	FOC & Reject Response Completeness (Multiple Responses) / LNP (Standalone) / Manual (B.1.19.17) (October) BellSouth met the benchmark for 924 of the 990 orders for this sub-metric in October 2001. The 95% benchmark set a requirement of 941 of the 990 orders based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet

23 <u>Flow-Through</u>

Attachment 1E, Items F.1.1 - F.1.3, shows Flow-Through data disaggregated
by customer type and for the Summary/Aggregate. Detailed flow-through
results for individual CLECs are included in Attachment 2E. The following
table shows the Regional Flow-Through results for August, September and
October 2001 as compared with the Interim SQM benchmarks.

7

1

<u>August 2001</u>	September 2001	<u>October 2001</u>	Benchmark
90.86%	90.39%	89.40%	95%
72.14%	68.47%	70.17%	90%
80.82%	79.33%	76.74%	85%
84.40%	86.96%	89.09%	85%
	90.86% 72.14% 80.82%	90.86% 90.39% 72.14% 68.47% 80.82% 79.33%	90.86% 90.39% 89.40% 72.14% 68.47% 70.17% 80.82% 79.33% 76.74%

8 <u>% Flow-through Service Requests (F.1.1.1 – F.1.3.4)</u>

9

10 The August Flow Through results have been modified to include an 11 adjustment for CLEC orders that improperly fell out and were reflected as 12 Planned Manual Fallout (See Revised Attachment 2C filed on November 28, 13 2001). This adjustment showed orders clarified back to the CLECs as CLEC 14 Caused Fallout and all other LSRs in this category as BellSouth Caused Fallout. In July new computer code to include "Dummy FOCs" in the Flow 15 Through Report was added. Dummy FOCs occur when a CLEC submits an 16 LSR supplement to cancel an existing LSR before BellSouth returns a FOC or 17 18 Reject message on the original LSR. Finally, new computer code was

implemented in August to more accurately account for "post-FOC" Service
Orders that require manual handling to pass downstream system edits.
These orders should in fact be counted as "Issued SO's" in accordance with
the SQM, but the computer programming was in error and was corrected.
The overall result for all changes was that the August Flow-Through results
for aggregate, residence, business and UNE metrics were reduced with these
changes, as reflected above.

8

9 Business flow-through rates are well below the 90% objective. Business 10 LSRs are more complex than the typical LSRs and, as a result, there is a 11 greater probability for error. For example, an LSR requesting 10 lines with 12 series completion hunting that are located over multiple floors and have a 13 variation of features on the lines presents many more opportunities for system 14 mismatches than one that adds just lines and features.

15

16 BellSouth established Flow-Through Improvement Program has а 17 Management process that includes seven different internal organizations. Ongoing analysis is being done to determine trends and identify flow-through 18 problems. To date, fifteen system enhancements have been identified and 19 20 are targeted for Encore releases. Three of the enhancements were 21 implemented in August. The remainder of the enhancements are scheduled 22 for release between October 2001 and January 2002.

23

1 <u>2. UNE Provisioning Measures</u>

2	BellSouth met 86% of the overall UNE Provisioning measurements in the
3	month of August, 87% of these measurements in September and 87% in
4	October 2001

- 5
- 6 The following sub-metrics did not meet the applicable retail analogues in the
 7 months of August, September and/or October 2001:
- 8

9 Order Completion Interval / Combo (Loop & Port) / < 10 Circuits / Switch

- 10 Based Orders (B.2.1.3.1.3) (September)
- 11 This sub-metric is a further disaggregation of Item B.2.1.3.1.2. The 12 completion interval difference between the CLEC result and the result for the 13 BellSouth retail analogue for this sub-metric was only 0.01 days. Both 14 measures were approximately one-third day. This indicates virtually identical 15 service for both the CLECs and the retail analogue. BellSouth met the retail 16 analogue comparison for this sub-metric in August and October 2001.
- 17

18 Order Completion Interval / Combo Other / < 10 Circuits / Dispatch

- 19 (B.2.1.4.1.1) (October)
- 20 The primary factor for the miss in this sub-metric is that the standard
- 21 installation interval for this product is 10 days. This is much longer than for
- the retail analogue product. Even though the committed dates to the
- customer are being met, the intervals are longer than for the retail analogue

1	product. There was no CLEC activity for this sub-metric in either August or
2	September 2001.
3	
4	Order Completion Interval / Other Non-Design / < 10 Circuits / Dispatch
5	(B.2.1.15.1.1) (October)
6	The average order completion interval for CLEC orders in this sub-metric for
7	October was 4.29 days compared to an average of 3.81 days for the retail
8	analogue. The "standard" offered completion interval for this sub-metric is
9	longer than for the retail analogue it is compared against. Nevertheless, the
10	difference of less than one half day, on average, does not hinder the CLECs'
11	ability to compete in this area. BellSouth met the retail analogue comparison
12	for this sub-metric in August and September 2001.
13	
14	Order Completion Interval / Other Non-Design / < 10 Circuits / Non-Dispatch
15	(B.2.1.15.1.2) (August)
16	In August 2001, the average OCI for this sub-metric was 3.79 days for CLECs
17	compared to 0.82 days for the retail analogue. Ten of the orders in August
18	should have received an "L" code due to customer requested extended
19	intervals or customer caused missed installation appointments. Also, the
20	"standard" offered completion interval for this sub-metric is longer than for the
21	retail analogue it is compared against. BellSouth met the retail analogue for

this sub-metric in September and October 2001.

1	Order Completion Interval / Other Non-Design / >= 10 Circuits / Dispatch
2	(B.2.1.15.2.1) (August)
3	There were only a total of four orders completed in this sub-metric in August
4	2001. This small universe of orders does not provide a statistically conclusive
5	comparison with the retail analogue. There was no CLEC activity for this sub-
6	metric in September 2001. BellSouth met the retail analogue comparison for
7	this sub-metric in October 2001.
8	
9	% Jeopardies / Other Non-Design (B.2.5.15) (August/September/October)
10	There were a total of 29 jeopardies issued for the 916 orders that were
11	scheduled for this sub-metric in August, 3 jeopardies issued for the 119
12	orders scheduled for September and 12 jeopardies issued for the 288 orders
13	scheduled for October 2001. While the data indicates that BellSouth placed a
14	higher percentage of CLEC orders in jeopardy status, all but 11 of the orders
15	placed in jeopardy in August and all of the jeopardy orders in September were
16	actually worked on time. There was only one missed installation appointment
17	resulting from the jeopardy orders in October.
18	
19	% Jeopardy Notice Interval >= 48 hours / Combo (Loop & Port) / < 10
20	Circuits (B.2.10.3) (August/September)
21	The calculations for this measure have been determined to be incorrect. A
22	portion of the coding modifications required to correct this problem were

implemented in September 2001. BellSouth is continuing to prepare and test

- the remainder of the modifications necessary to correct the calculations for
 this measure.
- 3

4 <u>% Missed Installation Appointments / Combo (Loop & Port) / < 10 Circuits /</u>

5 Non-Dispatch (B.2.18.3.1.2) (August/September/October)

6 BellSouth missed 38 of the 12.462 scheduled appointments in this sub-metric 7 for August, missed 25 of the 9,659 appointments for September and missed 29 of the 10,375 appointments for October 2001. BellSouth met over 99% of 8 9 the scheduled appointments for both retail and CLEC orders in this sub-metric for all three months. When BellSouth provisions high quality service coupled 10 with very large universe sizes, it can cause an apparent out of equity 11 12 condition from a quantitative viewpoint. In these cases, there is very little 13 variation and the universe size is so large that the Z-test becomes overly sensitive to any difference. In other words, the statistical test shows that the 14 measurement does not meet the fixed critical value when compared with the 15 retail analogue, but BellSouth's actual performance for both CLECs and its 16 own retail operations is at a very high level – in this case over 99%. From a 17 practical point of view, the CLECs' ability to compete has not been hindered 18 even though the statistical results may technically show that BellSouth failed 19 20 to meet the benchmark/analogue.

21

22 <u>% Missed Installation Appointments / Combo (Loop & Port) / < 10 Circuits /</u>

23 Dispatch In (B.2.18.3.1.4) (August/September/October)

This is a further disaggregation of Item B.2.18.3.1.2, above. BellSouth
missed 38 of the 6,812 appointments in this sub-metric scheduled in August,
missed 25 of the 4,091 appointments scheduled in September and missed 29
of the 4,612 appointments scheduled in October 2001. BellSouth completed
99.3% or more of the appointments as scheduled in August, September and
October.

7

8 <u>% Missed Installation Appointments / Combo (Loop & Port) / >= 10 Circuits /</u>

9 Non-Dispatch (B.2.18.3.2.2) (August)

10 There were only seven orders scheduled for this sub-metric in August 2001.

11 The small universe of orders for this sub-metric does not provide a statistically 12 conclusive comparison to the retail analogue. There was no CLEC activity for 13 this sub-metric in September 2001. BellSouth met the retail analogue 14 comparison for this sub-metric in October 2001.

15

16 <u>% Missed Installation Appointments / Combo (Loop & Port) / >= 10 Circuits /</u>

17 <u>Dispatch In (B.2.18.3.2.4) (August)</u>

18 There were only three orders scheduled for this sub-metric in August 2001.

The small universe of orders for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. There was no CLEC activity for this sub-metric in September 2001. BellSouth met the retail analogue

- comparison for this sub-metric in October 2001.
- 23

1 <u>% Missed Installation Appointments / Combo Other / < 10 Circuits / Dispatch</u>

2 (B.2.18.4.1.1) (October)

BellSouth missed four of the thirty-seven installation appointments scheduled
for this sub-metric in October. None of these appointment misses resulted in
held orders. No systemic installation issues or patterns were identified for
these missed appointments. There was no CLEC activity for this sub-metric
in either August or September 2001.

8

9 <u>% Provisioning Troubles w/i 30 Days / Combo (Loop & Port) / >= 10 Circuits /</u>

10 Dispatch (B.2.19.3.2.1) (September)

11 There were five troubles reported for the twenty-one orders completed in the 12 30 days prior to September for this sub-metric. No systemic problems were 13 identified for this small number of troubles. BellSouth met or exceeded the 14 retail analogue for this sub-metric in August and October 2001.

15

16 <u>% Provisioning Troubles w/i 30 Days / Other Design / < 10 Circuits / Dispatch</u>

17 (B.2.19.14.1.1) (August/September/October)

There were 13 troubles reported for the 192 orders that completed in the 30 days prior to August, 44 troubles reported for the 725 orders completed in the 30 days prior to September and 10 troubles reported for the 104 orders completed in the 30 days prior to October 2001 for this sub-metric. Three of the August trouble reports were closed as "no trouble found," and four troubles were for the same installation. In September, 9 of the trouble

1	reports, or 21%, were closed as "no trouble found." In October, one of the
2	troubles was closed as "no trouble found." The remainder of the October
3	troubles were for various facility and central office problems with no patterns
4	or systemic issues identified.

6 <u>% Provisioning Troubles w/i 30 Days / Other Design / >= 10 Circuits /</u> 7 Dispatch (B.2.19.14.2.1) (September/October)

8 There were only ten orders completed for this sub-metric in the 30 days prior 9 to September and only one order completed in the 30 days prior to October 10 2001. The small universe of orders for this sub-metric does not provide a 11 statistically conclusive comparison to the retail analogue. BellSouth met the 12 retail analogue comparison for this sub-metric in August 2001.

13

14 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits /

15 Dispatch-In (B.2.21.3.1.4) (September)

16 The root cause analysis of this measure indicated that the only differences between the performance between BellSouth retail and CLECs are the 17 18 mismatches found when the orders are compared with the original LSRs. 19 The start of the completion interval is the point at which the technician 20 completes the order, and the interval ends when the completion notice is 21 sent. Any change to a name, number of items, etc., occurring during the 22 provisioning process will generate inconsistencies with the original LSRs that 23 must be resolved before a final completion notice can be sent. Any time to

1 resolve these inconsistencies with the original LSRs is included in the 2 average. Because of numerous CLEC changes and order updates, 3 mismatches on CLECs orders exceed those for BellSouth retail orders. 4 Combining this with the smaller base for the CLECs' measurement raises the average, which results in a miss. Specific Service Representatives within the 5 6 Work Management Centers have been assigned to resolve any completion 7 issues that are required. Providing specific training and dedicating personnel to this task should reduce the difference between the CLEC and retail 8 9 analogue results. BellSouth met the retail analogue comparison for this sub-10 metric in August and October 2001.

11

12 <u>Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch</u> 13 (B.2.34.1.1.1) (August/October)

BellSouth met the standard for 86 of the 108 orders reviewed in this submetric for August and for 36 of the 38 orders reviewed in October 2001. The 95% benchmark set a requirement of 103 orders for August and 39 orders for October based on the quantity of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September 2001.

21

22 <u>Service Order Accuracy / Design (Specials) / < 10 Circuits / Non-Dispatch</u>

23 (B.2.34.1.1.2) (August)

BellSouth met the standard for 88 of the 127 orders reviewed in this submetric for August 2001. The 95% benchmark set a requirement of 121 orders in August based on the quantity of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September and October 2001.

7

8 Service Order Accuracy / Design (Specials) / >= 10 Circuits / Non-Dispatch

9 (B.2.34.1.2.2) (August)

10 There were only two orders reviewed for this sub-metric in August 2001. The 11 small universe for this sub-metric does not provide a conclusive benchmark 12 comparison. BellSouth met the benchmark for this sub-metric in September 13 2001. There were no CLEC orders reviewed for this sub-metric in October 14 2001.

15

Service Order Accuracy / Loops Non-Design / < 10 Circuits / Dispatch (B.2.34.2.1.1) (August/September/October)

BellSouth met the standard for 14 of the 20 orders reviewed for this submetric in August, for 23 of the 28 orders reviewed in September and for 21 of the 32 orders reviewed in October 2001. The 95% benchmark set a requirement of 19 of the 20 orders in August, 27 of the 28 orders in September and 31 of the 32 orders in October based on the quantity of orders

1	in the sub-metric. BellSouth continues to focus on this measurement in order
2	to improve results to meet the benchmark.

4 <u>Service Order Accuracy / Loops Non-Design / < 10 Circuits / Non-Dispatch</u>

5 (B.2.34.2.1.2) (August/September/October)

BellSouth met the standard for 228 of the 293 orders reviewed in this submetric for August, for 120 of the 200 orders reviewed in September and for
128 of the 188 orders reviewed in October 2001. The 95% benchmark set a
requirement of 279 orders for August, for 190 orders for September and for
179 orders in October based on the quantity of orders for this sub-metric.
BellSouth continues to focus on this measurement in order to improve results
to meet the benchmark.

13

14 Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Dispatch

15 (B.2.34.2.2.1) (August)

There were only three orders reviewed in this sub-metric for August 2001. Such a small universe does not produce a statistically conclusive benchmark comparison. There was no CLEC activity for this sub-metric in September 2001. BellSouth met or exceeded the benchmark for this sub-metric in October 2001.

21

22 Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Non-Dispatch

23 (B.2.34.2.2.2) (August/September/October)

1 There were only 9 orders reviewed for this sub-metric in August, 4 orders 2 reviewed in September and 11 orders reviewed in October 2001. The small 3 universe of orders for this sub-metric combined with the 95% benchmark 4 required that all orders reviewed in each month be trouble free. A problem 5 with any order would cause a miss for the entire sub-metric. BellSouth 6 continues to focus on this measurement in order to improve results to meet 7 the benchmark.

8

9 3. UNE Maintenance and Repair (M&R) Measures

BellSouth met the applicable performance standard for 74% in August, 90%
in September and 87% in October 2001 of the overall UNE M&R
measurements. The sub-metrics that did not meet the fixed critical value for
this checklist item in August, September and/or October are as follows:

14

15 <u>% Missed Repair Appointments / Combo (Loop & Port / Non-Dispatch</u>

16 (B.3.1.3.2) (September)

BellSouth completed 635 of the 662 repair appointments (96%) as scheduled for this sub-metric in September 2001. Eleven of the twenty-seven missed appointments were orders that were grouped together for one customer for the same trouble. Even though the statistical test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, BellSouth's actual performance for both CLECs and its own retail operations is at a high level. From a practical point of view, the CLECs'

1	ability to compete has not been hindered even though the statistical results
2	may technically show that BellSouth failed to meet the retail analogue
3	comparison. BellSouth met the retail analogue comparison for this sub-metric
4	in August and October 2001.
5	
6	<u>% Missed Repair Appointments / Other Design / Dispatch (B.3.1.10.1)</u>
7	(August)
8	BellSouth missed 2 of the 21 repair appointments scheduled for this sub-
9	metric in August 2001. No systemic problems were identified for the 2
10	appointments missed in August. BellSouth met the retail analogue
11	comparison for this sub-metric in September and October 2001.
12	
13	% Missed Repair Appointments / Other Design / Non-Dispatch (B.3.1.10.2)
14	(August/September)
15	BellSouth missed 1 of the 17 repair appointments scheduled for this sub-
16	metric in August and 1 of the 11 appointments scheduled for September
17	2001. No systemic problems were identified for either of the appointments
18	missed in August or September. BellSouth met the retail analogue
19	comparison for this sub-metric in October 2001.
20	-
21	<u>% Missed Repair Appointments / Other Non-Design / Non-Dispatch</u>
22	(B.3.1.11.2) (August)

1	BellSouth missed 4 of the 74 repair appointments scheduled for this sub-
2	metric in August 2001. No systemic problems were identified for the four
3	appointments missed in August. BellSouth met or exceeded the retail
4	analogue for this sub-metric in September and October 2001.
5	
6	Customer Trouble Report Rate / Combo Other / Dispatch (B.3.2.4.1)
7	(September/October)
8	Over 96% of the lines in service for this sub-metric for both CLECs and the
9	retail analogue provided trouble free service in September and October 2001.
10	Of the 31 troubles reported for this sub-metric in September, 5 (16%) were
11	closed as "no trouble found." In October, 8 (18%) of the 45 trouble reports
12	were closed as "no trouble found." Major emphasis is being placed on
13	improving field documentation of test results during the closeout process.
14	BellSouth met or exceeded the retail analogue for this sub-metric in August
15	2001.
16	
17	Customer Trouble Report Rate / Combo Other / Non-Dispatch (B.3.2.4.2)

18 <u>(October)</u>

There were 35 troubles reported for the 1,317 lines in service for this submetric-in October. Both the CLECs and BellSouth retail had over 97% trouble free service for the month. Of the 35 October trouble reports for this submetric, 14 (40%) were closed as "no trouble found." With the exclusion of these TOK/FOK reports, BellSouth would have met the retail analogue

- comparison for October. BellSouth met the retail analogue comparison for
 this sub-metric in August and September 2001.
- 3

4 <u>Customer Trouble Report Rate / Other Design / Dispatch (B.3.2.10.1)</u> 5 (August/October)

6 The difference between the retail analogue and the CLEC aggregate was 1% 7 or less in both August and October 2001. Both the CLECs and BellSouth 8 retail had greater than 98% trouble free service for all in service lines in this 9 sub-metric in both months. In August and October, 48% and 14%, 10 respectively, of the trouble reports for this sub-metric were closed as "no 11 trouble found." From a practical point of view, the CLECs' ability to compete 12 has not been hindered even though the statistical results may technically 13 show that BellSouth failed to meet the benchmark/analogue. BellSouth met 14 the retail analogue comparison for this sub-metric in September 2001.

15

16 <u>Customer Trouble Report Rate / Other Design / Non-Dispatch (B.3.2.10.2)</u> 17 (August)

The difference between the retail analogue and the CLEC aggregate was less than 1% for this sub-metric in August 2001. Both the CLECs and BellSouth retail had greater than 98% trouble free service for all in service lines in this sub-metric. In August, 7 of the 17 troubles reported for this sub-metric were associated with a conversion project for one CLEC. No patterns or systemic

- issues were identified for the remaining reports. BellSouth met the retail
 analogue comparison for this sub-metric in September and October 2001.
- 3

4 Customer Trouble Report Rate / Other Non-Design / Dispatch (B.3.2.11.1)

5 (August/September/October)

6 There were a total of 71 trouble reports for the 702 in service lines for this 7 sub-metric in August, 67 trouble reports for the 697 lines in service in September and 49 trouble reports for the 688 lines in service in October 8 9 2001. In August, 16% of the troubles were either caused by damaged cable 10 facilities or were closed as "no trouble found." In September, 33 of the 67 11 total trouble reports (49%), and in October, 34 of the 49 reports (69%) were 12 identified as being BellSouth customers rather than CLEC customers. Of the 13 remaining 34 September reports, 17 reports (50%) were from the same customer for the same trouble incident. There were no trends identified in an 14 15 analysis of the remaining 15 October reports. Continuing analysis is 16 underway to determine if any systemic issues exist with this sub-metric.

17

18 <u>Customer Trouble Report Rate / Other Non-Design / Non-Dispatch</u> 19 (B.3.2.11.2) (August/September/October)

There-were a total of 71 troubles reports for the 702 in service lines for this sub-metric in August, 45 troubles reported for the 697 lines in service in September and 28 troubles reported for the 688 in service lines for October 2001. An analysis revealed that 42 of the 71 trouble reports (59%) for

August, 30 of the 45 reports (67%) for September and 17 of the 28 trouble
reports (61%) for October were closed out as "no trouble found," or over half
of the troubles reported had minimal impact on the end-user customer.
Continuing analysis is underway to determine any systemic issues with this
sub-metric.

6

7 <u>% Repeat Troubles within 30 Davs / Combo Other / Dispatch (B.3.4.4.1)</u>
 8 (August/September)

9 There were 11 repeat trouble reports for this sub-metric in August and 13 repeat troubles in September 2001. BellSouth is currently investigating this 10 11 sub-metric to determine if all orders shown as repeats actually had trouble 12 reports within the previous 30 days. Three of the August reports were for the 13 same customer due to an intermittent trouble. Five of the thirteen repeat 14 reports in September should have been classified as "information only" and 15 not counted as a repeat report, and three of the remaining reports were 16 closed as 'no trouble found." The other reports revealed no patterns or 17 systemic issues. BellSouth met the retail analogue comparison for this sub-18 metric in October 2001.

19

20 Out of Service > 24 Hours / Other Design / Dispatch (B.3.5.10.1) (August)

In August 2001, only 2 repair orders were out of service longer than 24 hours
 of the 21 total repair orders for this sub-metric. BellSouth met the retail
 analogue comparison for this sub-metric in September and October 2001.

1 2 Out of Service > 24 hours / Other Design / Non-Dispatch (B.3.5.10.2) 3 (August/September) 4 In August, only 1 of the 17 repair orders scheduled for this sub-metric was out 5 of service longer than 24 hours. In September, 1 of the 11 repair orders was 6 out to service longer than 24 hours. No systemic problems were identified for 7 either of these repair orders. BellSouth met the retail analogue comparison 8 for this sub-metric in October 2001. 9 10 Out of Service > 24 Hours / Other Non-Design / Dispatch (B.3.5.11.1) 11 (October) 12 14 of the 37 repair appointments scheduled for this sub-metric in October 13 2001 were out of service longer than 24 hours. Of these 14 trouble reports, 7 14 were identified as BST customers rather than CLEC customers. Of the 15 remaining 7 CLEC reports, 6 met the offered commitment repair interval (4 of 16 the 6 were taken on Friday or Saturday and scheduled due for Monday). 17 BellSouth met the retail analogue comparison for this sub-metric for August 18 and September 2001. 19 20 4. Other UNE Measures

- 21
- 22 Pre-Ordering

1	Service Inquiry for xDSL loops (F.3.1.1), Loop Makeup Manual (F.2.1) and
2	Loop Makeup Electronic (F.2.2) are included in the Pre-Ordering
3	measurements. All measures met the established benchmarks for August
4	2001. The sub-metrics that did not meet the benchmarks in September and
5	October 2001 are as follows:
6	
7	Loop Makeup Inquiry (Manual) (F.2.1) (October)
8	BellSouth met the 3-business day benchmark interval for 45 of the 48
9	inquiries submitted in October 2001. This was one order short of the 46
10	required by the 95% benchmark. No ordering process issues were identified
11	for the longer interval orders.
12	
13	Service Inquiry with Firm Order / xDSL (F.3.1.1) (September)
14	BellSouth met 6 of the 7 inquiries within the 5-day interval in September 2001.
15	The 95% benchmark for this quantity of orders required all 7 to be returned in
16	the benchmark period in September. BellSouth met the benchmark for this
17	sub-metric in August and October 2001.
18	
19	The remainder of the UNE measurements for which BellSouth did not meet
20	the applicable analogue or benchmark in August, September and/or October
21	2001 is as follows:
22	

23 Operations Support Systems (OSS)

1	The OSS/Pre-Ordering measures for which BellSouth did not meet the
2	penchmark/retail analogue in August, September and/or October 2001 were:

4 Average Response Interval / CRIS / Region (D.2.4.1.1)

5 (August/September/October)

6 The average response interval for this sub-metric is measured in three 7 separate disaggregations -- the percentage of queries that are responded to 8 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 9 The average response interval for the CLEC requests did not meet the retail 10 analogue intervals for the less than 4-second disaggregation but exceeded 11 both the less than 10 and greater than 10 seconds responses. For the 4-12 second interval, there was only approximately 1% difference between the 13 CLEC responses as compared with the retail analogue in all three months. 14 Both the CLECs and the retail analogue received approximately 99% within the less than 10 second response interval. Similarly, for the greater than 10 15 16 seconds interval measure, the CLECs and the BellSouth retail analogue 17 received approximately 1% of responses in over 10 seconds. These very small differences in response intervals indicate equivalent service levels for 18 19 the CLECs and BellSouth retail.

20

Average Response Interval / LMOS / Region (D.2.4.4.1, D.2.4.4.2, D.2.4.4.3)
 (August/September/October)

1 The average response intervals for these sub-metrics are measured in three 2 separate disaggregations -- the percentage of queries that are responded to 3 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 4 For all three measurements, the results were virtually identical in August and 5 September, with all the measures being less than 1% apart. In October, the 6 difference in the less than 4-second interval responses was about 1.5%, while 7 the differences in the less than 10-second and greater than 10-second 8 interval responses were less than 0.5%. These results indicate virtually 9 equivalent service levels for both the CLECs and BellSouth retail.

10

Average Response Interval / LMOSupd / Region (D.2.4.5.1, D.2.4.5.2, D.2.4.5.3) (August/September/October)

The average response interval for this sub-metric is measured in three separate disaggregations. The percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. For each of the three sub-metrics, there was less than a 3% difference in the responses received by the CLECs and BellSouth retail in each month. Differences of about 4%, or less, for all of these intervals indicate virtually equivalent service levels for both the CLECs and BellSouth retail.

20

21 <u>Average Response Interval / LNP/ Region (D.2.4.6.1) (August/October)</u>

22 Average Response Interval / LNP/ Region (D.2.4.6.2, D.2.4.6.3) (September)

1 The average response interval for this measurement is measured in three 2 separate disaggregations -- the percentage of queries that are responded to 3 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 4 In August and October, the average response interval for the CLEC requests 5 did not meet the retail analogue intervals for the less than 4-second 6 disaggregation but exceeded both the less than 10 and greater than 10 7 seconds responses. In August, September and October 2001, both the 8 CLECs and BellSouth retail received over 99.4% of responses in less than 4 9 seconds and less than 0.2% in more than 10 seconds. The less than one-half 10 percent difference for these intervals indicates virtually equivalent service 11 levels for the CLECs and BellSouth retail.

12

13 Average Response Interval / MARCH / Region (D.2.4.7.1, D.2.4.7.2,

14 <u>D.2.4.7.3) (August)</u>

The average response interval for this sub-metric is measured in three separate disaggregations -- the percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. BellSouth missed the retail analogue comparison for this measure in August but met the retail analogue comparison for these sub-metrics in September and October 2001.

- 21
- 22 Average Response Interval / OSPCM / Region (D.2.4.8.1) (August)

<u>Average Response Interval / OSPCM / Region (D.2.4.8.2, D.2.4.8.3)</u> (August/September)

3 The average response interval for these sub-metrics is measured in three 4 separate disaggregations -- the percentage of queries that are responded to 5 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 6 In August and September 2001, the CLEC response intervals were 35.16% 7 and 44.19% within 4 seconds as compared to 43.74% and 42.76%. 8 respectively, for the retail analogue. For the less than 10 second response 9 interval, the CLECs received 93.75% and 94.19% of their responses and the 10 retail analogue received 97.38% and 97.18% in August and September, 11 respectively. For the greater than 10 second response interval, the CLECs 12 received 6.25% and 5.81% of their responses and the retail analogue 13 received 2.62% and 2.82% in August and September, respectively. With 14 activity levels of only 128 and 86 requests from this system for the two 15 months, only one to five additional responses within 10 seconds would have 16 brought the sub-metric into parity with the retail analogue. BellSouth met the 17 retail analogue comparison for all three of these sub-metrics in October 2001.

18

19 Average Response Interval / NIW / Region (D.2.4.11.1) (August/October)

The average response interval for this sub-metric is measured in three separate disaggregations -- the percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. In both August and October, the average response interval for the CLEC

1 requests did not meet the retail analogue intervals for the less than 4-second 2 disaggregation but exceeded both the less than 10 and greater than 10 3 seconds responses. The CLEC response intervals were 77.81% and 71.22% 4 within 4 seconds in August and October, respectively, as compared with 5 79.85% and 72.73% for the retail analogue. These small differences between 6 the CLEC and retail analogue results should not impede the CLECs' ability to 7 compete in this area. BellSouth met the retail analogue comparison for this 8 sub-metric in September 2001.

9

10 General – Billing

11 Usage Data Delivery Timeliness / Region (F.9.2) (August)

12 This measure tracks the percentage of usage data delivered within six days 13 for both BellSouth retail and the CLEC aggregate. The CLECs experienced 14 usage data delivery timeliness rates that were slightly lower than the rates for 15 BellSouth customers during August 2001 (98.80% for BellSouth retail 16 compared to 98.30% for CLECs). The difference in performance each month 17 was the result of some input files being left out of the ADUF job before the 18 files were recovered and processed. It is important to point out that the CLEC 19 result 98.30% still provide the CLECs a meaningful opportunity to compete. 20 BellSouth has developed a fix that should prevent this type of error from 21 occurring in the future. The fix was implemented on September 1, 2001. 22 BellSouth met the retail analogue comparison for this sub-metric in 23 September and October 2001.

2 Mean Time to Deliver Usage (F.9.4) (August)

3 This measure compares the average number of days to deliver usage to 4 CLECs with the BellSouth retail analogue. In August 2001, the BellSouth 5 result was 3.37 days compared to the CLEC result of 3.60 days. The 6 difference in the performance was the result of input files being left out of the 7 ADUF job before the files were recovered and processed. While the CLEC 8 measurement is slightly greater than the BellSouth results, the CLECs are 9 provided with substantially the same opportunity to bill end users as is 10 BellSouth. BellSouth met or exceeded the retail analogue comparison for this 11 sub-metric in September and October 2001.

12

13 <u>Recurring Charge Completeness / UNE (F.9.5.2) (September)</u>

14

In September 2001, the result for this sub-metric was 86.34% against a benchmark of 90%. The benchmark was not met in September because of problems encountered in correcting some service order problems in a timely manner. The CLECs are provided with a meaningful opportunity to compete, as this issue does not impede the ability to serve end users. BellSouth met the retail analogue comparison for this sub-metric in August and October 2001.

22

1 <u>Recurring Charge Completeness / Interconnection (F.9.5.3)</u>

2 (August/September)

3 This measure tracks the ability of the ordering and billing systems to begin 4 billing an CLEC recurring charges for local interconnection services on the 5 next invoice after an order has "completed". A benchmark of 90% has been 6 set as the level of performance to meet. In August and September 2001, the 7 results for this measure were 48.13% and 38.01%, respectively. These 8 results were negatively impacted by service orders issued to move billed 9 amounts from one billing account to another connected with CLECs which 10 have filed for bankruptcy. These orders were backdated several months to 11 the date of the bankruptcy. None of these orders impacted the CLECs' total 12 billed amounts but were issued to separate pre-bankruptcy billed amounts 13 from post-bankruptcy amounts. The CLECs are provided with a meaningful 14 opportunity to compete, as these issues do not impede the ability to serve 15 end users. BellSouth met the benchmark for this sub-metric in October 2001.

16

17 Non-Recurring Charge Completeness / Interconnection (F.9.6.3)

18 (August/September/October)

19 This measure tracks the ability of the ordering and billing systems to begin

20 billing a CLEC non-recurring charges for local interconnection services on the

- 21 next invoice after an order has "completed". A benchmark of 90% has been
- set as the level of performance to meet. In August, September and October
- 23 2001, BellSouth's performance was 58.53%, 87.61% and 63.16%,

1	respectively. This measure was missed in all three months because of
2	problems encountered in correcting service order errors in a timely manner.
3	The differences between the benchmark and the CLEC results do not impair
4	a CLEC's ability to support its own end users or to effect billing to those end
5	users in any meaningful way.
6	
7	<u>General - Change Management</u>
8	% Software Release Notices Sent On Time (F.10.1) (October)
9	Average Software Release Notice Delay Days (F.10.2) (October)
10	BellSouth met the specified benchmark intervals for one of the two software
11	releases issued in October 2001. BellSouth met the benchmark intervals for
12	all releases in August and September 2001.
13	
14	% Change Management Documentation Sent On Time (F.10.3) (August)
15	Average Documentation Release Delay Days (F.10.5) (August)
16	One of the three change management documentation letters issued in August
17	2001 was released with less than the 30-day benchmark window. All of these
18	changes were, however, primarily dealing with clarifications and information
19	on existing documentation and/or business rules and did not require CLEC
20	coding changes. There was no activity in these sub-metrics in September
21	2001. BellSouth met the benchmark for these measures in October 2001.
22	

23 General – New Business Requests

<u>% Quotes Provided in 10 Business Days (F.11.2.1) (September)</u>

In September 2001, four items were inadvertently counted in this sub-metric
that were not appropriate. The removal of these items would meet the
benchmark requirement for September. BellSouth met this benchmark in
August 2001. There was no CLEC activity for this sub-metric in October
2001.

7

8 General - Ordering

9 <u>% Acknowledgement Message Timeliness / EDI (F.12.1.1) (August)</u>

In August 2001, BellSouth returned almost 81,000 acknowledgement
messages within the 30-minute benchmark period. With a 95% benchmark,
almost 82,000 messages would need to meet the criteria. BellSouth met the
benchmark for this sub-metric in September and October 2001.

14

15 <u>% Acknowledgement Message Completeness / EDI (F.12.2.1)</u>

16 (August/September/October)

BellSouth failed to satisfy the completeness criteria for 302 of the 86,217 messages returned in August 2001. In September 2001, there were only 2 failed messages (0.003%) of the 67,850 total messages returned for the month, and there were only 18 failed messages (0.02%) of the 87,896 total messages in October 2001. A Stability Plan to improve EDI availability has been put into effect. This plan includes implementing both a manual application monitoring schedule (24 / 7) and increased mechanized

1	application alarms to more adequately monitor and react to application
2	outages. The database parameters have also been adjusted to allow for
3	maximum processing in the EDI system.

5 <u>% Acknowledgement Message Completeness / TAG (F.12.2.2)</u>

6 (August/September/October)

BellSouth failed to deliver 20 (0.01%) of the 199,829 messages in August, 5
(0.003%) of the 167,159 messages in September and 4 (0.002%) of the
195,248 messages in October 2001 for this sub-metric. Analysis continues to
identify any issues in this process. However, such a small number of failed
records have not revealed any systemic process problems.

12

13

D. CHECKLIST ITEM 4 - UNBUNDLED LOCAL LOOPS

As discussed in Checklist Item 2, Sections B.2 and B.3 of Attachment 1E provide data for provisioning and maintenance & repair measures for unbundled local loops.

17

For purposes of discussion in this checklist item, the local loop sub-metrics have been separated into two mode-of-entry groups, xDSL and SL1/SL2/Digital. The xDSL group includes xDSL (ADSL, HDSL, UCL), ISDN and Line Sharing sub-metrics. The SL1/SL2/Digital group includes the design and non-design 2-wire analog loops, as well as the 2-wire and 4-wire digital loop sub-metrics.

2 <u>xDSL Group</u>

3 <u>1. Provisioning Measures</u>

4 The xDSL group sub-metrics that did not meet the fixed critical value 5 comparison requirements for August, September and/or October 2001 are as 6 follows:

7

8 Order Completion Interval / Line Sharing / < 6 Circuits / Non-Dispatch

9 (B.2.1.7.3.2) (August)

10 A root cause analysis for OCI for Non-Dispatch orders revealed that 11 BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS 12 orders, but the wholesale non-dispatched orders were receiving the same 13 interval as "dispatched" orders. On June 2, 2001, a release was added to the 14 due date calculator software to correct this error. However, due to problems 15 with the software load, it had to be removed. In addition to the appointment 16 interval issue, OCI is adversely affected by LSRs for which CLECs request 17 intervals beyond the offered interval. When a CLEC requests an interval beyond the available interval offered by BellSouth, an "L" code is entered on 18 19 the Service Order generated by BellSouth. "L" coded orders are excluded 20 from the OCI metrics. BellSouth met the retail analogue comparison for this 21 sub-metric in September and October 2001.

22

23 <u>% Jeopardy Notice >= 48 Hours / xDSL (B.2.10.5) (August/September)</u>

1 The calculations for this measure have been determined to be incorrect. A 2 portion of the coding modifications required to correct this problem were 3 implemented in September 2001. BellSouth is continuing to prepare and test 4 the remainder of the modifications necessary to correct the calculations for 5 this measure.

6

7 <u>% Missed Installation Appointments / Line Sharing / < 10 Circuits / Dispatch</u>
 8 (B.2.18.7.1.1) (October)

9 There were only seven orders for this sub-metric in October 2001. Such a 10 small universe does not provide a statistically conclusive comparison to the 11 retail analogue. BellSouth met the retail analogue comparison for this sub-12 metric in August and September 2001.

13

14 <u>% Missed Installation Appointments / Line Sharing / < 10 Circuits / Non-</u>

15 Dispatch (B.2.18.7.1.2) (August)

16 There was only one missed appointment for the one hundred twenty-four 17 scheduled orders for this sub-metric in August 2001. There was no systemic 18 problem identified for the one missed appointment. BellSouth met the retail 19 analogue comparison for this sub-metric in September and October 2001.

20

21 <u>% Provisioning Troubles within 30 Days / UNE ISDN / < 10 Circuits / Dispatch</u>

22 (B.2.19.6.1.1) (October)

In October, there were 24 troubles reported for orders that completed in the
prior 30 days in this sub-metric. Five (21%) of the twenty-four trouble reports
were closed as "no trouble found." BellSouth has implemented an improved
procedure to document circuit test results in the order closeout narratives.
This initiative, along with added emphasis on cooperative testing procedures,
should improve the results for this sub-metric. BellSouth met the retail
analogue for this sub-metric in August and September 2001.

8

9 <u>% Provisioning Troubles within 30 Days / Line Sharing / < 10 Circuits /</u>

10 Dispatch (B.2.19.7.1.1) (September)

11 There were only eight orders for this sub-metric in September 2001. The 12 small universe of orders for this sub-metric does not provide a statistically 13 conclusive comparison to the retail analogue. BellSouth met the retail 14 analogue comparison for this sub-metric in August and October 2001.

15

16 <u>% Provisioning Troubles within 30 Days / Line Sharing / < 10 Circuits / Non-</u>

17 Dispatch (B.2.19.7.1.2) (September/October)

18 There were 20 troubles reported in this sub-metric for the 125 orders 19 completed in the 30 days prior to September and 16 trouble reports for the 77 20 orders completed in the 30 days prior to October 2001. In both September 21 and October, 50% of the trouble reports were closed as "no trouble found." 22 An analysis of the remainder of the reports did not reveal any distinct patterns

- or systemic installation problems. BellSouth met the retail analogue
 comparison for this sub-metric in August 2001.
- 3

4 Average Completion Notice Interval / xDSL / < 10 Circuits / Dispatch

5 (B.2.21.5.1.1) (September)

6 The root cause analysis of this measure indicated that the only differences 7 between the performance between BellSouth retail and CLECs are the 8 mismatches found when the orders are compared with the original LSRs. 9 The start of the completion interval is the point at which the technician 10 completes the order, and the interval ends when the completion notice is 11 sent. Any change to a name, number of items, etc., occurring during the 12 provisioning process will generate inconsistencies with the original LSRs that 13 must be resolved before a final completion notice can be sent. Any time to 14 resolve these inconsistencies with the original LSRs is included in the 15 average. Because of numerous CLEC changes and order updates, 16 mismatches on CLECs orders exceed those for BellSouth retail orders. 17 Combining this with the smaller base for the CLECs' measurement raises the 18 average, which results in a miss. Specific Service Representatives within the 19 Work Management Centers have been assigned to resolve any completion 20 issues that are required. Providing specific training and dedicating personnel 21 to this task should reduce the difference between the CLEC and retail 22 analogue results.

23

2. Maintenance & Repair Measures

2 The xDSL group sub-metrics that did not meet the fixed critical value 3 comparison requirements for August, September and/or October 2001 are as 4 follows:

5

6 <u>% Missed Repair Appointments / UNE ISDN / Dispatch (B.3.1.6.1) (August)</u>

7 BellSouth missed 17 of the 149 scheduled repair appointments scheduled for 8 Factors contributing to the missed appointments included August 2001. 9 access issues, problems in coordination of cooperative testing with CLECs, 10 cable and facilities problems, etc. Seven of the seventeen trouble reports 11 were due to a flooded remote terminal site that could not be restored until 12 floodwater receded. Analysis of the other orders did not reveal distinctive 13 patterns or systemic issues. BellSouth met the retail analogue comparison 14 for this sub-metric in September and October 2001.

15

16 <u>% Missed Repair Appointments / ISDN Loops / Non-Dispatch (B.3.1.6.2)</u> 17 (August)

BellSouth missed four of the ninety-six scheduled appointments for this submetric in August 2001. There was no systemic problem found for the missed appointments. BellSouth met the retail analogue comparison for this submetric in September and October 2001.

22

<u>% Missed Repair Appointments / Line Sharing / Non-Dispatch (B.3.1.7.2)</u> (August)

BellSouth missed eight of forty-seven appointments scheduled for this sub metric in August 2001. An action plan has been implemented to cover central
 office technicians on proper handling of Line Sharing troubles. BellSouth met
 the retail analogue comparison for this sub-metric in September and October
 2001.

8

9 Customer Trouble Report Rate / xDSL Loops / Dispatch (B.3.2.5.1)

10 <u>(August/September/October)</u>

11 A total of 76 troubles were reported for the 5,685 in service lines for this sub-12 metric in August, 57 troubles for the 5,448 in service lines in September and 13 82 troubles reported for the 5,558 lines in service in October 2001. Both the 14 CLECs and BellSouth retail had 98% or more trouble free service for all in 15 service lines in this sub-metric in all three months. Even though the 16 measurement indicated that BellSouth did not meet the retail analogue, both BellSouth and the CLECs were being provided a high level of service for this 17 18 sub-metric.

19

20 Customer Trouble Report Rate / xDSL / Non-Dispatch (B.3.2.5.2) (August)

A total of 21 troubles were reported for the 5,685 in service lines for this submetric in August 2001. Both the CLECs and BellSouth retail had 99% trouble free service for all in service lines in this sub-metric in August. Even though

1	the measurement indicated that BellSouth did not meet the retail analogue,
2	both BellSouth and the CLECs were being provided a high level of service for
3	this sub-metric. BellSouth met the retail analogue comparison for this sub-
4	metric in September and October 2001.
5	
6	Customer Trouble Report Rate / UNE ISDN / Dispatch (B.3.2.6.1)
7	(August/September/October)
8	Both the CLECs and BellSouth retail had 97% to 98% trouble free service for
9	all in service lines in this sub-metric in August, September and October 2001.
10	Even though the measurement indicated that BellSouth did not meet the retail
11	analogue, both BellSouth and the CLECs were being provided a high level of
12	service for this sub-metric. BellSouth is developing an action plan to improve
13	circuit testing and turn-up documentation. ISDN test jacks have been
14	installed in each central office to facilitate improved testing and turn-up control
15	procedures.
16	
17	Customer Trouble Report Rate / Line Sharing / Dispatch (B.3.2.7.1) (August)

18 There were a total of 14 troubles reported for the 1,007 in service lines for this 19 sub-metric in August 2001. Of the 14 August trouble reports, 4 (29%) were 20 closed as "no trouble found." There were no distinctive trends or systemic 21 problems identified for any of the troubles reported for this sub-metric. 22 BellSouth met the retail analogue comparison for this sub-metric in 23 September and October 2001.

<u>Customer Trouble Report Rate / Line Sharing / Non-Dispatch (B.3.2.7.2)</u> (August/October)

There were a total of 47 troubles for the 1,007 in service lines for this sub-4 metric in August and 33 troubles reported for the 1.051 lines in service in 5 6 October 2001. In August, 30 of the 47 troubles (64%) were closed as "no 7 trouble found." In October, 28 of the 33 troubles (85%) were closed as "no 8 trouble found." Even though the measurement indicated that BellSouth did 9 not meet the retail analogue, both BellSouth and the CLECs were being 10 provided a high level of service for this sub-metric. BellSouth met the retail 11 analogue comparison for this sub-metric in September 2001.

12

1

13 Maintenance Average Duration / UNE ISDN / Dispatch (B.3.3.6.1) (August)

14 BellSouth missed this sub-metric for August 2001 with an average duration of 15 10.92 days for CLECs compared to 7.49 days for the retail analogue. Factors 16 contributing to the longer interval maintenance orders included access issues, 17 problems in coordination of cooperative testing with CLECs, cable and 18 facilities problems, etc. In August, 7 of the orders had longer durations due to 19 flooded remote terminal facilities. Restoration work on those facilities could not begin until flood water receded and the facilities dried. Analysis of the 20 21 remaining orders did not reveal distinctive patterns or systemic issues. 22 BellSouth is placing additional focus on ISDN orders in scheduling and

1	prioritizing maintenance activities. BellSouth met the retail analogue
2	comparison for this sub-metric in September and October 2001.
3	
4	% Repeat Troubles within 30 Days / Line Sharing / Dispatch (B.3.4.7.1)
5	(August)
6	There were only fourteen trouble reports for this sub-metric in August 2001.
7	The small universe for this sub-metric does not provide a statistically
8	conclusive comparison to the retail analogue. BellSouth met the retail
9	analogue comparison for this sub-metric in September and October 2001.
10	
11	% Repeat Troubles within 30 Days / Line Sharing / Non-Dispatch (B.3.4.7.2)
12	(August)
13	Twenty-three of the forty-seven reports for this sub-metric in August 2001
14	were repeat reports. All 23 of the trouble reports were from one CLEC, and 19
15	of the 23 reports (83%) were closed as "TOK/FOK." With the exclusion of
16	these reports, BellSouth would have met the retail analogue comparison for
17	this sub-metric in August. BellSouth met the retail analogue comparison for
18	this sub-metric in September and October 2001.
19	
20	% Out of Service > 24 hours / UNE ISDN / Dispatch (B.3.5.6.1) (August)
21	Factors contributing to the longer interval maintenance orders for this sub-
22	metric in August 2001 included access issues, problems in coordination of
23	cooperative testing with CLECs, cable and facilities problems, etc. Seven of

the seventeen long duration troubles were due to flooding of remote terminal facilities. Restoral of service could not begin until flood water receded and the remote terminals dried. Analysis of the remainder of the orders did not reveal distinctive patterns or systemic issues. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

- 6
- 7 <u>% Out of Service > 24 hours / UNE ISDN / Non-Dispatch (B.3.5.6.2) (August)</u>

8 In August 2001, four out of ninety-six total trouble reports were out of service 9 longer than 24 hours. No systemic maintenance problems were identified for 10 the small number of orders out of service in this sub-metric. BellSouth met 11 the retail analogue comparison for this sub-metric in September and October 12 2001.

13

14 SL1/SL2/Digital Loop Group

15 1. Provisioning Measures

16 The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed 17 critical value comparison requirements for August, September and/or October 18 2001 are as follows:

19

20 Order Completion Interval (OCI)

A root cause analysis for OCI for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same

1 interval as "dispatched" orders. On June 2, 2001, a release was added to the 2 due date calculator software to correct this error. However, due to problems 3 with the software load, it had to be removed. In addition to the appointment 4 interval issue, OCI is adversely affected by LSRs for which CLECs request 5 intervals beyond the offered interval. When a CLEC requests an interval 6 beyond the available interval offered by BellSouth, an "L" code is entered on 7 the Service Order generated by BellSouth. "L" coded orders are excluded 8 from the OCI metrics.

9

10 Order Completion Interval / 2w Analog Loop Design / < 10 Circuits / Dispatch

11 (B.2.1.8.1.1) (August/September/October)

There were a total of 175 orders completed for this sub-metric in August, 209 12 orders completed in September and 47 orders completed in October 2001. A 13 14 detailed analysis indicated that 17 of the 175 orders for August had intervals 15 that were longer than the due date calculator system would have assigned and should have been given an "L Code" for extended interval. When an LSR 16 is received, the due date calculator determines what the current available 17 18 interval for that product is, based on the available resources from Network. If the CLEC requests a longer interval ("extended interval"), the order is given 19 20 an "L Code" and excluded from the OCI measurement. The primary factor for the miss in this sub-metric, however, is that the standard installation interval 21 for this product is 4 business days. Even though the committed dates to the 22 23 customer are being met, the intervals are longer than for the retail analogue

1	product. BellSouth continues to work to lower the interval for this sub-metric
2	to meet the "3 calendar day" interval ordered for the POTS type retail
3	analogue services in Florida.
4	
5	Order Completion Interval / 2w Analog Loop Non-Design / < 10 Circuits /
6	Dispatch (B.2.1.9.1.1) (October)
7	The primary contributor to the miss in this sub-metric for October was that 58
8	(56%) of the 103 orders had extended intervals requested by the customers.
9	These orders should have been given and "L" code and excluded from the
10	measurement. BellSouth met the retail analogue comparison for this sub-
11	metric in August and September 2001.
12	
13	Order Completion Interval / 2w Analog Loop w/LNP Design / < 10 Circuits /
14	Dispatch (B.2.1.12.1.1) (August/September/October)
15	There were a total of 255 orders that completed for this sub-metric in August,
16	178 orders that completed in September and 225 orders that completed in
17	October 2001. A detailed analysis indicated a significant number of orders
18	with customer requested extended intervals were not "L coded" and should
19	have been excluded from the measurement. BellSouth continues to work to
20	lower the interval for this sub-metric to meet the "3 day" interval ordered for
21	the POTS type retail analogue services in Florida. The current standard
22	interval for orders in this sub-metric is four business days as compared to the
23	three calendar day interval for the retail analogue.

Order Completion Interval / 2w Analog Loop w/LNP Non Design / < 10 Circuits / Dispatch (B.2.1.13.1.1) (September/October)

There were a total of 266 orders that completed for this sub-metric in September and 266 orders that completed in October 2001. BellSouth continues to work to lower the interval for this sub-metric to meet the "3 calendar day" interval ordered for the POTS type retail analogue services in Florida. The current standard interval for this sub-metric is four business days as compared to the three-day interval for the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in August 2001.

11

1

12 The remainder of the provisioning measures that did not meet the retail 13 analogue for provisioning is as follows:

14

15 Held Orders / 2w Analog Loop w/LNP Design / >= 10 Circuits / Facility

16 (B.2.3.12.2.1) (August/October)

There was only one order associated with this sub-metric in August and four orders in October 2001. The small universe size for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September 2001.

- 21
- 22 Held Orders / Digital Loop >= DS1 / < 10 Circuits / Facility (B.2.3.19.1.1)
- 23 (August)

1 There was only one order associated with this sub-metric in August 2001. 2 The small universe size for this sub-metric does not provide a statistically 3 conclusive comparison to the retail analogue. BellSouth met the retail 4 analogue comparison for this sub-metric in September and October 2001.

5

6 <u>% Jeopardies / 2w Analog Loop Design (B.2.5.8)</u>

7 (August/September/October)

In August 2001, there were a total of 37 jeopardies issued for the 291 orders 8 9 that were scheduled for this sub-metric. All but 10 of the jeopardies were 10 resolved prior to the due date and the orders worked as scheduled. None of 11 these jeopardies or missed appointments resulted in held orders in August. 12 In September 2001, there were a total of 33 jeopardies issued for the 292 13 orders that were scheduled for this sub-metric. All but 6 of the jeopardies 14 were resolved prior to the due date and the orders worked as scheduled. 15 Only one of the missed appointments resulted in a held order – which was 16 resolved and completed in 3 days. In October 2001, there were a total of 9 17 jeopardies issued for the 44 orders that were scheduled for this sub-metric. All but 5 of the jeopardies were resolved prior to the due date and the orders 18 19 worked as scheduled. None of these jeopardies or missed appointments 20 resulted in held orders in October. There were no missed appointments for BellSouth company reasons in August, September or October. 21

22

1 <u>% Jeopardies / 2w Analog Loop Non-Design (B.2.5.9)</u>

2 (August/September/October)

In August 2001, there were a total of 49 jeopardies issued for the 620 orders 3 4 that were scheduled for this sub-metric. All but 16 of the jeopardies were resolved and the orders were worked as scheduled. Only 3 of the 16 missed 5 appointments in this sub-metric resulted in held orders that were resolved and 6 7 completed in an average of 3.33 days. In September 2001, there were a total of 31 jeopardies issued for the 463 orders that were scheduled for this sub-8 metric. All but 10 of the jeopardies were resolved and the orders were 9 10 worked as scheduled. Only 3 of the 31 jeopardies in this sub-metric resulted 11 in a held order that were resolved and completed in an average of 4 days. In 12 October 2001, there were a total of 4 jeopardies issued for the 64 orders that were scheduled for this sub-metric. None of the 4 October jeopardies resulted 13 14 in a missed installation appointment.

15

16 <u>% Jeopardies / 2w Analog Loop w/LNP Design (B.2.5.12) (September)</u>

In September 2001, there were a total of 82 jeopardies issued for the 3,707
orders that were scheduled for this sub-metric. All but 5 of the jeopardies
were resolved and the orders were worked as scheduled. BellSouth met the
retail analogue comparison for this sub-metric in August and October 2001.

21

22 <u>% Jeopardies / Digital Loop >= DS1 (B.2.5.19) (August/September/October)</u>

1 There were a total of 65 jeopardies issued for the 157 installation 2 appointments that were scheduled for this sub-metric in August, 37 jeopardies 3 for the 168 appointments scheduled for September and 48 leopardies issued 4 for the 101 orders scheduled for October 2001. While the data indicates that BellSouth placed a higher percentage of CLEC orders in jeopardy status, all 5 6 but 17 of the orders that were placed in jeopardy in August, all but 19 of the 7 None of the 48 jeopardies for October jeopardy orders in September. resulted in missed installation appointments or held orders. Of the 17 missed 8 9 appointments in August, only 1 resulted in a held order that was completed 10 within 10 days. None of the orders placed in jeopardy in September resulted 11 in a held order.

12

- 13 <u>% Jeopardy Notices issued >= 48 Hours / 2w Analog Loop Non-Design</u>
 14 (B.2.10.9) (August)
- 15 <u>% Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/LNP Design</u>
- 16 (B.2.10.12) (August)
- 17 <u>% Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/LNP Non</u>
- 18 Design (B.2.10.13) (August/October)
- 19 <u>% Jeopardy Notices issued >= 48 Hours / Digital Loop < DS1 (B.2.10.18)</u>
- 20 <u>(August)</u>

The calculations for this measure have been determined to be incorrect. A portion of the coding modifications required to correct this problem were implemented in September 2001. BellSouth is continuing to prepare and test

- the remainder of the modifications necessary to correct the calculations for
 this measure.
- 3

4 <u>% Missed Installation Appointments / Digital Loop >= DS1 / < 10 Circuits /</u>

5 Dispatch (B.2.18.19.1.1) (September/October)

BellSouth completed 208 of the 227 installation appointments as scheduled 6 7 for this sub-metric in September and 263 of the 282 installation appointments scheduled in October 2001. In September, nine of the nineteen missed 8 9 appointments, and in October ten of the nineteen missed appointments were 10 due to unavailability of facilities. The remainder of the missed appointments 11 in both months were due to various scheduling and prioritization problems. 12 BellSouth is refocusing its efforts on this area to improve its performance on these orders. BellSouth met the retail analogue comparison for this sub-13 14 metric in August 2001.

15

16 <u>% Provisioning Troubles w/i 30 Days / 2w Analog Loop Design / < 10 Circuits</u>

17 / Dispatch (B.2.19.8.1.1) (August/September)

In August 2001, there were 21 troubles reported for the 224 orders completed in the prior 30 days. Five of the twenty-one troubles were closed as "no trouble found." An analysis of the remainder of the troubles revealed no specific patterns or trends. In September 2001, 29 troubles were reported for the 302 orders completed in the prior 30 days. Ten of the twenty-nine troubles were closed as "no trouble found" in September. Without these

1	reports, the CLEC result would have been virtually identical to the result for
2	the retail analogue. Twenty of the twenty-nine trouble reports in September
3	for this sub-metric came from one CLEC. BellSouth met the retail analogue
4	comparison for this sub-metric in October 2001.
5	
6	% Provisioning Troubles w/i 30 Days / 2w Analog Loop Design / >= 10
7	Circuits / Dispatch (B.2.19.8.2.1) (October)
8	There was only one order associated with this sub-metric in October 2001.
9	This small universe of orders does not provide a statistically conclusive
10	comparison to the retail analogue. BellSouth met the retail analogue
11	comparison for this sub-metric in August and September 2001.
12	
13	<u>% Provisioning Troubles w/i 30 Days / Digital Loops < DS1 / < 10 Circuits /</u>
14	Dispatch (B.2.19.18.1.1) (August)
15	There were a total of 47 troubles reported for the 901 orders that completed
16	for this sub-metric in the 30 days prior to August 2001. Analysis of the trouble
17	reports indicates that a significant portion were closed as "no trouble found."
18	BellSouth met the retail analogue comparison for this sub-metric in
19	September and October 2001.
20	-
21	<u>% Provisioning Troubles w/i 30 Days / Digital Loops >= DS1 / < 10 Circuits /</u>
22	Dispatch (B.2.19.19.1.1) (August/September/October)

•

1 There were a total of 19 troubles reported for this sub-metric for the 236 2 orders that completed in the 30 days prior to August, 15 troubles reported for 3 the 251 orders that completed in the 30 days prior to September and 12 4 troubles reported for the 227 orders that completed in the 30 days prior to 5 October 2001. In September and October, 44% and 25%, respectively, of the 6 trouble reports in this sub-metric were closed as "no trouble found" indicating 7 minimal impact on the end user. BellSouth is currently investigating this sub-8 metric. There are no trouble reports indicated for the retail analogue for this 9 sub-metric in August, and less than 1% trouble reports in September and 10 October -- which is also being reviewed.

11

12 Average Completion Notice Interval / 2w Analog Loop Design / < 10 Circuits /

13 Dispatch (B.2.21.8.1.1) (August/September/October)

14 Average Completion Notice Interval / 2w Analog Loop w/LNP Design / < 10

15 <u>Circuits / Dispatch (B.2.21.12.1.1) (August/September/October)</u>

16 Average Completion Notice Interval / 2w Analog Loop w/LNP Non-Design / <

17 <u>10 Circuits / Dispatch (B.2.21.13.1.1) (August/September/October)</u>

The root cause analysis of these measures indicated that the only differences between the performance between BellSouth retail and CLECs are the mismatches found when the orders are compared with the original LSRs. The start of the completion interval is the point at which the technician completes the order, and the interval ends when the completion notice is sent. Any change to a name, number of items, etc., occurring during the

1 provisioning process will generate inconsistencies with the original LSRs that 2 must be resolved before a final completion notice can be sent. Any time to 3 resolve these inconsistencies with the original LSRs is included in the 4 average. Because of numerous CLEC changes and order updates. mismatches on CLECs orders exceed those for BellSouth retail orders. 5 6 Combining this with the smaller base for the CLECs' measurement raises the 7 average, which results in a miss. Specific Service Representatives within the 8 Work Management Centers have been assigned to resolve any completion 9 issues that are required. Providing specific training and dedicating personnel 10 to this task should reduce the difference between the CLEC and retail 11 analogue results.

12

13 2. Maintenance & Repair Measures

The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed
critical value comparison requirements for August, September and/or October
2001 are as follows:

17

18 % Missed Repair Appointments / 2W Analog Loop Non-Design / Dispatch

19 (B.3.1.9.1) (August)

There-were a total of 128 missed appointments out of the 842 scheduled for this sub-metric in August 2001. A significant cause for the missed appointments in August was found to be wet or damaged cable facilities. BellSouth is refocusing on its existing cable damage prevention plan.

BellSouth met or exceeded the retail analogue for this sub-metric in
 September and October 2001.

3

<u>% Missed Repair Appointments / 2W Analog Loop Non-Design / Non-</u> <u>Dispatch (B.3.1.9.2) (September/October)</u>

6 BellSouth completed 34 of the 36 repair appointments as scheduled in 7 September and 49 of the 57 appointments scheduled for October 2001. 8 There were no distinct patterns or systemic maintenance problems identified 9 for the two missed appointments in September or for the eight missed 10 appointments in October. BellSouth met the retail analogue comparison for 11 this sub-metric in August 2001.

12

13 Maintenance Average Duration / 2w Analog Loop Non-Design / Non-Dispatch

14 (B.3.3.9.2) (October)

There were 57 repair orders completed for this sub-metric in October 2001. 15 Of the 57 total October reports, 33 (58%) were finally closed as "no trouble 16 found." Reports closed as TOK/FOK often have longer duration intervals due 17 to multiple and time consuming test procedures and investigations without 18 19 finding any cause for a problem. Excluding the reports closed to "no trouble found" in October, this sub-metric would have met the retail analogue 20 comparison for the month. BellSouth met the retail analogue comparison for 21 22 this sub-metric in August and September 2001.

23

<u>% Repeat Reports w/i 30 Days / 2W Analog Loop Non-Design / Non-Dispatch</u> (B.3.4.9.2) (October)

There were a total of 57 trouble reports of which 16 were repeats in this submetric for October 2001. Of the 16 repeat reports for October, 11 (69%) were closed as "no trouble found." Excluding these TOK/FOK reports, this submetric would have met the retail analogue comparison for the month. BellSouth met the retail analogue comparison for this sub-metric in August and September 2001.

9

10 Out of Service > 24 Hours / 2W Analog Loop Non-Design / Non-Dispatch

11 (B.3.5.9.2) (October)

Of the 12 troubles classified as "out of service" for this sub-metric in October 2001, only 5 caused out of service conditions longer than 24 hours. All 5 of these troubles for October were associated with a central office failure. BellSouth met the retail analogue comparison for this sub-metric in August and September 2001.

- 17
- 18

19

E. <u>CHECKLIST ITEM 5 – UNBUNDLED LOCAL TRANSPORT</u>

20

21 The sub-metrics that did not meet the retail analogue in August, September 22 and /or October 2001 associated with Checklist Item 5 are as follows:

23

1	<u>% Missed Installation Appointments / Local Interoffice Transport / < 10</u>
2	Circuits / Dispatch (B.2.18.2.1.1) (September)
3	BellSouth completed 24 of the 26 installation appointments for this sub-metric
4	as scheduled in September 2001. There were no systemic installation issues
5	identified for the two missed appointments. BellSouth met the retail analogue
6	comparison for this sub-metric in August and October 2001.
7	
8	% Provisioning Troubles w/i 30 Days / Local Interoffice Transport / < 10
9	Circuits / Dispatch (B.2.19.2.1.1) (August)
10	There was only 1 trouble reported for this sub-metric for orders that were
11	completed in the 30 days prior to August 2001. The small universe for this
12	sub-metric does not provide a statistically conclusive comparison to the retail
13	analogue. BellSouth met the retail analogue comparison for this sub-metric in
14	September and October 2001.
15	
16	Maintenance Average Duration / Local Interoffice Transport / Non-Dispatch
17	(B.3.3.2.2) (August)
18	There were only four troubles reported for this sub-metric in August 2001.
19	This small universe does not provide a statistically conclusive comparison
20	with the retail analogue. BellSouth met the retail analogue comparison for
21	this sub-metric in September and October 2001.

1	F. CHECKLIST ITEM 6 - UNBUNDLED LOCAL SWITCHING
2	
3	The data in these measures indicate that BellSouth met the
4	benchmark/analogue requirements for all measurements in Checklist Item 6
5	for August, September and October 2001.
6	
7	G. CHECKLIST ITEM 7a - 911 AND E911 SERVICES
8	H. CHECKLIST ITEM 7b - DIRECTORY ASSISTANCE/OPERATOR
9	SERVICES
10	
11	As indicated in Attachment 1E, Sections F.6, F.7 and F.8, BellSouth met the
12	benchmark/analogue requirements of Checklist Items 7a and 7b in August,
13	September and October 2001. Even though BellSouth tracks and reports
14	these measures, the processes used in providing these services are designed
15	to provide parity for all users.
16	
17	I. CHECKLIST ITEM 10 – ACCESS TO DATABASES AND ASSOCIATED
18	SIGNALING
19	BellSouth met the benchmarks for three out of four sub-metrics in this
20	Checklist Item in August, one out of four sub-metrics in September and three
21	out of four sub-metrics in October 2001. See items F.13.1.1 through F.13.3 in
22	Attachment 1E for further details of the October data. The items that did not

- meet the appropriate benchmark in August, September and/or October 2001
 are as follows:
- 3

4 % Update Accuracy / LIDB (F.13.2.1) (/September)

5 The results in this sub-metric are based on a statistical sample of LSRs and 6 service orders which are manually checked for the accuracy of information 7 that impacts the LIDB database. In September, all but 23 of the 174 orders were error free for this sub-metric. BellSouth has refocused its effort on all 8 9 LSRs processed in the partial mechanized and manual categories to eliminate basic errors made by the representatives that should meet the 10 benchmark for this sub-metric. BellSouth met the benchmark for this sub-11 12 metric in August and October 2001.

13

14 <u>% Update Accuracy / Directory Listings / Region (F.13.2.2) (September)</u>

15 The results in this sub-metric are based on a statistical sample of LSRs and 16 service orders, which are manually checked for the accuracy of information that impacts the Directory Listings database. The September 2001 results 17 were based on a sample size of 89 orders, of which 23 orders were found to 18 contain errors. BellSouth has refocused its effort on all LSRs processed in 19 the partial mechanized and manual categories to eliminate basic errors made 20 by the representatives that should meet the benchmark for this sub-metric. 21 BellSouth met the benchmark for this sub-metric in August and October 2001. 22

23

<u>% NXXs / LRNs Loaded by LERG Effective Date (Region) (F.13.3)</u> (August/September/October)

The measure indicated that 23 of 24 NXXs were loaded by their effective date in August and 39 of 40 NXXs were loaded by their effective date in September 2001. In October 2001, 45 of 48 NXXs were loaded by their effective date across the BellSouth region, with all NXXs completed as scheduled in Florida.

- 8
- 9

J. CHECKLIST ITEM 11 - NUMBER PORTABILITY

10

All the measurements in this Checklist Item were met or exceeded for August,
 September and/or October 2001 except for the following:

13

14 % Missed Installation Appointments / LNP (Standalone) / < 10 Circuits / Non-

15 Dispatch (B.2.18.17.1.2) (August/September/October)

16 BellSouth missed only 9 of the 1,715 scheduled appointments for this sub-17 metric in August, missed only 4 of the 1,381 appointments scheduled in September and missed only 3 of the 2,219 appointments scheduled in 18 19 October 2001. BellSouth met over 99% of the scheduled appointments for 20 both retail and the CLECs in this sub-metric for August, September and 21 October. When BellSouth provisions high quality service coupled with very 22 large universe sizes, it can cause an apparent out of equity condition from a 23 quantitative viewpoint. In these cases, there is very little variation and the 1 universe size is so large that the Z-test becomes overly sensitive to any 2 difference. In other words, the statistical test shows that the measurement 3 does not meet the fixed critical value when compared with the retail analogue, 4 but BellSouth's actual performance for both CLECs and its own retail operations is at a very high level - in this case over 99%. From a practical 5 point of view, the CLECs' ability to compete has not been hindered even 6 7 though the statistical results may technically show that BellSouth failed to 8 meet the benchmark/analogue.

9

10 Average Completion Notice Interval / LNP (Standalone) / < 10 Circuits / Non-

11 Dispatch (B.2.21.17.1.2) (August/September/October)

12 Average Completion Notice Interval / LNP (Standalone) / >= 10 Circuits /

13 Non-Dispatch (B.2.21.17.2.2) (August/October)

14 The root cause analysis of these measures indicated that the only differences 15 between the performance between BellSouth retail and CLECs are the 16 mismatches found when the orders are compared with the original LSRs. 17 The start of the completion interval is the point at which the technician 18 completes the order, and the interval ends when the completion notice is 19 sent. Any change to a name, number of items, etc., occurring during the provisioning process will generate inconsistencies with the original LSRs that 20 21 must be resolved before a final completion notice can be sent. Any time to 22 resolve these inconsistencies with the original LSRs is included in the Because of numerous CLEC changes and order updates, 23 average.

1 mismatches on CLECs orders exceed those for BellSouth retail orders. 2 Combining this with the smaller base for the CLECs' measurement raises the 3 average, which results in a miss. Specific Service Representatives within the 4 Work Management Centers have been assigned to resolve any completion 5 issues that are required. Providing specific training and dedicating personnel 6 to this task should reduce the difference between the CLEC and retail 7 analogue results. 8

9 Disconnect Timeliness / LNP / < 10 Circuits (B.2.31)

10 The Disconnect Timeliness measure is supposed to track the time it takes to 11 disconnect a number in the central office switch after the message has been 12 received from the Local Number Portability (LNP) Gateway that it is ready. 13 However, this measurement does not track the relevant time to perform this 14 function.

15

16 On a great majority of LNP orders, BellSouth creates what is referred to as a 17 "trigger" in conjunction with the order. This trigger gives the end user 18 customer the ability to make and receive calls from other customers who are 19 served by the customer's host switch at the time of the LNP activation. This 20 ability is not dependent upon BellSouth working a disconnect order in the 21 central office switch. In other words, when a trigger is involved, an end user 22 customer can receive calls from other customers served by the same host 23 switch before the disconnect order is ever worked.

2 As it currently exists, Performance Measure P-13 does not recognize the 3 importance of triggers and their effect on the LNP process. Rather. the 4 current measure calculates the end time of the LNP activity as the processing 5 of the actual disconnect order in the host switch, even though, from a 6 customer's perspective, this activity is totally meaningless on most LNP 7 orders. It is the activation of the LNP and the routing function accomplished 8 by the LSMS that ultimately determines whether the end user is back in full 9 service and is able to make and receive calls when a trigger is used in porting 10 a telephone number. So, while BellSouth may be missing this measure, the 11 actual impact on CLECs and their end users, for a great majority of the orders 12 is minimal, or nonexistent. The Georgia PSC is currently evaluating a change 13 in this measure that more accurately reflects the LNP process and its impacts 14 on end users, and, therefore, the measurements will be shown blank until a 15 resolution is reached on this issue.

- 16
- 17

K. CHECKLIST ITEM 14 - RESALE

BeilSouth has met or exceeded the benchmarks/analogues for 87% of the 19 191 Resale metrics for the month of August, for 86% of the 211 metrics in 20 September and for 80% of the 223 metrics in October 2001. The details are 21 delineated in Attachment 1E, Items A.1.1.1 through A.4.2.

22

1 For the three-month period, August through October 2001, there were 169 2 sub-metrics in the Resale measurements for which there was CLEC activity in all three months and were compared to retail analogues or benchmarks. Of 3 169 4 those sub-metrics. 155 sub-metrics (92%) met the retail 5 analogue/benchmark comparisons in at least two of the three months.

6

7

1. Resale Ordering Measures

8 **Reject Interval**

9 The benchmark for electronic rejects is 97% within 1 hour. In August 2001, there was a total of 16.628 resale LSRs rejected, with 94% meeting the 10 relevant benchmark or retail analogue. Of the 16,628 rejected LSRs, 61% 11 12 were processed electronically with 92% of them meeting the 1-hour 13 benchmark interval. In September 2001, 14,963 resale LSRs were rejected, 14 with 96% meeting the relevant benchmark or retail analogue. Of the 14,963 15 rejected LSRs, 60% were processed electronically with 95% of them meeting 16 the 1-hour benchmark interval. In October 2001, there was a total of 23,820 17 resale LSRs rejected, with 94% meeting the relevant benchmark. Of the 23.820 rejected LSRs, 67% were processed electronically with 94% of them 18 meeting the 1-hour benchmark interval. See Attachment 1E, Items A.1.4 19 20 through A.1.8 for further details.

21

22 FOC Timeliness

1 In August, BellSouth issued FOCs for 53,972 resale LSRs and met the 2 relevant benchmark for 98% of them. Of the 53,972 FOCs returned, 41,729 were fully mechanized with 98% meeting the 3-hour benchmark interval. In 3 4 September, BellSouth issued FOCs for 48,475 resale LSRs and met the 5 relevant benchmark for 99% of them. Of the 48,475 FOCs returned, 36,875 6 were fully mechanized with 99% meeting the 3-hour benchmark interval. In 7 October, BellSouth issued FOCs for 71,611 resale LSRs and met the relevant benchmark for 98% of them. Of the 71,611 FOCs returned, 54,852 were fully 8 9 mechanized with 99% meeting the 3-hour benchmark interval. See 10 Attachment 1E, Sections A.1.9 through A.1.13 for further details.

11

12 The Ordering sub-metrics for which BellSouth did not meet the 13 benchmarks/analogues for August, September and/or October 2001 were:

14

15 Reject Interval / Residence / Electronic (A.1.4.1) (August/September/October) 16 The current benchmark for this sub-metric is $\geq 97\%$ within one hour. In 17 August 2001, 8,815 of the 9,536 total rejected LSRs for this sub-metric met 18 the 1-hour benchmark interval. In September 2001, 7,954 of the 8,395 total 19 rejected LSRs met the one-hour benchmark, and in October, 14,285 of the 20 15,140 rejected LSRs in this sub-metric met the benchmark interval. 21 BellSouth is conducting a detailed root cause analysis of the process for 22 electronic rejects. This analysis addresses the ordering systems (EDI, TAG,

- and LENS) used by the CLECs and the back-end legacy applications, such
 as SOCS, that are accessed by the ordering systems.
- 3

Thus far, the analysis has determined that many of the LSRs that did not meet the one-hour benchmark in August and September were issued between 11:00 p.m. and 4:30 a.m. Between these hours, the system is unable to process LSRs because certain of the back-end legacy systems are out of service. LSRs submitted during these periods should have been excluded from the measurement. BellSouth implemented a program coding change in September to exclude these LSRs from this measure.

11

12 With the May 2001, data month, BellSouth was directed to change the time 13 stamp identification for the start and complete times of the interval for this 14 measurement from the Local Exchange Ordering (LEO) System to the CLEC 15 ordering interface system (TAG or EDI). However, with this change, BellSouth is currently unable to identify multiple issues of the same version of 16 17 LSRs that have been rejected (fatal rejects). These rejected LSRs should be excluded from the measurement. If there are multiple issues of the same 18 version, the measure currently calculates the interval from the initial issue to 19 20 the final issue of the LSR returned to the CLEC, Reject or FOC. Consequently, BellSouth's performance level is inappropriately understated. 21 22 BellSouth is currently working to determine a fix for this issue.

23

1 <u>Reject Interval / Business / Electronic (A.1.4.2) (August/September/October)</u> 2 The current benchmark for this sub-metric is >= 97% within one hour. There 3 were 643 LSRs rejected in this sub-metric in August 2001 with 596 or 93% 4 meeting the one hour benchmark. In September 2001, 533 of the 563 5 rejected LSRs for this sub-metric met the one-hour benchmark, and in 6 October, 839 of the 892 rejected LSRs met the 1-hour benchmark. BellSouth 7 is conducting a detailed root cause analysis of the process for electronic 8 orderina. This analysis addresses the ordering systems (EDI, TAG, and 9 LENS) used by the CLECs and the back-end legacy applications, such as 10 SOCS, that are accessed by the ordering systems. For further information 11 see the explanation included with the electronic reject interval measurement. 12 item A.1.4.1.

13

14 <u>Reject Interval / PBX / Electronic (A.1.4.4) (August)</u>

There was only one LSR rejected for this sub-metric in August 2001. The small universe size for this sub-metric does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in either September or October 2001.

19

20 Reject Interval / ISDN / Electronic (A.1.4.6) (October)

There were only two LSRs rejected for this sub-metric in October 2001. This small universe does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in either August or September 2001.

1	
2	Reject Interval / PBX / Partial Electronic (A.1.7.4) (August)
3	There were only two LSRs rejected for this sub-metric in August 2001. The
4	small universe size for this sub-metric does not provide a conclusive
5	benchmark comparison. BellSouth met the benchmark for this sub-metric in
6	September 2001. There was no CLEC activity for this sub-metric in October
7	2001.
8	
9	Reject Interval / ISDN / Partial Electronic (A.1.7.6) (October)
10	There was only one LSR rejected for this sub-metric in October 2001. This
11	small universe does not provide a conclusive benchmark comparison. There
12	was no CLEC activity for this sub-metric in either August or September 2001.
13	
14	FOC Timeliness / Design (Specials) / Partial Electronic (A.1.12.3) (October)
15	There was only one LSR rejected for this sub-metric in October 2001. This
16	small universe does not provide a conclusive benchmark comparison. There
17	was no CLEC activity for this sub-metric in either August or September 2001.
18	
19	FOC Timeliness / PBX / Partial Electronic (A.1.12.4) (August/September)
20	There was only one order for which FOCs were returned in this sub-metric in
21	each of August and September 2001. Such a small universe does not
22	provide a conclusive benchmark comparison. There was no CLEC activity for
23	this sub-metric in October 2001.

2 FOC Timeliness / ISDN / Partial Electronic (A.1.12.6) (October)

There were only two LSRs rejected for this sub-metric in October 2001. This
small universe does not provide a conclusive benchmark comparison. There
was no CLEC activity for this sub-metric in either August or September 2001.

6

7 FOC & Reject Response Completeness and FOC & Reject Response

8 <u>Completeness (Multiple Responses) Measures</u>

9 BellSouth has determined that the coding for the FOC & Reject Completeness and FOC & Reject Response Completeness (Multiple 10 11 Responses) measures failed to include rejections that were classified as "auto 12 clarifications." BellSouth is in the process of rewriting the code to correct this 13 problem, and the change will impact all FOC & Reject Completeness and 14 FOC & Reject Response Completeness (Multiple Responses) measures. 15 Effective with this Exhibit update for October data, the program coding has been corrected for all the FOC & Reject Completeness sub-metrics for 16 17 Checklist Item No. 14, Resale products. The individual sub-metrics with corrected coding and that missed the required benchmarks in September 18 19 and/or October 2001 will be addressed separately following the next section. 20 BellSouth did not meet the benchmark in August 2001 for the FOC and Reject Response Completeness or for the FOC & Reject Response Completeness 21 22 (Multiple Responses) metrics listed below:

23

- 1 FOC Reject & Response Completeness / PBX / Partial Electronic (A.1.15.4)
- 2 (August)
- 3 FOC Reject & Response Completeness / Residence / Manual (A.1.16.1)
- 4 (August)
- 5 FOC Reject & Response Completeness / Business / Manual (A.1.16.2)
- 6 (August)
- 7 FOC Reject & Response Completeness / Design (Specials) / Manual
- 8 (A.1.16.3) (August)
- 9 FOC Reject & Response Completeness / PBX / Manual (A.1.16.4) (August)
- 10 FOC Reject & Response Completeness (Multiple Responses) / Residence /
- 11 Partially Electronic (A.1.18.1) (August)
- 12 FOC Reject & Response Completeness (Multiple Responses) / Business /
- 13 Partially Electronic (A.1.18.2) (August)
- 14 FOC Reject & Response Completeness (Multiple_Responses) / PBX /
- 15 Partially Electronic (A.1.18.4) (August)
- 16 FOC Reject & Response Completeness (Multiple Responses) / Residence /
- 17 Manual (A.1.19.1) (August)
- 18 FOC Reject & Response Completeness (Multiple Responses) / Business /
- 19 Manual (A.1.19.2) (August)
- 20 FOC Reject & Response Completeness (Multiple Responses) / Design
- 21 (Specials) / Manual (A.1.19.3) (August)
- 22 FOC Reject & Response Completeness (Multiple Responses) / Centrex /
- 23 <u>Manual (A.1.19.5) (August)</u>

FOC Reject & Response Completeness (Multiple Responses) / ISDN / Manual (A.1.19.6) (August)

3 BellSouth determined that the coding for the FOC & Reject Completeness and FOC & Reject Response Completeness (Multiple Responses) measures 4 5 failed to include rejections that were classified as "auto clarifications." 6 BellSouth has rewritten the code to correct this problem. The coding changes were implemented for some products effective with September data and for 7 8 the remainder of the products effective with October data. The sub-metric 9 "misses" listed above were for operations prior to the implementation of the 10 coding modifications.

11

12 Effective with October 2001 data, each sub-metric in the Electronic and 13 Partially Electronic sections have been disaggregated between LSRs 14 submitted from the EDI and TAG systems. The following FOC & Reject 15 Response Completeness sub-metrics, for which the program code has been 16 corrected, did not meet the benchmarks for September and/or October 2001:

17

18 FOC Reject & Response Completeness / Design (Specials) / TAG / Electronic

19 (A.1.14.3.2) (October)

20 There was only one order associated with this sub-metric in October 2001.

- 21 This small universe does not provide a conclusive benchmark comparison.
- 22

1	FOC Reject & Response Completeness / ISDN / Electronic (A.1.14.6)
2	(September)
3	There was only one order for this sub-metric in September 2001. The small
4	universe size for this sub-metric does not provide a conclusive benchmark
5	comparison.
6	
7	FOC Reject & Response Completeness / Residence / Manual (A.1.16.1)
8	(September/October)
9	BellSouth met the completeness criteria for 833 of the 922 orders for this sub-
10	metric in September and 1,114 of the 1,176 orders in October 2001. The
11	95% benchmark required that 887 of 933 LSRs for September and 1,118 of
12	the 1,176 LSRs in October meet the criteria. BellSouth continues to focus on
13	this measurement in order to improve results to meet the benchmark.
14	
15	FOC Reject & Response Completeness / Business / Manual (A.1.16.2)
16	(September/October)
17	BellSouth met the completeness criteria for 903 of the 969 orders for this sub-
18	metric in September and for 1,168 of the 1,238 orders in October 2001. The
19	95% benchmark required that 921 of 969 LSRs in September and 1,177 of
20	1,238 LSRs for October 2001 meet the criteria. BellSouth continues to focus
21	on this measurement in order to improve results to meet the benchmark.
22	

FOC Reject & Response Completeness / Design (Specials) / Manual (A.1.16.3) (September/October)

BellSouth met the completeness criteria for 127 of the 139 orders for this submetric in September and for 165 of the 177 orders in October 2001. The 95%
benchmark required that 133 of 139 LSRs for September and 169 of the 177
LSRs for October meet the criteria. BellSouth continues to focus on this
measurement in order to improve results to meet the benchmark.

8

9 FOC Reject & Response Completeness / PBX / Manual (A.1.16.4)

10 (September/October)

BellSouth met the completeness criteria for 61 of the 66 orders for this submetric in September and for 79 of 84 orders in October 2001. The 95%
benchmark required that 63 of 66 LSRs in September and 80 of 84 LSRs in
October meet the criteria. BellSouth continues to focus on this measurement
in order to improve results to meet the benchmark.

16

17 <u>FOC Reject & Response Completeness / Centrex / Manual (A.1.16.5)</u> 18 (September/October)

BellSouth met the completeness criteria for 16 of the 17 orders for this submetric in September and for 11 of the 14 orders in October 2001. The 95%
benchmark required that all 17 of 17 LSRs for September and all 14 of 14
LSRs in October meet the criteria. With universe sizes of only 17 or 14
orders and a 95% benchmark, a problem on even one order causes a miss

1	for the entire sub-metric. BellSouth continues to focus on this measurement
2	in order to improve results to meet the benchmark.
3	
4	FOC Reject & Response Completeness / PBX / Manual (A.1.16.6)
5	(September)
6	BellSouth met the completeness criteria for 30 of the 33 orders for this sub-
7	metric in September 2001. The 95% benchmark required that 32 of 33 LSRs
8	meet the criteria. BellSouth continues to focus on this measurement in order
9	to improve results to meet the benchmark. BellSouth met the benchmark for
10	this sub-metric in October 2001.
11	
12	FOC Reject & Response Completeness (Multiple Responses) / Residence /
13	EDI / Electronic (A.1.17.1.1) (October)
14	BellSouth met the completeness criteria for 769 of the 965 orders for this sub-
15	metric in October 2001. The 95% benchmark required that 917 of 965 LSRs
16	meet the criteria. BellSouth continues to focus on this measurement in order
17	to improve results to meet the benchmark.
18	
19	FOC Reject & Response Completeness (Multiple Responses) / Business /
20	EDI / Electronic (A.1.17.2.1) (October)
21	BellSouth met the completeness criteria for 23 of the 47 orders for this sub-
22	metric in October 2001. The 95% benchmark required that 45 of 47 LSRs

1	meet the criteria. BellSouth continues to focus on this measurement in order
2	to improve results to meet the benchmark.
3	
4	FOC Reject & Response Completeness (Multiple Responses) / Residence /
5	Partially Electronic (A.1.18.1) (September)
6	BellSouth met the completeness criteria for 11,829 of the 12,767 orders for
7	this sub-metric in September 2001. The 95% benchmark required that
8	12,129 of 12,767 LSRs be returned. BellSouth continues to focus on this
9	measurement in order to improve results to meet the benchmark.
10	
11	FOC Reject & Response Completeness (Multiple Responses) / Residence /
12	TAG / Partial Electronic (A.1.18.1.2) (October)
13	BellSouth met the completeness criteria for 16,528 of the 17,932 orders for
14	this sub-metric in October 2001. The 95% benchmark required that 17,036 of
15	17,932 LSRs meet the criteria. BellSouth continues to focus on this
16	measurement in order to improve results to meet the benchmark.
17	
18	FOC Reject & Response Completeness (Multiple Responses) / Business /
19	Partially Electronic (A.1.18.2) (September)
20	BellSouth met the completeness criteria for 1,660 of the 1,861 orders for this
21	sub-metric in September 2001. The 95% benchmark required that 1,768 of
22	1,861 LSRs be returned. BellSouth continues to focus on this measurement
23	in order to improve results to meet the benchmark.

1 2 FOC Reject & Response Completeness (Multiple Responses) / Business / 3 EDI / Partial Electronic (A.1.18.2.1) (October) 4 BellSouth met the completeness criteria for 17 of the 19 orders for this submetric in October 2001. The 95% benchmark required that all 19 of 19 LSRs 5 6 meet the criteria. BellSouth continues to focus on this measurement in order 7 to improve results to meet the benchmark. 8 9 FOC Reject & Response Completeness (Multiple Responses) / Business / 10 TAG / Partial Electronic (A.1.18.2.2) (October) 11 BellSouth met the completeness criteria for 2,355 of the 2,628 orders for this 12 sub-metric in October 2001. The 95% benchmark required that 2,497 of 13 2,628 LSRs meet the criteria. BellSouth continues to focus on this 14 measurement in order to improve results to meet the benchmark. 15 16 FOC Reject & Response Completeness (Multiple Responses) / ISDN / TAG / 17 Partial Electronic (A.1.18.6.2) (October) There were only two orders for this sub-metric in October 2001. This small 18 universe size does not provide a conclusive benchmark comparison. 19 20 21 FOC Reject & Response Completeness (Multiple Responses) / Residence / 22 Manual (A.1.19.1) (September/October)

1	BellSouth met the completeness criteria for 748 of the 833 orders for this sub-
2	metric in September and for 1,001 of the 1,114 orders in October 2001. The
3	95% benchmark required that 792 of 833 LSRs for September and 1,059 of
4	1,114 LSRs meet the criteria. BellSouth continues to focus on this
5	measurement in order to improve results to meet the benchmark.
6	
7	FOC Reject & Response Completeness (Multiple Responses) / Business /
8	Manual (A.1.19.2) (September/October)
9	BellSouth met the completeness criteria for 837 of the 903 orders for this sub-
10	metric in September and for 1,066 of the 1,168 orders in October 2001. The
11	95% benchmark required that 858 of 903 LSRs for September and 1,110 of
12	the 1,168 LSRs for October meet the criteria. BellSouth continues to focus on

- 13 this measurement in order to improve results to meet the benchmark.
- 14

15 FOC Reject & Response Completeness (Multiple Responses) / Centrex /

16 Manual (A.1.19.5) (September)

BellSouth met the completeness criteria for 15 of the 16 orders for this submetric in September and for 10 of the 11 orders in October 2001. The 95% benchmark required that all 16 of 16 LSRs for September and all 11 of 11 LSRs in October meet the criteria. With universe sizes of 15 and 11 orders and a 95% benchmark, problems with even one order causes a miss for the entire sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark.

2 2. Resale Provisioning Measures

3

For the months of August, September and October 2001, BellSouth met or
exceeded the benchmark or retail analogue for 86%, 92% and 91% of all
Resale provisioning measures. The details supporting the October
percentage are delineated in Items A.2.1.1.1.1 through A.2.25.3.2.2 of
Attachment 1E.

9

10 The following are the Resale provisioning measures for which BellSouth did 11 not meet the retail analogue in August, September and/or October 2001.

12

13 Held Orders / Residence / < 10 Circuits / Other (A.2.2.1.1.3) (August)

There was only one held order for this sub-metric in August 2001. The small universe size for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

18

19 <u>Held Orders / Business / >= 10 Circuits / Facility (A.2.2.2.2.1) (August)</u>

There was only one held order for this sub-metric in August 2001. The small universe size for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

1	
2	Held Orders / PBX / < 10 Circuits / Facility (A.2.2.4.1.1) (August)
3	There was only one held order for this sub-metric in August 2001. The small
4	universe size for this sub-metric does not provide a statistically conclusive
5	comparison to the retail analogue. BellSouth met the retail analogue
6	comparison for this sub-metric in September and October 2001.
7	
8	<u>% Jeopardy Notice >= 48 hours / Residence / Mechanized (A.2.9.1) (August)</u>
9	The calculations for this measure have been determined to be incorrect. A
10	portion of the coding modifications required to correct this problem were
11	implemented in September 2001. BellSouth is continuing to prepare and test
12	the remainder of the modifications necessary to correct the calculations for
13	this measure.
14	
15	% Missed Installation Appointments / Residence / < 10 Circuits / Non-
16	Dispatch (A.2.11.1.1.2) (August/September/October)
17	BellSouth missed only 37 of the 41,062 installation appointments scheduled
18	for this sub-metric in August, missed 32 of the 35,349 appointments
19	scheduled in September and missed only 82 of the 54,436 installation
20	appointments scheduled in October 2001. Both the CLECs and BellSouth
21	retail had over 99% of all orders completed as scheduled in August,
22	September and October 2001. When BellSouth provisions high quality
23	service coupled with very large universe sizes, it can cause an apparent out

1 of equity condition from a quantitative viewpoint. In these cases, there is 2 very little variation and the universe size is so large that the Z-test becomes 3 overly sensitive to any difference. In other words, the statistical test shows 4 that the measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual performance for both CLECs 5 6 and its own retail operations is at a very high level – in this case over 99%. 7 From a practical point of view, the CLECs' ability to compete has not been 8 hindered even though the statistical results may technically show that 9 BellSouth failed to meet the benchmark/analogue.

10

11 <u>% Missed Installation Appointments / Business / < 10 Circuits / Dispatch</u> 12 (A.2.11.2.1.1) (August/October)

There were a total of 23 missed appointments out of the 572 appointments scheduled for this sub-metric in August and 25 missed appointments of the 636 appointments scheduled for October 2001. Both BellSouth retail and the CLECs had at least 96% of all scheduled appointments completed on time in August and October. BellSouth met the retail analogue comparison for this sub-metric in September 2001.

19

20 <u>% Missed Installation Appointments / Business / < 10 Circuits / Non-Dispatch</u>

- 21 (A.2.11.2.1.2) (August/September/October)
- 22 BellSouth missed 6 of the 2,700 scheduled appointments for this sub-metric 23 in August, missed 7 of the 2,410 appointments scheduled for September and
 - 119

1	missed 10 of the 3,375 installation appointments scheduled in October 2001.
2	Both the CLECs and BellSouth retail had over 99% of all orders completed as
3	scheduled in August, September and October 2001.
4	·
5	<u>% Missed Installation Appointments / PBX / < 10 Circuits / Dispatch</u>
6	(A.2.11.4.1.1) (September)
7	BellSouth missed 3 of the 11 scheduled appointments for this sub-metric in
8	August 2001. The small universe of orders for this sub-metric does not
9	provide a statistically conclusive comparison to the retail analogue. BellSouth
10	met the retail analogue for this sub-metric in August and October 2001.
11	
12	<u>% Missed Installation Appointments / PBX / < 10 Circuits / Non-Dispatch</u>
13	(A.2.11.4.1.2) (August)
14	BellSouth missed 3 of the 78 scheduled appointments for this sub-metric in
15	August 2001. Both the CLECs and BellSouth retail had over 96% of all
16	orders completed as scheduled in August. BellSouth met the retail analogue
17	comparison for this sub-metric in September and October 2001.
18	
19	<u>% Missed Installation Appointments / ISDN / < 10 Circuits / Non-Dispatch</u>
20	(A.2.11.6.1.2) (October)
21	BellSouth completed 24 of the 25 scheduled appointments for this sub-metric
22	in October 2001. Both the CLECs and BellSouth retail had 96% of all orders

- completed as scheduled in October. BellSouth met the retail analogue
 comparison for this sub-metric in August and September 2001.
- 3

<u>% Provisioning Troubles w/i 30 days / Residence / < 10 Circuits / Non-</u> <u>Dispatch (A.2.12.1.1.2) (August/September/October)</u>

6 In August 2001, there were 1,388 troubles reported for the 35,349 orders that 7 completed in the prior 30 days. 1,321 (95%) of the August trouble reports for 8 this sub-metric were from one CLEC. Thirty-two percent of the reported 9 troubles were closed as "TOK/FOK." In September 2001, there were 1,905 10 troubles reported for the 41,062 orders that completed in the prior 30 days. 11 Twenty-nine percent of the reported troubles were closed as "TOK/FOK." In 12 October 2001, there were 1,796 troubles reported for the 35,349 orders that 13 completed in the prior 30 days. 33% of those troubles were closed as 14 "TOK/FOK." The only significant trend identified in the October data showed 15 that 995, or 55%, of the total trouble reports for this sub-metric were for one 16 CLEC, with 55% of those troubles being cleared as TOK/FOK. With the exclusion of the "no trouble found" reports, this sub-metric would have met 17 18 the retail analogue comparison in each of the three months. BellSouth is 19 conducting an analysis of the provisioning situation with this particular CLEC 20 and will conduct joint sessions to determine how to avoid the no trouble found 21 reports.

22

1 <u>% Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Dispatch</u>

2 (A.2.12.2.1.1) (August/September/October)

There were 29 troubles reported for the 429 orders that completed for this sub-metric in the 30 days prior to August 2001. In September 2001, there were 39 troubles reported for the 572 orders that completed in the prior 30 days. Of the 39 troubles reported, 21 (54%) were closed as "no trouble found." There were 42 troubles reported for the 486 orders that completed for this sub-metric in the 30 days prior to October 2001. Of the 42 troubles reported in October, 18 (43%) were closed as "no trouble found."

10

11 <u>% Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Non-Dispatch</u>

12 (A.2.12.2.1.2) (August)

13 There were 141 troubles reported for the 2,462 orders that completed for this 14 sub-metric in the 30 days prior to August 2001. Of the total August trouble 15 reports for this sub-metric, 56% were closed as "TOK/FOK." For two CLECs, 16 50% or more of their trouble reports for the month were closed as 17 "TOK.FOK." Without these "no trouble found" reports, this sub-metric would 18 have met the retail analogue comparison for August. BellSouth is conducting 19 an analysis of the provisioning situation with these particular CLECs and will 20 conduct joint sessions to determine how to avoid the no trouble found reports. 21 BellSouth met the retail analogue comparison for this sub-metric in 22 September and October 2001.

23

<u>% Provisioning Troubles w/i 30 days / PBX / >= 10 Circuits / Dispatch</u> (A.2.12.4.2.1) (September)

There was only one order for this sub-metric in September 2001. The small universe for this measurement does not provide a statistically conclusive comparison with the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in August 2001. There was no CLEC activity for this sub-metric in October 2001.

8

9 Service Order Accuracy / Business / < 10 Circuits / Dispatch (A.2.25.2.1.1)

10 (August/October)

11 There were only six orders reviewed for this sub-metric in August and thirteen 12 orders reviewed in October 2001. The small universe for this sub-metric does 13 not provide a conclusive benchmark comparison. BellSouth met the 14 benchmark for this sub-metric in September 2001.

15

16 Service Order Accuracy / Business / < 10 Circuits / Non-Dispatch

17 (A.2.25.2.1.2) (August/September/October)

BellSouth met the standard for 159 of the 182 orders reviewed in this submetric for August, for 204 of the 221 orders reviewed in September and for 128 of the 145 orders reviewed in October 2001. The 95% benchmark set requirements of 173 orders for August, 210 orders for September and 139 orders in October based on the quantity of orders for this sub-metric.

- BellSouth continues to focus on this measurement in order to improve results
 to meet the benchmark.
- 3

4 <u>Service Order Accuracy / Business / >= 10 Circuits / Non-Dispatch</u>

5 (A.2.25.2.2.2) (August/September)

6 There were only five orders reviewed for this sub-metric in August and seven 7 orders reviewed in September 2001. The small universe for this sub-metric 8 does not provide a conclusive benchmark comparison. BellSouth met the 9 benchmark for this sub-metric in October 2001.

10

11 <u>Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch</u>

12 (A.2.25.3.1.1) (October)

There were only four orders reviewed for this sub-metric in October 2001.
This small universe size does not provide a conclusive benchmark
comparison. BellSouth met or exceeded the benchmark for this sub-metric in
August and September 2001.

17

18 3. Resale Maintenance and Repair (M&R) Measures

19

20 BellSouth met the relevant retail analogues for 90%, 89% and 79% of all the 21 Resale Maintenance & Repair measurements in August, September and 22 October, respectively. The sub-metrics for which BellSouth did not meet the 23 retail analogues were:

1	
2	Missed Repair Appointments / Design (Specials) / Non-Dispatch (A.3.1.3.2)
3	(September)
4	BellSouth completed 16 of the 22 repair appointments as scheduled for this
5	sub-metric in September 2001. There were no maintenance issues or
6	patterns identified for the 6 missed appointments. BellSouth met the retail
7	analogue comparison for this sub-metric in August and October 2001.
8	
9	
10	
11	Missed Repair Appointments / PBX / Dispatch (A.3.1.4.1) (October)
12	BellSouth completed 27 of the 40 repair appointments as scheduled for this
13	sub-metric in October 2001. There were no maintenance issues or patterns
14	identified for the 13 missed appointments. Six of the thirteen missed
15	appointments were dispatched on time but did not finish by the committed
16	time (all completed within 1.5 hours of the committed time). BellSouth met
17	the retail analogue comparison for this sub-metric in August and September
18	2001.
19	
20	Missed Repair Appointments / ISDN / Non-Dispatch (A.3.1.6.2) (October)
21	There were only nine orders for this sub-metric in October 2001. The small
22	universe for this sub-metric does not provide a statistically conclusive

1	comparison to the retail analogue. BellSouth met the retail analogue
2	comparison for this sub-metric in August and September 2001.
3	
4	Customer Trouble Report Rate / Residence / Dispatch (A.3.2.1.1)
5	(August/October)
6	There were 3,633 troubles reported for the approximately 145,000 in service
7	lines for this sub-metric in August and 4,304 trouble reports for the 173,600
8	lines in services in October 2001. Both the CLECs and BellSouth retail had
9	no trouble reports for over 97% of the in service lines in both August and
10	October. There was only about 0.1% difference in the report rates between
11	retail and resale results for this sub-metric in both months. BellSouth met the
12	retail analogue for this sub-metric in September 2001.
13	
14	Customer Trouble Report Rate / Business / Dispatch (A.3.2.2.1)
15	(August/September/October)
16	There were 1,118 troubles reported for the approximately 57,000 in service
17	lines for this sub-metric in August, 980 troubles reported for the 56,000 lines
18	in service in September and 1,038 trouble reports for the 55,500 lines in
19	service in October 2001. Both the CLECs and BellSouth retail had trouble
20	free service for over 98% of the in service lines in all three months. Of the
21	1,118 trouble reports in August, 22% were closed as "TOK/FOK." In
22	September, 258 (26%) of the trouble reports were closed as "TOK/FOK." In
23	October, 145 (14%) of the 1,038 trouble reports were closed as "TOK/FOK."

1	Without	these	reports,	BellSouth	would	have	met	the	retail	analogue
2	comparis	son for	each of th	ne three mo	nths.					

4 Customer Trouble Report Rate / PBX / Dispatch (A.3.2.4.1)

5 (September/October)

6 There were only 26 trouble reports for the 3,995 in service lines for this sub-7 metric in September and 40 trouble reports for the 6,477 lines in service for 8 October 2001. BellSouth provided over 99% trouble free service for both 9 retail and the CLECs for this sub-metric for the months of September and 10 October. From a practical point of view, the CLECs' ability to compete has 11 not been hindered even though the statistical results may technically show 12 that BellSouth failed to meet the benchmark/analogue. BellSouth met the 13 retail analogue comparison for this sub-metric in August 2001.

14

15 Customer Trouble Report Rate / Centrex / Non-Dispatch (A.3.2.5.2) (October)

16 There were only 14 trouble reports for the 2,145 in service lines for this sub-17 metric in October 2001. Of the 14 trouble reports in October, 8 (57%) were 18 closed as "no trouble found." BellSouth provided over 99% trouble free 19 service for both retail and the CLECs for this sub-metric for the month. From 20 a practical point of view, the CLECs' ability to compete has not been hindered 21 even though the statistical results may technically show that BellSouth failed 22 to meet the benchmark/analogue. BellSouth met the retail analogue 23 comparison for this sub-metric in August and September 2001.

-	

2 Customer Trouble Report Rate / ISDN / Dispatch (A.3.2.6.1) (October)

3 There were only 13 trouble reports for the 5,484 in service lines for this sub-4 metric in October 2001. Of the 13 reports for October, 6 (46%) reports were 5 closed as "no trouble found." BellSouth provided over 99% trouble free 6 service for both retail and the CLECs for this sub-metric for the month. From 7 a practical point of view, the CLECs' ability to compete has not been hindered 8 even though the statistical results may technically show that BellSouth failed 9 to meet the benchmark/analogue. BellSouth met the retail analogue 10 comparison for this sub-metric in August and September 2001.

11

12 Maintenance Average Duration / PBX / Dispatch (A.3.3.4.1) (October)

13 Of the 40 total trouble reports for this sub-metric in October, 19 exceeded the 14 average maintenance duration time for the retail analogue. However, 12 of 15 the 19 longer duration repair reports me the offered commitment intervals. 16 Five of these twelve reports were received late on a Friday afternoon, and 17 were committed and completed before noon on Monday. Six of the twelve 18 reports were taken late on a weekday afternoon and were completed the 19 following day. One report could not be completed because the technician 20 could not gain access to the customer's equipment location. The remaining 21 seven longer duration reports were due to cable facility problems (four at the 22 same customer location). BellSouth met the retail analogue comparison for 23 this sub-metric in August and September 2001.

1	
2	Maintenance Average Duration / PBX / Non-Dispatch (A.3.3.4.2) (August)
3	There were only five orders for this sub-metric in August 2001. The small
4	universe for this sub-metric does not provide a statistically conclusive
5	comparison to the retail analogue. BellSouth met the retail analogue for this
6	sub-metric in September and October 2001.
7	
8	Maintenance Average Duration / Centrex / Dispatch (A.3.3.5.1) (September)
9	There were only nine orders for this sub-metric in September 2001. The
10	small universe for this sub-metric does not provide a statistically conclusive
11	comparison to the retail analogue. BellSouth met the retail analogue for this
12	sub-metric in August and October 2001.
12 13	sub-metric in August and October 2001.
	sub-metric in August and October 2001. Maintenance Average Duration / ISDN / Non-Dispatch (A.3.3.6.2) (October)
13	
13 14	Maintenance Average Duration / ISDN / Non-Dispatch (A.3.3.6.2) (October)
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13 14 15 16	Maintenance Average Duration / ISDN / Non-Dispatch (A.3.3.6.2) (October) There were only nine orders for this sub-metric in October 2001. The small universe for this sub-metric does not provide a statistically conclusive
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23 universe of orders does not provide a statistically conclusive comparison to

1 the retail analogue. In September 2001, there were 12 repeat trouble reports, 2 10 of which were by the same customer for the same trouble. Nine of the 3 repeat reports were closed as "No trouble found." In October 2001, there 4 were 13 repeat reports for this sub-metric. Of the 13 October repeats, 5 were 5 from one customer due to facilities problems, 5 were from another customer 6 due to service wire problems, 2 were closed as "no trouble found," and 1 was 7 from an unrelated incident. There were only three actual different trouble 8 situations for the month.

9

10 <u>Out of Service > 24 Hours / Design (Specials) / Non-Dispatch (A.3.5.3.2)</u> 11 (September)

12 Of the 22 trouble reports for this sub-metric in September 2001, 6 of the 13 troubles caused out of service conditions longer than 24 hours. These 6 14 situations did not reveal any systemic maintenance issues. BellSouth met the 15 retail analogue for this sub-metric in August and October 2001.

16

17 Out of Service > 24 Hours / PBX / Dispatch (A.3.5.4.1) (October)

Of the 28 "out of service" reports for this sub-metric in October, 11 of the reports were out of service longer than 24 hours. Of these 11 reports, 5 were for one customer received late on a Friday afternoon, committed and completed before noon of Monday. The remaining 6 reports out of service longer than 24 hours were due to wet cable facilities that had to be repaired

1	by a cable technician. BellSouth met the retail analogue comparison for this
2	sub-metric in August and September 2001.
3	
4	Out of Service > 24 Hours / PBX / Non-Dispatch (A.3.5.4.2) (August)
5	There were only three trouble reports for this sub-metric in August 2001. The
6	small universe for this sub-metric does not provide a statistically conclusive
7	comparison to the retail analogue. BellSouth met the retail analogue for this
8	sub-metric in September and October 2001.
9	
10	Out of Service > 24 Hours / Centrex / Dispatch (A.3.5.5.1) (October)
11	There were only six orders for this sub-metric in October 2001. The small
12	universe for this sub-metric does not provide a statistically conclusive
13	comparison to the retail analogue. BellSouth met the retail analogue for this
14	sub-metric in September and October 2001.
15	
16	Out of Service > 24 Hours / Centrex / Non-Dispatch (A.3.5.5.2) (August)
17	There were only three orders for this sub-metric in August 2001. The small
18	universe for this sub-metric does not provide a statistically conclusive
19	comparison to the retail analogue. BellSouth met the retail analogue for this
20	sub-metric in September and October 2001.
21	

22 Out of Service > 24 Hours / ISDN / Non-Dispatch (A.3.5.6.2) (October)

There were only nine orders for this sub-metric in October 2001. The small
universe for this sub-metric does not provide a statistically conclusive
comparison to the retail analogue. BellSouth met the retail analogue for this
sub-metric in August and September 2001. **Resale – Billing**Invoice Accuracy / Resale (A.4.1) (September)

The CLECs experienced Resale invoice rates that were slightly less than the invoices BellSouth sends to its retail customers during September 2001 (98.61% accuracy for BellSouth versus 97.84% for the CLEC invoices). The difference in performance was the result of provisioning and system errors that caused the over billing of one CLEC customer. BellSouth met the retail analogue for this sub-metric in August and October 2001.

14

15

II. Summary

16

As stated in the Introduction to the Analysis of Performance Measurements section, BellSouth met or exceeded the criteria for 622 of the 750 sub-metrics (83%) for which there was CLEC activity in August, for 687 of 816 sub-metrics (84%) in September and for 733 of 901 sub-metrics (81%) in October 2001.

21

22 During the three-month period of August through October 2001, there were a 23 total of 680 sub-metrics that had CLEC activity for all three months and that

1	were compared with either a benchmark or retail analogue. Of those 680
2	sub-metrics, 595 or 88% satisfied the comparison criteria for a minimum of
3	two of the three months.

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Equity

BellSouth Monthly State Summary

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0-8 Instances/FL(%) >= 85% win 24 hrs 99.20% 624 0-8 Design (Specials)/FL(%) >= 85% win 24 hrs 94.92% 59 0-8 PBX/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 Centrav/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 Centrav/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 Centrav/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 Centrav/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 K5DN/FL(%) >= 85% win 24 hrs 90.48% 42 FOC Timelinese - Mechanized 0-9 Residenca/FL(%) >= 95% win 3 hrs 99.46% 52.497 0-9 Design (Specials)/FL(%) >= 95% win 3 hrs 98.77% 2.355 0-9 Design (Specials)/FL(%) >= 95% win 3 hrs 99.46% 52.497 0-9 Centrav/FL(%) >= 95% win 3 hrs 99.77% 2.355 0-9 Centrav/FL(%) >= 95% win 3 hrs 90.46% 90.46%	
0-8 Design (Specials)/FL(%) 0-8 PBX/FL(%) 0-8 Centrew/FL(%) 0-8 Centrew/FL(%) 0-8 Centrew/FL(%) 0-9 Centrew/FL(%) 0-9 Design (Specials)/FL(%) 0-9 Design (Specials)/FL(%) 0-9 Centrew/FL(%) 0-9 Centrew/FL(%) 0-9 Centrew/FL(%) 0-9 Design (Specials)/FL(%) 0-9 Centrew/FL(%) 0-9 Centrew/FL(%) <td>,</td>	,
0-8 Centrav/FL(%) >= 85% win 24 hrs 90.48% 42 0-8 Centrav/FL(%) >= 85% win 24 hrs 100.00% 6 0-8 ISDN/FL(%) >= 85% win 24 hrs 90.48% 42 FOC Timeliness - Mechanized 0-9 Residence/FL(%) >= 95% win 3 hrs 99.46% 52.497 0-9 Design (Specials/FL(%) >= 95% win 3 hrs 99.77% 2,355 0-9 Design (Specials/FL(%) >= 95% win 3 hrs 99.77% 2,355 0-9 Design (Specials/FL(%) >= 95% win 3 hrs 91.11 0-9 Centrav/FL(%) >= 95% win 3 hrs 91.11 0-9 Centrav/FL(%) >= 95% win 3 hrs 91.11	
Ord FORTUL(%) O-8 Centrav/FL(%) O-8 iSDN/FL(%) O-8 iSDN/FL(%) FOC Timeliness - Mechanized O-9 Residence/FL(%) O-9 Business/FL(%) O-9 Design (Specials)/FL(%) O-9 PBX/FL(%) O-9 Design (Specials)/FL(%) O-9 PBX/FL(%) O-9 Design (Specials)/FL(%) O-9 Centrav/FL(%) O-9 PBX/FL(%)	······ 、
D-8 Control VFL(%) >= 85% win 24 hrs 95.24% 21 FOC Timeliness - Machanized >= 95% win 3 hrs 99.46% 52,497 D-9 Business/FL(%) >= 95% win 3 hrs 98.77% 2,355 D-9 Design (Specials)/FL(%) >= 95% win 3 hrs 98.77% 2,355 D-9 PBX/FL(%) >= 95% win 3 hrs 98.77% 2,355 D-9 PBX/FL(%) >= 95% win 3 hrs 98.77% 2,355 D-9 Controv/FL(%) >= 95% win 3 hrs 98.77% 2,355	,
FOC Timeliness - Mechanized >= 95% win 3 hrs 99 46% 52,497 O-9 Business/FL(%) >= 95% win 3 hrs 98,77% 2,355 O-9 Design (Specials)/FL(%) >= 95% win 3 hrs 98,77% 2,355 O-9 Design (Specials)/FL(%) >= 95% win 3 hrs 98,77% 2,355 O-9 Contrex/FL(%) >= 95% win 3 hrs 98,77% 2,355	
O-9 Residence/FL(%) >= 95% win 3 hrs 99 46% 52,497 O-9 Business/FL(%) >= 95% win 3 hrs 98,77% 2,355 O-9 Design (Specials)/FL(%) >= 95% win 3 hrs 98,77% 2,355 O-9 PBX/FL(%) >= 95% win 3 hrs	
O-9 Business/FL(%) >= 95% w in 3 hrs 98.77% 2,355 O-9 Design (Specials)/FL(%) >= 95% w in 3 hrs	
O-9 Design (Specials)/FL(%) >= 95% w in 3 hrs O-9 PBX/FL(%) >= 95% w in 3 hrs O-9 Centrex/FL(%) >= 95% w in 3 hrs	· · · · · · · · · · · · · · · · · · ·
O-9 Centrex/FL(%) >= 95% w in 3 hrs	
O-9 Contract/FL(%) >= 95% w in 3 hrs	
U-9 ISUNFL(%)	
FOC Timeliness - Partially Mechanized - 10 hours >= 85% w in 10 hrs 91 40% 13,604	
O-9 Residence/FL(%) >= 85% w in 10 hrs 91 40% 13,604 O-9 Business/FL(%) >= 85% w in 10 hrs 95 87% 1,839	

Benchmark /

Analog

BST

Measure

BST

Volume

CLEC

Measure

CLEC

Volume

Standard Standard

Error

ZScore

Deviation

BellSouth Monthly State Summary

	Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A.1.12 3	O-9 Design (Specials)/FL(%)	>≃ 85% w in 10 hrs			0.00%	1				NO
A 1 12 4	0-9 PBX/FL(%)	>= 85% w in 10 hrs >= 85% w in 10 hrs					-			
A 1.12.5 A.1.12.6	O-9 Centrex/FL(%) O-9 ISDN/FL(%)	>= 85% win 10 hrs			0 00%	2				NO
	FOC Timeliness - Non-Mechanized O-9 Residence/FL(%)	>= 85% w in 36 hrs			98 25%	570	-			YES
A 1.13 1 A.1 13 2	O-9 Business/FL(%)	>= 85% win 36 hrs			99 65%	569	-			YES
A.1 13 3	O-9 Design (Specials)/FL(%)	>= 85% w in 36 hrs			95.15%	103				YES
A.1.13.4	O-9 PBX/FL(%)	>= 85% w in 36 hrs	1		96.97%	33				YES
A 1.13.5	O-9 Centrex/FL(%)	>= 85% win 36 hrs			100 00%	5				YES
A.1.13.6	0-9 ISDN/FL(%)	>= 85% w in 36 hrs			93.94%	33				YES
	FOC & Reject Response Completeness - Mechanized									
A.1 14 1 1	O-11 Residence/EDI/FL(%)	>= 95%			100 00%	965	_			YES
A.1.14.1.2	O-11 Residence/TAG/FL(%)	>= 95%			99.20%	66,968	-			YES
A.1.14 2.1 A.1.14.2.2	O-11 Business/EDVFL(%) O-11 Business/TAG/FL(%)	>= 95% >= 95%			100.00% 96.02%	47 3,289				YES
A.1 14 3 1	O-11 Design (Specials)/EDI/FL(%)	>= 95%			30.02 /8	0,203				
A.1.14.3.2	O-11 Design (Specials)/TAG/FL(%)	>= 95%			0.00%	1				NO
A 1 14 4.1	O-11 PBX/EDI/FL(%)	>= 95%								
A.1.14.4.2	O-11 PBX/TAG/FL(%)	>= 95%								
A 1.14 5 1	O-11 Centrex/ED/FL(%)	>= 95%								
A 1.14.5.2 A.1 14 6.1	O-11 Centrex/TAG/FL(%) O-11 ISDN/EDI/FL(%)	>= 95% >= 95%				· · · ·				
A.1.14.6.2	0-11 ISDN/TAG/FL(%)	>= 95%			100.00%	2				YES
	FOC & Relect Response Completeness - Partially Mechanized									
A.1 15.1.1	O-11 Residence/EDVFL(%)	>= 95%			100 00%	506				YES
A 1.15.1.2	O-11 Residence/TAG/FL(%)	>= 95%			99 83%	17,963				YES
A.1 15.2 1	O-11 Business/EDI/FL(%)	>= 95%			100 00%	19				YES
A 1.15.2.2	O-11 Business/TAG/FL(%)	>= 95%			99.70%	2,636				YES
A 1 15.3 1	O-11 Design (Specials)/EDI/FL(%)	>= 95%				1				1000
A 1 15 3 2	0-11 Design (Specials)/TAG/FL(%) 0-11 PBX/ED/FL(%)	>= 95% >= 95%			100 00%	1				YES
A 1 15 4 1 A 1.15 4 2	0-11 (PBX/EDVFL(%) 0-11 PBX/TAG/FL(%)	>= 95%								
A 1.15 5 1	O-11 Centrex/EDI/FL(%)	>= 95%								
A.1 15.5.2	O-11 Centrex/TAG/FL(%)	>= 95%								
A.1 15 6.1	O-11 ISDN/EDI/FL(%)	>= 95%								
A.1 1562	O-11 ISDN/TAG/FL(%)	>= 95%			100.00%	2				YES
	FOC & Reject Response Completeness - Non-Mechanized									
A.1 16.1	O-11 Residence/FL(%)	>= 95%			94.73%	1,176				NO
A.1 16 2	O-11 Business/FL(%)	>= 95% >= 95%			94.35% 93 22%	1,238				NO
A.1 16 3 A.1 16 4	O-11 Design (Specials)/FL(%) O-11 PBX/FL(%)	>= 95%			94.05%	84				NO .
A.1 16 5	0-11 Centrex/FL(%)	>= 95%			78 57%	14				NO
A 1 16 6	0-11 ISDN/FL(%)	>= 95%			100.00%	52				YES
	FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
A 1.17 1 1	O-11 Residence/ED/FL(%)	>= 95%			79.69%	965				NO
A.1.17.12	O-11 Residence/TAG/FL(%)	>= 95%			99.28%	66,431				YES
A 1 17 2.1	O-11 Business/EDVFL(%)	>= 95%			48.94%	47				NO
A 1 17.2 2	O-11 Business/TAG/FL(%)	>= 95%			98 13%	3,158				YES
A 1.17.3 1	O-11 Design (Specials)/ED/FL(%)	>= 95% >= 95%			0.00%	0				NO
A.1 17.3 2 A.1 17.4 1	0-11 Design (Specials)/TAG/FL(%) 0-11 PBX/EDI/FL(%)	>= 95%			0.00%	<u> </u>				
A.1 17.4 1 A 1 17.4 2	0-11 PBX/EDVFL(%)	>= 95%								├ ─── ─
A 1 17.5 1	O-11 Centrex/EDVFL(%)	>= 95%								
A.1 17 5 2	O-11 Centrex/TAG/FL(%)	>= 95%								
A 1 17 6 1	Q-11 ISDN/EDI/FL(%)	>= 95%								
A 1.17.62	O-11 ISDN/TAG/FL(%)	>= 95%			100 00%	2				YES

YES

YES YES YES YES

BallSouth Monthly State Summary

	BellSouth Monthly State Summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		•								• •
	FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized									
A 1 18.1 1	O-11 Residence/EDI/FL(%)	>= 95%			97.23%	506				YES
A.1.18.1.2	O-11 Residence/TAG/FL(%)	>= 95%			92.17%	17,932				NO
A.1.18.2.1	O-11 Business/EDI/FL(%)	>= 95%			89.47%	19				NO
A 1.18.2.2	O-11 Business/TAG/FL(%)	>= 95%			89.61%	2,628				NO
A.1.18 3.1	O-11 Design (Specials)/EDI/FL(%)) >= 95%								
A 1 18 3.2	O-11 Design (Specials)/TAG/FL(%)	>= 95%			100 00%	1				YES
A 1 18 4.1	O-11 PBX/EDI/FL(%)	>= 95%								
A 1.18.4.2	O-11 PBX/TAG/FL(%)	>= 95%								
A 1 18 5.1	O-11 Centrex/EDI/FL(%)	>= 95%								
A.1.18.5.2	O-11 Centrex/TAG/FL(%)	>= 95%								
A 1.18 6.1	O-11 ISDN/EDV/FL(%)	>= 95%					-			
A.1 1862	O-11 ISDN/TAG/FL(%)	>= 95%			50.00%	2				NÔ
	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized									
A 1.19.1	O-11 Residence/FL(%)	>= 95%			89 86%	1,114				NO
A 1.19.2	O-11 Business/FL(%)	>= 95%			91 27%	1,168				NO
A.1 19 3	O-11 Design (Specials)/FL(%)	>= 95%			96 97%	165				YES
A 1 19 4	Q-11 (PBX/FL(%)	>= 95%			98.73%	7 9				YES
A 1.19.5	O-11 Centrex/FL(%)	>= 95%			90.91%	11				NO
A 1.196	0-11 [ISDN/FL(%)	>= 95%			98 08%	52				YES
		· · · · · · · · · · · · · · · · · · ·								
	Resale - Provisioning									
		······································								
	Order Completion Interval P-4 Residence/<10 circuits/Dispatch/FL(days)	1 Dec	4 39	51,456	3.18	3.244	4.623	0 08369	14 5522	YES
A.2 1.1.1 1 A 2.1.1.1.2	P-4 Residence/<10 circuits/Dispatch/FL(days) P-4 Residence/<10 circuits/Non-Dispatch/FL(days)	Res Res	0.90	687,446	0.53	52,124	2 051	0 00932	39 3600	YES
A 2 1.1.2 1	P-4 Residence/>=10 circuits/f0irplispatch/FL(days)	Res	4 73	95	4 17	6	3.775	1 58889	0 3543	YES
A 2.1.1.2.1	P-4 Residence/>=10 circuits/Non-Dispatch/FL(days)	Res	473				3.775	1 30003	0.3543	160
A 2.1 2.1 1	P-4 Business/<10 circuits/Dispatch/FL(days)	Bus	3 00	39,358	2 73	433	7 132	0 34461	0 7726	YES
A 2.1 2.1.2	P-4 Business/<10 circuits/Non-Dispatch/FL(days)	Bus	1 45	48,849	0.78	2,816	3 707	0 07184	9 2624	YES
A 2 1.2.2.1	P-4 Business/>=10 circuits/Dispatch/FL(days)	Bus	10 21	278	6 46	8	13.051	4 68029	0 8022	YES
A.2.1 2.2.2	P-4 Business/>=10 circuits/Non-Dispatch/FL(days)	Bus	7 62	7	-		7 537	100020	0.0011	
A 2.1.3.1.1	P-4 Design (Specials)/<10 circuits/Dispatch/FL(days)	Design	28.00	2.016	2 50	2	32 603	23 06488	1 1057	YES
A.2.1 3.1.2	P-4 Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Design	27.24	80	5.40	5	20 524	9 46096	2 3082	YES
A.2.1 3.2.1	P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Design	33.00	1			0.000			
A 2 1 3.2.2	P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Design			1					[1
A.2.1.4.1.1	P-4 PBX/<10 circuits/Dispatch/FL(days)	РВХ	19 46	92	8 50	2	36 450	26 05233	0 4207	YES
A.21412	P-4 PBX/<10 circuits/Non-Dispatch/FL(days)	PBX	4 61	207	2 79	21	8 969	2 05415	0 8865	YES
A.2 1.4.2.1	P-4 PBX/>=10 circuits/Dispatch/FL(days)	РВХ	13 86	7	2 00	1	10 367	11 08290	1 0699	YES
A.2 1 4.2.2	P-4 PBX/>=10 circuits/Non-Dispatch/FL(days)	PBX	2.82	26	3 47	5	3.832	1 87112	-0.3455	YES
A.2 1.5.1.1	P-4 Centrex/<10 circuits/Dispatch/FL(days)	Centrex	6 79	684			9 907			
A 2 1.5 1 2	P-4 Centrex/<10 circuits/Non-Dispatch/FL(days)	Centrex	1 26	1,066	2.96	16	4.438	1 11780	1.5136	YES
A.2 1.5 2 1	P-4 Centrex/>=10 circuits/Dispatch/FL(days)	Centrex	11 25	44			12.404			
A21522	P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days)	Centrex	2 20	60	500	1	4 278	4 31340	-0 6493	YES
A.2.1.6.1.1	P-4 ISDN/<10 circuits/Dispatch/FL(days)	ISDN	51.79	982	4 19	7	56 528	21 44 163	2 2198	YES
A 2.1.6 1 2	P-4 ISDN/<10 circuits/Non-Dispatch/FL(days)	ISDN	2 35	1,564	2 26	20	6.289	1 41514	0 0629	YES
A 2 1.6.2.1	P-4 ISDN/>=10 circuits/Dispatch/FL(days)	ISDN	1 67	4			1 744			
A21622	P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days)	ISDN	3 54	50		L	5 241			L
	Heid Orders									
A 2 2 1.1 1	P-1 Residence/<10 circuits/Facility/FL(days)	Res	9 26	414	6 81	21	13 519	3 02389	0 8091	YES
A 2.2.1.1 2	P-1 Residence/<10 circuits/Equipment/FL(days)	Res	3 00	1	0.00	0	0 000			YES
A 2.2.1.1 3	P-1 Residence/<10 circuits/Other/FL(days)	Res	17.83	41	0.00	0	21 644			YES
A.2.2.1.2.1	P-1 Residence/>=10 circuits/Facility/FL(days)	Res	0 00	0	0 00	0				YES
A.2 2 1.2.2	P-1 Residence/>=10 circuits/Equipment/FL(days)	Res	0 00	0	0 00	0				YES
400100	P.1 Pasidanca/18 circuits/Other/El (days)	Ree	0.00	0	0.00	0				VES

	Heid (Orders
A 2 2 1.1 1	P-1	Residence/<10 circuits/Facility/FL(days)
A 2.2.1.1 2	P-1	Residence/<10 circuits/Equipment/FL(days)
A 2.2.1.1 3	P-1	Residence/<10 circuits/Other/FL(days)
A.2.2.1.2.1	P-1	Residence/>=10 circuits/Facility/FL(days)
A.2 2 1.2.2	P-1	Residence/>=10 circuits/Equipment/FL(days)
A.2 2.1.2 3	P-1	Residence/>=10 circuits/Other/FL(days)
A.22211	P-1	Business/<10 circuits/Facility/FL(days)
A22212	P-1	Business/<10 circuits/Equipment/FL(days)
A 2 2 2 1.3	P-1	Business/<10 circuits/Other/FL(days)
A 2.2 2.2.1	P-1	Business/>=10 circuits/Facility/FL(days)

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- r .	lorida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equi
									230018	•
P-		Bus	0.00	0	0.00	0				YES
Ρ.		Bus	9.00	1	0.00	0	0 000			YES
P-		Design	0.00	0	0.00	0				YE
P.		Design	0.00	0	0.00	0				YE
P.	1 Design (Specials)/<10 circuits/Other/FL(days)	Design	5.80	5	0.00	0	5 020			YE
Ρ.	1 Design (Specials)/>=10 circuits/Facility/Ft.(days)	Design	0.00	0	l		· · · ·		· · · · · ·	.
P.		Design	0.00	0						_
Ρ-		Design PBX	0.00	0	0.00	0	· · · · · · · · · · · · · · · · · · ·			
P.		РВХ	0.00	0	0.00	0				
P.			55.00	1	0.00	0	0 000			
F.	1 PBX/sto circuits/Onei/FL(days) 1 PBX/s=10 circuits/Facility/FL(days)	PBX	10.00	1	0.00	0	0 000			
P- P-		PBX	0.00	0	0.00	0				Η Y
Р- Р-		PBX	0.00	ő	0.00	ŏ				
F		Centrex	8 75	8	0.00	ō	7.536		·	Η Y
F		Centrex	0.00	0	0.00	0	7.550			t vi
Þ.		Centrex	0.00	0	0.00	0				Y
늡	1 Centrex/>=10 circuits/Facility/FL(days)	Centrex	0.00	0	0.00	ŏ	1			† - '
5		Centrex	0.00	ō	0.00	Ö	1			Ý
P-		Centrex	0.00	0	0.00	0				Ý
P.		ISDN	10.00	3	0 00	0	4 000			Ý
p.		ISDN	0.00	0	0.00	0				Y
Þ.		ISDN	9 00	1	0.00	0	0 000			Y
P-	1 ISDN/>=10 circuits/Facility/FL(days)	ISDN	0.00	0						
P-		ISDN	0.00	0						
Ρ-	1 ISDN/>=10 circuits/Other/FL(days)	ISDN	2 00	2			0 000			
%	Jeopardies - Mechanized									
P-		Res	0.39%	810,810	0.30%	48,344		0 00029	3 0369	Y
P.	2 Business/FL(%)	Bus	1 06%	90,760	0 57%	2,444		0 00210	2 3124	Ý
P.	2 Design (Specials)/FL(%)	Design	7 21%	2,733	0 00%	5		0 11577	0 6227	Y
P-	2 PBX/FL(%)	PBX	3 94%	355	0.00%	16		0.04974	0 7928	Y
P		Centrex	4 83%	1,945	0 00%	17	_	0 05224	0.9251	Y
Ρ.	2 [ISDN/FL(%)	ISDN	5.03%	2,943	0 00%	18	•	0 05167	0 9 733	Υ
%	Jeoperdies - Non-Mechanized									
P.		Diagnostic			1 47%	340				Diag
Ρ.		Diagnostic			0.65%	309				Diag
P		Diagnostic			0.00%	11				Diag
P.		Diagnostic			0.00%	23				Diag
P.		Diagnostic			0.00%	5 25				Diag
P-	2 ISDN/FL(%)	Diagnostic			0.00%	25				Diag
	verage Jeopardy Notice Interval - Mechanized									
Ρ.		>= 48 hrs			135.50	144				Y
Ρ.		>= 48 hrs			126.86	14	~			Y
P		>= 48 hrs								
P		>= 48 hrs					-			
P	2 Centrex/FL(hours)	>= 48 hrs >= 48 hrs					-			
		>= 40 113								
	verage Jeopardy Notice Interval - Non-Mechanized						-			
	2 Residence/FL(hours)	Diagnostic			168 00	5				Diag
	2 Business/FL(hours)	Diagnostic			84 00	2				Diag
	2 Design (Specials)/FL(hours)	Diagnostic								Diag
P.		Diagnostic								Diag Diag
	2 Centrex/FL(hours)	Diagnostic								Diag
P	2 (SDN/FL(hours)	Diagnostic								LUB
	Jeopardy Notice >= 48 hours - Mechanized			_			_		_	
					100 00%	144				Υ
P	2 Residence/FL(%)	95% >= 48 hrs								
P P		95% >≃ 48 hrs 95% >= 48 hrs 95% >= 48 hrs			100.00%	14				YE

BellSouth Monthly State Summary Elorida October 2001

Florids, Colober 2001 Benchmerk / Marcel National Volume Bit CLE Color Standard Banched Bunched Bunch			South Monthly State Summary							- · · ·		
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A 2 12:11 P-9 Resdemox/-10 circuits/Dispatch/FL(%) Res 7.78% 53,786 7.28% 2.398 0.00559 9.934 YES A 2 12:12 P-9 Resdemox/-10 circuits/Non-Dispatch/FL(%) Res 3.69% 600.500 5.08% 3.349 0.0017 10.4719 NO A 2:12:12 P-9 Resdemox/-10 circuits/Non-Dispatch/FL(%) Res 3.69% 600.500 5.08% 3.249 0.01637 0.0117 NO A 2:12:21 P-9 Residenca/-10 circuits/Non-Dispatch/FL(%) Res -<		% Prov	isloning Troubles within 30 Devs									
A 212.112 P-9 Residence/~10 circuits/Non-Dispatch/FL(%) Res 3.96% 600,500 5.08% 35,349 0.00107 -10.4719 NO A 212.212 P-9 Residence/~10 circuits/Dispatch/FL(%) Res 8.0% 87 0.00% 3 0.14880 0.4435 YES A 212.212 P-9 Business/-10 circuits/Dispatch/FL(%) Res 3.2% 3.3,249 8.64% 4.86 0.00455 YES A 212.212 P-9 Business/-10 circuits/Dispatch/FL(%) Bus 3.2% 3.3,249 8.64% 4.86 0.00455 YES A 212.212 P-9 Business/-10 circuits/Non-Dispatch/FL(%) Bus 3.2% 3.249 8.64% 4.86 0.00455 YES A 212.21 P-9 Business/-10 circuits/Non-Dispatch/FL(%) Bus 10.40% 327 0.00% 6 0.12576 0.8269 YES A 212.311 P-9 Design (Specials/L/10 circuits/Non-Dispatch/FL(%) Design 3.84% 1.642 0.00% 3 0.15822 0.4862 YES A 212.312 P-9 Design (Specials/L/10 circuits/Non-Dispatch/FL	A 2 12 1 1 1			Res	7 78%	53,786	7 26%	2,398		0.00559	0 9354	YES
A 2 12.12.2 P-9 Residence/>=10 circuits/Non-Dispatch/FL(%) Res 0 A 2 12 2.11 P-9 Business/-10 circuits/Dispatch/FL(%) Bus 3.22% 33,249 8.64% 4.86 0.00607 6.712 NC A 2 12 2.11 P-9 Business/-10 circuits/Dispatch/FL(%) Bus 4.95% 42,374 5.52% 2.410 0.00607 6.71219 YES A 2 12 2.12 P-9 Business/-10 circuits/Dispatch/FL(%) Bus 10.40% 327 0.00% 6 0.12675 0.8269 YES A 2 12 3.12 P-9 Design (Specials)/<10 circuits/Dispatch/FL(%)				Res	3.96%		5.08%	35,349		0 00107	-10 4719	
A 2 12 2 1.1 P-9 Business/<10 circuits/Dispatch/FL(%)	A.2 12 1 2 1	P-9	Residence/>=10 circuits/Dispatch/FL(%)	Res	6 90%	87	0 00%	3		0.14880	0 4635	YES
A 2 12 2 12 P-3 Business/<10 circuits/Non-Dispatch/FL(%)	A.2 12.1 2.2	P-9										
A.2.12.2.21 P:9 Business/>=10 crcults/Dispatch/FL(%) Bus 10.40% 327 0.00% 6 0.12676 0.8269 YES A.2.12.2.2 P.9 Business/>=10 crcults/Non-Dispatch/FL(%) Bus 0.00% 8									-			
A 2 12 2 2 2 P-9 Business/>Business/>E10 circuits/Non-Dispatch/FL(%) Bus 0 00% 0 0 0 01686 2 2761 YES A 2 12 3 1 2 P-9 Design (Specials)/<10 circuits/Dispatch/FL(%)												
A 2 12 3.11 P-9 Design (Specials)/>Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) A 2 12 3 1.2 P-9 Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) Design (Specials)/>E10 circuits/Non-Dispatch/FL(%) Design (Specials)/=10 circuits/Non-Dispatch/FL(%)							0 00%	6		0 12575	0 8269	YES
A 2 12.3 1.2 P.9 Design (Specials)/>Equals)/>E10 circuits/Dispatch/FL(%) Design (Specials)/>E10 circuits/Dispatch/FL(%) Design (Specials)/=10 circuits/Dispatch/FL(%) <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>141</td><td></td><td>0.01690</td><td>0.0761</td><td>VEC</td></t<>								141		0.01690	0.0761	VEC
A 2 12.3 2.1 P-9 Design (Specials/)>=10 circults/Dispatch/FL(%) A.2 12.3 2.1 P-9 Design (Specials/)>=10 circults/Non-Dispatch/FL(%) Design A.2 12 3.2.2 P-9 Design (Specials/)>=10 circults/Non-Dispatch/FL(%) Design 0.00% 4 A.2 12 3.2.1 P-9 Design (Specials/)>=10 circults/Non-Dispatch/FL(%) Design 0.00% 1 0.05127 0.5066 YES A.2 12 4.1 2 P-9 PBX/<10 circults/Non-Dispatch/FL(%)												
A.2.12 3.2.2 P-9 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%) Design (Specials)/>=10 circuits/Non-Dispatch/FL(%) Pesign Pesign <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.00%</td> <td>3</td> <td></td> <td>0 10022</td> <td>0 4002</td> <td>160</td>							0.00%	3		0 10022	0 4002	160
A.2.12.4.1.1 P-9 PBX/<10 circuits/Dispatch/FL(%)					000/0		<u> </u>					<u>+</u> 1
A.2 12 4.12 P.9 PBX/<10 circuits/Non-Dispatch/FL(%) PBX 2.05% 293 3 13% 32 0.02637 .0 4085 YES A 2 12 4.2 1 P.9 PBX/>=10 circuits/Dispatch/FL(%) PBX 0.00% 2					2 60%	77	0.00%	11	·	0.05127	0.5066	VES
A 2 12 4.2 1 P-9 PBX/>PBX/>=10 circuits/Dispatch/FL(%) PBX 0.00% 2 0.06755 -1.4018 YES A 2 12 4 2 2 P-9 PBX/>=10 circuits/Non-Dispatch/FL(%) PBX 3.03% 3.3 12.50% 8 0.06755 -1.4018 YES A 2 12 5 1 1 P-9 Centrex/<10 circuits/Dispatch/FL(%)												
A 2 12 4 2 2 P-9 PBX/s=10 circuits/Non-Dispatch/FL(%) PBX 3 03% 33 12 50% 8 0 06755 -1 4018 YES A 2 12 5 1 1 P-9 Centrex/<10 circuits/Dispatch/FL(%)							1					t1
A 2 12 5 11 P-9 Centrex/<10 circuits/Dispatch/FL(%) Centrex 1 55% 709 0 00% 1 0 12368 0 1254 YES A 2 12 5 12 P-9 Centrex/<10 circuits/Dispatch/FL(%)						33	12 50%	8		0 06755	-1 4018	YES
A 2.12.5 1 2 P-9 Centrex/<10 circuits/Non-Dispatch/FL(%) Centrex 0.92% 2.059 0.00% 23 0.02005 0.4603 YES A.2 12.5 2 1 P-9 Centrex/>=10 circuits/Dispatch/FL(%) Centrex 3.17% 63						709	0.00%				0 1254	YES
A.2 12.5 2 1 P-9 Centrex/>centrex/>centrex/>centrex/>centrex/>centrex/>centrex/>centrex/>centrex/>centrex/>centrex/ 3 17% 63 A.2 12 5 2.2 P-9 Centrex/>centrex/>centrex/>centrex/>centrex/ 0 08908 0 08908 0 08908 A 2 12 5 2.2 P-9 Centrex/>centrex/>centrex/>centrex/ 0 79% 126 0.00% 1 0 08908 0 04102 0 4949 YES A 2 12 6.1.2 P-9 ISDN/<10 circuits/Non-Dispatch/FL(%)				Centrex	0 92%	2,059	0.00%	23		0 02005	0 4603	YES
A 2.12.6.1.1 P-9 ISDN/s10 circuits/Non-Dispatch/FL(%) ISDN 2.03% 788 0.00% 12 0.04102 0.4949 YES A 2.12.6.1.2 P-9 ISDN/s10 circuits/Non-Dispatch/FL(%) ISDN 1.05% 948 0.00% 14 0.02751 0.3835 YES		P-9										
A 2.12.6.1.2 P-9 ISDN<10 circuits/Non-Dispatch/FL(%) ISDN 105% 948 0.00% 14 0.02751 0.3835 YES	A 2 12 5 2.2	P-9	Centrex/>=10 circuits/Non-Dispatch/FL(%)									
	A 2.12.6.1.1											
A 2.12 6 2 1 P-9 ISDN/>=10 circuits/Dispatch/FL(%) ISDN 0.00% 2	A 2.12.6.1.2						0.00%	14		0.02751	0 3835	YES
	A 2.12 6 2 1	P-9	ISDN/>=10 circuits/Dispatch/FL(%)	ISDN	0.00%	2	1					LI

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FION	ida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equi
P-9	ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN	0.00%	62	0 00%	2		0 00000		YE
Averag	ge Completion Notice Interval - Mechanized									
P-5	Residence/<10 circuits/Dispatch/FL(hours)	Res	4 06	43,273	096	2,741	20 001	0 39394	7 8842	YE
P-5	Residence/<10 circuits/Non-Dispatch/FL(hours)	Res	1.55	552,268	0.77	41,083	7.298	0 03732	20.7729	YE
P-5	Residence/>=10 circuits/Dispatch/FL(hours)	Res	4 66	83	0.51	2	19.872	14.21991	0.2919	YE
P-5	Residence/>=10 clrcuits/Non-Dispatch/FL(hours)	Hes								
P-5	Business/<10 circuits/Dispatch/FL(hours)	Bus	4.76	9,797	1 42	431	24.251	1 19355	2 7975	YE
P-5	Business/<10 circuits/Non-Dispatch/FL(hours)	Bus	1.96	35,790	0.75	2,142	14.731	0 32768	3 7158	YE
P-5	Business/>=10 circults/Dispatch/FL (hours)	Bus	8.34	190	0.43	7	26.203	10 08444	0 7850	YE
P-5	Business/>=10 circuits/Non-Dispatch/FL(hours)	Bus	0.12 244 45	8			0.161 661 524			
P-5	Design (Specials)/<10 circuits/Dispatch/FL(hours)	Design	105.78	1,251 23	0.02		257 184	262.71496	0.4026	YE
P-5	Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)	Design	186.93	1	0.02		0.000	202.7 1490	0.4020	116
P-5	Design (Specials)/>=10 circuits/Dispatch/FL(hours)	Design	100.93				0.000			
P-5	Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)	PBX	298 57	48	i		1326 956	r		
P-5 P-5	PBX/<10 circuits/Dispatch/FL(hours) PBX/<10 circuits/Non-Dispatch/FL(hours)	PBX	11.83	134	0.85	1	50.466	50 65355	0.2167	YE
P-5	PBX/>=10 circuits/Dispatch/FL(hours)	PBX	20.26	3	0.00		35 051	0000000	0.2107	<u> </u>
P-5	PBX/>=10 circuits/Dispatch/FL(routs)	PBX	0.57	22			0 267			1
P-5	Centrex/<10 circuits/Dispatch/FL(hours)	Centrex	6.10	523	1 1		27.389	1 1		t
P-5	Centrex/<10 circuits/Non-Dispatch/FL(hours)	Centrex	391	776			24 435			1
P-5	Centrex/>=10 circuits/Dispatch/FL(hours)	Centrex	1.04	30			3.125	1		
P-5	Centrex/>=10 circuits/Non-Dispatch/FL(hours)	Centrex	0.72	46			0 342	1		1
P-5	ISDN/<10 circuits/Dispatch/FL(hours)	ISDN	1212 62	657			1513 503	1		Ì
P-5	ISDN/<10 circuits/Non-Dispatch/FL(hours)	ISDN	7 62	866	0 48	1	47 923	47 95096	0 1489	Ì YE
P-5	ISDN/>=10 circuits/Dispatch/FL(hours)	ISDN	52 25	4			40 936			
P-5	ISDN/>=10 circuits/Non-Dispatch/FL(hours)	ISDN	20 65	44			79 626			1
Avere	ge Completion Notice Interval - Non-Mechanized									
P-5	Residence/<10 circuits/Dispatch/FL(hours)	Diagnostic			24 64	119				Diagn
P-5	Residence/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			19 25	222				Diagn
P-5	Residence/>=10 circuits/Dispatch/FL(hours)	Diagnostic					-			Diagn
P-5	Residence/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagn
P-5	Business/<10 circuits/Dispatch/FL(hours)	Diagnostic			21 21	54				Diagn
P-5	Business/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			19 69	256				Diagn
P-5	Business/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagn
P-5	Business/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagn
P-5	Design (Specials)/<10 circuits/Dispatch/FL(hours)	Diagnostic			35 17	5				Diagn
P-5	Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			50.71	4				Diagn
P-5	Design (Specials)/>=10 circults/Dispatch/FL(hours)	Diagnostic								Diagn
P-5	Design (Specials)/>=10 circults/Non-Dispatch/FL(hours)	Diagnostic								Diagn
P-5	PBX/<10 circuits/Dispatch/FL(hours)	Diagnostic			107 46	4	_			Diagn
P-5	PBX/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			27 49	14	-			Diagn
P-5	PBX/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagn
P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			13.30	5				Diagn
P-5	Centrex/<10 circuits/Dispatch/FL(hours)	Diagnostic					_			Diagn
P-5	Centrex/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			23.60	5	_			Diagn
P-5	Centrex/>=10 circuits/Dispatch/FL(hours)	Diagnostic					_			Diagn
P-5	Centrex/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			40.00	10				Diagn
P-5	ISDN/<10 circuits/Dispatch/FL(hours)	Diagnostic			42 80	10	-			Diagn
P-5	ISDN/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			17.88	15				Diagn
P-5	ISDN/>=10 circuits/Dispatch/FL(hours)	Diagnostic Diagnostic				· · · · · ·	-			Diagr Diagr
	ISDN/>=10 circuits/Non-Dispatch/FL(hours)	Diagnosuc								Ulagi
	Service Order Cycle Time - Mechanized	Diagnostic			3 76	2,269				Diagr
P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			0 64	37,463				Diagr
	Residence/<10 circuits/Non-Dispatch/FL(days) Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic			4 75	37,463				Diagn
P-10	UNASIGADCA/S-10 CICUITS/USDATCD/FL (GAVS)				4/0					Diagn
P-10		Organostic								
P-10 P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.24	228				
P-10		Diagnostic Diagnostic Diagnostic			3 24 0 99	228 1,171				Diagn

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BellSouth Monthly State Summary Electida October 2001

	Bensouth Monthly State Summary							
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard
	•	Analog	Measure	Volume	Measure	Volume	Deviation	Error
	Dealer Destant dame italian Disastatifi (dam)	Disamastia				·		
A 2.17 2.2 2	P-10 Business/>=10 circuits/Non-Dispatch/FL(days) P-10 Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic						
A 2.17 3.1.1		Diagnostic						
A 2.17.3.1 2	P-10 Design (Specials)/<10 clrcuits/Non-Dispatch/FL(days)							
A 2 17 3.2.1	P-10 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A.2 17.3.2.2	P-10 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A.2.17.4 1 1	P-10 PBX/<10 circuits/Dispatch/FL(days)	Diagnostic						
A.2.17.4.1.2	P-10 PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A.2.17.4.2 1	P-10 PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A.2 17 4.2.2	P-10 PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A.2.17 5.1.1	P-10 Centrex/<10 circuits/Dispetch/FL(days)	Diagnostic						
A.2 17 5.1 2	P-10 Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				·		
A 2.17 5.2.1	P-10 Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 17 5 2 2	P-10 Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2 17 6.1 1	P-10 ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic						
A 2.17.6.1.2	P-10 ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A.2.17621	P-10 ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 17.6.2.2	P-10 ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic		A 400000 (000 (000 (000 (000))))		L		
	Total Service Order Cycle Time - Partially Mechanized	-						
A.2 18 1 1.1	P-10 Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			3 03	550		
A.2.18.1.1.2	P-10 Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.62	9,935		
A 2 18.1 2.1	P-10 Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A.2.18 1.2.2	P-10 Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2.18 2.1.1	IP-10 Business/<10 circuits/Dispatch/FL(days)	Diagnostic			3.64	125		
A.2.18 2.1.2	P-10 Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1 54	932		
A.2.18 2 2 1	P-10 Business/>=10 circuits/Dispatch/FL(days)	Diagnostic			14.11	3		
A.2.18.2.2.2	P-10 Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2 18.3 1.1	P-10 Design (Specials)/<10 circults/Dispatch/FL(days)	Diagnostic						
A.2 18.3 1.2	P-10 Design (Specials)/<10 clrcults/Non-Dispatch/FL(days)	Diagnostic			9.00	1		
A 2 18.3.2.1	P-10 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 18.3.2.2	P-10 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2.18 4.1 1	P-10 PBX/<10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 18 4 1 2	P-10 PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.00	. 1		
A.2.18.4 2 1	P-10 PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A.2 18.4.2.2	P-10 PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2.18 5 1 1	P-10 Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic				<u> </u>		
A 2.18 5.1.2	P-10 Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				L		
A.2.18.5.2.1	P-10 Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 18.5 2 2	P-10 Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				L		
A.2 18.6.1 1	P-10 ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 18 6 1 2	P-10 ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.00	1		
A.2.18 6.2.1	P-10 ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic			-			
A 2.18.6.2.2	P-10 ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
	Total Service Order Cycle Time - Non-Mechanized	-						
A.2.19.1 1 1	P-10 Residence/<10 circuits/Dispatch/FL(days)	Diagnostic	-		4 61	89		
A.2 19 1 1.2	P-10 Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.92	205		
A 2 19 1 2.1	P-10 Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2.19 1 2 2	P-10 Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2 19 2.1 1	P-10 Business/<10 circuits/Dispatch/FL(days)	Diagnostic			5.50	30		
A.2.19.2.1 2	P-10 Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.60	215		
A 2 19.2 2.1	P-10 Business/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2.19 2 2.2	P-10 Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A 2 19 3.1.1	P-10 Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic			6 00	2		
A 2 19.3.1.2	P-10 Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			9 67	3		
A.2 19 3 2 1	P-10 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 19 3 2 2	P-10 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic						
A.2.19.4 1 1	P-10 PBX/<10 circuits/Dispatch/FL(days)	Diagnostic						
A 2 19 4 1 2	P-10 PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			5.22	9		
A 2 19.4 2 1	P-10 PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic						
A.2 19.4.2 2	P-10 PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			3 50	2		

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	1 1011		Analog
			Animog
A 2 19 5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic
A.2 19512	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2.19 5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 19 5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2.19 6.1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2.19 6.1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2.19 6.2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 19 6 2 2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
	Totel S	Service Order Cycle Time (offered) - Mechanized	
A 2.21 1.1.1	P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic
A221112	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 1 2 1	P-10	Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 1 2 2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A.2 21 2 1 1	P-10	Business/<10 circuits/Dispatch/FL(days)	Diagnostic
A.2.21 2 1 2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A.2 21 2.2.1	P-10	Business/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 2 2 2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 3 2 1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 3 2 2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 4 1 1	P-10 P-10	PBX/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 4 1.2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days) PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic
A 2 21 4 2.1 A 2 21 4 2 2	P-10	PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21.5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 5 2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 6 1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 6 1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 21 6.2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 21 6.2 2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
	Total S	Service Order Cycle Time (offered) - Partially Mechanized	
A 2 22 1 1 1	P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 1 1 2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 1 2 1	P-10	Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22.1 2 2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22.2 1 1	P-10	Business/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 2 1 2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 2.2 1	P-10	Business/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 2 2 2 2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 3 2 1	P-10 P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic
A 2 22 3 2 2 A 2 22 4 1.1	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days) PBX/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 4 1.1 A 2 22 4 1 2	P-10	PBX/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 4 2 1	P-10	PBX/>=10 circuits/f0ispatch/FL(days)	Diagnostic
A 2 22 4 2 2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 5 1 2	P-10	Centres/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 5 2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 6 1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 6 1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic
A 2 22 6 2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic
A 2 22 6 2 2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic

BST BST CLEC CLEC Standard Standard Measure Volume Measure Volume Deviation Error ZScore Equity Diagnostic 8 00 4 Diagnostic Diagnostic Diagnostic Diagnostic 8.00 4 7.90 10 Diagnostic Diagnostic Diagnostic 3.68 2,070 Diagnostic 0.68 28,157 Diagnostic 5.33 Diagnostic Diagnostic 3 3.22 227 Diagnostic 1.02 1,084 Diagnostic 3.44 з Diagnostic Dlagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic 2.91 514 1.55 8,435 Diagnostic Diagnostic Diagnostic Diagnostic 3.66 120 1.56 809

Diagnostic Diagnostic 21.00 2 Diagnostic 9 00 1 Diagnostic Diagnostic Diagnostic Diagnostic 3 00 1 Diagnostic Diagnostic

Total Service Order Cycle Time (offered) - Non-Mechanized

Benchmark/

BellSouth Monthly State Summary Florida, October 2001

	FION		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
						moreare	v onanio	Demanon	2.00	2000.0	Equity
A 2.23 1.1 1	P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			4 84	76				Diagnostic
A 2.23 1.1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.02	165				Diagnostic
A.2.23.1.2 1	P-10	Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23.1 2 2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 23.2.1 1	P-10	Business/<10 circuits/Dispatch/FL(days)	Diagnostic			4.67	27				Diagnostic
A.2.23.2.1 2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.55	180				Diagnostic
A 2 23.2 2.1	P-10	Business/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23 2 2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2.23 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2.23 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			9.67	3				Diagnostic
A.2.23.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23.3 2.2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23.4 1 1	P-10	PBX/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23.4 1.2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.00	5				Diagnostic
A.2 23.4 2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23 4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.00	1				Diagnostic
A.2.23 5 1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic			10.00					Diagnostic
A 2.23 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			12.00	1				Diagnostic
A 2.23 5 2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
A 2.23 5 2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2.23 6 1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic			6.33 9.00	3				Diagnostic
A 2.23612	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			9.00	/				Diagnostic
A.2 23.6 2.1 A.2.23.6.2.2	P-10 P-10	ISDN/>=10 circuits/Dispatch/FL(days) ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic Diagnostic
A.2.23.0.2.2			Diagnosic			L	·	-		- · · ·	Diagnostic
		pletions w/o Notice or < 24 hours				100.000		-			.
A 2.24 1 1	P-6	Residence/Dispatch/FL(%)	Diagnostic			100.00%	3,250	- 11 A			Diagnostic
A 2.24 1 2	P-6	Residence/Non-Dispatch/FL(%)	Diagnostic			100.00%	52,124 441	· · · ·			Diagnostic
A 2.24 2.1	P-6	Business/Dispatch/FL(%)	Diagnostic			100.00%	2,816	-			Diagnostic
A 2 24 2.2	P-6 P-6	Business/Non-Dispatch/FL(%)	Diagnostic Diagnostic			100.00%	2,810				Diagnostic Diagnostic
A 2 24.3.1	P-6	Design (Specials)/Dispatch/FL(%)	Diagnostic			100.00%					Diagnostic
A.2 24 3.2	P-6	Design (Specials)/Non-Dispatch/FL(%) PBX/Dispatch/FL(%)	Diagnostic			100.00%	3	-			Diagnostic
A.2 24.4 1 A.2 24 4 2	P-6	PBX/Non-Dispatch/FL(%)	Diagnostic			100.00%	26				Diagnostic
A 2 24 5.1	P-6	Centrex/Dispatch/FL(%)	Diagnostic			100.0070					Diagnostic
A 2 24 5 2	P-6	Centrex/Non-Dispatch/FL(%)	Diagnostic			100.00%	17				Diagnostic
A 2 24 6 1	P-6	ISDN/Dispatch/FL(%)	Diagnostic			100.00%	7				Diagnostic
A 2.24.6.2	P-6	ISDN/Non-Dispatch/FL(%)	Diagnostic			100.00%	20				Diagnostic
A 2 25 1.1 1	P-11	Porder Accuracy Residence/<10 circuits/Dispatch/FL(%)	>= 95%								
A 2 25 1.1 2	P-11	Residence/<10 circuits/Non-Dispatch/FL(%)	>= 95%			98.94%	94				YES
A.2 25.1 2 1	P-11	Residence/>=10 circuits/Dispatch/FL(%)	>= 95%			00.01.0		-			
A 2 25.1 2 2	P-11	Residence/>=10 circuits/Non-Dispatch/FL(%)	>= 95%								1
A.2.25.2.1.1	P-11	Business/<10 circuits/Dispatch/FL(%)	>= 95%			61 54%	13				NO
A 2.25 2 1.2	P-11	Business/<10 circuits/Non-Dispatch/FL(%)	>= 95%			88.28%	145				NO
A 2 25 2 2 1	P-11	Business/>=10 circuits/Dispatch/FL(%)	>= 95%								
A 2 25 2.2.2	P-11	Business/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	1				YES
A.2.25.3.1.1	P-11	Design (Specials)/<10 circuits/Dispatch/FL(%)	>= 95%			75.00%	4				NO
A 2 25 3 1.2	P-11	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	1				YES
A.2 25.3 2 1	P-11	Design (Specials)/>=10 circults/Dispatch/FL(%)	>= 95%								
A 2 25.3 2 2	P-11	Design (Specials)/>=10 circuits/Non-Dispetch/FL(%)	>= 95%								
	Resale	- Maintenance and Repair									
		Repair Appointments									
A3111		Residence/Dispatch/FL(%)	Res	12.63%	105,462	7 66%	4,306		0 00516	9 6193	YES
A3112		Residence/Non-Dispatch/FL(%)	Res	2 31%	55,635	1 64%	2,135		0 00331	2 0143	YES
A3121		Business/Dispatch/FL(%)	Bus	14 26%	21,017	12 04%	1,038		0 01112	1 9945	YES
A3122		Business/Dispatch/FL(%)	Bus	2 58%	11,008	1.16%	432		0 00778	1 8294	YES
A3131		Design (Specials)/Dispatch/FL(%)	Design	4.51%	1,640	0 00%	22		0 04455	1 0128	YES

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Standard Standard

BellSouth Monthly State Summary Florida, October 2001

	Densouth monthly State Summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		• • • • •								• •
A3132	M&R-1 Design (Specials)/Non-Dispatch/FL(%)	Design	0 95%	1,901	0 00%	21		0 02125	0 4456	YES
A.3 1.4.1	M&R-1 PBX/Dispatch/FL(%)	PBX	17 83%	443	32.50%	40		0 06320	-2.3208	NO
A3142	M&R-1 PBX/Non-Dispatch/FL(%)	PBX	8.46%	263	20.00%	5		0.11100	-1.2194	YES
	M&R-1 Centrex/Dispatch/FL(%)	Centrex	23.64%	1,565	27.27%	11		0.12856	-0 2824	YES
A.3.1.5.1		Centrex	6.77%	916	0 00%	14		0.06765	1 0005	YES
A.3.1.5 2	M&R-1 Centrex/Non-Dispatch/FL(%)		7 02%	413	7.69%	13		0.007197	-0.0932	YES
A.31.61	M&R-1 ISDN/Dispatch/FL(%)	ISDN ISDN	1.86%	413	22 22%	9		0.04545	-4.4806	NO
A.3 1.6.2	M&R-1 (ISDN/Non-Dispatch/FL(%)	ISDN	1.00%	404	44.4270	3		0.04545	~4.4000	NO
	Customer Trouble Report Rate									
A 3 2 1.1	M&R-2 [Residence/Dispatch/FL(%)	Res	2 36%	4,473,459	2 48%	173,555		0.00038	-3.2891	NO
A 3 2 1.2	M&R-2 Residence/Non-Dispatch/FL(%)	Res	1.24%	4,473,459	1.23%	173,555		0 00027	0 4952	YES
A 3 2.2 1	M&R-2 Business/Dispatch/FL(%)	Bus	1 70%	1,233,640	1 87%	55,517		0.00057	-2 9321	NO
A 3 2.2.2	M&R-2 Business/Non-Dispatch/FL(%)	Bus	0.89%	1,233,640	078%	55,517		0 00041	2.7860	YES
A.3.2.3.1	M&R-2 (Design (Specials)/Dispatch/FL(%)	Design	0.46%	357,349	0.40%	5,563		0 00092	0.6934	YES
	M&R-2 Design (Specials/Non-Dispatch/FL(%)	Design	0.53%	357,349	0.38%	5,563		0 00099	1 5676	YES
A.3 2.3.2		PBX	0.35%	167,761	0.62%	6,477		0 00065	-5.4325	NO
A.32.41	M&R-2 PBX/Dispatch/FL(%)	PBX	0 16%	167,761	0.02%	6,477		0 00050	1 5871	YES
A.3 2.4.2	M&R-2 PBX/Non-Dispatch/FL(%)		0.66%	236,328		2,145		0 00050	0 8484	YES
A 3.2.5.1	M&R-2 Centrex/Dispatch/FL(%)	Centrex			0 51%					
A.3 2.5.2	M&R-2 Centrex/Non-Dispatch/FL(%)	Centrex	0.39%	236,328	0.65%	2,145		0 00135	1 9631	NO
A.3 2.6.1	M&R-2 ISDN/Dispatch/FL(%)	ISDN	0.13%	326,522	0.24%	5,484		0 00048	-2 2832	NO
A.3 2.6.2	M&R-2 (ISDN/Non-Dispatch/FL(%)	ISDN	0 15%	326,522	0 16%	5,484		0 00052	-0 3030	YES
	Maintenance Average Duration									
	M&R-3 Residence/Dispatch/FL(hours)	Res	24 55	105,462	21.86	4,306	29 303	0 45557	5 9030	YES
A 3 3.1.1		Res	8 61	55,635	5 62	2,135	22.377	0.49348	6 0467	YES
A 3 3 1.2	M&R-3 Residence/Non-Dispatch/FL(hours)									YES
A 3 3.2.1	M&R-3 Business/Dispetch/FL(hours)	Bus	18 58	21,017	16.40	1,038	29 729	0 94526	2 3076	
A.3 3.2.2	M&R-3 Business/Non-Dispatch/FL(hours)	Bus	5.42	11,008	2 78	432	20.914	1.02579	2 5703	YES
A.3 3.3.1	M&R-3 Design (Specials)/Dispatch/FL(hours)	Design	7 16	1,640	6 0 9	22	14 229	3.05398	0 3533	YES
A.3 3.3.2	M&R-3 Design (Specials)/Non-Dispatch/FL(hours)	Design	273	1,901	2 15	21	6 422	1 40903	0 4125	YES
A 3 3 4.1	M&R-3 PBX/Dispatch/FL(hours)	PBX	16 79	443	23 30	40	23 318	3 84972	-1.6894	NO
A 3 3 4.2	M&R-3 PBX/Non-Dispatch/FL(hours)	PBX	6 02	263	032	5	11 334	5 11676	1.1137	YES
A 3.3 5.1	M&R-3 Centrex/Dispatch/FL(hours)	Centrex	19.86	1,565	14 50	11	35 752	10 81751	0.4957	YES
A.3 3 5.2	M&R-3 Centrex/Non-Dispatch/FL(hours)	Centrex	4 6 1	916	366	14	8.902	2 39730	0 3974	YES
A 3 3 6 1	M&R-3 ISDN/Dispatch/FL(hours)	ISDN	7.86	413	8 34	13	11 676	3 28894	-0 1458	YES
A.3.3.6.2	M&R-3 (ISDN/Non-Dispatch/FL(hours)	ISDN	3 27	484	26.70	9	9.064	3 04936	-7.6847	NÖ
M.J.J.D.Z	Mari-3 (SDIVINOI-Dispatcive L(nouis)	10011			20.70		0.004	0,0000	1.0041	
	% Repeat Troubles within 30 Days									
A 3.4 1.1	M&R-4 Residence/Dispatch/FL(%)	Res	21 13%	105,462	18 00%	4,306		0.00635	4 9357	YES
A 3.4.1.2	M&R-4 Residence/Non-Dispatch/FL(%)	Res	18 41%	55,635	16 91%	2,135		0 00855	1 7577	YES
A 3.4 2.1	M&R-4 Business/Dispatch/FL(%)	Bus	18 30%	21,017	17 44%	1,038		0.01229	0 7012	YES
A 3 4.2 2	M&R-4 Business/Non-Dispatch/FL(%)	Bus	16 62%	11,008	17.36%	432		0 01826	-0 4035	YES
A3431	M&R-4 [Design (Specials)/Dispatch/FL(%)	Design	39 76%	1,640	36 36%	22		0 10504	0 3230	YES
A.3.4.3.2	M&R-4 Design (Specials)/Dispatch/FL(%)	Design	36 51%	1,901	23 81%	21		0 10564	1 2020	YES
	M&R-4 (PBX/Dispatch/FL(%)	PBX	20 54%	443	32.50%	40		0 06670	-1 7928	NO
A.3.4.4.1		PBX	14 45%	263	20 00%	5		0 15872	-0 3498	YES
A.344.2	M&R-4 PBX/Non-Dispatch/FL(%)		15 46%	1,565	9 09%	11		0 10940	0 5825	YES
A.3.4 5 1	M&R-4 Centrex/Dispatch/FL(%)	Centrex								YES
A 3.4.5 2	M&R-4 Centrex/Non-Dispatch/FL(%)	Centrex	15 72%	916	21.43%	14		0.09802	-0 5823	
A.3461	M&R-4 ISDN/Dispatch/FL(%)	ISDN	34.38%	413	23 08%	13		0 13379	0 8450	YES
A 3.4.6 2	M&R-4 ISDN/Non-Dispatch/FL(%)	ISDN	33 06%	484	44 44%	9		0 15826	-0 7195	YES
	Out of Service > 24 hours									
		Res	27.28%	70,621	23 77%	3,080		0 00820	4 2801	YES
A.3 5 1.1	M&R-5 Residence/Dispatch/FL(%)	Res	11 19%	17.059	4 10%	805		0 00820	6 2366	YES
A 3 5 1.2	M&R-5 Residence/Non-Dispatch/FL(%)		19.53%	13,682	17.97%	679		0.01559	1 0020	YES
A3521	M&R-5 Business/Dispatch/FL(%)	Bus								
A3522	M&R-5 Business/Non-Dispatch/FL(%)	Bus	4 65%	4,519	1.67%	240		0.01394	2.1374	YES
A 3.5 3 1	M&R-5 Design (Specials)/Dispatch/FL(%)	Design	4 51%	1,640	0 00%	22		0 04455	1 0128	YES
A 3 5 3.2	M&R-5 Design (Specials)/Non-Dispatch/FL(%)	Design	0 95%	1,901	0 00%	21		0 02125	0 4456	YES
A 3 5 4 1	M&R-5 PBX/Dispatch/FL(%)	PBX	14.62%	342	39.29%	28		0 06945	-3 5517	NO
A3542	M&R-5 PBX/Non-Dispatch/FL(%)	PBX	11 64%	146	0 00%	5		0 14588	0 7982	YES
A 3 5 5 1	M&R-5 Centrex/Dispatch/FL(%)	Centrex	21 18%	1,100	50 00%	6		0 16726	-1 7229	NO
A3552	M&R-5 Centrex/Non-Dispatch/FL(%)	Centrex	2 13%	376	0 00%	8		0 05156	0 4 1 2 7	YES
A.3561	M&R-5 ISDN/Dispatch/FL(%)	ISDN	7 28%	412	7 69%	13		0 07319	-0 0561	YES
A.0001						·				

	BeliSouth Monthly State Summary Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEĊ Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A 3 5.6.2	M&R-5 [ISDN/Non-Dispatch/FL(%)	ISDN	1 86%	484	22 22%	9		0 04545	-4 4806	NO
	Resale - Billing									
A.4.1	Involce Accuracy B-1 (FL(%)	BST - State	97 93%	\$492,661,862	99.94%	\$11,864,047		0.00004	-481.2079 }	YES
A 4.2	Mean Time to Deliver Involces - CRIS B-2 Region(business days)	BST - Region	3.91	1	3.38	1,820				YES

Florida, October 2001

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2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity

	Unbundled Network Elements - Ordering				
	% Rejected Service Requests - Mechanized				
B.1.1 1	O-7 Switch Ports/FL(%)	Diagnostic			Diagnostic
B.1.1.2	O-7 Local Interoffice Transport/FL(%)	Diagnostic			Diagnostic
8.1 1.3	O-7 Loop + Port Combinations/FL(%)	Diagnostic	21.44%	9,995	Diagnostic
8.114	O-7 Combo Other/FL(%)	Diagnostic			Diagnostic
B.1.15	O-7 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic	28 75%	400	Diagnostic
8116	O-7 ISDN Loop (UDN, UDC)/FL(%)	Diagnostic	20.00%	10	Diagnostic
B.1.1.7	O-7 Line Sharing/FL(%)	Diagnostic	24.05%	79	Diagnostic
B 1 1.8	O-7 2W Analog Loop Design/FL(%)	Diagnostic	7 03%	1,110	Diagnostic
B.1 1.9	O-7 2W Analog Loop Non-Design/FL(%)	Diagnostic	11.39%	553	Diagnostic
B.1 1 10	Q-7 2W Analog Loop w/INP Design/FL(%)	Diagnostic			Diagnostic
B1111	O-7 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			Diagnostic
B.1 1 12	O-13 2W Analog Loop w/LNP Design/FL(%)	Diagnostic	26.36%	110	Diagnostic
B.1.1.13	O-13 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic	85 82% 28,24%	141 255	Diagnostic
B.1 1 14 B.1.1.15	O-7 Other Design/FL(%) O-7 Other Non-Design/FL(%)	Diagnostic Diagnostic	43.58%	7.854	Diagnostic Diagnostic
B.1 1.16	0-7 INP Standalone/FL(%)	Diagnostic	43.36%	/,004	Diagnostic
B.1 1.17	0-13 LNP Standalone/FL(%)	Diagnostic	10.81%	1,739	Diagnostic
5.7 1.17			1010170	1,100	Diagroup
B.1 2.1	K Rejected Service Requests - Partially Mechanized O-7 [Switch Ports/FL(%)	Diagnostic			Diagnostic
B12.2	Q-7 Local Interoffice Transport/FL(%)	Diagnostic			Diagnostic
B 1 2.3	0-7 Loop + Port Combinations/FL(%)	Diagnostic	34,21%	6,335	Diagnostic
B.1 2.4	0-7 Combo Other/FL(%)	Diagnostic	01,2170	0,000	Diagnostic
B.1 2.5	0-7 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic	0.00%	43	Diagnostic
B.1.2.6	O-7 ISDN Loop (UDN, UDC)/FL(%)	Diagnostic	0.00%	5	Diagnostic
B.1.2.7	O-7 Line Shanng/FL(%)	Diagnostic	15.63%	64	Diagnostic
8.1.2 8	Q-7 2W Analog Loop Design/FL(%)	Diagnostic	25.39%	319	Diagnostic
B129	O-7 2W Analog Loop Non-Design/FL(%)	Diagnostic	16 39%	848	Diagnostic
81210	O-7 2W Analog Loop w/INP Design/FL(%)	Diagnostic			Diagnostic
B.1.2 11	O-7 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			Diagnostic
B.1.2.12	O-13 2W Analog Loop w/LNP Design/FL(%)	Diagnostic	43.63%	832	Diagnostic
81213	O-13 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic	19.81%	1,863	Diagnostic
B12.14	O-7 Other Design/FL(%)	Diagnostic	47.37%	209	Diagnostic
B 1.2 15	O-7 Other Non-Design/FL(%)	Diagnostic	79.25%	2,496	Diagnostic
B.1 2.16	O-7 INP Standalone/FL(%)	Diagnostic	43.35%	1 405	Diagnostic
B.1.2.17	O-13 LNP Standalone/FL(%)	Diagnostic	43.35%	1,135	Diagnostic
	% Rejected Service Requests - Non-Mechanized				
B.1 3.1	O-7 Switch Ports/FL(%)	Diagnostic	50.00% 27,42%	2 62	Diagnostic
B.1 3.2	O-7 Local Interoffice Transport/FL(%) O-7 Loco + Port Combinations/FL(%)	Diagnostic Diagnostic	48.31%	62 859	Diagnostic Diagnostic
B 1 3.3	O-7 Loop + Port Combinations/FL(%) O-7 Combo Other/FL(%)	Diagnostic	40.31%	009	Diagnostic
B.1.3.4	O-7 Combo Cinei/FL(%) O-7 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic	22 36%	313	Diagnostic 1
B 1.3.5		Diagnostic	12.06%	622	Diagnostic
B 1.36 B 1.37	0-7 ISDN Loop (UDN, UDC)/FL(%) 0-7 Line Sharing/FL(%)	Diagnostic	16.99%	153	Diagnostic
B 1.3 8	O-7 2W Analog Loop Design/FL(%)	Diagnostic	40.28%	211	Diagnostic
B.1.39	O-7 2W Analog Loop Non-Design/FL(%)	Diagnostic	33.09%	1,378	Diagnostic
B.1.3 10	O-7 2W Analog Loop w/INP Design/FL(%)	Diagnostic	50.00%	6	Diagnostic
B.1.3.10 B.1.3.11	O-7 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic	53.85%	13	Diagnostic
B.1 3.12	O-13 2W Analog Loop w/LNP Design/FL(%)	Diagnostic	47 23%	307	Diagnostic
B.1 3.13	O-13 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic	39.52%	248	Diagnostic
B 1.3 14	O·7 Other Design/FL(%)	Diagnostic	26 30%	441	Diagnostic
B 1 3 15	O-7 Other Non-Design/FL(%)	Diagnostic	36.12%	1,744	Diagnostic .
B1316	O-7 INP Standalone/FL(%)	Diagnostic	41.79%	67	Diagnostic
B1317	O-13 LNP Standatone/FL(%)	Diagnostic	30 61%	1,016	Diagnostic

Reject interval - Mechanized

Equity

NO

YES YES NO

NO NO

NO NO

NO NO

NO

YES

NO NO YES

NO

YES

NO

YES

YE\$

YES

YES YES

YES

YES YES

YES YES YES

YES YES YES

YES

YES NO

ZScore

Standard Standard

BellSouth Monthly State Summary Florida, October 2001

FION	ida, October 2001	Anaiog	Measure	Volume	Measure	Volume	Deviation	Error
					i 1			
0-8 0-8	Switch Ports/FL(%) Local Interoffice Transport/FL(%)	>= 97% win 1 hr >= 97% win 1 hr						
0-8 0-8	Loop + Port Combinations/FL(%)	>= 97% win 1 hr			92.91%	2,144		
0-8	Combo Other/FL(%)	>= 97% win 1 hr			32.31%	2,144		
0-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 97% win 1 hr			100.00%	115	· · -	
0-8	ISDN Loop (UDN, UDC)/FL(%)	>= 97% win 1 hr			100.00%	2		
0-8	Line Sharing/FL(%)	>= 97% win 1 hr			73.68%	19	-	
0-8	2W Analog Loop Design/FL(%)	>= 97% win 1 hr			74.36%	78		
0-8	2W Analog Loop Non-Design/FL(%)	>= 97% win 1 hr			72.31%	65		
0-8	2W Analog Loop w/INP Design/FL(%)	>= 97% win thr			12.31%	05		
0-8	2W Analog Loop w/NP Non-Design/FL(%)	>= 97% win 1 hr						
0-14	2W Analog Loop w/LNP Design/FL(%)	>= 97% win 1 hr			96.55%	29		
0-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 97% win 1 hr			89.34%	122		
0-8	Other Design/FL(%)	>= 97% win 1 hr			83.33%	72		
<u>О-в</u>	Other Non-Design/FL(%)	>= 97% win 1 hr			70.83%	3,463		
0-8	INP Standalone/FL(%)	>= 97% win 1 hr			/0.00/0	0,400		
0-14	LNP Standalone/FL(%)	>= 97% w in 1 hr			96.83%	189		
	t Interval - Partially Mechanized - 10 hours							
O-8	Switch Ports/FL(%)	>= 85% w in 10 hrs						_
0-8	Local Interoffice Transport/FL(%)	>= 85% w in 10 hrs						
0-8	Loop + Port Combinations/FL(%)	>= 85% win 10 hrs			96.33%	2,177		
0-8	Combo Other/FL(%)	>= 85% win 10 hrs			00.3576	£,177		
0-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% win 10 hrs						
0-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% win 10 hrs			0.00%	1		
0-8	Line Sharing/FL(%)	>= 85% win 10 hrs			63.64%	11		
0-8	2W Analog Loop Design/FL(%)	>= 85% w in 10 hrs			92.94%	85		
0-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 10 hrs			84 25%	146		
0-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 10 hrs						
0-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 10 hrs						
0-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 10 hrs			86 30%	365		
0-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 10 hrs			81 74%	460		
0-8	Other Design/FL(%)	>= 85% w in 10 hrs			87 25%	102		
0-8	Other Non-Design/FL(%)	>= 85% w in 10 hrs			87 84%	2,014		
O-8	INP Standalone/FL(%)	>= 85% w in 10 hrs						
0-14	LNP Standalone/FL(%)	>= 85% w in 10 hrs			89 26%	503		
Reject	t Interval - Non-Mechanized							
0-8	Switch Ports/FL(%)	>= 85% w in 24 hrs			100 00%	1		
0-8	Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs			94.12%	17		
O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs			98.80%	417		
O-8	Combo Other/FL(%)	>= 85% w in 24 hrs						
O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs			100 00%	71		
O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs			100.00%	75		
O-8	Line Sharing/FL(%)	>= 85% w in 24 hrs			96 43%	28		
O-8	2W Analog Loop Design/FL(%)	>= 85% w in 24 hrs			100.00%	86		
O-8	2W Analog Loop Non-Design/FL(%)	 >= 85% w in 24 hrs			99.78%	463		
O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 24 hrs			100 00%	5		
0-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 24 hrs			85 71%	7		
0-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs			99.32%	148		
0-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs			96 97%	99		
O-8	Other Design/FL(%)	>= 85% w in 24 hrs			97 52%	121		
0-8	Other Non-Design/FL(%)	>= 85% w in 24 hrs			98.76%	643		
O-8	INP Standalone/FL(%)	>= 85% w in 24 hrs			100 00%	29		
0-14	LNP Standalone/FL(%)	>= 85% w in 24 hrs			97 80%	318		
101	nehne B-Mect Ink							
0-9	Switch Ports/FL(%)	>= 95% w in 3 hrs						
0-9	Local Interoffice Transport/FL(%)	>= 95% w x1 3 hrs						
		0.000			98 33%	7.682		
0-9	Loop + Port Combinations/FL(%)	>= 95% w in 3 hrs	1		00000	1,002		
	Loop + Port Combinations/FL(%) Combo Other/FL(%)	>= 95% w in 3 hrs >= 95% w in 3 hrs			94 62%	7,002		

Benchmark /

BST

BST

CLEC

CLEC

	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B196	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 95% w in 3 hrs		-	100.00%	8		i.		YES
B.1.97	O-9 Line Sharing/FL(%)	>= 95% w in 3 hrs			100.00%	61				YES
B 1 9.8	O-9 2W Analog Loop Design/FL(%)	>= 95% w in 3 hrs			99.41%	1,023				YES
B 1.9 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 95% w in 3 hrs			99.17%	480				YES
B.1 9.10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 95% win 3 hrs								
B.1.9.11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 95% win 3 hrs								
B.1.9.12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 95% win 3 hrs			100.00%	80				YES
B.1.9.13 B.1.9.14	O-15 2W Analog Loop w/LNP Non-Design/FL(%) O-9 Other Design/FL(%)	>= 95% w in 3 hrs >= 95% w in 3 hrs			95.24% 100.00%	178				YES
B.1.9.14 B.1 9.15	O-9 Other Design/FL(%) O-9 Other Non-Design/FL(%)	>= 95% win 3 hrs			99.94%	5,134				YES
B.1.9.16	O-9 INP Standalone/FL(%)	>= 95% win 3 hrs			38.3476	5,134				
B.1 9.17	O-15 LNP Standalone/FL(%)	>= 95% win 3 hrs			98.90%	1,551				YES
	FOC Timeliness - Partially Mechanized - 10 hours									
B.1.12 1	O-9 Switch Ports/FL(%)	>= 85% w in 10 hrs								
B 1 12 2	O-9 Local Interoffice Transport/FL(%)	>= 85% w in 10 hrs								
B 1.12 3	O-9 Loop + Port Combinations/FL(%)	>= 85% w in 10 hrs			95.94%	4,232				YES
B.1.12 4	O-9 Combo Other/FL(%)	>= 85% w in 10 hrs					-			
B 1.12 5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% win 10 hrs			85.00%	20				YES
B 1.12 6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 85% win 10 hrs			100 00% 86.89%	<u>6</u> 61				YES YES
B 1.12.7	O-9 Line Sharing/FL(%)	>=85% win 10 hrs >=85% win 10 hrs			86.14%	267				YES
B1128 B1129	0-9 2W Anaiog Loop Design/FL(%) 0-9 2W Anaiog Loop Non-Design/FL(%)	>= 85% win 10 hrs			95.46%	771				YES
B 1.12 10	0-9 2W Analog Loop w/INP Design/FL(%)	>= 85% win 10 hrs			50.4076					160
B 1.12 11	O-9 2W Analog Loop w/INP Non-Design/EL(%)	>= 85% w in 10 hrs								
B.1 12.12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 10 hrs			86 85%	517		-		YES
B 1.12 13	O-15 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 10 hrs			94 68%	1,653				YES
B 1.12 14	O-9 Other Design/FL(%)	>=85% win 10 hrs			80.14%	146	· ·			NO
B 1 12 15	O-9 Other Non-Design/FL(%)	>= 85% w in 10 hrs			92 70%	534	-			YES
B 1.12 16	O-9 INP Standalone/FL(%)	>= 85% w in 10 hrs								
B.1.12 17	O-15 LNP Standalone/FL(%)	>=85% w in 10 hrs			90 63%	662				YES
	FOC Timeliness - Non-Mechanized									
B 1 13 1	O-9 Switch Ports/FL(%)	>= 85% w in 36 hrs			100.00%	1				YES
B.1 13.2	O-9 Local Interoffice Transport/FL(%)	>= 85% w in 36 hrs			100 00%	44				YES
B 1 13 3	0-9 Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs			99.03%	413				YES
B 1 134	0-9 Combo Other/FL(%)	>= 85% w in 36 hrs >= 85% w in 36 hrs			99 59%	241				YES
B 1.13.5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%) O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 85% win 36 hrs			99 59% 99.43%	528				YES
B.1.13.6 B.1 13.7	0-9 [ISDN Loop (ODN, ODC)/FL(%) 0-9 Line Sharing/FL(%)	>= 85% win 36 hrs			100.00%	121				YES
B.1 13.8	O-9 2W Analog Loop Design/FL(%)	>= 85% win 36 hrs			99 18%	122				YES
B 1 13 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 85% win 36 hrs			99 40%	836				YES
B 1.13 10	O-9 (2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs			83 33%	6				NO
B 1.13.11	O-9 2W Analog Loop w/INP Non-Desigr/FL(%)	>= 85% w in 36 hrs			100.00%	7				YES
B 1.13 12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs			97 24%	181				YES
B 1.13.13	O-15 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 36 hrs			99 40%	166				YES
B.1 13 14	O-9 Other Design/FL(%)	>= 85% w in 36 hrs			99.04%	312				YES
B.1 13.15	O-9 Other Non-Design/FL(%)	>= 85% w in 36 hrs			99 26%	1,079				YES
B 1 13 16	O-9 INP Standalone/FL(%)	>= 85% w in 36 hrs			100 00%	41				YES
B 1 13 17	O-15 LNP Standalone/FL(%)	>= 85% w in 36 hrs			99.43%	699				YE\$
	FOC & Reject Response Completeness - Mechanized									
B 1.14 1 1	O-11 Switch Ports/EDI/FL(%)	>= 95%								
B.1.14.1.2	O-11 Switch Ports/TAG/FL(%)	>= 95%								
B.1.14 2.1	O-11 Local Interoffice Transport/EDI/FL(%)	>= 95%								
B.1.14 2 2	O-11 Local Interoffice Transport/TAG/FL(%)	>= 95%								
B 1 14 3 1	O-11 Loop + Port Combinations/EDI/FL(%)	>= 95%			100 00%	237				YES
B 1 14.3.2	O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			97.85%	9,758				YES
B 1 14 4 1	O-11 Combo Other/EDVFL(%)	>= 95%							1.1.1	
B 1.14 4 2	O-11 Combo Other/TAG/FL(%)	>= 95%			00.000	10				NO
B11451	0-11 xDSL (ADSL, HDSL and UCL)/EDI/FL(%)	>≃ 95% >= 95%			80 00% 83 33%	10 390			· .	NO NO
B.1 14 5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%)	>= 9070			03 3370	390				

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B 1.14 6.1	0-11	ISDN Loop (UDN, UDC)/EDVFL(%)
B 1 14 6 2	0-11	ISDN Loop (UDN, UDC)/TAG/FL(%)
B.1.14.7.1	0-11	Line Sharing/EDI/FL(%)
B 1 14 7.2	0-11	Line Sharing/TAG/FL(%)
B 1.14.8.1	0-11	2W Analog Loop Design/EDI/FL(%)
B11482	0-11	2W Analog Loop Design/TAG/FL(%)
B11491	0-11	2W Analog Loop Non-Design/EDI/FL(%)
B 1.14 9 2	0-11	2W Analog Loop Non-Design/TAG/FL(%)
B 1.14 10.1	0-11	2W Analog Loop w/INP Design/EDI/FL(%)
B 1 14 10 2	0-11	2W Analog Loop w/INP Design/TAG/FL(%)
B 1.14 11 1	0-11	2W Analog Loop w/INP Non-Design/EDI/FL(%)
B 1 14.11.2	0-11	2W Analog Loop w/INP Non-Design/TAG/FL(%)
B 1.14 12 1	0-11	2W Analog Loop w/LNP Design/EDI/FL(%)
B 1,14 12 2	0-11	2W Analog Loop w/LNP Design/TAG/FL(%)
B 1 14 13 1	0-11	2W Analog Loop w/LNP Non-Design/EDI/FL(%)
B 1.14.13 2	0-11	2W Analog Loop w/LNP Non-Design/TAG/FL(%)
B 1.14.14 1	0-11	Other Design/EDI/FL(%)
B 1 14 14 2	0-11	Other Design/TAG/FL(%)
B 1 14 15 1	0-11	Other Non-Design/EDI/FL(%)
B 1 14 15 2	0-11	Other Non-Design/TAG/FL(%)
B 1.14.16 1	0-11	INP Standalone/EDI/FL(%)
B.1,14 16 2	0-11	INP Standalone/TAG/FL(%)
B 1 14.17 1	0-11	LNP Standalone/EDI/FL(%)
B 1 14 17.2	0-11	LNP Standalone/TAG/FL(%)
		Reject Response Completeness - Partially Mechanized
B 1 15 1.1	0-11 0-11	Switch Ports/EDVFL(%)
B 1 15 1 2		Switch Ports/TAG/FL(%)
B 1 15 2 1	0-11	Local Interoffice Transport/EDI/FL(%)
B 1.15 2.2	0-11 0-11	Local Interoffice Transport/TAG/FL(%)
B 1 15.3 1	0-11	Loop + Port Combinations/EDI/FL(%)
B 1 15 3.2	-	Loop + Port Combinations/TAG/FL(%)
B 1 15 4 1	0-11	Combo Other/EDVFL(%)
B 1 15 4 2		Combo Other/TAG/FL(%)
B 1.15 5.1	0-11	xDSL (ADSL, HDSL and UCL)/EDVFL(%)
B 1.15 5 2	0-11	xDSL (ADSL, HDSL and UCL)/TAG/FL(%)
B 1.15 6 1	0-11	ISDN Loop (UDN, UDC)/ED/FL(%)
B11562	0-11	ISDN Loop (UDN, UDC)/TAG/FL(%)
311571	0-11	Line Sharing/EDI/FL(%)
311572	0-11	Line Sharing/TAG/FL(%)
B 1 15 8.1	0-11	2W Analog Loop Design/EDI/FL(%)
3 1.15 8.2	0.11	2W Analog Loop Design/TAG/FL(%)
B 1 15 9.1	0-11	2W Analog Loop Non-Design/EDI/FL(%)
31.1592	0-11	2W Analog Loop Non-Design/TAG/FL(%)
3 1.15.10.1	0-11	2W Analog Loop w/INP Desigr/ED/FL(%)
3 1 15 10 2	0-11	2W Analog Loop w/INP Design/TAG/FL(%)
3 1 15.11 1	0-11	2W Analog Loop w/INP Non-Design/ED//FL(%)
3 1 15.11.2	0-11	2W Analog Loop w/INP Non-Design/TAG/FL(%)
3.1 15 12 1	0-11	2W Analog Loop w/LNP Design/EDI/FL(%)
3.1 15 12 2	0-11	2W Analog Loop w/LNP Design/TAG/FL(%)
3 1 15 13 1	0-11	2W Analog Loop w/LNP Non-Design/EDI/FL(%)
3 1 15.13 2	0-11	2W Analog Loop w/LNP Non-Design/TAG/FL(%)
3 1 15 14.1	0-11	Other Design/EDVFL(%)
3 1 15 14.2	0-11	Other Design/TAG/FL(%)
B 1 15 15 1	0-11	Other Non-Design/EDI/FL(%)
B 1 15 15.2	0-11	Other Non-Design/TAG/FL(%)
3 1 15 16 1	0.11	INP Standalone/EDI/FL(%)
3 1 15 16 2	0-11	INP Standalone/TAG/FL(%)
3 1 15 17 1	0-11	LNP Standalone/EDI/FL(%)
B 1 15 17 2	0-11	LNP Standalone/TAG/FL(%)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
							_	
>= 95% >≈ 95%			100.00%	10				YES
>= 95%			100.00 %					
>= 95%			98.73%	79				YES
>≂ 95%			95.87%	242	1			YE\$
>= 95%			98.62%	868				YES
>= 95%								
>= 95% >= 95%			96.02%	553				YES
>= 95%								
>= 95%			· · · · · · · · · · · · · · · · · · ·					
>= 95%					1			
>= 95%			100.00%	74				YES
>= 95%			97 22%	36				YES
>= 95%			100.000					
>= 95% >= 95%			100.00%	<u>141</u> 17				YES
>= 95%			100.00% 96.64%	238				YES YES
>≈ 95%			100 00%	6,900				YES
>= 95%			99.27%	954				YES
>= 95%								
>= 95%								
>= 95%			100 00%	1,411				YES
>= 95%			100 00%	328				YËS
>= 95%								
>= 95%								
>= 95%								
>≈ 95% >≈ 95%			100.00%	075				
>= 95%			100.00% 99.64%	255 6,080				YES YES
>= 95%			33 44 /8	0,000				160
>= 95%								
>= 95%								
>= 95%			46.51%	43				NO
>= 95%			100 00%	1				YES
>= 95%			100.00%	4				YES
>= 95% >= 95%				64				
>≈ 95%			98.44% 100.00%	184				YES
>= 95%			98.52%	135				YES
>= 95%			00.02 /0					,00
>= 95%			99.88%	848				YES
>= 95%								
>= 95%								
>= 95%								
>≈ 95% >≈ 95%								
>≈ 95% >≈ 95%			99.50% 100.00%	<u>601</u> 231				YES
>= 95%			100.00%	3				YES
>= 95%			100.00%	1,860				YES
>≕ 95%			100 00%	26				YES
>= 95%			100 00%	183				YES
>= 95%			100.00%	1,958				YES
>= 95%			100 00%	538				YES
>= 95%								
>≈ 95% >= 95%			00.07%	700				VC0
>= 95% > = 95%			99.37% 99.71%	790 345				YES
~~ 30 10			33.1176	040				

8.1168 0.11 WAnking Loop Design (%)		Bensouth Monthly State Summary									
TO 2 Fight Regione Completions: Non-Machanized 101012 101012 101012 101012 101012 101012 101012 101020 101012 1		Florida, October 2001									
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $											
B II 10 Col II. Accel insequents. Transport FL(s). >> 99% B I 34% G2 NO 01104 Col II. Org. Proc. Name Part (S). >> 99% B I 35% G2 NO 01104 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G3 YES 01105 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01106 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01106 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01106 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01106 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01107 Col II. Org. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01108 Col II. Org. Proc. Proc. Name Part (S). >> 99% B 0 45% G2 YES 01108 Col II. Org. Proc. Proc			7	-							
B II 63 Chi L. Got , Fin Corbitation/TLQ;											
b in 4 C11 Control Construction											
B II 66 C.11 dds./Add_, iddS_, iddS_						54.0376	009				
B 116 0 C.11 SM 1000 (URL URC)(T1(4)) >>> 95% P6 25% S22 YES B 116 0 C.11 MAD Standy(T1(4)) >>> 95% B2 35% T/T YES B 116 0 C.11 XY Ansol Loop Non Darger(T4) >>> 95% B2 35% T/T YES B 116 0 C.11 XY Ansol Loop Non Darger(T4) >>> 95% B2 35% T/T YES B 116 10 C.11 XY Ansol Loop Non Darger(T4) >>> 95% B2 35% T/T YES B 116 10 C.11 XY Ansol Loop Non Darger(T4) >>> 95% B2 35% T/T YES B 116 10 C.11 XY Ansol Loop Non Darger(T4) >>> 95% B2 35% YES B 116 17 C.11 XY Ansol Loop Non Darger(T4) >>>> 95% B2 35% YES B 116 17 C.11 XY Ansol Loop Non Darger(T4) >>>>>>>>>>>>>>>>>>>>>>>>>>>>			-			99.04%	212				VEC
B 18.7 O T. Les Standard T.(a)											
B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 96.8%, 111 (YES) B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 97.20 100.00%, 0. YES B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 97.20 YES B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 98.25%, 1.67 YES B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 98.25%, 244. YES B. 18 B. 0.11 204. Analy Log, Desg/T1(a), >> 69%, 98.25%, 244. YES B. 18 B. 0.11 1.67 204. Analy Log, Desg/T1(a), >> 69%, 98.25%, 244. YES B. 18 B. 0.11 1.68 50.16%, 32.25%, 244. YES B. 18 B. 0.11 1.68 50.16%, 32.25%, 244. YES B. 18 B. 0.11 1.68 50.16%, 32.25%, 244. YES B. 17 B. 0.11 1.68 50.16%, 32.25%, 34.4 YES B. 17 B. 0.11 1.68 50.16%, 32.25%, 34.4 YES B. 17 B. 0.11 1.68	B 1.16.7										
Bit 160 Cit 1 24 Analog Loop Nucl Design FL(S) 26 State 100 26 State 100 26 State 100 26 State 100 27 State 100 28 State 100 27 State 27 State 100 27 State 27 State 100 27 State	B.1 16 8										
B. III 61 O.I. 204 Antig Loop with B Documpart (%) >> 65% 100,00% 10 YES B. III 61 O.I. 204 Antig Loop with B Documpart (%) >> 85% 100,00% 10 YES B. III 61 O.I. 204 Antig Loop with B Documpart (%) >> 85% 100,00% 10 YES B. III 61 O.I. 204 Antig Loop with B Documpart (%) >> 85% 100,00% 10 YES B. III 61 O.I. 204 Antig Loop with B Documpart (%) >> 85% 100,00% 10 YES B. III 71 O.I. 1 204 Antig Loop with B Documpart (%) >> 85% 100,00% 10 YES B. III 71 O.I. 1 204 Antig Loop with B Documpart (%) >> 85% 10,00% 10 YES B. III 71 O.I. 1 204 Antig Loop with B Documpart (%) >> 85% 10,00% 10 YES B. III 71 O.I. 1 204 Antig Loop with B Documpart (%) >> 85% 257 10 <td< td=""><td>B.1 16 9</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></td<>	B.1 16 9										
0 II 011 0.11 201 Arabia (200 wWP Araba (200 wWP Araba (201	B.1 16 10		>= 95%								
B II 612 0.11 274 Ansag Loop wLMP for Design(FL(s)) >= 6% 66.3% 327 VFES B II 618 0.11 277 Ansag Loop wLMP for Design(FL(s)) >= 6% 66.3% 2424 NB B II 618 0.11 Diffs Standson, FL(s) >= 6% 66.3% 2424 NB B II 61 0.11 Diffs Standson, FL(s) >= 6% 66.3% 2424 NB B II 61 0.11 Diffs Standson, FL(s) >= 6% 66.3% 2424 NB B II 61 0.11 Diffs Standson, FL(s) >= 6% 66.3% 2424 NB B II 712 0.11 Stands Fract ArcH L(s) >= 6% 67.44% 1.616 YES B II 712 0.11 Stands Fract ArcH L(s) >= 6% 97.44% 1.616 YES B II 712 0.11 Loop 1 Fort Combination EMPL(s) >= 6% 97.44% 1.616 YES B II 712 0.11 Loop 1 Fort Combination EMPL(s) >= 6% 97.44% 1.616 YES B II 712 0.11 Loop 1 Fort Combination EMPL(s) >= 6% 97.44% 97.44%	B11611	O-11 2W Analog Loop w/INP Non-Design/FL(%)	>= 95%			100 00%	13				
61.613 O-11 201 204 YES 61.614 O-11 207 241 YES 61.614 O-11 207 241 YES 81.1616 O-11 207 241 YES 81.161 O-11 207 241 YES 81.111 O-11 207 241 201 201 81.111 O-11 201 201 201 201 201 81.111 O-11 201 2	B 1 16 12		>= 95%			98.37%					
B 16 16 0.11 Opt Non-Design FL(S) >= 95% 98.35% 1/244 YES B 16 16 16 0.11 IMP Standaure FL(S) >= 95% 98.51% 67 YES B 16 17 0.11 IMP Standaure FL(S) >= 95% 98.51% 67 YES B 17 10 0.11 IMP Standaure FL(S) >= 95% 98.51% 67 YES B 17 12 0.11 Loss It Bandaure Tangort/EXPUTAS >= 95% 98.55% 0 <td>B.1.16.13</td> <td></td> <td>>∞ 95%</td> <td></td> <td></td> <td>98.39%</td> <td>248</td> <td></td> <td></td> <td></td> <td></td>	B.1.16.13		> ∞ 95%			98.39%	248				
B 16 16 0.11 IAP Standakower L(x) >= 85% 98.5 % 97.6 % 97.6 % B 16 17 0.11 IAP Standakower L(x) >= 85% 97.6 % 97.6 % 97.6 % B 17 11 0.11 Switch Press/Parket (k) >= 85% 97.6 % <td< td=""><td></td><td></td><td>>= 95%</td><td></td><td></td><td>92.97%</td><td>441</td><td></td><td></td><td></td><td>NO</td></td<>			>= 95%			92.97%	441				NO
B 16 11 D* 11 LVP StandatomeTL(S) > = 95% B0.1711 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1711 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1721 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1721 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1721 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1721 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1722 D-11 Settiop Processor Complements and Multiple Response) - Mechanized > = 95% B0.1722 D-11 Settiop Processor Complement and Multiple Response) - Mechanized > = 95% B1.1722 D-11 Settiop Processor Complement and Multiple Response) - Mechanized > = 95% B1.1722 D-11 Settiop Processor Complement and Multiple Response) - Mechanized > = 95% B1.1721 D-11 Settiop Processor Complement and Multiple Response > = 95% <t< td=""><td>B 1 16 15</td><td></td><td>>= 95%</td><td></td><td></td><td>96.39%</td><td>1,744</td><td></td><td></td><td></td><td>YES</td></t<>	B 1 16 15		>= 95%			96.39%	1,744				YES
DC C A Type: Program Completiones (Multiple Response) - Machanized B1 /12 1 Dit Switch Protect AGPFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Local Interdice Transport/EDFL(%) >= 95% B1 /12 2 Dit Mask Mark (Disk and UCL/EDFL(%) >= 95% B1 /12 2 Dit Mask Mark (Disk and UCL/EDFL(%) >= 95% B1 /12 2 Dit Mask Mark (Disk (Disk (Disk (Disk (Disk (Dis		O-11 INP Standalone/FL(%)	>= 95%			98.51%	67				YES
B.1/111 O-11 Setto Ports/ED/FL(%) >= 95% B.1/122 O-11 Local Instructions Transport CoVL(%) >= 95% B.1/122 O-11 Combo Obser/TAGFL(%) >= 95% B.1/124 O-11 Combo Obser/TAGFL(%) >= 95% B.1/121 Comb Obs	B 1 16.17	O-11 [LNP Standalone/FL(%)	>= 95%			97.44%	1,016				YES
B.1/111 O-11 Setto Ports/ED/FL(%) >= 95% B.1/122 O-11 Local Instructions Transport CoVL(%) >= 95% B.1/122 O-11 Combo Obser/TAGFL(%) >= 95% B.1/124 O-11 Combo Obser/TAGFL(%) >= 95% B.1/121 Comb Obs		FOC & Relect Response Completeness (Multiple Responses) - Mechanized									
B11121 C11 Settion Proter XAGFL(%) >= 95% B11221 C11 Local Interdifies Transport XAGFL(%) >= 95% B11221 C11 Local Interdifies Transport XAGFL(%) >= 95% B11721 C11 Local Interdifies Transport XAGFL(%) >= 95% B11721 C11 Local Interdifies Transport XAGFL(%) >= 95% B11721 C11 Conto Combation EVPL(%) >= 95% B11721 Conto Comparison EVPL(%) >= 95% B11721 D11 Conto Comparison EVPL(%) >= 95% B11721 Conto Comparison EVPL(%) >= 95% B11722 Conto Comparin EVPL(%) >= 95% <tr< td=""><td>B.1.17 1 1</td><td></td><td>→= 95%</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr<>	B.1.17 1 1		→ = 95%								
B 11 / 21 C 11 Local Interofiles Transport/ED/FL(%) >= 99% B 11 / 22 C 11 Local Interofiles Transport/ED/FL(%) >= 99% B 11 / 23 C 11 Local Interofiles Transport/ED/FL(%) >= 99% B 11 / 23 C 11 Local Interofiles Transport/ED/FL(%) >= 99% B 11 / 24 C 11 Local Interofiles Transport/ED/FL(%) >= 99% B 11 / 24 C 11 Commo Dim/ED/FL(%) >= 99% B 11 / 25 D 11 ASL, HOSL, HOSL, and UC/FL(%) >= 99% B 11 / 26 D 11 LOSL, HOSL, HOSL, and UC/FL(%) >= 99% B 11 / 26 D 11 LOSL, HOSL, and UC/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Loss Stating/CM/FL(%) >= 95% B 11 / 26 D 11 Z4 / Anneg/CD/FL(%) <t< td=""><td>B.1 17 1 2</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<>	B.1 17 1 2										
B 17 22 C-11 Loge + Port Combinitions TAGFL(%) >= 95% 64 49% 327 NO B 17 32 C-11 Conto Onbinitions TAGFL(%) >= 95% 91.60% 9.648 NO B 17 32 C-11 Conto Onbinitions TAGFL(%) >= 95% 91.60% 9.648 NO B 17 42 C-11 Conto Onig/TAGFL(%) >= 95% 95% 91.67% 92.648 NO B 17 42 C-11 Conto Onig/TAGFL(%) >= 95% 965% 96 95% 96 95% 96 95% 96 96 95% 96 95% 96 95% 96 95% 96 96 95% 96 95% 96 95% 96 95% 96 95% 96 95% 96 95% </td <td>B 1 17 2 1</td> <td></td>	B 1 17 2 1										
B1 17 22 0-11 Loop - Fort Combinations/TAGFL(%) >> 95% 91,80% 92,846 NO B1 17 41 0-11 Combo Other/TAGFL(%) >> 95% 95% 97,85% 98,84% 325,85% 97,85% 98,95% 98,	B11722										
8.1 17 32 0-11 Loop + Part Combinations(AGFL(%) >= 95% 91,80% 9,546 NO 8.1 17 41 0-11 Combo Other(TAGFL(%) >= 95% >= 95% >= 95% >= >= 95% >= >= 95% >= >= 95% >= >= 95% >= >= >= 95% >= >= 95% >= >= >= >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% 96 00.00% 0.00% NO	B.1.17 3 1	O-11 Loop + Port Combinations/EDI/FL(%)	>= 95%			64 98%	237				NO
B 1.17 4.2 O.11 Conto Other/TAG/FL/%) >= 95% 1000% B B 117 5.2 O.11 ADSL, (ADSL, HOSL and UCL/TAG/FL/%) >= 95% 98.46% 325 B 117 5.2 O.11 SDNL Loop (UDN, UCC/TAG/FL/%) >= 95% 98.46% 325 B 117 6.1 O.11 SDNL Loop (UDN, UCC/TAG/FL/%) >= 95% 98.46% 325 B 117 6.2 O.11 Line Sharing/TAG/FL/%) >= 95% 98.46% 325 B 117 7.2 O.11 Line Sharing/TAG/FL/%) >= 95% 97.55% 222 NO B 117 8.1 O.11 SWA naing Loop Design/TAG/FL/%) >= 95% 95% 97.55% 222 NO B 117 9.1 O.11 SWA naing Loop Design/TAG/FL/%) >= 95% 95%	B.1 17 3 2	O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			91.80%					
B11751 O-11 AOSL	B.1 17 4 1		>= 95%								
B1 17 52 0.11 SSL (ADSL HOSL and UCL)/TAGFL(%). >= 95% 95.46% 325 B1 17 61 0.11 ISON Loop (UDN, UCC)/TAGFL(%). >= 95% 96.06% 10 B1 17 62 0.11 Line Sharing/TAGFL(%). >= 95% 96.06% 10 B1 17 71 0.11 Line Sharing/TAGFL(%). >= 95% 96.06% 10 B1 17 72 0.11 Line Sharing/TAGFL(%). >= 95% 96.05% 97.759% 32.22 NO B1 17 81 0.11 2W Antiog Loop Design/TAGFL(%). >= 95% 95% 97.59% 32.22 NO B1 17 91 0.11 2W Antiog Loop Design/TAGFL(%). >= 95% 95% 95.22% 531 NO B1 17 91 0.11 2W Antiog Loop Nor-Design/TAGFL(%). >= 95% 95.5% 92.22% 531 NO B1 17 010 0.11 2W Antiog Loop WiNP Design/TAGFL(%). >= 95% 95.5% 92.22% 531 NO B1 17 010 0.11 2W Antiog Loop WiNP Design/TAGFL(%). >= 95% 95.5% 92.22% 531 NO B1 17 120 0.11 2W Antiog Loop Wi			>= 95%								
B11761 0.11 ISDN Loop (UDN, UDC/EDVFL(%) >= 85%, >= 85%, 80.00%, 10 B11762 0.11 ISDN Loop (UDN, UDC/IAGFL(%), >= 85%, 80.00%, 10 NO B11771 0.11 Lune Sharng/EDVFL(%), >= 85%, 80.00%, 10 NO B11771 0.11 Lune Sharng/EDVFL(%), >= 85%, 96.00%, 10 NO B11771 0.11 Line Sharng/EDVFL(%), >= 85%, 96.03%, 94.67%, 78.98%, 232.28 NO B11781 0.11 2W Anitol Loop Design/EDVFL(%), >= 85%, 96.03%, 856 YE B11791 0.11 2W Anitol Loop WINP Design/EDVFL(%), >= 85%, 93.22%, 531 NO B11702 0.11 2W Anitol Loop WINP Design/EDVFL(%), >= 85%, 93.22%, 531 NO B117112 0.11 2W Anitol Loop WINP Design/EDVFL(%), >= 85%, 93.22%, 531 NO B11712.12 0.11 2W Anitol Loop WINP Design/EDVFL(%), >= 85%, 93.22%, 531 NO B11712.12 0.11 2W Anitol Loop WINP D			>= 95%			100.00%					YES
B117 2 0-11 ISDN Loop (UDD) UDC) TAGFL(%) >= 95% 00 00 B117 71 0-11 Luns Sharhg/TAGFL(%) >= 95% 00 00 NO B117 72 0-11 Luns Sharhg/TAGFL(%) >= 95% 00 00 NO B117 10 0-11 ZW Analog Loop Design/TAGFL(%) >= 95% 00 00 NO B117 10 0-11 ZW Analog Loop Nor-Design/TAGFL(%) >= 95% 96 03% 866 YES B117 10 0-11 ZW Analog Loop Nor-Design/TAGFL(%) >= 95% 96 93 22% 531 NO B117 10 0-11 ZW Analog Loop WiNP Design/TAGFL(%) >= 95% = 95% =						98.46%	325				YES
B1177 1 O:11 Line Sharing/ED/FL(%) >= 95% NO B1177 2 O:11 Line Sharing/ED/FL(%) >= 95% 96 03% 856 YES B117 21 O:11 2W Analog Loop Design/ED/FL(%) >= 95% 96 03% 856 YES B117 91 O:11 2W Analog Loop Design/ED/FL(%) >= 95% 96 03% 856 YES B117 92 O:11 2W Analog Loop Non-Design/TAGFL(%) >= 95% 96%											
B117 2 O-11 Line Sharlog TAG/FL(%) >= 95% 76 NO B117 8 1 O-11 2W Analog Loop Design/EM/FL(%) >= 95% 96 03% 856 YES B117 8 1 O-11 2W Analog Loop Design/EM/FL(%) >= 95% 96 03% 856 YES B117 9 1 O-11 2W Analog Loop Nor-Design/EM/FL(%) >= 95% 96 03% 856 YES B117 9 1 O-11 2W Analog Loop WiNP Design/EM/FL(%) >= 95% 99% 93 22% 531 NO B117 10 1 O-11 2W Analog Loop WiNP Design/EM/FL(%) >= 95% 99% 93 22% 531 NO B117 11 2 O-11 2W Analog Loop WINP Design/EM/FL(%) >= 95% 95% 93 22% 531 NO B117 11 2 O-11 2W Analog Loop WINP Design/EM/FL(%) >= 95% 95% 93 22% 531 NO B117 12 0 O-11 2W Analog Loop WINP Design/EM/FL(%) >= 95% 95% 96 030% 74 YES B117 12 0 O-11 2W Analog Loop WINP Design/EM/FL(%) >= 95% 100 00% 74 YES B117 12 0						80 00%	10				NO
B 117 0 1 O-11 2W Analog Loop Design/ED/FL(%) >= 95% 96 03 03% 96 03% 96 03 03% 96 03 03% 96 03 03% 96 03 03% 96 03 03% 96 03 03% 96 03 03% 96 03 03 00% 96 03 03 00% 96 03 03 00% 96 03 03 00% 96 03 03 00% 96 03 03 00 00% 96 03 03 00 00% 96 03 03 00 00% 96 03 00 00% 96 03 00 00% 96 03											
B117 8.2 O-11 2W Analog Loop Design/TAG/FL(%) >= 95% 96 03% 856 YES B117 9 2 O-11 2W Analog Loop Non-Design/ED/FL(%) >= 95% 93 22% 531 NO B117 9 1 O-11 2W Analog Loop NiNP Design/TAG/FL(%) >= 95% 93 22% 531 NO B1 17 0 1 O-11 2W Analog Loop NiNP Design/TAG/FL(%) >= 95% = = = = = = = = 95% = </td <td></td>											
B 117 9 1 O 11 2W Analog Loop Non-Design/TAG/FL(%) >= 95% 00 0000 00 0000 00 0000 B 117 9 2 O 11 2W Analog Loop NNP Design/TAG/FL(%) >= 95% 99 32% 531 NO B 117 10 2 O 11 2W Analog Loop NNP Design/TAG/FL(%) >= 95% 00 00% 00 00% 00 B 117 10 2 O 11 2W Analog Loop NNP Design/TAG/FL(%) >= 95% 00 00% 74 B 117 11 2 O 11 2W Analog Loop NNP Non-Design/TAG/FL(%) >= 95% 00 00% 74 B 117 11 2 O 11 2W Analog Loop NNP Design/TAG/FL(%) >= 95% 00 00% 74 B 117 12 2 O 11 2W Analog Loop NNP Design/TAG/FL(%) >= 95% 00 00% 74 B 117 12 2 O 11 2W Analog Loop NLP Design/TAG/FL(%) >= 95% 100 00% 35 YES B 117 12 0 O 11 2W Analog Loop NLP Non-Design/TAG/FL(%) >= 95% 100 00% 141 YES B 117 12 0 O 11 2W Analog Loop NLP Non-Design/TAG/FL(%) >= 95% 58 82% 17 NO B 117 12 0 O 11 2W Analog Loop NLP Non-Design/TAG/FL(%) >= 95% 52 45%											
B11792 O:11 2W Analog Loop Non-Design/TAGFL(%) >= 95% 93 22% 531 NO B117010 O:11 2W Analog Loop wiNP Design/TAGFL(%) >= 95% =						96 03%	856				YES
B117 101 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B117 102 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B117.112 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B117.112 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Dosign/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B117.12 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B117.12 O-11 OHner Design/ED/FL(%) >= 95% B117.14 O-11 Ohner Design/ED/FL(%) >= 95% B117.15 O-11 Ohner Design/ED/FL(%) >= 95% B117.15 O-11 Ohner Design/ED/FL(%) >= 95% B117.16 O-11 INP Standianon/ED/FL(%) >= 95% B117.16 O-11											
B117 102 O-11 2W Analog Loop wNP Design/TAG/FL(%) >= 95% B117.111 O-11 2W Analog Loop wNP Non-Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 2W Analog Loop wLNP Design/TAG/FL(%) >= 95% B17.12 O-11 O-11 O-11 O-11 WHNP Non-Design/TAG/FL(%) >= 95% B17.17.12 O-11 O-11 O-11 O-11 O-11 O-11 O-11 WHNP Non-Design/TAG/FL(%) >= 95% 52.46% 6.900 NO B1.17.15 O-11 O-11 O-11 O-11 WHNP Non-Design/TAG/FL(%) >= 95%						93 22%	531				NO
B 117 111 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.121 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.121 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B 117.122 O-11 2W Analog Loop wINP Design/ED/FL(%) >= 95% B 117.121 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.122 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.122 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.122 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.122 O-11 2W Analog Loop wINP Non-Design/ED/FL(%) >= 95% B 117.12 O-11 Other Design/ED/FL(%) >= 95% 0000% B 117.14 O-11 Other Design/ED/FL(%) >= 95% 52.45% 52.45% 6,900 NO B 117.15 O-11 O-11 INP Standalone/ED/FL(%) >= 95% 52.45% 6.900 NO B 117.16 O-11 INP Standalone/ED/FL(%) >= 95% 95% 95% 95% 95% 95% 9											
B117.112 O-11 2W Analog Loop wiLNP Non-Design/TAG/FL(%) >= 95% B117.121 O-11 2W Analog Loop wiLNP Design/TAG/FL(%) >= 95% B117.122 O-11 2W Analog Loop wiLNP Design/ED/FL(%) >= 95% B117.121 O-11 2W Analog Loop wiLNP Non-Design/ED/FL(%) >= 95% B117.122 O-11 2W Analog Loop wiLNP Non-Design/ED/FL(%) >= 95% B117.123 O-11 2W Analog Loop wiLNP Non-Design/ED/FL(%) >= 95% B117.124 O-11 Other Design/ED/FL(%) >= 95% B1.17.142 O-11 Other Non-Design/ED/FL(%) >= 95% B1.17.151 O-11 Other Non-Design/ED/FL(%) >= 95% B1.17.152 O-11 Other Non-Design/ED/FL(%) >= 95% B1.17.151 O-11 Other Non-Design/ED/FL(%) >= 95% B1.17.151 O-11 INP Standalone/ED/FL(%) >= 95% B1.17.171 O-11 LNP Standalone/ED/FL(%) >= 95% B1.17.171 O-11 LNP Standalone/ED/FL(%) >= 95% B1.17.172 O-11 LNP Standalone/ED/FL(%) >= 95% B1.18.12 O-11 L											
B 117.12 1 O-11 2W Analog Loop wLNP Desigr/ED/FL(%) >= 95% 100.00% 74 YES B 117.12 0 O-11 2W Analog Loop wLNP Desigr/TAG/FL(%) >= 95% 100.00% 35 YES B 117.13 2 O-11 2W Analog Loop wLNP Non-Desigr/TAG/FL(%) >= 95% 100.00% 35 YES B.1 17.13 2 O-11 2W Analog Loop wLNP Non-Desigr/TAG/FL(%) >= 95% 100.00% 141 YES B.1 17.14 1 O-11 Other Desigr/TAG/FL(%) >= 95% 58 82% 17 NO B.1 17.15 1 O-11 Other Desigr/TAG/FL(%) >= 95% 52 46% 6,900 NO B.1 17.15 2 O-11 Other Non-Desigr/TAG/FL(%) >= 95% 52 46% 6,900 NO B.1 17.15 1 O-11 Other Non-Desigr/TAG/FL(%) >= 95% 78 56% 947 NO B.1 17.15 2 O-11 INP Standalone/TAG/FL(%) >= 95% 52 46% 6,900 NO B.1 17.16 2 O-11 INP Standalone/TAG/FL(%) >= 95% 95% 100 00% 1,411 YES B.1 17 17 1 O-11 LNP Standalon						+					
B 117 122 0-11 2W Analog Loop w/LNP Design/TAG/FL(%) >= 95% 100.00% 35 YES B 117.131 0-11 2W Analog Loop w/LNP Non-Design/ED/FL(%) >= 95% 100.00% 35 YES B.1 17.132 0-11 2W Analog Loop w/LNP Non-Design/ED/FL(%) >= 95% 100.00% 141 YES B.1 17.142 0-11 Other Design/ED/FL(%) >= 95% 58.82% 17 NO B.1.17.142 0-11 Other Design/ED/FL(%) >= 95% 58.82% 17 NO B.1.17.14 0-11 Other Non-Design/ED/FL(%) >= 95% 58.82% 17 NO B.1.17.15 0-11 Other Non-Design/ED/FL(%) >= 95% 52.44% 6,900 NO B.1.17.16 0-11 INP Standalone/TAG/FL(%) >= 95% 77.856% 947 NO B.1.17.16 0-111 INP Standalone/TAG/FL(%) >= 95% 95% 95% YES B.1.17.16 0-111 LNP Standalone/TAG/FL(%) >= 95% 100.00% 1,411 YES B.1.17.12 0-11 LNP Standalone/TAG/FL(%) >= 95% 100.00						100.009/	74				
B 117.131 O-11 2W Analog Loop w/LNP Non-Design/ED/FL(%) >= 95% 100 00% 141 YES B.1 17.132 O-11 Other Design/ED/FL(%) >= 95% 100 00% 141 YES B.1 17.141 O-11 Other Design/ED/FL(%) >= 95% 58 82% 17 NO B.1 17.142 O-11 Other Design/ED/FL(%) >= 95% 58 82% 17 NO B.1 17.15.1 O-11 Other Non-Design/ED/FL(%) >= 95% 52 46% 6,900 NO B.1 17.15.2 O-11 Other Non-Design/ED/FL(%) >= 95% 78 56% 947 NO B.1 17.16 1 O-11 INP Standalone/ED/FL(%) >= 95% 78 56% 947 NO B 117.16 2 O-11 INP Standalone/TAG/FL(%) >= 95% 78 56% 947 NO B 117.17 1 O-11 LNP Standalone/TAG/FL(%) >= 95% 20 00% YES B 117.17 2 O-11 LNP Standalone/TAG/FL(%) >= 95% 100 00% 1,411 YES B 118 11 O-11 Switch Poris/TAG/FL(%) >= 95% 100 000% 328 YES											
B.1 17.132 O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%) >= 95% 100 00% 141 YES B.1 17.141 O-11 Other Design/ED//FL(%) >= 95% 58 82% 17 NO B.1.17.142 O-11 Other Design/ED//FL(%) >= 95% 58 82% 17 NO B.1.17.151 O-11 Other Non-Design/ED//FL(%) >= 95% 52 46% 6,900 NO B.1.17.151 O-11 Other Non-Design/ED//FL(%) >= 95% 78 56% 947 NO B.1.17.152 O-11 Other Non-Design/ED//FL(%) >= 95% 58 82% 100 00% 1,411 YES B.1.17.161 O-11 INP Standalone/ED//FL(%) >= 95% 95% 100 00% 1,411 YES B.1.17.162 O-11 LNP Standalone/ED//FL(%) >= 95% 100 00% 1,411 YES B.1.17.17.2 O-11 LNP Standalone/ED//FL(%) >= 95% 100 00% 1,411 YES FOC & Reject Response Completeness (Multiple Response) - Partially Mechanized B1181.1 O-11 Switch Pots//ED//FL(%) >= 95% Se 95% S						100.00%				-	TES
B.1 17 14 1 O-11 Other Design/EDU/FL(%) >= 95% 58 82% 17 NO B.1.7.14 2 O-11 Other Design/TAG/FL(%) >= 95% 77 83% 230 NO B.1.7.15 1 O-11 Other Non-Design/TAG/FL(%) >= 95% 52 46% 6,300 NO B.1.7.15 1 O-11 Other Non-Design/TAG/FL(%) >= 95% 52 46% 6,300 NO B.1.7.16 1 O-11 IMP Standalone/EDU/FL(%) >= 95% 58 5% 0 0 B.1.7.17 62 O-11 IMP Standalone/TAG/FL(%) >= 95% 95% 0 0 B.1.7.17 2 O-11 IMP Standalone/TAG/FL(%) >= 95% 00 0% 1,411 YES FOC & Reject Response Completeness (Multiple Response) - Partially Mechanized >= 95% 100 00% 328 YES B118 11 O-11 Switch Pois/EDV/FL(%) >= 95% 0 0 0 0 0 0 0 1 11 YES YES 95% 0 0 0 0 0 0 0 0 0 0 <td< td=""><td></td><td></td><td></td><td></td><td></td><td>100.00%</td><td>141</td><td></td><td></td><td></td><td>VEO</td></td<>						100.00%	141				VEO
B.1.17.14 2 O-11 Other Design/TAG/FL(%) >= 95% 77.83% 230 NO B.1.7.15.1 O-11 Other Non-Design/ED/FL(%) >= 95% 52.46% 6,900 NO B.1.7.15.1 O-11 Other Non-Design/ED/FL(%) >= 95% 52.46% 6,900 NO B.1.7.16.1 O-11 IMP Standalone/ED/FL(%) >= 95% 78.56% 947 NO B.1.7.16.1 O-11 IMP Standalone/ED/FL(%) >= 95% 78.56% 947 NO B.1.7.16.2 O-11 IMP Standalone/TAG/FL(%) >= 95% 78.56% 947 NO B.1.7.16.2 O-11 IMP Standalone/TAG/FL(%) >= 95% 78.56% 947 NO B.1.7.17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% 100.00% 1,411 YES FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B.1.18.1 O-11 Switch Ports/TAG/FL(%) >= 95% 100.00% 328 YES B118.11 O-11 Switch Ports/TAG/FL(%) >= 95% 0 0 0 B 118.2 O-1	B.1 17 14 1										
B 1.17.15.1 O-11 Other Non-Design/TAG/FL(%) >= 95% 52 46% 6,900 NO B 1.17.15 1 O-11 Other Non-Design/TAG/FL(%) >= 95% 78 56% 947 NO B 1.17.16 1 O-11 INP Standalone/TAG/FL(%) >= 95% >= 95% NO B 1.17.16 2 O-11 INP Standalone/TAG/FL(%) >= 95% Image: Completeness (Multiple Responses) - Partially Mechanized B 1.17.17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% Image: Completeness (Multiple Responses) - Partially Mechanized FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 1.18 1.1 O-11 Switch Ports/FL(%) >= 95% Image: Completeness (Multiple Responses) - Partially Mechanized B 1.18 1.1 O-11 Switch Ports/FL(%) >= 95% Image: Completeness (Multiple Responses) - Partially Mechanized B 1.18 1.1 O-11 Switch Ports/FL(%) >= 95% Image: Completeness (Multiple Responses) - Partially Mechanized Image:											
B.1 17 152 O-11 Other Non-Design/TAG/FL(%) >= 95% 947 NO B 117.161 O-11 INP Standalone/ED/FL(%) >= 95% >= 95% B 117.162 O-11 INP Standalone/ED/FL(%) >= 95% >= 95% >= 95% >= 95% >= 95% >= 95% >= 95%	B 1.17.15.1										
B 117.161 O-11 INP Standalona/EDVFL(%) >= 95% INP B 117.162 O-11 INP Standalona/EDVFL(%) >= 95% INP B 117.17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% INP B 117.17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% INP B 117.17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% INP FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 118 11 O-11 Switch Ports/EDVFL(%) >= 95% INP B 118 11 O-11 Switch Ports/EDVFL(%) >= 95% INP B 118 12 O-11 Local Interofice Transport/EDVFL(%) >= 95% INP B 118 22 O-11 Local Interofice Transport/TAG/FL(%) >= 95% INP INP B 118 22 O-11 Local Interofice Transport/TAG/FL(%) >= 95% INP INP	B.1 17 15 2										
B 1 17 16 2 O-11 INP Standalone/TAG/FL(%) >= 95% 100 00% 1,411 YES B 1 17 17.1 O-11 LNP Standalone/TAG/FL(%) >= 95% 100 00% 328 YES FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 118 1.1 O-11 Switch Ports/ED/FL(%) >= 95% 100 00% 328 YES FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 118 1.1 O-11 Switch Ports/ED/FL(%) >= 95% Image: Colspan="2">Image: Colspan="2">Colspan="2">Image: Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2">Colspan="2"Colspan=	B 1 17.16 1										<u> </u>
B 1 17 17 1 O-11 LNP Standalone/EDVFL(%) >= 95% 100 00% 1,411 YES B 1.17 17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% 100 00% 328 YES FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 118 11 O-11 Switch Ports/ED/FL(%) >= 95%	B 1 17 16 2										1
B 1.17 17.2 O-11 LNP Standalone/TAG/FL(%) >= 95% 100 00% 328 YES FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B 118 11 O-11 Switch Pois/ED/FL(%) >= 95%	B 1 17 17 1					100.00%	1.411				YES
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized B11811 O-11 Switch Ports/EDVFL(%) >= 95%	B 1.17 17.2										
B 118 11 O-11 Switch Ports/ED//FL(%) >= 95% B 118 12 O-11 Switch Ports/TAG/FL(%) >= 95% B 118 21 O-11 Local Interofice Transport/ED//FL(%) >= 95% B 118 22 O-11 Local Interofice Transport/TAG//FL(%) >= 95%		FOC & Balant Basnonee Completenees (Multiple Basnonees) - Bertially Machanized	_								f
B 118 12 O-11 Switch Ports/TAG/FL(%) >= 95% B 118 21 O-11 Local Interoffice Transport/EDI/FL(%) >= 95% B.118 22 O-11 Local Interoffice Transport/TAG/FL(%) >= 95%	B 1 18 1 1										
B 1 18 2 1 O-11 Local Interoffice Transport/EDI/FL(%) >= 95% B.1 18 2 2 O-11 Local Interoffice Transport/TAG/FL(%) >= 95%											
B.118 2 0-11 Local Interoffice Transport/TAG/FL(%) >= 95%						├─ ──					
>= 3376 30.98% 255 NO						00.000/	055				
	0 1 10.3 1		J >= 90%			90,98%	255				NU

BellSouth Monthly State Summary Florida, October 2001

	Fionda, October 2001	Dencimatik /	BSI	BSI	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		-								
B 1.18 3 2	O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			92 60%	6,058				NO
B 1.18 4 1	O-11 Combo Other/EDI/FL(%)	>= 95%								
B 1 18 4 2	O-11 Combo Other/TAG/FL(%)	>= 95%								
B 1 18 5.1	O-11 xDSL (ADSL, HDSL and UCL)/EDI/FL(%)	>= 95%								
B 1 18.5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%)	>= 95%			100.00%	20				YES
B 1.1861	O-11 ISDN Loop (UDN, UDC)/EDI/FL(%)	>= 95%			100.00%	1				YES
B 1 18.6 2	O-11 ISDN Loop (UDN, UDC)/TAG/FL(%)	>= 95%			100.00%	4				YES
B 1 18 7.1	O-11 Une Sharing/EDI/FL(%)	>= 95%								
B.1 1872	O-11 Line Sharing/TAG/FL(%)	>= 95%			93.65%	63				NO
B.1 18 8.1	O-11 2W Analog Loop Design/EDI/FL(%)	>= 95%			92 39%	184				NO
B.1 1882	O-11 2W Analog Loop Design/TAG/FL(%)	>= 95%			90 23%	133				NO
B 1.18 9 1	O-11 2W Analog Loop Non-Design/EDI/FL(%)	>= 95%			50 23 /6	135				<u>NV</u>
B11892	O-11 2W Analog Loop Non-Design/TAG/FL(%)	>= 95%			93.74%	847				NÖ
B 1 18 10 1	O-11 2W Analog Loop w/INP Design/ED//FL(%)				93.7470	04/				NU
B 1 18 10 2	0-11 2W Analog Loop w/INP Design/TAG/FL(%)	>= 95%								
B.1 18 11 1	0-11 2W Analog Loop w/INP Non-Design/ED/FL(%)	>= 95%					-			
B.1.18 11 2		>= 95%								
	0-11 2W Analog Loop w/INP Non-Design/TAG/FL(%) 0-11 2W Analog Loop w/LNP Design/EDI/FL(%)	>= 95%								
B 1.18 12 1		>= 95%			93 81%	598				NO
B.1 18 12 2	O-11 2W Analog Loop w/LNP Design/TAG/FL(%)	>= 95%			94.81%	231				NO
B 1 18 13 1	O-11 2W Analog Loop w/LNP Non-Design/EDI/FL(%)	>= 95%			66 67%	3				NO
B.1 18.13 2	O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%)	>= 95%			96.45%	1,860				YES
B 1 18 14.1	O-11 Other Design/EDI/FL(%)	>= 95%			92.31%	26				NO
B 1 18 14 2	O-11 Other Design/TAG/FL(%)	>= 95%			80.87%	183				NO
B 1.18 15 1	O-11 Other Non-Design/ED/FL(%)	>= 95%			91.98%	1,958				NO
B.1.18 15 2	O-11 Other Non-Design/TAG/FL(%)	>= 95%			96 10%	538				YES
B 1 18 16 1	O-11 INP Standalone/EDI/FL(%)	>= 95%								
B 1 18 16 2	O-11 INP Standalone/TAG/FL(%)	>= 95%								
8118171	O-11 LNP Standalone/EDI/FL(%)	>= 95%			98 34%	785				YES
8118172	O-11 LNP Standalone/TAG/FL(%)	>= 95%			96.22%	344				YES
	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized									
B.1 19 1	0-11 Switch Ports/FL(%)	>= 95%			400.000					1/20
B.1.192	O-11 Local Interoffice Transport/FL(%)				100 00%	2				YES
B 1 19 3	0-11 Loop + Port Combinations/FL(%)	>= 95%			80.70%	57				NÖ
B 1 19 4	0-11 Combo Other/FL(%)	>= 95%			93.23%	812				NO
B 1.19 5	O-11 Combo Onen/FL(%) O-11 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%								
		>= 95%			95.81%	310				YES
B.1 19 6	0-11 ISDN Loop (UDN, UDC)/FL(%)	>= 95%			95.36%	603				YES
B 1 19 7	O-11 Line Sharing/FL(%)	>= 95%			95 77%	142				YES
B.1.198	O-11 2W Analog Loop Design/FL(%)	>= 95%			94.61%	204				ŇÖ
B 1 19.9	O-11 2W Analog Loop Non-Design/FL(%)	>= 95%			92 47%	1,275				NO
B 1 19 10	O-11 2W Analog Loop w/INP Design/FL(%)	>= 95%			83.33%	6				NŌ
B 1.19 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)	>= 95%			100.00%	13				YES
B 1 19 12	O-11 2W Analog Loop w/LNP Design/FL(%)	>= 95%			87 75%	302				NO
B 1 19 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%			86.89%	244				NÖ
B 1.19.14	O-11 Other Design/FL(%)	>= 95%			91 46%	410				NO
B 1 19.15	O-11 Other Non-Design/FL(%)	>= 95%			96 49%	1,681				YES
B 1 19 16	O-11 INP Standalone/FL(%)	>= 95%			95 45%	66				YES
B 1 19 17	O-11 [LNP Standalone/FL(%)	>= 95%			93 33%	990				NÖ
	Unburg flad Mahurada Flammanta - Decidatantan							_		
	Unbundled Network Elements - Provisioning									
	Order Completion Interval									
B 2 1 1.1.1	P-4 Switch Ports/<10 circuits/Dispatch/FL(days)	R&B (POTS)	379	90,814	· · · ·		5.885			
B2111.2	P-4 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0 93	736,295			2.204			I
B21121	P-4 Switch Ports/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	8.82					I		
B21122	P-4 Switch Ports/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	8.82	373			11.669		/	I
B21211	P-4 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)			7			7 537			
		DS1/DS3	16 60	2,445	21 63	8	19 367	6 85843	-0 7321	YES
B21212		DS1/DS3			· · · · · · ·					
821221	P-4 Local interoffice Transport/>=10 circuits/Dispatch/FL(days)	DS1/DS3			I					
B21222	P-4 Local interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	DS1/DS3								

Benchmark /

BST

BST

CLEC

CLEC

Standard Standard

	0,000		
B 2 1 1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL(days)	
32111.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL(days)	
321121	P-4	Switch Ports/>=10 circuits/Dispatch/FL(days)	
21122	P-4	Switch Ponts/>=10 circuits/Non-Dispatch/FL(days)	
21211	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	
21212	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	
21221	P-4	Local interoffice Transport/>=10 circuits/Dispatch/FL(days)	
21222	P-4	Local interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	
321311	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	

16 60	2,445	21 63	8	19 367	6 85843	-0 7321	Ŷ
7 62	7			7 537			
8.82	373			11.669			
0 93	736,295			2.204			
379	90,814			5.885			

DS1/DS3 R&B

		South Monthly State Summary						0	04		
	Florid	da, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard	ZScore	Caraller
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	Zacone	Equity
B213.12	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	R&B	0.94	737,944	0.62	7.697	2.209	0.02531	12 3526	YES
B21313	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(days)	BåB	0.33	452,732	0 34	5,710	0.387	0.00516	-0.5673	YES
B 2 1 3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL(days)	R&B	1 89	285,213	1 45	1,987	3.301	0.07431	5.9864	YES
B21321	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	R&B	9.15	424	6.67	7	11.740	4.47386	0.5557	YES
B 2 1.3.2 2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	R&B	2.78	93	3,17	2	4.629	3.30849	-0.1161	YES
B.2 1 3 2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(days)	R&B	0 40	19	0.33	1	0 2 1 1	0 21679	0.3253	YES
B 2 1 3.2 4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(days)	R&B	3.39	74	6.00	1	5.015	5.04827	-0.5166	YES
B 2 1 4.1.1	P-4	Combo Other/<10 circuits/Dispatch/FL(days)	R&B&D - Disp	4.84	94,587	16.23	22	11.253	2 39938	-4.7458	NÓ
B 2.1 4 1 4	P-4	Combo Other/<10 circuits/Dispatch In/FL(days)	R&B&D - Disp	4 84	94,587			11.253			
B21421	P-4	Combo Other/>=10 circuits/Dispatch/FL(days)	R&B&D - Disp	9.14	429			11.752			
B.2 1 4.2 4	P-4	Combo Other/>=10 circuits/Dispatch In/FL(days)	R&B&D - Disp	9,14	429			11 752			
B21531	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/FL(days)	ADSL to Retail	4 23	3,025	4.95	200	6.337	0.46266	-1.5562	YES
B 2.1 5.3.2	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	3.20	2,125			1.444			
B21541	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/FL(days)	ADSL to Retail	3 88	8	↓ ,		0.835			
B.2 1 5.4.2	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B 2 1.5 5 1	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/FL(days)	ADSL to Retail			 		ļ			
B 2 1 5.5 2	P-4	xDSL (ADSL, HDSL and UCL)/>=14 clrcuits/Non-Dispatch/FL(days)	ADSL to Retail ISDN - BRI	14.09	419	10.67	235	12.400	1.01057	3.3842	YES
B21631	P-4	UNE ISDN/<6 circuits/Dispatch/FL(days)	ISDN - BRI	3.00	501	10.07	230	4.889	1.01057	3.3042	163
B 2.1632	P-4 P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL(days)	ISDN - BRI	3.00	501			4.009			
B21641	P-4 P-4	UNE ISDN/6-13 circuits/Dispatch/FL(days)	ISDN - BRI	0 33	1			0.000			
B21642 B21651	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL(days) UNE ISDN/>=14 circuits/Dispatch/FL(days)	ISDN - BRI		1			0.000			
B21651	P-4	UNE ISDN/>=14 circuits/bispatch/FL(days)	ISDN - BRI			<u> </u>					
B21731	P-4	Line Sharing/<6 circuits/Dispatch/FL(days)	ADSL to Retail	4.23	3,025	1 00	1	6 337	6 33795	0 5096	YES
B21732	P-4	Line Sharing/c6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	3 20	2,125	3.50	14	1.444	0 38722	-0.7748	YES
B21741	P-4	Line Shanng/6-13 circuits/Dispatch/FL(days)	ADSL to Retail	3.88	8	1		0.835			
B21.742	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B21751	P-4	Line Sharing/>=14 circuits/Dispatch/FL(days)	ADSL to Retail								
B21752	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B21811	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	3.81	91,516	6.64	47	5 934	0.86579	-3.2666	NO
B21812	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	3 81	91,516			5 934			
B21821	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	9.15	424			11.740			
B21822	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	9.15	424			11 740			
B21911	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	3 79	90,813	4 83	103	5.885	0.58016	-1.7863	NO
B 2.1.9 1 4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	1.89	284,394	300	2	3.294	2 32886	-0.4765	YES
B21921	₽-4	2W Analog Loop Non-Design/>=10 circuits/Dispetch/FL(days)	R&B (POTS) excl SB Or	8 82	373	9 33	3	11.669	6 76402	-0 0765	YES
B21924	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch in/FL(days)	R&B (POTS) excl SB Or	8 83	6			7.468			
B211011	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	3 81	91,516	5.00	11	5 934	5 93403	-0.2005	YES
B211012	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	3.81	91,516 424			5.934 11.740			
B211021	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	9 15 9 15	424			11.740			/I
B211022	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	3.79	90,813		· · · · · · · · · · · · · · · · · · ·	5.885			I
B211111	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	1.89	284,394			3.294			
B211114	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	8 82	373	+		11.669			
B21112.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	8 83	6	<u>+</u>		7.468			
B 2 1 11.2.4	P-4 P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(days) 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	381	91,516	5 49	225	5 934	0 39609	-4.2412	NO
B.2.1.12.1.1 B 2 1 12 1 2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days) 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	381	91,516			5.934	0.00000	712712	
B211212 B211221	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days) 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	9 15	424	12.33	3	11.740	6.80214	-0.4673	YES
B 2.1.12 2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	9 15	424	12.00	v	11.740	0.00211	0.1070	
B 2 1 13 1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	3.79	90,813	4.94	266	5 885	0 36134	-3 1856	NO
B 2 1 13 1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	1 89	284,394			3 294			
B211321	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	8.82	373	673	11	11.669	3 56978	0 5843	YES
B211321	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	8.83	6	1		7 468			
B211411	P-4	Other Design/<10 circuits/Dispatch/FL(days)	Design	35.54	3,072	4.93	221	43 320	3.01704	10.1439	YES
B 2.1.14.1 2	P-4	Other Design/<10 circuits/Dispatch/EL(days)	Design	4 64	1,268			10 625			
B211421	P-4	Other Design/>=10 circuits/f0ir Dispatch/FL(days)	Design	7 93	5	7 00	1	14 095	15 43986	0 0604	YES
B2114.22	P-4	Other Design/>=10 circuits/Non-Dispatch/FL(days)	Design	3 54	50			5.241			
B211511	P-4	Other Non-Design/<10 circuits/Dispatch/FL(days)	R&B	3.81	91,516	4 29	445	5 934	0 28198	-1.6960	NO
B211512	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	R&B	0 94	737,944	2.03	11	2 209	0 66604	-1.6417	YES
B211521	P-4	Other Non-Design/>=10 circuits/Dispatch/FL(days)	R&B	9 15	424	0.33	1	11.740	11 75404	0.7505	YES
B211522	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	R&B	2 78	93			4 629			
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	Densouth monthly state summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
							•			
B211611	P-4 INP (Standaione)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	3.79	90,814			5 885			
B 2 1.16 1 2	P-4 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0.93	736,295			2 204			
B.2 1 16 2 1	P-4 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	8.82	373			11.669			
B 2.1.16 2 2	P-4 INP (Standalone)/>=10 circults/Non-Dispatch/FL(days)	R&B (POTS)	7.62	7		· · · · · ·	7.537		11176	1000
B.2.1 17 1.1	P-4 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	3.79	90,814	0.50	4	5.885	2.94236	1.1179	YES
B.2 1 17 1.2	P-4 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0.93	736,295	0.77	2,196	2 204	0.04710	3.4968	YES
B.2 1 17 2 1	P-4 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	8.82	373					1 05/0	
B211722	P-4 UNP (Standatone)/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	7.62		0 33	5	7.537	4 41304	1.6516	YES
B.2.1 18 1 1	P-4 Digital Loop < DS1/<10 circuits/Dispaich/FL(days)	Digital Loop < D\$1	7.25	4,207	8.06	430	9.902	0.50134	-1.6173	YES
B.2.1 18 1 2	P-4 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1	4 25	3,371			4.671			
B 2.1 18 2 1	P-4 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Digital Loop < DS1	3.88	8						
B 2 1.18.2.2	P-4 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1	0 33	647	6.04	132	0.000	5,75336	11.4038	YES
B.2 1.19 1 1	P-4 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days) P-4 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop >≖ DS1			6.94	132	60.241	5.75336	11.4038	TES
B 2 1.19 1 2		Digital Loop >= DS1	240	1,096			7.064			
B211921	P-4 Digital Loop >= D\$1/>=10 circuits/Dispatch/FL(days) P-4 Digital Loop >= D\$1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop >= DS1 Digital Loop >= DS1	1.67	4			1.744			
B.2 1 19.2 2	P-4 Digital Loop >- DS1/>=10 circuits/Non-Dispatch/FL(days)	Ugital Loop >= DS1	3.54	<u> </u>	L		0.241	l	L	
	Order Completion Interval within X days									
B.2 2 1	P-4 [xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL(days)	14 days			13.00	1				YËS
B.2 2 2	P-4 xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL(days)	7 days			4 91	199				YES
	Held Orders		-							
B.2.3 1 1 1	P-1 Switch Ports/<10 circuits/Facility/FL(days)	R&B (POTS)	9.43	502	· · · · · ·		12.998	r		
B 2 3 1.1.2	P-1 Switch Ports/<10 circuits/Facing/r c(days)	R&B (POTS)	3.00	1	I		0.000			
B23113	P-1 Switch Ports/<10 circuits/Cither/FL(days)	R&B (POTS)	18.43	49			20 696	t		
8.23121	P-1 Switch Ports/>=10 circuits/Facility/FL(days)	R&B (POTS)	5 00	3			4.359			
B23122	P-1 Switch Ports/>=10 circuits/Equipment/FL(days)	R&B (POTS)	000	ő			4.000			
B.2.3 1.2.3	P-1 Switch Ports/>=10 circuits/Other/FL(days)	R&B (POTS)	9.00	1			0.000			
B.2 3 2.1 1	P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days)	DS1/DS3 - Interoffice	14.75	4	0.00	0	11.087			YES
B23212	P-1 Local Interoffice Transport/<10 circuits/Equipment/FL(days)	DS1/DS3 - Interoffice	0.00	0	0.00		11.00/			YES
B232.13	P-1 Local Interoffice Transport/<10 circuits/Other/FL(days)	DS1/DS3 - Interoffice	13 36	14	0.00	0	10,419			YES
B23221	P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days)	DS1/DS3 - Interoffice					1			
B23222	P-1 Local Interoffice Transport/>=10 circuits/Equipment/FL(days)	DS1/DS3 - Interoffice					1			
B 2 3.2 2.3	P-1 Local Interoffice Transport/>=10 circuits/Other/FL(days)	DS1/DS3 - Interoffice					1			
B23311	P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days)	R&B	9 42	510	200	1	12.926	12 93877	0 5733	YES
B23312	P-1 Loop + Port Combinations/<10 circuits/Equipment/FL(days)	RAB	3.00	1	0.00	0	0 000			YES
B.23313	P-1 Loop + Port Combinations/<10 circuits/Other/FL(days)	R&B	18 43	49	2 00	2	20.696	14 93010	1.1004	YES
B23321	P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days)	B&B	5.00	3	0.00	0	4.359			YES
B23322	P-1 Loop + Port Combinations/>=10 circuits/Equipment/FL(days)	R&B	0.00	0	0.00	0	1			YES
B23323	P-1 Loop + Port Combinations/>=10 circuits/Other/FL(days)	R&B	9 00	1	0.00	0	0 000			YES
B23411	P-1 Combo Other/<10 circuits/Facility/FL(days)	R&B&D - Disp	9.42	513	0.00	0	12.891			YES
B23412	P-1 Combo Other/<10 circuits/Equipment/FL(days)	R&B&D - Disp	3.00	1	0 00	0	0 000			YES
82341.3	P-1 Combo Other/<10 circuits/Other/FL(days)	R&B&D - Disp	18 09	55	0.00	0	19.932			YES
B23421	P-1 Combo Other/>=10 circuits/Facility/FL(days)	B&B&D - Disp	6.25	4			3.803			
B23422	P-1 Combo Other/>=10 circuits/Equipment/FL(days)	R&B&D Disp	0.00	0						
B23423	P-1 Combo Other/>=10 circuits/Other/FL(days)	R&B&D - Disp	4 33	3			3.343			
B23511	P-1 xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL(days)	ADSL to Retail	37.91	437	15.00	1	45.521	45 57305	0.5027	YES
B 2 3 5.1.2	P-1 xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL(days)	ADSL to Retail	0.00	0	0.00	0				YES
B23513	P-1 xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL(days)	ADSL to Retail	17 78	9	7,00	1	26 532	27 96718	0.3854	YES
B 2 3 5.2 1	P-1 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL(days)	ADSL to Retail	0.00	n						
B23522	P-1 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL(days)	ADSL to Retail	0.00	0						
B23523	P-1 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL(days)	ADSL to Retail	0.00	0			-			
B 2.3 6 1.1	P-1 UNE ISDN/<10 circuits/Facility/FL(days)	ISDN - BRI	10.00	2	7 00	1	5.657	6 92826	0.4330	YES
B 2 3.6.1.2	P-1 UNE ISDN/<10 circults/Equipment/FL(days)	ISDN - BRI	0.00	0	0.00	0				YES
B 2 3 6.1 3	P-1 UNE ISDN/<10 circuits/Other/FL(days)	ISDN - BRI	9.00	1	0 00	0	0.000			YÉS
B23621	P-1 UNE ISDN/>=10 circuits/Facility/FL(days)	ISDN - BRI								
B23622	P-1 UNE ISDN/>=10 circuits/Equipment/FL(days)	ISDN - BRI								
B23623	P-1 UNE ISDN/>=10 circuits/Other/FL(days)	ISDN - BRI					1			
B23711	P-1 Line Sharing/<10 circuits/Facility/FL(days)	ADSL to Retail	37 91	437	0 00	0	45.521			YES
B 2 3.7 1 2	P-1 Line Sharing/<10 circuits/Equipment/FL(days)	ADSL to Retail	0 00	0	0.00	0				YES
B23713	P-1 Line Sharing/<10 circuits/Other/FL(days)	ADSL to Retail	17 78	9	0 00	0	26 532			YES
			-							

	BeilSouth Monthly State Summary Florida, October 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B23721	P-1	Line Sharing/>=10 circuits/Facility/FL(days)	ADSL to Retail	0.00	0			T			
B23722	P-1	Line Sharing/>=10 circuits/Equipment/FL(days)	ADSL to Retail	0.00	0						
B23723	P-1	Line Sharing/>=10 circuits/Other/FL(days)	ADSL to Retail	0.00	0						
8.23811	P-1	2W Analog Loop Design/<10 circuits/Facility/FL(days)	R&B - Disp	9.42	510	0.00	0	12.926			YES
B 2 3.8.1 2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL(days)	R&B - Disp	3.00	1	0.00	0	0.000			YES
8 2 3.8 1.3	P-1	2W Analog Loop Design/<10 circuits/Other/FL(days)	R&B - Disp	18.43	49	0.00	0	20.757			YES
B.2.3.8.2 1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL(days)	R&B - Disp	5.00	3			4.359			L
B.2 3 8 2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL(days)	R&B - Disp	0.00	0						
B 2 3 8.2 3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL(days)	R&B - Disp	9 00	1			0.000			L
B23911	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	943	502	0.00	0	12.998			YES
B.2 3 9 1 2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	3 00	1	0.00	0	0.000			YES
B23913	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	18.43	49	0.00	0	20.696			YES
B23921	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	5.00	3	0.00	0	4.359			YES
B 2 3 9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES YES
B23923	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	9.00	1	0.00	0	0.000			YES
B 2 3 10 1 1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL(days)	R&B - Disp	9.42	510	0.00	0	12.926			YES
B 2 3 10 1 2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL(days)	R&B - Disp	3.00	1 49	0.00	0	0.000 20.757			YES
B 2 3 10 1 3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL(days)	R&B - Disp R&B - Disp	18.43	49		U	4.359			153
B 2 3 10 2 1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL(days)	R&B - Disp	0.00	0			4.305			
B231022	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL(days)	R&B - Disp	9.00	1			0.000			
B231023	P-1 P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL(days) 2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.00	502	0.00	0	12,998			YES
B231111	P-1	2W Analog Loop w/NP Non-Design/<10 circuits/Facility/F2(days) 2W Analog Loop w/NP Non-Design/<10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	3.00	1	0.00	0	0 000			YES
B231112 B231113	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	18.43	49	0.00	0	20.696			YES
B231133	P-1	2W Analog Loop w/NP Non-Design/<10 circuits/Grien/Fc(days)	R&B (POTS) excl SB Or	5 00	3	0.00	0	4 359			YES
B231121	P-1	2W Analog Loop w/NP Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	0 00	0	0.00	0				YES
8231123	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	9 00	1	0.00	0	0.000			YES
B231211	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/FL(days)	R&B - Disp	9 42	510	8.23	13	12 926	3 63046	0.3269	YES
B231212	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL(days)	R&B Disp	3.00	1	0.00	0	0.000			YES
B 2 3 12.1.3	P 1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL(days)	R&B Disp	18.43	49	0.00	0	20 757			YES
B231221	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL(days)	R&B - Disp	5 00	3	29.75	4	4.359	3.32916	-7.4343	NÓ
B 2 3 12 2 2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL(days)	R&B - Disp	0.00	0	0.00	0				YES
B 2 3 12 2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/FL(days)	R&B - Disp	9 00	1	0.00	0	0.000			YES
B 2 3 13 1 1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.43	502	0.00	0	12.998			YES
8231312	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	3.00	1	0.00	0	0 000			YES
B 2 3 13 1 3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	18 43	49	0.00	0	20.696			YES
B 2 3 13 2 1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	5 00	3	0.00	0	4 359			YES
B 2 3 13.2 2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B 2 3 13 2 3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	9 00	1	0.00	0	0 000			YES
B 2 3 14 1 1	P-1	Other Design/<10 circuits/Facility/FL(days)	Design	10.00	3	3 00	1	4.000	4.61880	1.5155	YES YES
B 2 3 14 1 2	P-1	Other Design/<10 circuits/Equipment/FL(days)	Design	0.00	0	0.00	0	10 000	ļ		YES
B 2.3.14 1 3	P-1	Other Design/<10 circuits/Other/FL(days)	Design	13.29	7	0.00	0	18.883			YES
B 2 3.14 2 1	P-1	Other Design/>=10 circuits/Facility/FL(days)	Design	10.00	1	0.00	0	0.000			YES
B 2 3 14 2 2	P-1	Other Design/>=10 circuits/Equipment/FL(days)	Design	0.00	Ö	0.00	0	0.000			YES
B 2 3 14 2 3	P-1	Other Design/>=10 circuits/Other/FL(days)	Design	2.00 9.42	2 510	0.00	3	12.926	7.48480	0.4121	YES
B.2 3 15 1.1	P-1	Other Non-Design/<10 circuits/Facility/FL(days)	R&B	3.00	1	6 33 0.00	0	0.000	7.40400	0.4121	YES
B 2 3 15 1 2	P-1	Other Non-Design/<10 circuits/Equipment/FL(days)	R&B R&B	18.43	49	0.00	0	20.696		<u>}</u>	YES
B 2 3 15 1 3	P.1	Other Non-Design/<10 circuits/Other/FL(days)	R&B	5 00		0.00	0	4.359			YES
B.2.3 15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/FL(days)	R&B	0.00	0	0.00	0	4.359			YES
B.2 3.15 2.2	P-1	Other Non-Design/>=10 circuits/Equipment/FL(days)	R&B	9.00	1	0.00	0	0.000	<u> </u>		YES
B 2 3 15 2.3	P-1	Other Non-Design/>=10 circuits/Other/FL/days)	R&B (POTS)	9 43	502	0.00		12 998			
B231611	P-1 P-1	INP (Standalone)/<10 circults/Facility/FL(days) INP (Standalone)/<10 circults/Equipment/FL(days)	R&B (POTS)	3.00	1	<u> </u>		0.000		<u> </u>	t
B 2 3 16 1 2 B 2 3 16.1 3	P-1	INP (Standalone)/<10 circuits/Equipment/FL(days)	R&B (POTS)	18.43	49	+		20.696		<u> </u>	L
B 2 3 16.1 3 B 2 3 16 2 1	P-1 P-1	INP (Standalone)/=10 circuits/Cine)/FL(days)	R&B (POTS)	5 00	3	+		4.359	1		1
B 2 3 16 2 1 B 2 3 16 2 2	P-1 P-1	INP (Standalone)/>=10 circuits/Equipment/FL(days)	R&B (POTS)	0 00	6	t		1	1		·
B231622	P-1	INP (Standalone)/>=10 circuits/Qther/FL(days)	R&B (POTS)	9 00	1	1		0.000	1		
B231711	P-1	INP (Standalone)/<10 circuits/Facility/FL(days)	R&B (POTS)	9 43	502	0.00	0	12.998	1		YES
B231712	P-1	LNP (Standalone)/<10 circuits/Equipment/FL(days)	R&B (POTS)	3 00	1	0.00	ő	0 000	I	· · · · · ·	YES
	P-1	LNP (Standalone)/<10 circuits/Other/FL(days)	R&B (POTS)	18 43	49	0.00	0	20.696	1		YES
B.2 3 17 1 3											

	Rell	South Monthly State Summary									
	Flori	da. October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
			-								
B.2 3 17 2 2	P-1	LNP (Standalone)/>=10 circuits/Equipment/FL(days)	R&B (POTS)	0.00	00	0.00	0				YES
B 2 3 17 2 3	P-1	LNP (Standalone)/>=10 circuits/Other/FL(days)	R&B (POTS)	9.00	1	0 00	0	0.000			YES
B 2 3 18 1 1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL(days)	Digital Loop < DS1	37.78	439	11.00	2	45 457	32.21588	0.8314	YES
B 2 3 18 1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL(days)	Digital Loop < DS1	0.00	0	0.00	0	05 100			YES
B 2 3 18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/FL(days)	Digital Loop < DS1	16.90	10	0.00	0	25.168			YES
B 2 3 18 2 1	P-1	Digital Loop < DS1/>=10 circuits/Facility/FL(days)	Digital Loop < DS1	0.00	0						
B 2 3 18 2.2	P-1 P-1	Digital Loop < DS1/>=10 circults/Equipment/FL(days)	Digital Loop < DS1	0.00	0						
B.2.3 18 2 3	P-1	Digital Loop < DS1/>=10 circults/Other/FL(days)	Digital Loop < DS1	0.00		14.00		19.799	24.24872	0.4124	YES
B 2 3.19 1 1	P-1	Digital Loop >= D\$1/<10 circuits/Facility/FL(days) Digital Loop >= D\$1/<10 circuits/Equipment/FL(days)	Digital Loop >= DS1 Digital Loop >= DS1	0.00	2	0.00	0	19.799	24.248/2	0.4124	YES
B 2 3 19 1 2 B 2 3 19 1 3	P-1	Digital Loop >= D\$1/<10 circuits/Ciher/FL(days)	Digital Loop >= DS1 Digital Loop >= DS1	0.00	0	0.00	0				YES
82319.21	P-1	Digital Loop >= DS1/>=10 circuits/Facility/FL(days)	Digital Loop >= DS1 Digital Loop >= DS1	0.00		0.00		 			160
B231922	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/FL(days)	Digital Loop >= DS1	0.00			·····		l		
B 2 3 19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Edupment/FE(days)	Digital Loop >= DS1	2.00	2			0 000			
02.0 /02.0				2.00				0.000	L		
		ardies - Mechanized									
B251	P-2	Switch Ports/FL(%)	R&B (POTS)	0.45%	901,570						
B 2.5 2	P-2	Local Interoffice Transport/FL(%)	DS1/DS3 - Interoffice	37.17%	2,661	0.00%	3		0.27916	1.3314	YES
B253	P-2	Loop + Port Combinations/FL(%)	R&B	0.46%	904,182	0.17%	7,568		0.00078	3.7083	YES
B254	P-2	Combo Other/FL(%) xDSL (ADSL, HDSL and UCL)/FL(%)	R&B&D - Disp	4.04%	112,374	12.50%	8 207		0.06962	-1.2150	YES
B 2 5.5 B.2 5 6	P-2 P-2	UNE ISDN/FL(%)	ADSL to Retail ISDN - BRI	14.07% 9.64%	19,061 996	2 42% 7.35%	68		0.02430	4.7968	YES
B.250 B.257	P-2	Une Sharing/FL(%)	ADSL to Retail	9.04%	19,061	0.00%	11		0.10487	1.3417	YES
B258	P-2	2W Analog Loop Design/FL(%)	R&B - Disp	0.46%	904,182	20.45%	44		0.010487	-19 5538	NO
8259	P-2	2W Analog Loop Non-Design/FL(%)	R&B (POTS) excl SB Or	0.91%	448,947	6.25%	64		0.01022	-4.4958	NÖ
B2510	P-2	2W Analog Loop w/INP Design/FL(%)	R&B - Disp	0.46%	904,182	0.00%	4		0 03391	0.1363	YES
B2511	P-2	2W Analog Loop w/INP Non-Design/FL(%)	R&B (POTS) excl SB Or	0.91%	448.947	0.00%	5		0.04249	0.2144	YES
B 2 5 12	P-2	2W Analog Loop w/LNP Design/FL(%)	R&B - Disp	0.46%	904,182	0.09%	4,396		0.00103	3.6287	YES
B 2 5 13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)	R&B (POTS) excl SB Or	0.91%	448,947	0.31%	10,555		0 00094	6.4222	YES
B 2 5 14	P-2	Other Design/FL(%)	Design	6.80%	5,364	8.95%	313		0 01464	-1.4621	YES
B 2 5 15	P-2	Other Non-Design/FL(%)	R&B	0.46%	904,182	4.17%	288		0.00400	-9.2687	NO
B.2 5 16	P-2	INP (Standalone)/FL(%)	R&B (POTS)	0.45%	901,570			-			
B 2 5 17	P-2	LNP (Standalone)/FL(%)	R&B (POTS)	0.45%	901,570	0.00%	2,692		0.00130	3.4974	YES
B 2 5 18	P-2	Digital Loop < DS1/FL(%)	Digital Loop < DS1	13 65%	21,727	3.58%	279		0.02068	4.8650	YES
B.2 5 19	P-2	Digital Loop >= DS1/FL(%)	Digital Loop >= DS1	4 35%	2,090	47.52%	101		0 02079	-20.7645	NO
	% Jeon	ardies - Non-Mechanized									
B261	P-2	Switch Ports/FL(%)	Diagnostic								Diagnostic
B262	P-2	Local Interoffice Transport/FL(%)	Diagnostic			0.00%	11				Diagnostic
B263	P-2	Loop + Port Combinations/FL(%)	Diagnostic			0.70%	285				Diagnostic
B264	P-2	Combo Other/FL(%)	Diagnostic			30.00%	40	1			Diagnostic
B265	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			6.03%	116				Diagnostic
B266	P-2	UNE ISDN/FL(%)	Diagnostic			16.06%	249				Diagnostic
B 2 6.7	P-2	Line Shanng/FL(%)	Diagnostic			0.00%	14				Diagnostic
B269	P-2	2W Analog Loop Design/FL(%)	Diagnostic			6 67%	15				Diagnostic
B269	P-2	2W Analog Loop Non-Design/FL(%)	Diagnostic			0 00%	44				Diagnostic
B 2 6 10	P-2	2W Analog Loop w/INP Design/FL(%)	Diagnostic			16.67%	6				Diagnostic
B 2 6 11	P-2	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			50.00%	2				Diagnostic
B 2.6 12	P-2	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			17 57%	2,299				Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			8 38%	2,065				Diagnostic
B.2 6 14	P-2	Other Design/FL(%)	Diagnostic			13.33%	30				Diagnostic
B 2 6 15	P-2	Other Non-Design/FL(%)	Diagnostic			7 50%	120				Diagnostic
B 2 6 16	P-2	INP (Standalone)/FL(%)	Diagnostic								Diagnostic
B 2 6 17	P-2	LNP (Standalone)/FL(%)	Diagnostic			0.00%	1,129				Diagnostic
B 2 6 18	P-2	Digital Loop < DS1/FL(%)	Diagnostic			12.53%	359				Diagnostic
B 2 6 19	P-2	Digital Loop >= DS1/FL(%)	Diagnostic			27.14%	140				Diagnostic
B281	P-2	Switch Ports/FL(hours)	>= 48 hrs								
B282	P-2	Local Interoffice Transport/FL(hours)	>= 48 hrs								
B283	P-2	Loop + Port Combinations/FL(hours)	>= 48 hrs			193.85	13				YES
B284	P-2	Combo Other/FL(hours)	>= 48 hrs			504 00	1				YES
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		ida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard
			Analog	Measure	Volume	Measure	Volume	Deviation	Error
			>= 48 hrs			148.80	5		
B285	P-2 P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)	>= 48 hrs			307.20	5	i de la companya de l	
B286	P-2 P-2	UNE ISDN/FL(hours)	>= 48 hrs			007.20	<u>v</u>		
B 2 8.7 B 2 8 8	P-2	2W Analog Loop Dasigr/FL(hours)	>= 48 hrs			506.67	9		
B.288	P-2	2W Analog Loop Design/FL(hours)	>= 48 hrs			132 00	4		
B 2 8.10	P-2	2W Analog Loop W/INP Design/FL(hours)	>= 48 hrs					1	
B28.11	P-2	2W Analog Loop w/INP Non-Design/FL(hours)	>= 48 hrs						
B.2.8.12	P 2	2W Analog Loop w/LNP Design/FL(hours)	>= 48 hrs			359 22	4		
B 2 8 13	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)	>= 48 hrs			134.41	33		
B2814	P-2	Other Design/FL(hours)	>= 48 hrs			141.43	28		
B 2 8 15	P-2	Other Non-Design/FL(hours)	>= 48 hrs			126 00	12		
B 2 8.16	P-2	INP (Standalone)/FL(hours)	>= 48 hrs						
B 2.8 17	P-2	LNP (Standalone)/FL(hours)	>= 48 hrs						
B 2 8 18	P-2	Digital Loop < DS1/FL(hours)	>= 48 hrs			228 00	10		
B 2.8 19	P-2	Digital Loop >= DS1/FL(hours)	>= 48 hrs			365 50	48		
	Avera	ge Jeopardy Notice Interval - Non-Mechanized							
B 2.9.1	P-2	Switch Ports/FL(hours)	Diagnostic						
B292	P-2	Local Interoffice Transport/FL(hours)	Diagnostic						
B 2.9 3	P-2	Loop + Port Combinations/FL(hours)	Diagnostic			528.00	2	-	
B294	P-2	Combo Other/FL(hours)	Diagnostic			388.00	12	-	
B295	P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)	Diagnostic			137.14	7	-	
B 2.9.6	P-2	UNE ISDN/FL(hours)	Diagnostic Diagnostic			232 20	40	-	
B297	P-2 P-2	Une Sharing/FL(hours)	Diagnostic			120.00	1		
B298	P-2 P-2	2W Analog Loop Design/FL(hours) 2W Analog Loop Non-Design/FL(hours)	Diagnostic			120.00	· · · · · · · · · · · · · · · · · · ·	-	
B 2 9 9 B 2 9 10	P-2 P-2	2W Analog Loop w/INP Design/FL(hours)	Diagnostic			816.00	1	1	
B2910	P-2	2W Analog Loop w/INP Design/FL(hours)	Diagnostic			144.00	1	1	
B2912	P-2	2W Analog Loop w/LNP Design/FL(hours)	Diagnostic			42.01	404	1	
B2913	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)	Diagnostic			64.16	173	1	
B2914	P-2	Other Design/FL(hours)	Diagnostic			258.00	4	1	
B 2 9.15	P-2	Other Non-Design/FL(hours)	Diagnostic			216.00	9	1	
B 2 9 16	P-2	INP (Standalone)/FL(hours)	Diagnostic						
B 2 9 17	P-2	LNP (Standalone)/FL(hours)	Diagnostic						
B 2 9 18	P-2	Digital Loop < DS1/FL(hours)	Diagnostic			221 87	45		
B.2.9 19	P-2	Digital Loop >= DS1/FL(hours)	Diagnostic			227.37	38		
	% Jec	pardy Notice >= 48 hours - Mechanized							
B 2 10 1	P-2	Switch Ports/FL(%)	95% >= 48 hrs						
B 2.10 2	P-2	Local Interoffice Transport/FL(%)	95% >= 48 hrs						
B 2.10 3	P-2	Loop + Port Combinations/FL(%)	95% >= 48 hrs			100 00%	13		
B 2 10 4	P-2	Combo Other/FL(%)	95% >= 48 hrs			100.00%	1	_	
B 2 10 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)	95% >= 48 hrs			100 00%	5	-	
B 2 10 6	P-2	UNE ISDN/FL(%)	95% >= 48 hrs			100 00%	5		
B 2 10 7	P-2	Line Shanng/FL(%)	95% >= 48 hrs 95% >= 48 hrs			100.00%	9	-	
B 2 10 8	P-2	2W Anatog Loop Design/FL(%)	95% >= 48 hrs 95% >= 48 hrs			100.00%			
B 2 10 9	P-2 P-2	2W Analog Loop Non-Design/FL(%) 2W Analog Loop w/INP Design/FL(%)	95% >= 48 hrs			100 00 /8	·	*	
B 2 10 10 B 2 10 11	P-2	2W Analog Loop w/INP Non-Design/FL(%)	95% >= 48 hrs						
B 2 10 12	P-2	2W Analog Loop w/LNP Design/FL(%)	95% >= 48 hrs			100.00%	4	-	
B.2 10.13	P-2	2W Analog Loop w/LIVE Design/FL(%)	95% >= 48 hrs			81.82%	33		
B 2 10 14	P-2	Other Design/FL(%)	95% >= 48 hrs			100 00%	28		
B 2 10 15	P-2	Other Non-Design/FL(%)	95% >= 48 hrs			100.00%	12		
B 2 10 16	P-2	INP (Standalone)/FL(%)	95% >= 48 hrs						
B 2 10 17	P-2	LNP (Standalone)/FL(%)	95% >= 48 hrs						
B2 10 18	P-2	Digital Loop < DS1/FL(%)	95% >= 48 hrs			100 00%	10		
B 2 10 19	P-2	Digital Loop >= DS1/FL(%)	95% >= 48 hrs			100 00%	48		
B2111	P-2	Switch Ports/FL(%)	Diagnostic				[
B2112	P-2	Local Interoffice Transport/FL(%)	Diagnostic						
B2113	P-2	Loop + Port Combinations/FL(%)	Diagnostic			100.00%			
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Florida.	October	2001
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	BellSouth Monthly State Summary									
	Florida. October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		Anteroy	medeuro	· oranic	mousere					
B 2.11 4	P-2 Combo Other/FL(%)	Diagnostic			100.00%	12				Diagnostic
8211.5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			100.00%	7				Diagnostic
B.2 11 6	P-2 UNE ISDN/FL(%)	Diagnostic			100.00%	40				Diagnostic
B2117	P-2 Line Sharing/FL(%)	Diagnostic								Diagnostic
B2118	P-2 2W Analog Loop Design/FL(%)	Diagnostic			100.00%	1				Diagnostic
B.2.11 9	P-2 2W Analog Loop Non-Design/FL(%)	Diagnostic								Diagnostic
B.2.11.10	P-2 2W Analog Loop w/INP Design/FL(%)	Diagnostic			100.00%	1				Diagnostic
B 2 11.11	P-2 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			100.00%	i				Diagnostic
B2 11.12	P-2 2W Analog Loop w/LNP Design/FL(%)	Diagnostic			16.34%	404				Diagnostic
B.2.11.12 B.2.11.13	P-2 2W Analog Loop w/LIVP Design/FL(%)	Diagnostic			30.06%	173				Diagnostic
	P-2 Other Design/FL(%)				100 00%	4				Diagnostic
B 2.11.14		Diagnostic				9				
B 2 11.15		Diagnostic			100.00%	9				Diagnostic
B 2 11 16	P-2 INP (Standalone)/FL(%)	Diagnostic								Diagnostic
B211.17	P-2 LNP (Standalone)/FL(%)	Diagnostic								Diagnostic
B 2.11.18	P-2 Digital Loop < DS1/FL(%)	Diagnostic			100.00%	45				Diagnostic
B 2 11 19	P-2 Digital Loop >= DS1/FL(%)	Diagnostic			100.00%	38				Diagnostic
	Coordinated Customers Conversions									
B 2.12 1	P-7 Loops with INP/FL(%)	>= 95% w in 15 min					1			
B 2.12.2	P-7 Loops with LNP/FL(%)	>= 95% w in 15 min			99.32%	7,357				YES
02.12.2		>= 30% WIN 10 MM			33.0270	1,001)			100
	% Hot Cute > 15 minutes Early									-
B 2 13 1	P-7A Time-Specific SL1/FL(%)	<= 5%			0.14%	1,396				YES
B 2.13 2	P-7A Time-Specific SL2/FL(%)	<= 5%			0.00%	693	1			YES
B 2.13 3	P-7A Non-Time Specific SL1/FL(%)	<= 5%			0.00%	7				YES
B 2 13 4	P-7A Non-Time Specific SL2/FL(%)	<= 5%			0.00%	380				YES
					•					
		· · · · ·								
B 2.14 1	P-7A Time-Specific SL1/FL(%)	>= 95% w in 15 min			99 00%	1,396				YES
B 2 14 2	P-7A Time-Specific SL2/FL(%)	>= 95% w in 15 min			99.28%	693				YES
B 2 14 3	P-7A Non-Time Specific SL1/FL(%)	>= 95% w in 15 min			100.00%	7				YES
B 2 14 4	P-7A Non-Time Specific SL2/FL(%)	>= 95% w in 15 min			100.00%	380				YES
	% Hot Cuts > 15 minutes Late									
B 2,15 1	P-7A Time-Specific SL1/FL(%)	<= 5%			0.86%	1,396				YES
B 2 15.2	P-7A Time-Specific SL2/FL(%)	<= 5%			0.72%	693				YES
B 2 15.2	P-7A Non-Time Specific SL1/FL(%)	<= 5%			0.00%	7				YES
B 2 15.3	P-7A Non-Time Specific SL2/FL(%)	<= 5%			0.00%	380				YES
02104	P-7A Nor-Time Specific Score(%)	< <u>-</u> 5%			0.0076					
	Average Recovery Time - CCC									
82161	P-78 Loops with INP/FL(minutes)	Diagnostic								Diagnostic
B 2 16 2	P-7B Loops with LNP/FL(minutes)	Diagnostic			449.33	16				Diagnostic
							· · · ·			
_	% Provisioning Troubles within 7 Days - Hot Cuts									
B.2.17 1.1	P-7C UNE Loop Design/Dispatch/FL(%)	<= 5%			2.15%	2,097				YES
B 2 17 1 2	P-7C UNE Loop Design/Non-Dispatch/FL(%)	<= 5%								
B 2 17 2 1	P-7C UNE Loop Non-Design/Dispatch/FL(%)	<= 5%			1 14%	4,135				YES
B 2 17 2.2	P-7C UNE Loop Non-Design/Non-Dispatch/FL(%)	<= 5%			0 70%	1,437				YES
	% Missed Installation Appointments									
B 2 18 1.1 1	P-3 [Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	3 97%	102,712	T					
B218112	P-3 Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.04%	794,943						
B218121	P-3 Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	6.65%	436	1					t1
							-			
B 2 18 1 2 2	P-3 Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	9	0.000	14		0 03756	0 5336	YES
B 2 18 2 1 1	P-3 Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3	2 00%	2,445	0.00%			0 03/50	0 5330	
B 2 18 2 1 2	P-3 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3			4					↓
B 2 18 2 2 1	P-3 Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3			ļ					
B 2 18 2 2 2	P-3 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3			1					
B 2 18 3 1 1	P-3 Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	3.98%	103,470	4 22%	593		0 00805	-0.2906	YES
B 2 18 3.1 2	P-3 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0.04%	796,616	0 28%	10,375		0 00020	-11.8652	NO
B.2 18 3 1 3	P-3 Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B	0 00%	453,156	0.00%	5,763		0.00000		YES
B 2 18 3 1 4	P-3 Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	R&B	0.10%	343,462	0 63%	4,612		0.00046	-11.6233	NO
B 2 18 3 2 1	P-3 Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	7.57%	489	15 38%	13		0 07432	-1.0520	YES
B 2 18 3.2 2	P-3 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&8	0 00%	97	0.00%	2		0 00000		YES
JETODIEE	And the second									·

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	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2 18.3 2 3	P-3 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)	R&B	0.00%	19	0.00%	1		0.00000		YES
B 2 18 3 2 4	P-3 Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	R&B	0.00%	78	0.00%	1		0.00000		YES
B 2 18 4.1 1	P-3 Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	4.01%	106,706	10.81%	37]	0.03224	-2.1107	NO
B 2.18.4 1 4	P-3 Combo Other/<10 circuits/Dispatch In/FL(%)	R&B&D - Disp	4.01%	106,706						L
B 2 18 4 2 1	P-3 Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	7.49%	494						
B 2 18 4 2 4	P-3 Combo Other/>=10 circuits/Dispetch In/FL(%)	R&B&D - Disp	7.49%	494	4 5000	309	-	0.01533	2.0527	YES
B 2 18.5 1 1	P-3 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail ADSL to Retail	7.68%	12,451 6,216	4.53%	309	-	0.01533	2.0527	TEO
B.2 18 5 1 2	P-3 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%) P-3 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	10.53%	19			-			
B 2 18.5.2.1 B 2.18 5.2.2	P-3 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	10.55 %	13						
B 2 18 6 1 1	P-3 UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	6 62%	438	7.40%	311		0.01844	-0.4201	YES
B 2 18.6 1.2	P-3 UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	1.39%	504						
B 2.18.6 2 1	P-3 UNE ISDN/>=10 circuits/Dispatch/FL(%)	ISDN - BRI								
8 2.18 6.2 2	P-3 UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI]			
B 2.18.7.1.1	P-3 Une Sharing/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7.68%	12,451	28.57%	7]	0.10066	-2.0757	NO
B 2.18 7 1 2	P-3 Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0.14%	6,216	0 00%	21		0.00831	0.1742	YES
B 2 18.7 2.1	P-3 Line Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	10.53%	19			-			
B.2 18722	P-3 Line Sharing/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail								
B 2.18 8.1 1	P-3 2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B · Disp	3.98%	103,470	5 88%	85	-	0.02122	-0.8957	YES
6218.812	P-3 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&8 - Disp	3 98%	103,470						└─── ┥
B 2 18 8 2 1 B 2 18 8 2.2	P-3 2W Analog Loop Design/>=10 circuits/Dispatch/FL(%) P-3 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp R&B - Disp	7.57% 7 57%	489			-			
82189.1.1	P-3 2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	397%	102,710	4.91%	224	-	0.01306	-0.7208	YES
B 2 18 9 1 4	P-3 2W Analog Loop Non-DesignV10 circuits/Dispatch/PL(%)	R&B (POTS) excl SB Or	0 09%	342,621	0.00%	2		0.01000	0.0436	VES
B.2 18 9 2 1	P-3 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.65%	436	0.00%	6	-	0.10242	0.6494	YES
B.2 18.9 2.4	P-3 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	8						
B 2 18 10 1.1	P-3 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	3 98%	103,470	0.00%	10		0.06184	0.6439	YES
B 2 18 10.1 2	P-3 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	3 98%	103,470			-			
B 2 18 10 2 1	P-3 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	7 57%	489						
B.2 18 10 2 2	P-3 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	7 57%	489						
B 2.18 11 1.1	P-3 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	3 97%	102,710	0.00%	3		0.11272	0.3521	YES
82181114	P-3 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 09%	342,621						
B 2 18 11 2 1	P-3 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6 65%	436	0.00%	1		0.24946	0 2666	YES
B 2.18 11.2 4	P-3 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 00%	8	1 0001	1 070	-	0.00000	4 1000	VE0
B 2 18 12 1 1	P-12 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp R&B - Disp	3 98%	103,470	1.30%	1,073		0.00600	4.4696	YES
B 2 18.12 1 2	P-12 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%) P-12 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	3.98% 7.57%	103,470	5.88%	17		0.06525	0.2585	YES
B 2 18 12 2 1 B 2 18 12 2 2	P-12 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%) P-12 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	7.57%	489	5.00 %		-	0.00325	0.2000	, <u>, , , , , , , , , , , , , , , , , , </u>
B 2 18 13 1 1	P-12 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	3.97%	102,710	0.83%	844		0 00675	4.6524	YES
B 2.18.13 1 4	P-12 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.09%	342,621	0.00 / 0					
B 2.18 13 2 1	P-12 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.65%	436	3.85%	26		0 05030	0 5569	YES
B 2 18 13 2 4	P-12 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	8						
B 2 18 14 1 1	P-3 Other Design/<10 circuits/Dispatch/FL(%)	Design	4 76%	3,238	1.07%	375		0 01161	3.1778	YES
82181412	P-3 Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	1.08%	1,301						
B 2 18 14 2 1	P-3 Other Design/>=10 circuits/Dispatch/FL(%)	Design	0.00%	5	0.00%	3		0 00000		YES
B 2 18 14 2 2	P-3 Other Design/>=10 circuits/Non-Dispatch/FL(%)	Design	0.00%	52						
B 2 18 15 1 1	P-3 Other Non-Design/<10 circuits/Dispatch/FL(%)	R&B	3 98%	103,470	3.57%	728		0.00727	0 5643	YES
B 2 18 15 1 2	P-3 Other Non-Design/<10 circuits/Non-Dispatch/FL(%)	R&B	0 04%	796,616	0 00%	18		0.00479	0 0862	YES
B 2 18 15 2 1	P-3 Other Non-Design/>=10 circuits/Dispatch/FL(%)	R&B	7.57%	489	0.00%	10	-	0.08448	0.8956	YES
B 2 18.15 2 2	P-3 Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)	R&B	0.00%	97	ļ		-			
B 2 18.16 1.1	P-3 INP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS) R&B (POTS)	397% 004%	102,712						<u>├</u>
B 2 18 16 1 2 B 2 18 16 2 1	P-3 INP (Standalone)/<10 circuits/Non-Dispatch/FL(%) P-3 INP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6 65%	794,943 436						┟┫
B 2 18 16 2 1 B 2 18 16 2 2	P-3 INP (Standalone)/>=10 circuits/Dispatch/FL(%) P-3 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	9	·····					<u>├</u>
82181622	P-3 INP (Standalone)/>= 10 circuits/Nor-Dispatch/FL(%)	R&B (POTS)	3.97%	102,712	0.00%	4		0.09762	0.4066	YES
B 2 18.17.1.2	P-12 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.04%	794,943	0.14%	2,219		0.00043	-2.3064	NO
B 2.18 17.2.1	P-12 LNP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6.65%	436	0,0770				2.000	<u> </u>
B 2.18 17.2.1 B 2.18.17 2.2	P-12 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	9	0.00%	10		0.00000	•	YES
B .2 18 18 1 1	P-3 Digital Loop < DS1/<10 circuits/Dispatch/FL(%)	Digital Loop < DS1	7 35%	13,697	5 95%	622		0.01070	1 3117	YES
B 2 18 18 1 2	P-3 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	0 21%	7,533	1					
B 2 18 18 2 1	P-3 Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop < DS1	10.53%	19						
			-		·					

		South Monthly State Summary da. October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2,18 18 2 2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	0.00%	1						
B 2.18 19 1 1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)	Digital Loop >≈ DS1	1.47%	680	6 74%	282		0.00853	-6.1777	NO
B 2 18.19.1 2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	0.45%	1,121						L
B 2 18.19 2 1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	0.00%	4						L
B 2.18 19.2 2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	0.00%	52						
	% Prov	isioning Troubles within 30 Days	_								
B.2.19 1.1 1	P-9	Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	6.04%	87,035						
B219112	P-9	Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	4.02%	642,874						
B 2.19.1 2 1	P-9	Switch Ports/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	9.66%	414						L
B.2 19 1 2 2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	8						
B 2.19 2 1 1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3	4.80%	1,958	7.69%	26		0.04220	-0.6851	YES_
B 2.19 2 1 2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3	0.00%	1	II					
B 2 19 2 2 1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3								ł
B 2 19.2 2 2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3	0.000/	87,747	4.87%	534		0.01031	1.0964	YES
B 2.19 3 1 1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B R&B	. <u>6.00%</u> 4.01%	645,760	4.87%	9,659		0.00201	3.9820	YES
B 2 19 3 1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	- R&B	4.01%	385,080	3.21%	5,568		0.00269	3.5494	YES
B 2 19 3 1 3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B	3.80%	260,680	3.23%	4,091	•	0.00209	1.9035	YES
B.2 19 3 1 4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/FL(%) Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	8.77%	479	13.33%	15		0.00301	-0.6156	YES
B.2 19 3.2 1	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	1.20%	167	10.0076		•	00/410	-0.0100	
B 2 19.3 2 2	P-9 P-9	Loop + Port Combinations/>=10 circuits/NorFDispatch/FL(%)	R&B	2.63%	76						ł
B.2 19 3 2 3	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	R&B	0.00%	<u>91</u>						h
B 2 19 3 2 4 B 2 19 4 1 1	P-9	Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	5.93%	90,251						
B219414	P-9	Combo Other/<10 circuits/Dispatch In/FL(%)	R&B&D · Disp	5.93%	90,251						
B219414 B219421	P-9	Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	8.66%	485						1
B 2 19 4.2 4	P-9	Combo Other/>=10 circuits/Dispatch In/FL(%)	R&B&D - Disp	8.66%	485						1
B 2 19.5 1.1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail	11.18%	10,729	3 98%	377		0.01652	4.3632	YES
B219512	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	10.31%	3,277						
B 2 19 5.2 1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0.00%	17				<u> </u>		
B219522	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail								
B219611	P-9	UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	3 48%	345	6 92%	347		0.01393	-2 4681	NO
B.2 19.6 1 2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	1.34%	673						
B 2 19 6.2 1	P-9	UNE ISDN/>=10 circuits/Dispatch/FL(%)	ISDN - BRI	0.00%	1						
B219622	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI								
B.2 19 7 1.1	P-9	Line Sharing/<10 circuits/Dispatch/FL(%)	ADSL to Retail	11.18%	10,729	20 00%	15		0.08144	-1.0825	YES
B 2 19 7 1 2	P-9	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	10.31%	3,277	20.78%	77		0.03507	-2.9844	NO
B 2 19 7 2 1	P-9	Line Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0.00%	17						
B 2 19 7 2 2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail								L
B 2 19.8.1 1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	6.00%	87,747	6 76%	296		0.01383	-0.5480	YES
8219.812	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	6.00%	87,747						l
8219821	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	8.77%	479	100.00%	1		0.28313	-3 2223	NO
B.2 19 8 2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	8.77%	479						
B 2.19 9 1 1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6 04%	87,035	0.00%	459		0.01115	5.4172	YES
B 2 19 9 1 4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3.82%	259,344	0.00%	10		0 06058	0.6298	YES YES
B 2 19.9 2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	9.66%	414	0.00%	12	-	0 08651	1.1168	+ "ES-
B 2 19 9 2 4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	8	0.00%			0.16702	0.3573	YES
B 2 19.10 1 1	P.9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	6 00%	87,747	0.00%	2		0.16792	0.3573	100
B 2 19 10 1 2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	6.00%	87,747					l	+
B 2 19 10 2 1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	8.77%	479	+				<u> </u>	
B 2 19 10 2 2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	8 77%	479 87,035	0.00%	22		0.05079	1 1889	YES
B 2 19 11 1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6 04%	259,344	0.00%	22		0.05079	0.2817	YES
B 2 19 11 1 4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or B&B (POTS) excl SB Or	3 82%	414	- 000%	<u> </u>	-	0 (3047	0.2017	+
B 2 19 11.2 1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or							 	+
B 2.19 11 2 4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	8 87,747	3 92%	739		0.00877	2.3701	YES
B 2 19 12 1 1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	6.00%		J 9270	138		0.00077	2.3/01	+ '53
B 2 19 12 1 2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp R&B - Disp	<u>6 00%</u> 8 77%	87,747 479	0.00%	6		0.11619	0.7547	YES
B 2 19 12.2 1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)		8 77%	479	0.00%	0		0.11019		+'
B 2 19 12 2 2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp R&B (POTS) excl SB Or	6 04%	479 87,035	0.00%	1,709		0.00582	10 3790	YES
B 2 19 13 1 1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)			259,344	0.00%	1,709		0.00302		1.150
B 2 19 13 1 4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3 82%	208,344					L	L

NORMA Under Freidag Name Value		Florida, October 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
B = 19131 FG FW Aveca Log = KLP Not Digget Coll Strate Object AT (1/2) FB FORM At 1 0.000 4.4 0.000 4.4 0.000 4.4 0.000 4.4 0.000 4.6 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 1.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.00000 0.00000 0.00000 0.00000 0.00000 0.00000 0.000000 0.00000		FIOTIC	ia, October 2001								ZScore	Equity
Bit				rine og								
Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Other Dary Chi Chara English (1) Description Bit 11 PS Description Description <thdescription< th=""> <thd< td=""><td>B 2 19 13 2 1</td><td>P-9</td><td>2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)</td><td></td><td></td><td></td><td>0 00%</td><td>45</td><td></td><td>0 04637</td><td>2.0835</td><td>YES</td></thd<></thdescription<>	B 2 19 13 2 1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)				0 00%	45		0 04637	2.0835	YES
Bit	B 2 19.13 2 4						0.60%	104		0.01902	3 4745	NO
Design Desif if i							9.0276	104		0.01602	-0.4740	
Bit Bit 101 Color			Other Design/<10 circuits/Non-Dispatch/FL(%)				100.00%	1		0.00000		NO
Bit Bit Pair Construction Construct							100 00 %	·		0.00000		
Page 18:12 Prior Description (Description (Desc							0.00%	92		0.02477	2.4218	YES
Bit Bit 2 PTA OTO: OTO: O OTO: O <tho< th=""> O</tho<>							0.00%	13		0.05442	0.7370	
B2:0:10:2 P4 Conv 12% 1				R&B	8.77%	479	0.00%	7		0.10768	0.8143	YES
Bit 111 PE File File <t< td=""><td></td><td></td><td></td><td>R&B</td><td>1.20%</td><td>167</td><td></td><td></td><td></td><td></td><td></td><td>í</td></t<>				R&B	1.20%	167						í
B2:0 91:2 P3:0 WR 20000000(10:0:0:0:0:0:0:0:0:0:0:0:0:0:0:0												
B 2 III 0 2.1 Fr. 6 Fill Standbragh-2 for clardbraght (S) PRA (POTE) 0.05% F14		P-9										
B 2 P 3 UP 2 D 40 P 3 D 40 P			INP (Standalone)/>=10 circuits/Dispatch/FL(%)									į
B 2 19 1/2 P= UP (Standardy L) documents departed r(Ly) PAS PAS </td <td></td> <td>P-9</td> <td>INP (Standalone)/>=10 circults/Non-Dispatch/FL(%)</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>i [</td>		P-9	INP (Standalone)/>=10 circults/Non-Dispatch/FL(%)									i [
B 2 IB 172 /r 2 /r	B 2 19 17.1 1	P-9										j
B 2: 91 F12: P3 B 2: 91 F12: P3 Digital Loop - D51: 10 dicutatione)-indicated PLC(N) Digital Loop - D51: 10 dicutation	B 2.19 17.1.2							l			··-	
Digit Logs - DS1 219 18.21 P-3 Digit Logs - DS1 Digit Logs - DS1 </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>· · · · · · · · · · · · · · · · · · ·</td> <td>-</td> <td></td> <td></td> <td> </td>								· · · · · · · · · · · · · · · · · · ·	-			
B = 16 P = 0 Operation 2 OBS = 0 7.4% 4.463							5 479	713		0.01182	A 2437	YES
2 19 2 20 2 000 2 0000 0 00000 0 00000 0 00000 0 00000 0 00000 0 00000 0 00000 0 00000 0 000000 0 000000 0 0000000000 0 00000000000000000000000000000000000							34/ 70-			0.01102	4.2.401	
2 19 18 22 P2 Deglial Loop - DS1												
12 19 17<					0.0070							
Display Display <t< td=""><td></td><td></td><td></td><td></td><td>0.79%</td><td>506</td><td>5 29%</td><td>227</td><td></td><td>0.00707</td><td>-6.3550</td><td>NO</td></t<>					0.79%	506	5 29%	227		0.00707	-6.3550	NO
PA Digital Loop >= DS1/=10 canades/Depart/PT (A) Digital Loop >= DS1 0.00% 1 22 19 19 22 PA Digital Loop >= DS1 0.00% 1 0.00% 1 22 19 19 22 PA Digital Loop >= DS1 0.00% 1 0.00% 1 0.00% 82 21 112 PS Smith Fords (10 accus/Dipart/PT (hours) PS 0.00% 1 0.00%												
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $						1			-			
Average Comparison Notice Internal - Mechanized B 221111 F.S. Switch Fonds / 10 clouid/Sepidor/E (Jouris) R48 (POTS) F.88 (POTS) 7.992						62						
B 221111 P-5 Switch Ports-10 circuits/Dispatch/FL/hours) R48 (POTS) 1.19 53,000 20.882												
B 221112 P-5 Switch Ports-to dicuus/Non-Dispatch/FL(hours) R48 (POTS) 7.28 7.862 7.862 B 221121 P-5 Switch Ports-to dicuus/Non-Dispatch/FL(hours) Distribution R48 (POTS) 7.2 27.3 24.475	B 2 21 1 1 1			R&B (POTS)	4.19	53,070			20.852			
B 22112 P Price Switch Perds-10 circulat/Dispatch/FL[hours] PA80 (POTS) 7.22 273 24.475 B 22112 P Price Local Interofice Transport/-10 circulat/Dispatch/FL[hours] DS1/DS3-Interofice 721 B				R&B (POTS)	1.58	588,058			7.952			
Base Base Control (Control				R&B (POTS)	7.22							
B 221 21 P.6 Local Interoffice Transport/-10 circuls/Nor-Dispatch/FL(hours) DS1/DS3 - Interoffice B 221 22 1 P.6 Local Interoffice Transport/-10 circuls/Nor-Dispatch/FL(hours) DS1/DS3 - Interoffice DS1/DS3 - Interoffice DS1/DS3 - Interoffice DS1/DS3 - Interoffice B 221 22 1 P.6 Local Interoffice Transport/-10 circuls/Nor-Dispatch/FL(hours) DS1/DS3 - Interoffice DS1/DS3 - Interoffice DS1/DS3 - Interoffice DS1/DS3 - Interoffice B 221 31 1 P.6 Local Interoffice Transport/-10 circuls/Nor-Dispatch/FL(hours) BAB 1.58 569_272 0.39 7.701 7.996 0.09171 6.5118 YES B 21 31 2 P.6 Loop + Port Combinations/-10 circuls/Switch Based Orders/FL(hours) RAB 0.88 2.77240 1.063 5.836 5.999 0.996 0.996 0.7714 7.996 0.09171 6.5118 YES B 21 312 P.6 Loop + Port Combinations/-10 circuls/Switch Based Orders/FL(hours) RAB 0.98 2.77240 1.0365 0.037 1.4819 YES B 21 32 P.6 Loop + Port Combinations/-> 10 circuls/Switch Based Orders/FL(hours) RAB			Switch Ports/>=10 circuits/Non-Dispatch/FL(hours)									
B 2 21 2 21 P.5 Local Insertifies Transport/s-10 circuls/Nor-Dispatch/FL(hours) DB1/DB3 - Interoffice B 2 21 2 2 P.5 Local Insertifies Transport/s-10 circuls/Nor-Dispatch/FL(hours) DB1/DB3 - Interoffice B 2 21 31 2 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 31 1 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 31 2 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 31 4 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 32 1 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 32 2 P.5 Loop - Fort Combinations/-10 circuls/Nor-Dispatch/FL(hours) BAB B 2 21 32 2 P.5 Loop - Fort Combinations/-10 circuls/Signatch In/FL(hours) BAB B 2 21 32 2 P.5 Loop - Fort Combinations/-10 circuls/Signatch In/FL(hours) BAB B 2 21 32 2 P.5 Loop - Fort Combinations/-10 circuls/Signatch In/FL(hours) BAB B 2 21 4 2 4 P.5 Comb Others/-10 circuls/Signatch In/FL(hours) BAB	B 2 21 2 1 1	P-5			72.13	1,719			277.398			
B 2 21 2 2 P-5 Local Interofice Transports - 10 circults/Non-Dispatch/FL(hours) DS1/DS3 - Interofice B 2 21 31 1 P-5 Local Interofice Transports - 10 circults/Non-Dispatch/FL(hours) AB 4.2 5.3 6.6 7.701 7.996 0.09171 6.65118 YES B 21 31 2 P-5 Local Interofice Transports - 10 circults/Non-Dispatch/FL(hours) R4B 1.53 569.272 0.98 7.701 7.996 0.09171 6.65118 YES B 21 31 3 P-5 Local Port Combinations/-10 circults/Non-Dispatch/FL(hours) R4B 1.53 569.272 0.98 7.701 7.996 0.09171 6.03820 YES B 21 31 3 P-5 Local Port Combinations/-10 circults/Dispatch/FL(hours) R4B 0.66 10 0.358 5.989 0.08960 -0.6654 YES B 21 32 3 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) R4B 0.66 15 0.02 1 0.420 0.43377 16662 YES B 21 41 1 P-5 Combo Other/-10 circults/Dispatch/FL(hours) R4B	B221212											
B 2 21 31 L P-5 Loop + Port Combinations/:10 circuits/Dispatch/FL(hours) Fab AB 1.56 58/27 0.96 7.71 1.19843 3.2599 YES B 221 31 L P-5 Loop + Port Combinations/:10 circuits/Dispatch/FL(hours) RAB 1.56 58/272 0.96 7.71 7.996 0.09171 6.5118 YES B 221 31 L P-5 Loop + Port Combinations/:10 circuits/Dispatch In/FL(hours) RAB 1.56 58/9.272 0.96 7.711 7.996 0.09171 6.5118 YES B 221 31 L P-5 Loop + Port Combinations/:-10 circuits/Note hased Orders/FL(hours) RAB 0.98 277,240 105 3.538 5.899 0.98925 YES B 221 32 L P-5 Loop + Port Combinations/:-10 circuits/Note hased Orders/FL(hours) RAB 0.62 76 0.02 1 0.34307 1.4819 YES B 221 4.11 P-5 Comb Other/:-10 circuits/Note hased Orders/FL(hours) RAB 0.60 61 0.3241 0.420 0.34137 1.4819 YES B 221	B.2 21 2 2 1	P-5										·
b221311 P-5 L00 + Pot Combinations/-10 circults/Signatch/FL(hours) R48 1.58 569,272 0.98 7,701 7,998 0.0971 6,5118 YES b221312 P-5 Loop + Pot Combinations/-10 circults/Signatch/FL(hours) R48 2.11 312,032 0.63 4,163 9446 0,14737 6,0362 YES b221314 P-5 Loop + Pot Combinations/-10 circults/Dispatch/FL(hours) R48 0.98 277,240 105 3.538 5.699 0.09980 0.9684 YES b221321 P-5 Loop + Pot Combinations/-10 circults/Dispatch/FL(hours) R48 0.62 76 0.02 1 0.383 4,4163 9446 0,43367 14819 YES b221324 P-5 Loop + Pot Combinations/-10 circults/Dispatch/FL(hours) R48 0.60 61 0.02 1 0.4367 14819 YES b2214.14 P-5 Combo Others/-10 circults/Dispatch/FL(hours) R480 0.60 61 0.0241 0.43067 14919 YES b2215.11					100			050	04 070	1 10040	0.0500	VEO
B 2 21 313 P-5 Loop + Port Combinations/-10 circults/Dispatch In/FL(hours) PAB 2.1 312 022 0.93 4,163 9.446 0.90362 YES B 221 314 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.93 4,163 9.446 0.4737 B 0.0362 YES B 221 321 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.93 2.11 312,022 0.93 4,163 9.446 0.4737 B 0.0362 YES B 221 322 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.62 76 0.02 1 0.356 0.35773 16662 YES B 221 324 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.66 1 0.02 1 0.340 0.341 VES B 221 421 P-5 Combo Other/-10 circults/Dispatch/FL(hours) PAB 0.66 1 0.23417 7.55549 230.672 - - - - - - - - <												
b 221314 P-5 Loop + Port Combinations/-10 circults/Dispatch In/FL(hours) PAB 0.98 277/240 105 3.538 5.899 0.09990 -0.6654 YES b 221321 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 6.75 306 0.03 10 23.417 7.55260 0.6925 YES b 221322 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.66 15 0.02 1 0.4307 1.4619 YES b 221322 P-5 Loop + Port Combinations/-10 circults/Dispatch/FL(hours) PAB 0.66 15 0.02 1 0.420 0.43367 1.4619 YES b 221424 P-5 Combo Other/-10 circults/Dispatch/FL(hours) PAB 0.66 15 0.02 1 0.420 1.4519 YES b 221424 P-5 Combo Other/-10 circults/Dispatch/FL(hours) PAB 0.66 15 0.02 1 0.420 1.4519												
B 21131 P-5 Loop + Pot Combinations/s-10 circults/Dispatch/FL(hours) R4B 6.75 306 0.03 10 23.417 7.52526 0.8925 YES B 211322 P-5 Loop + Pot Combinations/s-10 circults/Dispatch/FL(hours) R4B 0.62 76 0.02 1 0.3577 16662 YES B 211322 P-5 Loop + Pot Combinations/s-10 circults/Dispatch/FL(hours) R4B 0.62 76 0.02 1 0.3577 16662 YES B 211324 P-5 Loop + Pot Combinations/s-10 circults/Dispatch/FL(hours) R4B 0.66 15 0.02 1 0.430 1.4819 YES B 211421 P-5 Combo Other/s-10 circults/Dispatch/FL(hours) R4BAD - Disp 7.91 311 28.4872 B 211421 P-5 Combo Other/s-10 circults/Dispatch/FL(hours) R4BAD - Disp 7.91 311 28.4872 B 211512 P-5 XDSL (ADSL, HDSL and UCL)/<10 circults/Non-Dispatch/FL(hours)												
b 2 21 3 2 / P-5 L00p + Port Combinations/>-10 circuits/Switch Based Orders/L(fours) R4B 0.62 76 0.02 1 0.355 0.35773 1 6662 YES B 2 21 3 2.3 P-5 Loop + Port Combinations/>-10 circuits/Switch Based Orders/L(fours) R4B 0.68 15 0.02 1 0.420 0.43367 1 4819 YES B 2 21 3 2.4 P-5 Loop + Port Combinations/>-10 circuits/Dispatch In/FL(hours) R4B 0.60 61 0.341 - B 2 21 4 1.4 P-5 Combo Other/<10 circuits/Dispatch/FL(hours)												
0 221 32.2 P-5 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (hours) R4B 0.66 15 0.02 1 0.420 0.43367 1 4819 YES B 221 32.4 P-5 Loop + Port Combinations/>=10 circuits/Dispatch In/FL (hours) R4B 0.60 61 0.341												
B 21 32.4 P.5 Loop + Port Combinations/s=10 circuits/Dispatch In/FL(hours) PAB 0.60 61 0.341 Image: Combination of the circuits/Dispatch In/FL(hours) B 21 32.4 P.5 Combo Other/<10 circuits/Dispatch/FL(hours)								1			1 4819	YES
B 2 21.4 11 P-5 Combo Other/<10 circuits/Dispatch/FL(hours) P48.80 - Disp 24 17 55,549 238.872			Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)	B&B	0.60	61			0.341			
B 2 21 4.1.4 P-5 Combo Other/<10 circuits/Dispatch/FL(hours)						55,549						
B 2 21.4 21 P.6 Combo Other/s=10 circuits/Dispatch/FL(hours) R&B&D Disp 7.91 311 28.189				R&B&D - Disp								
B 2 21 4 24 P-6 Combo Other/s=10 circuits/Dispatch In/FL(hours) H&B&D 7.91 311 26.189 B 2 21 5 11 P-5 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)												
B 2 21 5 11 P-5 xDSL (ADSL, HDSL and UCL/<10 circuits/Dispatch/FL(hours)			Combo Other/>=10 circuits/Dispatch In/FL(hours)									[]
B 2215 12 P-5 xDSL (ADSL, HDSL and UCL)/~10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 1.06 4,963 6.516 B 2215 21 P-5 xDSL (ADSL, HDSL and UCL)/~-10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497 B 2215 21 P-5 xDSL (ADSL, HDSL and UCL)/~-10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497 B 2216 21 P-5 UNE [SDN/-10 circuits/Non-Dispatch/FL(hours) ISDN - BRI 58.60 270 2.43 2 118.217 83 90128 0.6695 YES B 2216 21 P-5 UNE [SDN/-10 circuits/Non-Dispatch/FL(hours) ISDN - BRI 676 387 24.926	B 2 21 5 1 1		xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)					ļ				ļ
b.221521 P-5 VOSL (ADSL Indo: Lindo: Li	B 2 21.5 1 2		xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)							i		
B 221 611 P-5 UNE ISDN/x10 circuits/Dispatch/FL(hours) ISDN - BRI 58.60 270 2.43 2 118.217 83 90128 0.6695 YES B 221 612 P-5 UNE ISDN/x10 circuits/Dispatch/FL(hours) ISDN - BRI 6.76 387 24.926					0.18	15	1			 		↓ I
B 221 6 1 2 P-5 UNE ISDN/×10 circuits/Non-Dispatch/FL(hours) ISDN - BRI 6 76 387 24.926								·		00.00100	0.0005	
B 221 6 12 P-5 UNE ISDIV>=10 circuits/Non-Dispatch/FL(hours) ISDN - BRI ISDN - BRI B 221 6 22 P-5 UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours) ISDN - BRI ISDN - BRI ISDN - BRI B 221 6 22 P-5 UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours) ISDN - BRI ISDN - BRI ISDN - BRI B 221 7 1.1 P-5 Line Sharing/<10 circuits/Non-Dispatch/FL(hours)							2.43	2		83 90128	0.6695	TES
B 221 0 2 /r P-5 UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours) ISDN - BRI Control Contro Contro Contro </td <td></td> <td></td> <td></td> <td></td> <td>6 76</td> <td>387</td> <td>+</td> <td><u> </u></td> <td>24.926</td> <td> </td> <td></td> <td><u> </u> </td>					6 76	387	+	<u> </u>	24.926			<u> </u>
B 2217 1.1 P-5 Line Sharing/x10 circuits/Dispatch/FL(hours) ADSL to Retail 8.53 9,688 0.53 4 26,685 13,34510 0.5996 YES B 2217 1.2 P-5 Line Sharing/x10 circuits/Dispatch/FL(hours) ADSL to Retail 1.06 4,963 0.64 4 6.516 3.25941 0.1291 YES B 2217 2.1 P-5 Line Sharing/x-10 circuits/Dispatch/FL(hours) ADSL to Retail 1.06 4,963 0.64 4 6.516 3.25941 0.1291 YES B 2217 2.1 P-5 Line Sharing/x-10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497 B 2217 2.2 P-5 Line Sharing/x-10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497							····	<u> </u>		<u> </u>]
B 2217 1.1 P-5 Line Shanng/+10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 1.06 4.963 0.64 4 6.516 3.25941 0.1291 YES B 2217 2.1 P-5 Line Shanng/>Line Shanng/>=10 circuits/Non-Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497					8.53	0.699	0.52	<u> </u>	26.685	13 34510	0 5996	VES
B 2217 12 P-5 Line Shamg/>>10 circuits/Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497 B.2217.2.2 P-5 Line Shamg/>>10 circuits/Dispatch/FL(hours) ADSL to Retail 0.18 15 0.497												
B.2217.2.2 P.5 Line Shanng/s=10 circuits/Non-Dispatch/FL(hours) ADSL to Retail							0.04	+ •		5.20541	0 1231	
							1	1	0.40/			<u> </u>
					4 23	53,618	41 79	67	21,973	2 68611	-13 9807	NO
	0221811	P-5	1211 Analog LOOP Designee to circular displation chronis		L ***							

		South monthly state Summary									
	Flor	ida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2 21 8 1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4 23	53,618			21.973			()
B221821	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)	R&B · Disp	675	306			23.417			
8221822	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6.75	306	· · · ·		23.417			
B.2 21.9 1 1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4.19	53,070	0.98	165	20.852	1.62586	1.9734	YES
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.97	276,573			5.770		1.0.01	
B.2 21 9 2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Discetch/FL(hours)	R&8 (POTS) excl SB Or	7.22	273	1.87	4	24.475	12.32657	0.4347	YES
B.2.21 9 2 4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.13	7			0.171	/2.0200/	0.1011	
B.2 21 10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	4.23	53,618			21.973			
B.2 21 10.1 2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4.23	53,618			21.973			
B 2.21 10.2 1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	6.75	306	łł		23.417			i
B 2 21.10.2 2	P-5	2W Analog Loop w/WP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6.75	306			23.417			
B 2.21.11.1.1	P-5	2W Analog Loop w/NP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4 19	53,070	. · ·					·
B 2.21.11.1.4	P-5		R&B (POTS) excl SB Or	0.97				20.852			
B.2.21.11.2.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)			276,573			5.770			L
B2211124		2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7.22	273			24.475			·
	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.13	7			0.171			
B.2.21.12 1 1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	4.23	53,618	21.91	413	21.973	1.08538	·16.2856	NO
B.2 21.12 1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B • Disp	4.23	53,618			21.973			
B.2.21.12 2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	6.75	306	0.82	2	23.417	16.61263	0.3567	YES
B.2 21.12 2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6.75	306			23 417			L
B 2 21.13 1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4.19	53,070	15.10	390	20.852	1.05976	-10.2924	NŌ
82.21.13.14	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.97	276,573			5.770			i
B 2 21 13 2 1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7 22	273	11.81	13	24.475	6.94774	-0.6601	YES
B 2 21.13 2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 clrcuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.13	7		-	0.171			
B 2 21 14.1 1	P-5	Other Design/<10 circuits/Dispatch/FL(hours)	Design	577.68	1,931	11.82	172	1145.110	91.11952	6.2100	YES
B 2 21 14 1 2	P-5	Other Design/<10 circuits/Non-Dispatch/FL(hours)	Design	16.81	585			81.966			
B.2 21.14.2.1	P-5	Other Design/>=10 clrcuits/Dispatch/FL(hours)	Design	79.19	5	0.73	2	69.890	58.47375	1.3418	YES
B 2 21.14 2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/FL(hours)	Design	20.65	44			79.626			
B.2.21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/FL(hours)	RAB	4 23	53,618	0 26	457	21.973	1.03222	3.8480	YES
B 2.21 15.1 2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)	R&B	1 58	589,272			7.996			
B 2 21 15 2 1	P-5	Other Non-Design/>=10 circuits/Dispatch/FL(hours)	RAB	6.75	306	0 02	1	23.417	23.45563	0.2867	YES
8.2 21 15 2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)	RAB	0.62	76			0.355			
B.2 21.16 1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/FL (hours)	R&B (POTS)	4.19	53,070			20.852			
B 2 21 16 1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	1.58	588,058			7.952			
B 2 21.16 2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/FL(hours)	R&B (POTS)	7.22	273			24.475			
B 2 21.16 2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	0.12	8	· · · · · · · · · · · · · · · · · · ·		0.161			
B 2 21 17 1.1	P-5	LNP (Standalone)/<10 circults/Dispatch/FL(hours)	R&B (POTS)	4.19	53,070	1.57	10	20.852	6.59467	0.3977	YES
B 2 21.17 1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	RAB (POTS)	1.58	588,058	113.71	1.239	7.952	0 22616	-495.8228	NO
B 2 21.17 2.1	P-5	LNP (Standatone)/>=10 circults/Dispatch/FL(hours)	R&B (POTS)	7.22	273	,,,,,,	1,200	24.475	022010	433.0220	
B 2 21.17 2.2	P-5	LNP (Standatone)/>=10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	0.12	8	49.50		0.161	0.17087	-288.9803	NO
B 2 21.18 1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)	Digital Loop < DS1	12 02	10,477	10.64	4	53.113	26.56132	0.0518	YES
B 2 21.18 1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)	Digital Loop < DS1	1.47	5,351	10.04	*	9 294	20.00132	0.0510	163
B 2 21.18 2.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)	Digital Loop < DS1		15			0 497			
				0.18	19		· · ·	0 48/			
B 2 21.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)	Digital Loop < DS1	10/0 /0		40.00		1550 050	170 07014	10.0010	
B.2 21.19 1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)	Digital Loop >= DS1	1846.18	431	43.30	91	1558.962	179.85041	10.0243	YES
B.2.21.19.1 2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)	Digital Loop >= DS1	9.69	495			62.074			
B 2 21 19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)	Digital Loop >= DS1	52.25	4	ļ		40.936			_
B 2 21 19 2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)	Digital Loop >= DS1	20 65	44			79.626			
	Averac	e Completion Notice Interval - Non-Mechanized									
B.2 22.1.1 1	P 5	Switch Ports/<10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 1 1 2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			· · ·					Diagnostic
B222121	P-5	Switch Ports/>=10 circuits/Dispatch/FL(hours)	Diagnostic			├ ─── }					Diagnostic
B222121	P-5	Switch Ports/>=10 circuits/Dispatch/FL(hours)									
	P-5		Diagnostic			05.00					Diagnostic
B 2 22 2 1.1		Local interoffice Transport/<10 circuits/Dispatch/FL(hours)	Diagnostic			25 80	11				Diagnostic
B 2 22 2 1 2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 2 2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 2 2 2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)	Dlagnostic								Diagnostic
B 2 22 3 1 1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)	Diagnostic			31.35	53				Diagnostic
B.2 22 3 1 2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			20.17	229				Diagnostic
B 2 22 3 1 3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)	Diagnostic			19 12	133				Diagnostic
B 2 22 3 1 4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/FL(hours)	Diagnostic			21.62	96				Diagnostic
			-								

		ida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	1 1011		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
											Discostia
B 2 22 3 2 1	P-5 P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic					-			Diagnostic Diagnostic
B 2 22.3 2 2 B 2 22 3 2.3	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B 2.22 3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)	Diagnostic					-			Diagnostic
B 2 22 4 1 1	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)	Diagnostic			75.90	30				Diagnostic
B.2 22 4 1 4	P-5	Combo Other/<10 circuits/Dispatch In/FL(hours)	Diagnostic								Diagnostic
B.2 22 4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B 2 22 4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)	Diagnostic			49,98	114	-			Diagnostic
B 2 22 5 1 1 B 2 22 5 1.2	P-5 P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours) xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic			48.90	114				Diagnostic Diagnostic
B 2 22.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/f0irDispatch/FL(hours)	Diagnostic					-			Diagnostic
B.2 22.5 2 2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 6.1 1	P-5	UNE ISDN/<10 circuits/Dispatch/FL(hours)	Diagnostic			37.82	232				Diagnostic
B.2.22.6 1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 6 2 1	P-5	UNE ISDN/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B.2.22 6.2 2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 7.1 1	P-5	Une Sharing/<10 circuits/Dispatch/FL(hours)	Diagnostic			18.04	3	-			Diagnostic
B 2 22.7.1 2	P-5 P-5	Line Sharing/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic			10.33	11	-			Diagnostic Diagnostic
B 2.22 7 2 1 B 2 22 7 2 2	P-5	Line Shanng/>=10 circuits/Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B.2 22 8 1 1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/FL(hours)	Diagnostic			41.78	13				Diagnostic
B 2 22 8 1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 8 2 1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B.2 22.8 2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22.9 1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic			27.57	44				Diagnostic
B 2 22 9 1 4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)	Diagnostic			16 64	2				Diagnostic
B 2 22 9 2 1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B 2 22 9 2 4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours) 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)	Diagnostic Diagnostic			76.32	6	-			Diagnostic Diagnostic
B 2 22.10 1.1 B 2 22 10 1 2	P-5 P-5	2W Analog Loop w/NP Design/<10 circuits/Oispatch/FL(hours)	Diagnostic			70.32	· · · ·				Diagnostic
B 2 22 10 7 2	P-5	2W Analog Loop with Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2.22.10 2 2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B.2.22 11 1 1	P 5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 11 1 4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)	Diagnostic								Diagnostic
B 2 22.11 2 1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic			0.02	1	_			Diagnostic
B 2 22 11.2 4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	Diagnostic				474	-			Diagnostic
B 2 22 12 1 1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)	Diagnostic Diagnostic			38.32	4/4				Diagnostic Diagnostic
B 2 22 12 1 2 B 2 22 12 2 1	P-5 P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours) 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic			54.17	8	-			Diagnostic
B 2.22.12.2 2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			•	· · · · ·	-			Diagnostic
B 2 22 13 1 1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic			22.81	376				Diagnostic
B 2 22 13.1 4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)	Diagnostic								Diagnostic
B 2 22 13 2 1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic			27 05	9				Diagnostic
B.2 22 13 2 4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)	Diagnostic					_			Diagnostic
B 2 22 14 1 1	P-5	Other Design/<10 circuits/Dispatch/FL(hours)	Diagnostic			33 08	25	-			Diagnostic
B 2 22 14 1.2	P-5	Other Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic								Diagnostic Diagnostic
B 2 22 14 2 1 B 2 22 14 2.2	P-5 P-5	Other Design/>=10 circuits/Dispatch/FL(hours) Other Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B 2 22 19 2.2 B 2 22 15 1 1	P-5	Other Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic			19.18	99				Diagnostic
B 2 22 15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			25.47	8	-			Diagnostic
B.2.22.15.2 1	P-5	Other Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B.2 22 15 2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 16 1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 16 1 2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B 2 22 16 2 1	P-5	INP (Standalone)/>=10 circults/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22.16 2 2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			10.00					Diagnostic
B 2 22.17.1 1	P-5	LNP (Standalone)/<10 circuits/Dispatch/FL(hours)	Diagnostic			16 30 27.81	36 463				Diagnostic Diagnostic
B 2 22 17 1 2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic			21.81	403				Diagnostic
B 2 22 17 2 1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/FL(hours) LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic Diagnostic			13 45	1				Diagnostic
B 2 22 17 2 2 B 2 22 18 1 1	P-5 P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)	Diagnostic			41 99	341				Diagnostic
02221011	1 ⁰	Digital Loop C Do //C to circulas/Diapatent/ c(notia)	Diagnosite					_			

BellSouth Monthly State Summary Florida. October 2001

		da, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	FION	da, October 2007	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
			Analog	medadie	Volume	incasare	101anito	PC-101001	<u>L</u>	20000	
B.2.22 18 1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2.22.18.2 1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2.22 18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22.19 1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)	Diagnostic			45.50	112	-			Diagnostic
B 2.22 19 1 2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic					-			Diagnostic
B.2.22 19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)	Diagnostic Diagnostic					-			Diagnostic Diagnostic
B 2 22.19.2.2	P-5	Digital Loop >= DS1/>=10 circults/Non-Dispatch/FL(hours)	Diagnostic								Diagnosic
		ervice Order Cycle Time - Mechanized	-					-			
B.2 24.1 1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24.1 1 2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24.1 2 1	P-10 P-10	Switch Ports/>=10 circuits/Dispatch/FL(days) Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic					-			Diagnostic Diagnostic
B.2 24.1.2 2 B 2 24.2 1 1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 2.1.2	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24 2.2 1	P-10	Local Interoffice Transport/>=10 circuits/IOIPoispatch/FL(days)	Diagnostic								Diagnostic
B 2.24.2 2 2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24 3.1 1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			3.73	149				Diagnostic
B 2 24.3 1 2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.81	4,219	1			Diagnostic
B 2 24 3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Dlagnostic			5.00	2				Diagnostic
B 2.24 3 2 2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 4 1 1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24 4.1 2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 4.2 1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 4 2 2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.24 5 1 1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic			3 80	137				Diagnostic
B 2.24 5 1 2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2.24 5 2 1	P-10 P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic Diagnostic
B 2.24 5 2 2 B 2 24 6 1 1	P-10 P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic					-			Diagnostic
B 2 24 6.1 2	P-10	UNE ISDN<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B224621	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 6.2 2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 7 1 1	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 7 1 2	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					1			Diagnostic
B 2 24 7 2 1	P-10	Line Sharing/>=10 clrcuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 7 2 2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 8 1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5.76	21				Diagnostic
B 2 24 8 1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 8 2 1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic					1			Diagnostic
B 2 24 8 2 2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 9 1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.63	19				Diagnostic
B 2 24 9.1 2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic			8 00	1	-			Diagnostic Diagnostic
B 2.24 9 2 1 B 2.24 9 2 2	P-10 P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days) 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 10 1 1	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2.24 10.1.2	P-10	2W Analog Loop w/NP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24.10 2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24 10 2 2	P-10	2W Analog Loop w/iNP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					· ·			Diagnostic
B.2 24 11.1 1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 11 1 2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 11 2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24.12 1 1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5 85	20				Diagnostic
B.2 24 12 1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 12 2.1	P 14	2W Analog Loop w/LNP Design/>=10 circults/Dispatch/FL(days)	Diagnostic			23 00	1				Diagnostic
B 2 24.12 2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 13 1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			3.80	5				Diagnostic
B 2 24 13 1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circu/ts/Non-Dispatch/FL(days)	Diagnostic			4.75	4				Diagnostic
B 2 24.13 2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			4 00	1				Diagnostic
B 2.24 13 2 2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Dlagnostic								Diagnostic

	Densouth monthly state summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
						101				Diagnostic
B 2.24 14.1 1	P-10 Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic			5.21	121				Diagnostic
B 2 24.14.1 2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.00	1	-			Diagnostic
B 2 24 14.2 1	P-10 Other Design/>=10 circuits/Dispatch/FL(days) P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.00	· · · · · · · · · · · · · · · · · · ·	-			Diagnostic
B 2.24.14.2 2 B 2 24.15.1 1	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			4.40	65				Diagnostic
B 2 24.15.1 2	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic				00				Diagnostic
B 2 24.15 2 1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24.15 2 2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24 16 1.1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24 16 1.2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24.16 2 1	P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24 16 2.2	P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24 17 1.1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2.24 17 1.2	P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.99	1,381	-			Diagnostic
B.2.24.17.2.1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 24 17 2.2	P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.24 18 1 1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 24 18.1.2	P-10 [Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 24 18 2 1	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 18 2 2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2.24 19.1 1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			9.57	7				Diagnostic
B.2.24.19.1 2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 19 2 1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 24 19 2.2	P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
		•								
	Total Service Order Cycle Time - Partially Mechanized									
B 2 25 1 1 1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25 1 1 2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25 1.2.1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25 1 2 2	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 2.1 1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25 2 1 2	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 2 2 1	P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B.2.25 2 2.2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25 3 1 1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			3.61	120	-			Diagnostic
B 2 25 3 1 2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.52	2,247	-			Diagnostic
B.2 25.3.2.1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic			6.67	3	-			Diagnostic Diagnostic
B 2 25 3.2 2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			4.00	11	-			Diagnostic
B 2.25.4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic
B.2 25.4.1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25.4 2 1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic				· · · · · · · · · · · · · · · · · · ·	-			Diagnostic
B 2 25 4 2 2	P-10 Combo Other/>=10 circults/Non-Dispatch/FL(days) P-10 xDSL (ADSL, HDSL and UCL)/<10 circults/Dispatch/FL(days)	Diagnostic			3.11	3				Diagnostic
B 2 25 5 1 1 B 2 25 5.1 2	P-10 XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) P-10 XDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 5 2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dspatch/FL(days)	Diagnostic								Diagnostic
B 2 25 5 2 1 B 2 25 5 2 2	P-10 XDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 6 1 1	P-10 IUNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic			12.50	2				Diagnostic
B 2 25 6 1.2	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic				<u> </u>	-			Diagnostic
B 2 25 6 2 1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B 2 25.6 2.2	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				<u> </u>				Diagnostic
B 2 25 7.1 1	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 7.1 2	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic			4 50	2				Diagnostic
B 2 25 7 2 1	P-10 Une Shanng/>=10 circuits//Dispatch/FL(days)	Diagnostic				t <u> </u>				Diagnostic
B 2 25 7 2 2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				<u> </u>				Diagnostic
8225811	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.25	16				Diagnostic
B 2 25 8 1.2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				1				Diagnostic
B.2 25.8 2.1	P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic				1				Diagnostic
B 2 25 8 2.2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				1				Diagnostic
B 2 25 9.1 1	P-10 2W Analog Loop Dasign/<10 circuits/Dispatch/FL(days)	Diagnostic			5.60	68				Diagnostic
B 2 25 9 1 2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 9 1 2 B 2 25 9 2 1	P-10 2W Analog Loop Non-Design/>=10 circuits/toir=Dispatch/FL(days)	Diagnostic			8.00	1				Diagnostic
0223921	To Jet and gloop for bodges to additional point clarity					•				

	BellSouth Monthly State Summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		—								
B 2 25 9 2 2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 10 1.1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 10.1 2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25.10 2 1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispetch/FL(days)	Diagnostic								Diagnostic
B.2 25.10.2 2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25.11 1 1	P-10 [2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days) P-10 [2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25 11.1 2		Diagnostic								Diagnostic
B 2 25.11 2 1		Diagnostic								Diagnostic
B 2 25.11.2 2 B 2 25.12.1 1	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic			6.79	132				Diagnostic
B 2.25 12 1 2	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			0.78	132				Diagnostic
B.2 25 12 2 1	P-14 2W Analog Loop wLNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			7.00	1				Diagnostic
B 2 25 12 2 2	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic		1114	7.00					Diagnostic
B 2 25 13.1.1	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5.89	210				Diagnostic Diagnostic
B 2.25 13 1.2	P-14 2W Analog Loop wLNP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			5.25	435				Diagnostic
B 2.25 13 2.1	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			6.86	7				Diagnostic
B.2 25 13 2.2	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.20	5				Diagnostic
B 2.25 14.1.1	P-10 Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.41	39				Diagnostic
8.2 25.14.1.2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.41					Diagnostic
B.2.25 14 2 1	P-10 Other Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 25 14 2 2	P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 15.1 1	P-10 Other Non-Deskn/<10 circuits/Dispatch/FL(days)	Diagnostic			4 59	254				Diagnostic
B.2.25 15 1 2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25.15 2 1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
8 2.25 15 2 2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	-							Diagnostic
B 2 25 16.1 1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25 16 1 2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 16.2.1	P-10 INP (Standalone)/>=10 circults/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.25 16 2 2	P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 17 1 1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic			3.00	1				Diagnostic
B 2 25.17 1 2	P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			5.05	336				Diagnostic
B 2 25 17.2 1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 17 2 2	P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.50	2				Diagnostic
B 2 25 18 1 1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			10 67	3				Diagnostic
B 2 25 18.1 2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 18 2 1	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.25 18.2.2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 19.1.1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			9.15	55				Diagnostic
B 2.25 19 1 2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25.19 2.1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 25 19 2.2	P-10 Digital Loop >= D\$1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
	Total Service Order Cycle Time - Non-Mechanized									
8.2.26 1 1 1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic				-				Diagnostic
B.2.26.1.1.2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 1 2 1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B226122	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 2 1 1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic			21.71	7				Diagnostic
B 2.26 2 1 2	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 2 2 1	P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 2 2.2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.26 3.1.1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			6 58	35				Diagnostic
B 2 26 3 1.2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.62	191				Diagnostic
B 2 26 3 2.1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 3 2.2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26.4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic			9.42	12				Diagnostic
B 2 26.4.1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 26 4 2 1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26.4 2 2	P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 26 5 1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic			8 33	57				Diagnostic
B 2 26 5 1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
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	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
- F	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
	P-10	Line Sharing/>=10 circuits/Dispatch/FL(days)
	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
	P-10	2W Analog Loop Design/>=10 circults/Dispatch/FL(days)
	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
· •	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
- 6	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
· •	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
- F	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
· •	P-10	2W Analog Loop w/INP Non-Design/>=10 circults/Dispatch/FL(days)
	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
- F	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
· •	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days) 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
	P-14	
	P-10	Other Design/<10 circuits/Dispatch/FL(days)
	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
	P-10 P-10	Other Design/>=10 circuits/Dispatch/FL(days) Other Design/>=10 circuits/Non-Dispatch/FL(days)
	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days) INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
	P-10 P-10	
1	P-10 P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days) INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
		LNP (Standalone)/<10 circuits/Dispatch/FL(days)
	P-14 P-14	
- 1	P-14 P-14	LNP (Standatone)/<10 circuits/Non-Dispatch/FL(days) LNP (Standatone)/>=10 circuits/Dispatch/FL(days)
	P-14 P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
- 1	P-14	
		Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
- 1	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
1		
1	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)

BST Sasure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
							Diagnostic
							Diagnostic
		10.96	203				Diagnostic Diagnostic
		· · · · · · · · · · · · · · · · · · ·					Diagnostic
							Diagnostic
							Diagnostic
		5.50	8				Diagnostic Diagnostic
							Diagnostic
		8 33	6				Diagnostic
							Diagnostic
							Diagnostic Diagnostic
		6.82	11				Diagnostic
							Diagnostic
							Diagnostic Diagnostic
		16.00	1				Diagnostic
		10.00					Diagnostic
							Diagnostic
							Diagnostic Diagnostic
							Diagnostic
							Diagnostic
		0.00					Diagnostic Diagnostic
		8.63	64				Diagnostic Diagnostic
		9.00	1				Diagnostic
							Diagnostic
		7.53 6.50	32				Diagnostic Diagnostic
		30.50	2				Diagnostic
		7.00	1				Diagnostic
		7.29	14				Diagnostic Diagnostic
							Diagnostic
							Diagnostic
		6.29	56				Diagnostic
		6.17	6				Diagnostic Diagnostic
							Diagnostic
							Diagnostic
							Diagnostic Diagnostic
							Diagnostic
		2 33	3				Diagnostic
		2.86	322				Diagnostic
		2 67	3				Diagnostic Diagnostic
		9.67	287				Diagnostic
							Diagnostic
							Diagnostic
		7 50	48				Diagnostic Diagnostic
			+0				Diagnostic
							Diagnostic
			I				Diagnostic
			L				Diagnostic

Diagnostic

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Benchmark /

Analog Diagnostic Diagnostic

Equity

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	Fiorida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard	70
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore
		Diagnostic							
B 2 28 1 1 2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 1 2 1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days) P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 1 2 2	P-10 Switch Policy>=10 circuits/Nor-Dispatch/PL(days) P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic							
B.2 28 2 1 1	P-10 Local Interoffice Transport<10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 2 1.2 B 2 28 2.2 1	P-10 Local Interoffice Transport/>=10 circuits/NoiPO/spatch/FL(days)	Diagnostic							
B 2 28 2.2 1 B 2 28.2.2 2	P-10 Local Interoffice Transport/>=10 circults/Non-Dispatch/FL(days)	Diagnostic							
B 2 28.3 1 1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			3.70	146			
B 2.28 3 1 2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.79	3,261			
B 2 28 3 2 1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic			5.00	2			
B.2 28 3 2 2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2.28 4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic							
B.2.28.4 1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B.2 28 4 2.1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B.2 28 4.2 2	P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 5.1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic			3.80	137			
B 2 28.5.1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
8.2.28 5.2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B.2.28 5 2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 6 1 1	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic							
B.2 28 6 1 2	P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	1						
B.2.28 6 2 1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B 2.28 6.2 2	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2.28 7.1.1	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 7.1 2	P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B.2 28 7 2 1	P-10 Line Sharing/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B 2.28 7 2 2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 8.1 1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5.76	21			
B 2 28 8.1.2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28.8.2 1	P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B.2 28 8 2 2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 9.1 1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.63	19			
B 2 28 9 1 2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28.9 2 1	P-10 2W Analog Loop Non-Design/>=10 circults/Dispatch/FL(days)	Diagnostic			8.00	1			
B 2 28 9.2 2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 10 1 1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic							
B.2 28 10 1.2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28.10 2.1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 10 2.2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 11 1.1	P-10 2W Anatog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 11 1 2	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic							
B 2 28 11 2.1	P-10 2W Analog Loop w/NP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 11 2 2	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			5.85	20			
B 2 28 12 1 1	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			0.00	EU			
B 2 28 12.1 2	P-14 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days) P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			23.00	1			
B 2.28.12 2 1		Diagnostic			20.00				
B 2 28.12.2 2	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days) P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			3.80	5			
B 2 28 13 1.1	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days) P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			4,75	4			
B 2 28 13 1 2	P-14 2W Analog Loop w/LNP Non-Design/>×10 circuits/Non-Dispatch/FL(days)	Diagnostic			4.00	1			
B 2 28 13 2.1	P-14 [2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) P-14 [2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.00	······			
B 2 28.13.2.2	C 10 Other Design (10 circuits/Dispetch/EL (deve)	Diagnostic			5.19	115			
B 2 28 14.1 1	P-10 Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 14 1.2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			7 00	1			
B 2 28.14.2.1	P-10 Other Design/>=10 circuits/Dispatch/FL(days)	Diagnostic				<u>.</u>			
B 2.28 14 2 2	P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			4.40	65			
B 2 28 15 1 1	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28 15 1.2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 15 2 1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic							
B 2 28.15 2 2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 16 1 1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days) P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							
B 2 28 16 1 2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Biagnostic							

Benchmark /

BST

BST

CLEC

CLEC

Standard Standard

	Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
		Dia								Diagnostic
B 2 28.16 2 1 B 2 28 16 2.2	P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic
B 2.28.17.1.1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28 17 1.2	P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.98	1,360				Diagnostic
B 2 28 17.2 1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 28.17.2 2	P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.28.18 1 1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.28 18 1 2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			·	·				Diagnostic
8.2 28.18 2 1 8.2 28.18 2 2	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic Diagnostic
B.2.28.19.1.1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			6.83	6	Sec.			Diagnostic
B 2 28.19.1.2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 28 19.2 1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.28 19.2 2	P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
	Total Service Order Cycle Time (offered) - Partially Mechanized									
B 2.29.1.1.1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 1.1 2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29.1.2.1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29 1 2 2	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29.2.1 1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic				· · · · · · · · · · · · · · · · · · ·				Diagnostic Diagnostic
B 2 29 2 1 2 B 2 29 2 2 1	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29 2.2 2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.3.1 1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			3.44	110				Diagnostic
B 2.29 3.1.2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.49	1,969				Diagnostic
B 2 29.3 2 1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic			6.67	3				Diagnostic
B 2 29 3.2.2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			4.00	1				Diagnostic
B 2 29 4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 4 1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic Diagnostic
B 2 29 4 2 1 B 2 29.4 2 2	P-10 Combo Other/>=10 circuits/Dispatch/FL(days) P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 5 1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic			3.11	3				Diagnostic
B 2 29.5 1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29.5 2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 5.2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29.6 1 1	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic			12.50	2				Diagnostic
B 2 29 6.1.2	P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 6.2 1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29.6.2.2 B 2 29 7 1 1	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days) P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic Diagnostic
B 2 29 7 1 2	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic			4,50	2				Diagnostic
B 2 29 7 2 1	P-10 Line Sharing/>=10 circuits/Dispatch/FL(days)	Diagnostic			1.00					Diagnostic
B 2 29 7 2 2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 8 1 1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.62	13				Diagnostic
B.2.29 8.1 2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 8 2 1	P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 29 8 2 2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29 9.1 1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5.60	68				Diagnostic Diagnostic
B 2 29 9 1 2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic Diagnostic			8.00	1				Diagnostic
B 2 29 9.2 1 B.2 29.9.2 2	P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days) P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.00	_				Diagnostic
B 2 29 10 1 1	P-10 2W Analog Loop Volt-Design/>=10 circuits/Nolt-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29 10 1 2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 10 2 1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 10.2 2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.29.11 1 1	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29.11.1 2	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29.11.2 1	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 29 11 2 2	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			6 83	127				Diagnostic Diagnostic
B 2 29 12 1 1	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Dragnostic			0 83	12/				Diagnosic

Diagnostic

BellSouth Monthly State Summary Florida, October 2001

2 29.12 1 2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
2 29.12 2 1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
2 29.12.2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
2 29.13.1 1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
2 29.13 1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
2.29.13 2 1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
2 29 13 2 2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
2 29.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
2 29 14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
2 29.14 2 1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
2.29.14 2 2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
2 29 15 1 1	P-10	Other Non-Design/<10 circuits/Dispatch/Ft.(days)
2 29 15 1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
2 29 15 2 1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
2 29.15 2 2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
2.29.16 1 1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
2.29 16 1 2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
2 29.16 2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
2 29 16 2 2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
2 29 17 1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
2.29.17 1 2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
2.29 17 2 1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
2 29 17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
2 29.18 1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
2.29 18 1 2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
2 29.18 2 1	P-10	Digital Loop < DS1/>=10 circults/Dispatch/FL(days)
2 29 18 2 2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
2 29 19 1 1	P-10	Digital Loop >= DS1/<10 circults/Dispatch/FL(days)
2 29 19 1 2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
2 29.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
2 29 19.2 2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)
		Service Order Cycle Time (offered) - Non-Mechanized
2 30.1 1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
2 30 1 1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
		Switch Ports/>=10 circuits/Dispatch/FL(days)
	P-10	
230122	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
230122 230.211	P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2	P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1	P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2	P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2 2 30.2 2.2 2 30.3 1 1	P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Photometrice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Photometrice Transport/>=10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30 3 1 2	P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30,2 1 1 2 30,2 1,2 2,30,2 2,1 2,30,2 2,2 2 30,3 1 1 2 30 3 1 2 2 30,3 2,1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30,2 1 1 2 30,2 1.2 2,30,2 2,1 2,30,2 2,2 2 30,3 2,2 2 30,3 1 1 2 30 3 1 2 2 30,3 2,1 2 30 3 2 2	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2.1 2 30 3 2.2 2 30 4 1 1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2.2 2 30 4 1 1 2 30 4 1 2	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30.3 1 2 2 30.3 2.1 2 30.3 2.1 2 30.3 2.2 2 30 4 1 1 2 30 4 2 1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2 2 2 30 4 1 1 2 30 4 1 2 2 30 4 2 1 2 30 4 2 2	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2 30.2 2.1 2 30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2 2 2 30 4 1 1 2 30 4 1 2 2 30 4 1 2 2 30 4 2 1 2 30 4 2 1 2 30 5.1.1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2 30.2 2.1 2 30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2.2 2 30 3 2.1 2 30 3 2.2 2 30 4 1 1 2 30 4 1 2 2 30 4 2 1 2 30 4 2 1 2 30 4 2 1 2 30 5.1.2	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Comb Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1 1 2 30.2 1.2 2 30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30 3 1 2 2 30 3 2 2 2 30 3 2 2 2 30 4 1 1 2 30 4 1 2 2 30 4 2 1 2 30 4 2 1 2 30 5 1.1 2 30 5.2.1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1 1 2 30.2 2.1 2 30.2 2.1 2 30.3 1 1 2 30.3 1 1 2 30.3 2 1 2 30.3 2.1 2 30.3 2.1 2 30.4 1 1 2 30.4 1 2 2 30.4 2 1 2 30.4 2 1 2 30.5 1.1 2 30.5 1.2 2 30.5 2.2	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.1 2.30.2 2.2 2 30.3 1 1 2 30 3 1 2 2 30.3 2.1 2 30 3 2 2 2 30 4 1 2 2 30 4 1 1 2 30 4 1 2 2 30 4 2 1 2 30 4 2 1 2 30 5.1.2 2 30 5.2.1 2 30 5.2 1 2 30 5 2 2 2 30 6 1 1	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<=10 circuits/Non-Dis
2 30 1 2 2 2 30.2 1 1 2 30.2 1.2 2.30.2 2.2 2 30.3 1 1 2 30.3 2.1 2 30.3 2.1 2 30.3 2.1 2 30.3 2.1 2 30.3 2.1 2 30.4 1 1 2 30.4 1 2 2 30.4 2 1 2 30.4 2 1 2 30.5 1.1 2 30.5 2.1 2 30.5 2.2 3 30.6 1 1 2 30.6 1 2	P.10 P.10 P.10 P.10 P.10 P.10 P.10 P.10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>-10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>-10 circuits/Dispatch/FL(days) Local Interoffice Transport/>-10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>-10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>-10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>-10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>-10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>-10 circuits/Dispatch/FL(days) Combo Other/>-10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days)
$\begin{array}{c} 2 \ 30 \ 1 \ 2 \ 2 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 2 \ 1 \\ 2 \ 30.2 \ 2 \ 1 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.5 \ 1 \ 1 \\ 2 \ 30.5 \ 1 \ 1 \\ 30.5 \ 1 \ 1 \\ 1 \ 1 \ 1 \\ 1 \ 1 \ 1 \ 1 \ 1$	P-10 P-10 P-10 P-10 P-10 P-10 P-10 P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/=10 circuits/Dispatch/FL(days) UNE ISDN/==10 circuits/Dispatch/FL(days) UNE ISDN/==10 circuits/Dispatch/FL(days) UNE ISDN/==10 circuits/Dispatch/FL(days)
$\begin{array}{c} 2 \ 30 \ 1 \ 2 \ 2 \\ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 2 \ 1 \\ 2 \ 30.2 \ 2 \ 1 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.4 \ 1 \ 2 \\ 30.5 \ 1 \ 1 \\ 2 \ 30.5 \ 1 \ 1 \\ 30.5 \ 1 \ 1 \\ 1 \ 1 \ 1 \ 1 \\ 1 \ 1 \ 1 \ 1$	P.10 P.10 P.10 P.10 P.10 P.10 P.10 P.10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
$\begin{array}{c} 2 \ 30 \ 1 \ 2 \ 2 \\ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 2 \\ 30.2 \ 2 \ 1 \\ 2 \ 30.2 \ 2 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.4 \ 1 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 30 \ 6 \ 1 \ 1 \\ 30 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ $	P.10 P.10 P.10 P.10 P.10 P.10 P.10 P.10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Comb Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/>=10 circuits/Dispatch/FL(days)
$\begin{array}{c} 2 \ 30 \ 1 \ 2 \ 1 \\ 2 \ 30 \ 1 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 1 \\ 2 \ 30 \ 2 \ 2 \\ 2 \ 30 \ 2 \ 2 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 3 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \\ 2 \ 30 \ 5 \ 1 \\ 2 \ 30 \ 5 \ 2 \\ 2 \ 30 \ 5 \ 1 \\ 2 \ 30 \ 5 \ 2 \\ 2 \ 30 \ 5 \ 1 \\ 2 \ 30 \ 5 \ 2 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 6 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 2 \ 30 \ 7 \ 1 \\ 30 \ 7 \ 1 \ 1 \\ 30 \ 7 \ 1 \ 1 \\ 30 \ 7 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1$	P.10 P.10 P.10 P.10 P.10 P.10 P.10 P.10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/>=10 circuits/Dispatch/FL(days)
$\begin{array}{c} 2 \ 30 \ 1 \ 2 \ 2 \\ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 1 \\ 2 \ 30.2 \ 1 \ 2 \\ 30.2 \ 2 \ 1 \\ 2 \ 30.2 \ 2 \\ 2 \ 30.3 \ 1 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.3 \ 2 \ 1 \\ 2 \ 30.4 \ 1 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \ 1 \\ 2 \ 30 \ 4 \ 1 \\ 2 \ 30 \ 4 \ 2 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 5 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 2 \ 30 \ 6 \ 1 \ 1 \\ 30 \ 6 \ 1 \ 1 \\ 30 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ 1 \ $	P.10 P.10 P.10 P.10 P.10 P.10 P.10 P.10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days) Local Interoffice Transport/>10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days) Loop + Port Combinations/<10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Loop + Port Combinations/>=10 circuits/Dispatch/FL(days) Comb Other/<10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Dispatch/FL(days) XDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/<10 circuits/Dispatch/FL(days) UNE ISDN/>=10 circuits/Dispatch/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
Diagnostic								Diagnostic
Diagnostic			7.00	1				Diagnostic
			7.00					Diagnostic
Diagnostic			E 00	208				Diagnostic
Diagnostic			5.90		-			
Diagnostic			5 25	435				Diagnostic
Diagnostic			6.86	7				Diagnostic
Diagnostic			7.20	5				Diagnostic
Diagnostic			6.49	37				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			4.60	252				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic				····				Diagnostic
Diagnostic			4.89	316				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			10 67	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			9.00	50				Diagnostic
			9.00	50				Diagnostic
Diagnostic								Diagnostic
Diagnostic Diagnostic			· · · ·		-			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic							-	Diagnostic
Diagnostic			21.71	7				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			6.78	32				Diagnostic
Diagnostic			2 62	175				Diagnostic
Diagnostic			£ 04	.,,,				Diagnostic
								Diagnostic
Diagnostic			10.18	11				
Diagnostic			10.10					Diagnostic
Diagnostic								Diagnostic Diagnostic
Diagnostic					-			
Diagnostic								Diagnostic
Diagnostic			8.33	57				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			11.50	166				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			5.71	7				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic

Diagnostic Diagnostic

	Belisouth Monthly State Summary									
	Florida, October 2001	Benchmark /	B\$T	BST	CLEC	CLEC	Standard	Standard		
	··, · · · · · · · · · · · · · ·	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B.2 30 8 1 1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			8 33	6				Diagnostic
B.2 30 8.1.2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 30.8 2 1	P-10 2W Analog Loop Design/>=10 clrcults/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 8 2.2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 9 1 1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.00	9				Diagnostic
B 2.30 9 1 2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 30 9 2 1	P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 9.2 2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30.10.1 1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			16.00	1				Diagnostic
B 2 30.10.1 2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.30.10.2 1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30.10.2.2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.30 11 1.1	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2 30 11 1.2	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30.11 2 1	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.30.11 2 2	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				58				Diagnostic
B 2.30 12 1.1	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			8.74	58				Diagnostic Diagnostic
B 2.30.12.1 2	P-14 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			9.00	1				
B 2.30.12 2 1	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			9.00					Diagnostic Diagnostic
B 2 30 12 2 2	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.20	30				Diagnostic
B.2.30.13 1 1	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.26	30				Diagnostic
B 2.30.13 1 2	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			30.50	2				Diagnostic
B 2 30 13 2 1	P-14 2W Analog Loop w/LNP Non-Design/>=10 clrcuits/Dispatch/FL(days)	Diagnostic			7.00	<u> </u>				Diagnostic
B 2 30 13 2 2	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.00	14				Diagnostic
B 2 30.14 1 1	P-10 Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.29	14				Diagnostic
B 2 30 14.1.2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 14 2 1	P-10 Other Design/>=10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic
B 2 30.14 2 2	P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			6.30	53				Diagnostic
B 2 30 15 1 1	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			6.80	5				Diagnostic
B 2 30 15 1 2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.00					Diagnostic
B 2 30 15 2 1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 15 2.2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30.16 1.1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 16 1 2 B 2 30 16 2 1	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days) P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 16 2 1 B 2 30.16 2 2	P-10 INP (Standalone)/>=10 circuits/Oispatch/FL(days) P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2.30 17 1 1	P-10 [NP (Standalone)/<10 circuits/IDispatch/FL(days)	Diagnostic			1.00	1				Diagnostic
B 2 30 17 1 2	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic			2.80	296				Diagnostic
B 2 30.17 1 2 1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic			2.00					Diagnostic
B 2 30 17 2 2	P-14 LNP (Standalonc)/>=10 circuits/Dispatch/FL(days)	Diagnostic			1.00	2				Diagnostic
B 2 30 18.1 1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			10.39	219				Diagnostic
B 2 30.18 1 2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					1			Diagnostic
B 2.30.18 2 1	P-10 Digital Loop < DS1/>=10 circuits/Dispetch/FL(days)	Diagnostic								Diagnostic
B 2.30 18.2 2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30.19.1 1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			7.64	44				Diagnostic
B.2 30 19.1 2	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 19 2 1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 30 19.2 2	P-10 Digital Loop >= DS1/>=10 circults/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
		-								
	Disconnect Timeliness								<u> </u>	
B 2 31	P-13 LNP/FL(%)	>= 95% w in 15 min								
	% Completions w/o Notice or < 24 hours						<u> </u>			
B 2 32 1 1	P-6 Switch Ports/Dispatch/FL(%)	Diagnostic								Diagnostic
B 2.32 1.2	P-6 Switch Ports/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32 2 1	P-6 Local Interoffice Transport/Dispatch/FL(%)	Diagnostic			100 00%	8				Diagnostic
823222	P-6 Local Interoffice Transport/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
823231	P-6 Loop + Port Combinations/Dispatch/FL(%)	Diagnostic			100.00%	410				Diagnostic
823232	P-6 Loop + Port Combinations/Non-Dispatch/FL(%)	Diagnostic			100.00%	7,699				Diagnostic
823241	P-6 Combo Other/Dispatch/FL(%)	Diagnostic			100.00%	22				Diagnostic
823242	P-6 Combo Other/Non-Dispatch/FL(%)	Diagnostic								Diagnostic

	BellSouth Monthly State Summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	·····	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2.32 5 1	P-6 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	Diagnostic			100.00%	200				Diagnostic
823252	P-6 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B23261	P-6 UNE ISDN/Dispatch/FL(%)	Diagnostic			100.00%	235				Diagnostic
B 2 32 6.2	P-6 UNE ISDN/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2 32 7 1	P-6 Line Sharing/Dispatch/FL(%)	Diagnostic			100.00%	1				Diagnostic
B 2 32 7 2	P-6 Une Sharing/Non-Dispatch/FL(%)	Diagnostic			100.00%	14				Diagnostic
B.2 32 8.1	P-6 2W Analog Loop Design/Dispatch/FL(%)	Diagnostic			100.00%	47				Diagnostic
B 2 32 8 2	P-6 2W Analog Loop Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32.9 1	P-6 2W Analog Loop Non-Design/Dispatch/FL(%)	Diagnostic			100.00%	106				Diagnostic
B 2 32 9 2	P-6 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32.10.1	P-6 2W Analog Loop w/INP Design/Dispatch/FL(%)	Diagnostic			100.00%	1				Diagnostic
8 2 32.10 2	P-6 2W Analog Loop w/INP Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32.11 1	P-6 2W Analog L ~ p w/INP Non-Design/Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32 11.2	P-6 2W Analog Loop w/INP Non-Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2.32 12.1	P-6 2W Analog Loop w/LNP Design/Dispatch/FL(%)	Diagnostic			100.00%	227				Diagnostic
B.2 32.12 2	P-6 2W Analog Loop w/LNP Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32 13 1	P-6 2W Analog Loop w/LNP Non-Design/Dispatch/FL(%)	Diagnostic			100.00%	278				Diagnostic
B 2 32 13.2	P-6 2W Analog Loop w/LNP Non-Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2 32 14.1	P-6 Other Design/Dispatch/FL(%)	Diagnostic			100.00%	222				Diagnostic
B.2 32 14 2	P-6 Other Design/Non-Dispetch/FL(%)	Diagnostic								Diagnostic
B 2 32 15 1	P-6 Other Non-Design/Dispatch/FL(%)	Diagnostic			100.00%	446				Diagnostic
B 2 32 15 2	P-6 Other Non-Design/Non-Dispatch/FL(%)	Diagnostic			100 00%	11				Diagnostic
B 2 32.16 1	P-6 INP (Standalone)/Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32 16 2	P-6 INP (Standalone)/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B 2 32.17 1	P-6 LNP (Standalone)/Dispatch/FL(%)	Diagnostic								Diagnostic
B.2 32 17 2	P-6 LNP (Standalone)/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
8232181	P-6 Digital Loop < DS1/Dispatch/FL(%)	Diagnostic			100.00%	430				Diagnostic
B.2.32 18 2	P-6 Digital Loop < DS1/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2 32 19.1	P-6 Digital Loop >= DS1/Dispatch/FL(%)	Diagnostic			100.00%	132				Diagnostic
8 2 32 19 2	P-6 Digital Loop >= DS1/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
OL GL IVE		-								
	% Cooperative Test Attempts for xDSL				100.000					YES
B 2 33 1	P-8 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% of requests			100 00%	267				TES
B 2 33 2	P-8 xDSL Other/FL(%)	>= 95% of requests								
	Service Order Accuracy									
8234.1.11	P-11 Design (Specials)/<10 circuits/Dispatch/FL(%)	>= 95%			94 74%	38				NO
8234112	P-11 Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	25				YES
B 2 34 1.2.1	P-11 Design (Specials)/>=10 circuits/Dispatch/FL(%)	>= 95%								
B 2 34 1.2.1 B 2 34.1 2.2	P-11 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	>= 95%								
B 2 34 2 1 1	P-11 Loops Non-Design/<10 circuits/Dispatch/FL(%)	>= 95%			65.63%	32				NO
	P-11 Loops Non-Design/<10 circuits/Dispatch/FL(%)	>= 95%			68.09%	188	1			NO
B.2 34 2 1 2	P-11 Loops Non-Design/>=10 circuits/Dispatch/FL(%)	>= 95%			100.00%	2				YES
B 2 34 2 2 1 B 2 34 2 2.2	P-11 Loops Non-Design/>=10 circuits/Dispatch/FL(%)	>= 95%			90 91%	11				NO
DZ 34 ZZ.Z	Ecops Non-Design >= 18 circulariter Disputer 1 2(10)									
		·····								
	Unbundled Network Elements - Maintenance and Repair									
	Missed Repair Appointments	R&B (POTS)	12 90%	126,479	1					
B3111	M&R-1 Switch Ports/Dispatch/FL(%)	R&B (POTS)	2.35%	66,643					i	<u> </u>
B3112	M&R-1 Switch Ports/Non-Dispatch/FL(%)	DS1/DS3	1.17%	1,030	0.00%	0				YES
B3121	M&R-1 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	0.00%	756	0.00%	16		0 00000		YES
B312.2	M&R-1 Local Interoffice Transport/Non-Dispatch/FL(%)	R&B	13 06%	128,404	9.92%	1.855		0.00788	3.9818	YES
831.3.1	M&R-1 Loop + Port Combinations/Dispatch/FL(%)	RåB	2.43%	67,789	1 90%	686		0.00590	0.8981	YES
83132	M&R-1 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B&D - Disp	12.90%	130,168	8 89%	45		0.04997	0.8018	YES
B3141	M&R-1 Combo Other/Dispatch/FL(%)	R&B&D - Disp	12.90%	130,168	0.00%	35		0.04997	2 2760	YES
B3142	M&R-1 Combo Other/Non-Dispatch/FL(%)	ADSL to Retail	56 90%	1,638	3.66%	82		0.05604	9.5004	YES
B 3.1 5.1	M&R-1 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	3 54%	5,108	0.00%	22		0.03950	0.8971	YES
83152	M&R-1 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ISDN - BRI	9.09%	286	4.65%	129		0.03930	1.4561	YES
B3161	M&R-1 UNE ISDN/Dispatch/FL(%)		2.42%	331	3 51%	57		0.03049	-0.4958	YES
B3162	M&R-1 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI ADSL to Retail	2.42%	1,638	3333%	3		0.28618	0.8235	YES
B3171	M&R-1 Line Sharing/Dispatch/FL(%)	AUSE TO HETAIL	00 90%	1,030	33 3376	<u>_</u>		0.20010	0.02.00	

Florida, Coldber 2001 Bart M, Bart M, Bart D, CED Coldber 2001 Barted M, Bart M, CLED C, CLED Standber M, Sandber M, Sa		Stude Orteban 2001	Developments (007	BST	CLEC	CLEC	Standard	Standard		
Bit 12 Halt 1 Lar BistryNor. Object/P1(N) Bit 20 Add (n Fig Distry 12 Distry 12 <thdistry 12<="" th=""> Distry</thdistry>		Florida, October 2001								76	Coults
Balled Balled<			Analog	Measure	volume	Méssiné	volume	Deviation	Entor	Lacore	Ednità
Balled Balled<		Trans 4 Non Object - Alex Discosts (Cl. (W)	1 ADSL to Retail	2.54%	5 108	6.06%	93		0.03220	0 7796	VES
B3 16 2 Mid: 12 /r Analy Loss Display Mr. Display Mr. (N) Provide Str. (N) B3 16 1 Mid. 12 /r Analy Loss Display Mr. Display Mr. (N) Display Mr. Display Mr. Display Mr. Display Mr. (N) Display Mr. Display Mr. Display Mr. Display Mr. (N) Display Mr. Display											
a) 141 Mich 12 /r Acada Loc Parce Dispect N1 (1x) PABE (PCT3) exit SPT 227% 56.0 (10 / 1.62%) 0.0112 1.4289 N15 B) 12 000 Disp 20 Dispect Dispect N1 (1x) Disp 20 D											
Bit 1: 20 Bit 1: 20 <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<>											
B.1 107 MAR 1 Design Topic Design Stageshift(x) Design Stages						14.04%	57		0.01955	-6.0409	NO
B 31 02 MAR 1 Description Degraphino Degra				4.00%	3,329		14		0.05245		
B.3 TAS Texts Add T. Constructive Tructs Engingthine Enginet Fig. No. Pass PC/TS 2.25% 8.6.6.0 B.3.11.22 MART 1 Low Free Constructive Engingthine Enginet Fig. No. B.3.11.22 MART 1 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive Enginet Fig. No. B.3.11.22 MART 2 Low Free Constructive E	B.3 1 10.2										
B.3 III.21 MAR. I.V.P. Sandardy Department PLYS III.22 IIII.22 III.22 IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII		M&R-1 Other Non-Design/Dispatch/FL(%)									
Bit I: 22 REF Diris Sandardiken, Expectivity. PAB (POTS) 2.39% 8.48.2 Image: Control of the	B 3.1 11.2					0.00%	28		0.02908	0 8340	YES
Customer Process Registry (1) Ref. (POTS) Ref. (POTS) Ref. (POTS) Ref. (POTS) B3211 MAR 2 Second France/Registry (1) Discond Processor (1)											
Balact Balact PostGregatorFl(s) PR4 (POTS) 22% 5.70,060 Balact Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregato	B.3.1.12.2	M&R-1 [LNP (Standalone)/Non-Dispatch/FL(%)	H&B (POTS)	2.35%	66,643		· · · · · · · · · · · · · · · · · · ·				
Balact Balact PostGregatorFl(s) PR4 (POTS) 22% 5.70,060 Balact Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregatorFl(s) Balact PostGregato		Customer Trouble Report Rate									
B3221 MAR2 Coal Vence Transport/Oppic/FL(%) DS1/053 20/FX 45/50 0.07/FX 44/52 0.04/17 44/52 0.04/17 44/52 0.04/57 44/52 0.04/57 44/52 0.04/57 44/52 0.04/57 44/52 0.02/57 0.04/57 44/52 0.02/57 <t< td=""><td>B.3.2.1.1</td><td></td><td>R&B (POTS)</td><td>2.22%</td><td>5,707,099</td><td></td><td></td><td></td><td></td><td></td><td></td></t<>	B.3.2.1.1		R&B (POTS)	2.22%	5,707,099						
Bit State Display High Strain St	B.3 2.1.2	M&R-2 Switch Ports/Non-Dispatch/FL(%)									
B 23 21 MAR2 Uop + Port Combestion/Displation/FL(%) PAR 211% 6.071/12 1.67% 124/482 0.0004/2 16.0008 YES B 32 32 MAR2 Uop + Port Combestion/Displation/FL(%) PAR 11% 6.071/12 1.67% 124% 0.0004/2 16.0008 14/85 B 32 41 MAR2 Comp Comparison/Displation/FL(%) PAR 11% 6.071/12 1.67% 124% 0.0004/2 16.0008 14/85 B 32 41 MAR2 Comp Comparison/Displation/FL(%) PAR 1.67% 2.42% 1.317 0.0006/2 1.6008 14/85 B 32 41 MAR2 Comp Comparison/Probation/FL(%) PAR 1.47% 2.5028 2.42% 1.317 0.0003 1.156 100 B 32 41 MAR2 Comp Comparison/FL(%) PAR PAR 1.47% 25.028 2.62% 1.057 0.0003 1.156 100 1.057 1.057 0.0003 1.156 100 1.156 100 1.156 100 1.156 100 1.156 100 1.156 100 1.1	B 3.2 2 1										
B3221 MAR2 Consc Orefron Combustance (NS) PRABD 11% 6.07.1127 0.55% 124.442 B3244 MAR2 Consc Orefron Comparator (NS) PRABD 12% 6.075.669 3.26% 1.317 0.0000 1.9991 3.2845 B3244 MAR2 Consc Orefron Comparator (NS) PRABD 12% 6.075.669 3.26% 1.317 0.0000 1.9992 1.9356 B3245 MAR2 Consc Orefron Comparator (NS) PRABD 12% 6.075.669 3.26% 1.317 0.0000 1.9492 NO B3245 MAR2 Consc Orefron Comparator (NS) PRABD 11% 5.058 0.0014 2.918 0.0014 2.918 0.0014 2.918 0.0014 2.918 0.0014 2.918 0.0014 2.918 0.0014 2.918 NO 0.0014 1.918 NO 0.0014 1.918 NO 0.0014 1.918 NO 0.0014 1.918 NO 0.0016 0.0014 1.918 NO 0.0016 0.0017 0.0003 0.0016 0.0017 <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<>											
B3.24.1 MAR.2 Control ChemoRono. Dagato/FTL(%) B3.24.2 MAR.2 Control ChemoRono. Dagato/FTL(%) B3.25.2 MAR.2 Control ChemoRono. Dagato/FTL(%) B3.25.2 MAR.2 Control ChemoRono. Dagato/FTL(%) B3.25.2 MAR.2 Control ChemoRono. Dagato/FTL(%) Control ChemoRonon. Dagato/FTL(%) Control ChemoRonon. Dagato/FTL(%) Control ChemoRonon. Dagato/FTL(%) Control ChemoRonon. Dagato/FTL(%) Contro ChemoRonon. Dagato/FTL(%) <td></td>											
B3242 M&R-2 Combo Chem/Hon-Depark/HC/N3 B0251 M&R-2 Combo Chem/Hon-Depark/HC/N3 B0251 MAR-2 Colit COST 1.9452 MO B3251 M&R-2 Colit MO B3251 MAR-2 Colit Coliti Signed Colit Colit Coliti <											
B 32 51 NAR2 LOSI, LOSI, LOSI, LOSI, LOSI, LOSI, MC, MO, Dispatch/TL(%) ADSI. to Reading 0.65% 283,648 1.46% 5.558 0.0017 7.3957 NO B 32.5.2 MAR2 LOSI, LOSI, MC, MO, Dispatch/TL(%) ADSI. to Reading 1.46% 283,668 1.46% 5.558 0.0017 7.3957 NO B 32.5.2 MAR2 LONE (MOND Dispatch/TL(%) ADSI. to Reading 1.46% 283,668 0.40% 5.558 0.0017 7.3957 NO B 32.6.1 MAR2 LONE (MOND Dispatch/TL(%) ADSI. to Reading 1.46% 25.568 0.29% 1.051 0.0017 7.3957 NO B 32.6.1 MAR2 LONE (MOND Dispatch/TL(%) ADSI. to Reading 1.46% 21.5% 6.071 3.016 YES B 32.6.1 MAR2 LOSI. Dosign/Mon Dispatch/TL(%) ADSI. to Reading 2.11% 6.071,127 0.39% 77.073 0.00005 2.26898 YES B 32.6.1 MAR2 LOSI. Dosign/Mon Dispatch/TL(%) BASI.6 0.0077 3.9877 MAR2 0.00077 3.9877 MAR2 0.00077											
32.5.2 VKR2 (DSI, (LOSI, (LOSI, (LOSI, (LOSI, (LOSI, Mot (Cs))))) 104% 28.561 0.409% 5.568 0.00158 3.710 NO 32.8.1 MKR2 (LINE EGNING-ObjectivFL(%)) 11.82% 28.568 0.29% 6.0015 0.00158 3.710 NO 32.8.1 MKR2 (Line Sharing/ObjectivFL(%)) ADSL to Feall 1.94% 28.568 0.29% 6.0015 0.00154 2.3918 VTCS 33.8.1 MKR2 (Line Sharing/ObjectivFL(%)) ADSL to Feall 1.94% 28.568 0.29% 6.0016 2.3918 VTCS 33.8.1 MKR2 (W Analys Loop Design/DespativFL(%)) ADSL to Feall 1.94% 28.568 0.29% 1.051 0.00024 2.7552 NCS 33.8.1 MKR2 (W Analys Loop Design/DespativFL(%)) RAB (POT5) wet SFT 2.97% 6.071 (27 1.55% 77.070 0.00024 2.752 NCS 33.2.1 MKR2 (Dore Design/DespativFL(%)) RAB (POT5) wet SFT 0.99% 1.95% 4.568 0.00077 3.6111 NO 33.2.1 MKR2 (Dore Design/DespativFL(%) RAB 1.11% 6.071 (127 7.12% 6.088 0.00077											
B32 81 MKR-2 (UKE (SONDenpect/VL(%)) DOTES -0.00153 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00157 -0.00177 -0.00											
B32 82 KKR2 UNKE SUMMON Dispatch FL(%) Ox014 2.818 YES B32.71 KKR2 Line Shaing/Non-Dispatch FL(%) Ox014 2.818 YES B32.71 KKR2 Line Shaing/Non-Dispatch FL(%) Ox024 0.878 0.2976 0.0014 2.818 YES B32.81 KKR2 Line Shaing/Non-Dispatch FL(%) B32.81 MAR2 0.0014 2.818 YES B32.81 MAR2 WAINGQ LOD Dispitch FL(%) B48-Disp 2.11% 6.071,127 0.58% 7.7073 0.00055 22.898 YES B32.81 MAR2 WAINGQ LOD Dispitch FL(%) B48-Disp 2.11% 6.071,127 0.58% 7.7073 0.00055 22.898 YES B32.81 MAR2 Diran Long Dispitch FL(%) B48-Disp 0.01% 4.285 0.00077 0.5111 NO B32.81 MAR2 Diran Long Dispitch FL(%) B48 1.28% 0.0054 9.0330 NO B32.11 MAR2 Line Kontainey Dispatch FL(%) B48 B48<											
13.2.1 MAR-2 Line ShampQto-DigatchTL(%) 0.02244 1.3783 YES 13.2.1 MAR-2 Line ShamQto-DigatchTL(%) 0.00244 1.3783 YES 13.2.1 MAR-2 Line ShamQto-DigatchTL(%) 0.00053 2.6696 1.051 0.00054 2.798 No 13.2.2 MAR-2 WAnaug Loop Design/DispatchTL(%) Rab 0.00051 2.178 6.071,127 1.59% 77.073 0.00053 2.6696 YES 13.2.2 MAR-2 WAnaug Loop Nun-DisgtchTL(%) Rab PBB PC115 5.077.049 2.01% 44.565 0.00071 2.2752 YES 13.2.1.0 MAR-2 Ohme Design/DispatchTL(%) Design/Nun-DisgtchTL(%) Design/Nun-DisgtchTL(%) Design/Nun-DisgtchTL(%) 0.00071 2.2752 YES 0.00071 2.2752 YES 13.2.1.1 MAR-2 Dimer Design/DispatchTL(%) Design/Nun-DisgtchTL(%) Design/Nun-DisgtchTL											
B 32 21 MAR-2 Line Sharing/Non-Dispatch/FL(%) ADSL to Reall 1.49%, 203,0868 3.14%, 1.051 0.00409 2.2796 NO B 32 21 MAR-2 WA nalig Loop Design/Non-Dispatch/FL(%) Rab 0.0053 1.11%, 6.071,127 1.53%, 77,073 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.1051 0.00053 11.11%, 1.11%,											
B 32 81 MAR 2 EW Anize Loop Design/Obspatch/FL(%) RAB PMB											
B 28 2 MBR-2 ZW Ansig Loop Non-Displat/PFL(%) R4B Obsor Z 11% 6.077.127 0.38% 77.073 0.00003 22.758 YES B 28 201 MBR-2 ZW Ansig Loop Non-Displat/PFL(%) Design/Displat/PFL(%) Design/Displat/PFL(%) 0.00071 2.17% 5.707.099 0.13% 44.595 0.000071 1.8272 YES B 22 101 MBR-2 Other Design/Displat/PFL(%) Design 0.44% 5.802.33 0.35% 1.03% 0.00017 0.427.22 YES B 22 112 MBR-2 Other Non-Design/Displat/PFL(%) R4B 1.01% 1.385 0.00176 0.427.2 YES B 22 112 MBR-2 Other Non-Design/Displat/PFL(%) R4B 1.11% 6.071.127 4.07% 688 0.00403 -7.3302 NO B 23 112 MBR-3 Switch Pots/Displat/PFL(%) R4B 1.11% 5.707.099 0.00403 -7.3302 NO B 33 11 MBR-3 Switch Pots/Displat/PFL(hours) R4B (POTS) 2.25% 1.26.479 2.2464											
B 3 2 9 1 MBA 2 ZW Ansig Loop Non-Design/Depatch/FL(%) PBB (POTS) soci SB FT 2.21% 5,707.090 2.01% 44.595 0.00071 2.722 YES B 3 2 0 1 MBA 2 ZW Ansig Loop Non-Design/Depatch/FL(%) Design Non-Design/Depatch/FL(%) 0.95% 5,707.090 0.13% 44.595 0.00071 3.8111 NO B 3 2 10 1 MBA 2 Other Design/Depatch/FL(%) R4B (POTS) 838.23 1.01% 1.385 0.00170 3.8111 NO B 3 2 112 MBA 2 Other Non-Design/Depatch/FL(%) R4B 0.077 4.475% 688 0.00054 9.033 NO B 3 2 112 MBA 2 IMBA 2 INF Non-Design/Depatch/FL(%) R4B POTS) 2.22% 5,707.099 0.0004 7.3302 NO B 3 2 12 MBA 2 IMBA 2 Sinth Poto/Dispatch/FL(%) R4B (POTS) 2.25% 5,707.099 0.000											
B 2 9 2 MAR 2 Dirk Analog Loop Non-Despto/PL(%) Desgn 0.99% 5/07.099 0.13% 44.695 0.00047 18.2230 YES B 22 10 1 MAR 2 Other Design/Depath/PL(%) Desgn 0.49% 892.233 0.39% 1.385 0.00047 18.2230 YES B 22 10 1 MAR 2 Other Design/Depath/PL(%) Base 11 5.077.099 0.13% 44.695 0.00047 18.2230 YES B 22 10 1 MAR 2 Other Non-Despto/PL(%) Base 11.2% 6.071.127 1.2% 688 0.00545 9.033 B 32 12 1 MAR 2 Other Non-Despto/PL(%) Base 11.2% 6.071.127 4.07% 688 0.00403 7.3302 NO B 32 12 1 MAR 3 Isota Non-Despto/PL(%) Base 11.2% 6.071.127 4.07% 688 0.00403 7.3302 NO B 33 11 MAR 3 Isota Non-Despto/PL(%) Base 112% 5.070.099 1.7% 7.70.699 1.7% 7.902 1.7% 7.902 1.7% 7.902 1.7% 1.7% 5.707.099 1.7% 1.7% 1.7% 1.7%											
B 22 101 MAR 2 Other Design/Despatch/FL(%) 0.00170 3.6111 NO B 22 102 MAR 2 Other Design/Despatch/FL(%) 0.00176 3.6111 NO B 22 101 MAR 2 Other Mon-Design/Despatch/FL(%) 0.00176 3.6111 NO B 32 112 MAR 2 Other Mon-Design/Despatch/FL(%) 0.00176 0.424 PES B 32 121 MAR 2 Other Mon-Design/Despatch/FL(%) 0.00176 0.427 YES B 32 122 MAR 2 Other Mon-Design/Despatch/FL(%) 0.00176 0.427 YES B 33 12 MAR 2 Other Mon-Despatch/FL(%) 0.00176 0.427 YES B 33 12 MAR 3 Switch Ports/Despatch/FL(murs) 0.00176 0.427 YES B 33 12 MAR 3 Switch Ports/Despatch/FL(hours) 0.00176 0.427 YES B 33 22 MAR 3 Switch Ports/Despatch/FL(hours) 0.00176 0.427 YES B 33 22 MAR 3 Switch Ports/Despatch/FL(hours) 0.00176 0.427 YES <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<>											
B 32 10 2 MAR 2 Other Design/Non-Dispatch/FL(%) Design 0.44% 692,233 0.39% 1,395 0.00554 9.0273 NO B 32 111 MAR 2 Other Non-Design/Non-Dispatch/FL(%) BAB 1,12% 6.03 0.00554 9.0273 NO B 32 112 MAR 2 Differ Non-Design/Non-Dispatch/FL(%) BAB 1,12% 6.03 0.00554 9.00054 9.0											
B 22 111 NARE 2 Oher Non-Design/VEspatch/FL(%) B 23 B 23 D 2012 NARE 2 D 2012											
B 3 2 11 2 M&R-2 [Dimer Non-Design/Non-Design/Non-Design/Non-Design/Non-Dispatch/FL(%) R&B 1.12% 6.071;127 4.07% 688 0.00403 7.3302 NO B 3 2 12 2 M&R-2 [LNP (Standialong)/Non-Dispatch/FL(%) R&B 1.12% 6.071;127 4.07% 688 0.00403 7.3302 NO Mather Summa Analysis Mather Summa Analysis Mather Summa Analysis A start for the Summa Analysis B 33 11 MARe Colspan="2">Colspan=2">Colspan=2">Colspan=2"Colspan="2">Colspan=2"Colspan="2">Colspan=2"Colspan="2"Colspan="2">Colspan=2"Colspan="											
B.3 2 12 1 MBR-2 LIAP (Standalone)/Dispatch/FL(%) RAB (POTS) 222% 5.707.099							688		0 00403	7.3302	NO
B 32 12 2 M&R-2 LNP (Standalone)/Non-Dispatch/FL(hours) M&R-3 Switch Ports/Non-Dispatch/FL(hours) PAB (POTS) RAB (POTS) B.33 11 M&R-3 Switch Ports/Non-Dispatch/FL(hours) B.33 12 M&R-3 Switch Ports/Non-Dispatch/FL(hours) B.33 12 B.33 12 M&R-3 Local Interoffice Transport/Non-Dispatch/FL(hours) B.33 12 M&R-3 Local Interoffice Transport/Non-Dispatch/FL(hours) B.33 12 B.33 12 M&R-3 Local Interoffice Transport/Non-Dispatch/FL(hours) B.33 22 B.33 22 <t< td=""><td></td><td></td><td>R&B (POTS)</td><td>2.22%</td><td>5,707,099</td><td></td><td></td><td></td><td></td><td></td><td></td></t<>			R&B (POTS)	2.22%	5,707,099						
B.33 11 M&R-3 Switch Ports/Nor-Dispatch/FL(hours) PAB (POTS) 23.58 128.479 23.56 23.68 23.11 B.33 12 M&R-3 Switch Ports/Nor-Dispatch/FL(hours) B.33.11 M&R-3 Local Interoffice Transport/Dispatch/FL(hours) PAB (POTS) 8.08 66.643 22.173 VES B.33.2 M&R-3 Local Interoffice Transport/Nor-Dispatch/FL(hours) DSI/DS3 1.78 756 0.92 16 2.404 0.60723 1.4118 VES B.33.31 M&R-3 Local Interoffice Transport/Nor-Dispatch/FL(hours) R&B 23.50 128.404 16.88 1.805 20.544 0.60723 1.4118 VES B.33.32 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 23.50 128.404 16.88 1.805 29.544 0.60723 1.4118 VES B.33.41 M&R-3 Comb O Uner/Oispatch/FL(hours) R&B 23.22 130.168 10.30 45 29.418 4.9624 VES B.33.51 M&R-3 LOSL INORDISpatch/FL(hours)			R&B (POTS)	1 17%	5,707,099						
B.33 11 M&R-3 Switch Ports/Nor-Dispatch/FL(hours) PAB (POTS) 23.58 128.479 23.56 23.68 23.11 B.33 12 M&R-3 Switch Ports/Nor-Dispatch/FL(hours) B.33.11 M&R-3 Local Interoffice Transport/Dispatch/FL(hours) PAB (POTS) 8.08 66.643 22.173 VES B.33.2 M&R-3 Local Interoffice Transport/Nor-Dispatch/FL(hours) DSI/DS3 1.78 756 0.92 16 2.404 0.60723 1.4118 VES B.33.31 M&R-3 Local Interoffice Transport/Nor-Dispatch/FL(hours) R&B 23.50 128.404 16.88 1.805 20.544 0.60723 1.4118 VES B.33.32 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 23.50 128.404 16.88 1.805 29.544 0.60723 1.4118 VES B.33.41 M&R-3 Comb O Uner/Oispatch/FL(hours) R&B 23.22 130.168 10.30 45 29.418 4.9624 VES B.33.51 M&R-3 LOSL INORDISpatch/FL(hours)		Malatananan Augurtan	-								
B.3 1 2 M&R-3 Switch Ports/Non-Dispatch/FL(hours) 9.09 66,643 22.02 0.00 0 8.085 YES B.3.2.1 M&R-3 Local Interoffice Transport/Dispatch/FL(hours) DS1/DS3 4.62 1,030 0.00 0 8.085 YES B.3.3.1 M&R-3 Local Interoffice Transport/Dispatch/FL(hours) DS1/DS3 4.62 1,030 0.00 0 8.085 YES B.3.3.1 M&R-3 Local Interoffice Transport/Dispatch/FL(hours) R&B 23.50 128,404 16.88 1,855 29.416 4.9673 YES B.3.3.4 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 8.02 67.769 3.83 686 22.024 0.04511 4.9634 YES B.3.3.4 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) RABBD - Disp 23.23 130,168 10.30 45 29.416 4.9721 4.0642 YES B.3.3.1 M&R-3 LOSL (ADSL HDSL and UCL/Non-Dispatch/FL(hours) ADSL to Retail 6.31 5.108 3.18 22 17.491 3.73714 0.8369 YES	B 2 2 1 1		BAB (POTS)	23.58	128 479			20 458			
B 33.2 1 M&R-3 Local interoffice Transport/Dispatch/FL(hours) DS1/DS3 1.78 756 0.92 18 2404 0.60723 1.4118 YES B 33.2 2 M&R-3 Local interoffice Transport/Non-Dispatch/FL(hours) DS1/DS3 1.78 756 0.92 18 2404 0.60723 1.4118 YES B 33.3 2 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 2350 128,404 16.88 1.825 24.64 0.60723 1.4118 YES B 33.3 2 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 0.02 67,789 3.83 686 22.024 0.84511 4.9623 YES B 33.4 1 M&R-3 Combo Other/Non-Dispatch/FL(hours) R&B 0.02 67,789 3.83 686 2.9416 4.9878 2.9476 YES B 33.5 1 M&R-3 XDSL (ADSL, HDSL and UCL/Non-Dispatch/FL(hours) ADSL to Retail 6.31 5.108 6.46 82 4.9783 6.6466 1.8255 YES B 33.6 1 M&R-3 UNE ISDN/Dispatch/FL(hours) ISDN - BRI 8.64 2.86 <td></td>											
B 33.22 MAR-3 Local Interoffice Transport/Non-Dispatch/FL(hours) DS1/DS3 1.78 756 0.92 16 2.404 0.60723 1.4116 YES B 33.31 MAR-3 Loog + Port Combinations/Non-Dispatch/FL(hours) RAB 23.50 128,404 16.86 1.855 28.534 0.69066 9.5841 YES B 33.32 MAR-3 Loog + Port Combinations/Non-Dispatch/FL(hours) RAB 8.02 67.769 1.866 22.024 0.84511 4.9634 YES B 33.34 MAR-3 Combo Other/Non-Dispatch/FL(hours) RABAD - Disp 23.23 130,168 10.30 45 29.416 4.9624 YES B 33.52 MAR-3 XDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(hours) ADSL to Retail 6.31 5.108 3.18 22 17.491 3.7714 0.8369 YES 8.84 ADSL to Retail 6.31 5.108 3.18 22 17.491 3.7714 0.8369 YES B 3361 MAR-3 UNE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 8.64 286 7.69 129 12.715 1.348448 0.7046 YES <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.00</td> <td>0</td> <td></td> <td></td> <td></td> <td>YES</td>						0.00	0				YES
B 3.3.31 M&R-3 Loop + Port Combinations/Depatch/FL(hours) R&B 23 50 128,404 16.88 1,855 29.534 0.69066 9.5841 YES B 3.3.32 M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours) R&B 8.02 67,789 3.83 0685 22.024 0.04511 4.96534 YES B 3.3.41 M&R-3 Combo Other/Non-Dispatch/FL(hours) R&B&D - Disp 23.23 130,168 10.30 45 29.416 4.987281 4.0642 YES B 3.3.51 M&R-3 xDSL (ADSL, HDSL and UCL/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.63468 11.5255 YES B 3.3.61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.64468 11.5255 YES B 3.3.61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 8.64 280 7.69 129 12.715 134848 0.7045 YES B 3.3.61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) SDSL to Retail 71.39							16	2 404	0.60723	1.4118	YES
B 3.3 32 M&R-3 Loop + Pot Combinations/Non-Dispatch/FL(hours) R&B 6.02 67,769 3.83 686 22.024 0.044511 4.9634 YES B 3.3 4.1 M&R-3 Combo Other/Non-Dispatch/FL(hours) RABAD - Disp 23.23 130,168 10.30 45 29.416 4.38578 2.9478 YES B 3.3 51 M&R-3 Combo Other/Non-Dispatch/FL(hours) RABAD - Disp 23.23 130,168 10.30 45 29.416 4.38578 2.9478 YES B 3.3 51 M&R-3 XDSL (ADSL, HDSL and UCL/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.63468 11.5255 YES B 3.3 61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.63468 11.5255 YES B 3.3 61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 8.64 228 7.69 129 12.715 13.4844 0.0451 YES B 3.3 71 M&R-3 Line Sharing/Dispatch/FL(hours) ADSL to Retail 6.31 5.10								29.534		9.5841	
B.3.34.1 M&R-3 Combo Other/Dispatch/FL(hours) R&B&D - Disp 23.23 130,168 10.30 45 29.478 YES B.3.34.2 M&R-3 xDSL (hours) R&B&D - Disp 23.23 130,168 3.02 35 29.478 YES B.3.35.2 M&R-3 xDSL (ADSL, HDSL and UCL//Dispatch/FL(hours) ADSL to Retail 71.39 1.688 0.42 49.789 2.9478 YES B.3.35.2 M&R-3 xDSL (ADSL, HDSL and UCL//Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.68 6.42 29.78 14.0642 YES B.3.36.1 M&R-3 UNE ISDN/Dispatch/FL(hours) ADSL to Retail 71.39 1.68 6.42 286 7.69 129 12.715 1.34646 0.7045 YES B.3.36.1 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ADSL to Retail 6.31 5.16 57 10.658 1.52846 -0.8470 YES B.3.37.2 M&R-3 UME Sharing/Dispatch/FL(hours) ADSL to Retail 6.31 5.108 8.46 33 17.491 3.0548.3 -0.0470 YES B.3.38.1			R&B		67,789	3.83		22.024	0.84511	4.9634	YES
B 3.3 4.2 M&R-3 Combo Other/Non-Dispatch/FL(hours) R&B&D Disp 23.23 130,168 3.02 35 29.416 4 97281 4.0642 YES B 3.3 51 M&R-3 XDSL (ADSL, HDSL and UCL//Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.63468 11.5255 YES B 3.3 61 M&R-3 UNE ISDN/Dispatch/FL(hours) ADSL to Retail 5.016 3.18 22 17.491 3.73714 0.3659 YES B 3.3 62 M&R-3 UNE ISDN/Dispatch/FL(hours) ISDN - BRI 8.64 286 7.69 129 12.715 1.34848 0.7045 YES B 3.3 61 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 8.64 286 7.69 129 12.715 1.34848 0.7045 YES B 3.3 71 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 21.62 3 1.72491 3.73714 0.8369 YES B 3.3 8.1 M&R-3 UM anlog Loop Design/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 2			R&B&D - Disp	23.23	130,168	10.30	45	29.416	4 38578	2.9478	
B 3.3 5 1 M&R-3 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours) ADSL to Retail 71.39 1.638 6.45 82 49.793 5.63468 11.5255 YES B 3.3 5 2 M&R-3 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours) ADSL to Retail 6.31 5.108 3.18 22 17.491 3.73714 0.8369 YES B 3.3 6 1 M&R-3 UNE ISDN/Dispatch/FL(hours) ISDN - BRI 8.64 236 7.69 129 12.715 134848 0.7045 YES B 3.3 6 1 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 8.64 236 7.69 129 12.715 134848 0.7045 YES B 3.3 7 1 M&R-3 Line Sharing/Dispatch/FL(hours) ISDN - BRI 8.64 236 7.10 1.52845 -0.0470 YES B 3.3 7 1 M&R-3 Line Sharing/Non-Dispatch/FL(hours) ADSL to Retail 71.39 1.638 21.62 3 49.793 28.77437 1.7333 YES B 3.3 8.1 M&R-3 Line Sharing/Non-Dispatch/FL(hours) R&B Disp 23.50 128.404 7.10			R&B&D - Disp	23.23	130,168	3.02	35	29.416	4 97281	4.0642	
B 3 3 5 2 M&R-3 xDSL (ADSL, HDSL and UCL//Non-Dispatch/FL(hours) ADSL to Retail 6.31 5,108 3.18 22 17.491 373714 0.8369 YES B 3 3 6 1 M&R-3 UNE ISDIV/Dispatch/FL(hours) ISDN - BRI 8.64 286 7.69 129 12.715 134848 0.7045 YES B 3 3 6 2 M&R-3 UNE ISDIV/Non-Dispatch/FL(hours) ISDN - BRI 3.87 310 5.16 57 10.658 1.52845 0.0470 YES B 3 3 7 2 M&R-3 Line Sharing/Dispatch/FL(hours) ADSL to Retail 6.31 5,108 846 33 17.491 3.05463 -07046 YES B 3 3 8.1 M&R-3 ZW Analog Loop Design/Dispatch/FL(hours) ADSL to Retail 6.31 5,108 846 33 17.491 3.05463 -07046 YES B 3 3 8.1 M&R-3 ZW Analog Loop Design/Dispatch/FL(hours) R&B Disp 23.50 128,404 4 11 296 29.534 1.72150 11.2634 YES B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Dispatch/FL(hours) R&B Disp	B 3.3 5 1		ADSL to Retail	71 39	1,638	6.45	82	49.793	5.63468	11.5255	
B 3 3 6 2 M&R-3 UNE ISDN/Non-Dispatch/FL(hours) INE ISDN/Non-Dispatch/FL(hours) ISDN - BRI 3.87 331 5.16 57 10.658 1.52845 -0.8470 YES B 3 3 7 1 M&R-3 Line Sharing/Dispatch/FL(hours) ADSL to Retail 71.39 1.638 21.52 3 49.793 28.77437 1.7333 YES B 3 3 7 2 M&R-3 Line Sharing/Dispatch/FL(hours) ADSL to Retail 6.31 5,108 846 33 17.491 3.05463 -0.7048 YES B 3 3 8.1 M&R-3 ZW Analog Loop Design/Dispatch/FL(hours) R&B - Disp 23.50 128,404 7.10 1.177 29.534 0.84690 18.9572 YES B 3 3 9 1 M&R-3 ZW Analog Loop Design/Dispatch/FL(hours) R&B - Disp 23.50 128,404 4.11 295 29.534 1.72150 11.2634 YES B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Non-Dispatch/FL(hours) R&B (POTS) excl SB FT 23.56 128,107 16.30 698 29.469 0.96690 7.3539 YES B 3 3 10 1 M&R-3 Other Design/Non-Dispatch/FL(ho	B3352		ADSL to Retail	6.31							
B 3 3 7 1 M&R-3 Line Sharing/Dispatch/FL(hours) B 3 3 7 2 M&R-3 Line Sharing/Dispatch/FL(hours) B 3 3 8.1 M&R-3 ZW Analog Loop Design/Dispatch/FL(hours) B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Dispatch/FL(hours) B 3 3 9 2 M&R-3 ZW Analog Loop Non-Design/Dispatch/FL(hours) R&B B 3 3 10 1 M&R-3 Other Design/Dispatch/FL(hours) R&B B 3 3 10 2 M&R-3 Other Non-Design/Dispatch/FL(hours) Design B 3 3 10 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design B 3 3 10 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design B 3 3 10 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design B 3 3 10 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design B 3 3 10 1 <td>B3361</td> <td>M&R-3 UNE ISDN/Dispatch/FL(hours)</td> <td>ISDN - BRI</td> <td></td> <td></td> <td>7.69</td> <td></td> <td></td> <td></td> <td></td> <td></td>	B3361	M&R-3 UNE ISDN/Dispatch/FL(hours)	ISDN - BRI			7.69					
B 3 3 7 2 M&R-3 Line Sharing/Non-Dispetch/FL(hours) ADSL to Retail 6.31 5,108 8.46 33 17/491 3.05463 -0.7046 YES B 3 3 8.1 M&R-3 2W Analog Loop Design/Dispetch/FL(hours) R&B Disp 23.50 128,404 7.10 1,177 29.534 0.86480 18 9572 YES B 3 3 8.1 M&R-3 2W Analog Loop Design/Dispetch/FL(hours) R&B Disp 23.50 128,404 7.10 1,177 29.534 0.86480 18 9572 YES B 3 3 9.1 M&R-3 2W Analog Loop Non-Design/Dispetch/FL(hours) R&B Disp 23.50 128,404 4.11 295 29.634 0.86480 18 9572 YES B 3 3 9.1 M&R-3 ZW Analog Loop Non-Design/Dispetch/FL(hours) R&B POS 23.50 128,404 4.11 295 29.634 4.20210 NO B 3 3 10.1 M&R-3 Other Design/Dispetch/FL(hours) Design Design 10.60 3.329 8.27 14 13.734 3.67838 0.6343 YES B 3 3 10.2 M&R-3 Other Non-Design/Dispetch/	B3362	M&R-3 UNE ISDN/Non-Dispatch/FL(hours)									
B 3 8.1 M&R-3 2W Analog Loop Design/Dispatch/FL(hours) P&B P P P P P S	B3371	M&R-3 Line Sharing/Dispatch/FL(hours)									
B 3 8 2 M&R-3 ZW Analog Loop Design/Non-Dispatch/FL(hours) R&B - Disp 23.50 128,404 4 11 295 29.534 1.72150 11 2634 YES B 3 3 9 1 M&R-3 ZW Analog Loop Design/Non-Dispatch/FL(hours) R&B - Disp 23.56 128,404 4 11 295 29.534 1.72150 11 2634 YES B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Non-Dispatch/FL(hours) R&B (POTS) excl SB FT 23.56 126,107 16 30 098 29.469 0.96690 7.3539 YES B 3 3 9 2 M&R-3 ZW Analog Loop Non-Design/Non-Dispatch/FL(hours) R&B (POTS) excl SB FT 8.45 56,477 14 62 57 23.045 3 05394 -2 0210 NO B 3 3 10 1 M&R-3 Other Design/Non-Dispatch/FL(hours) Design 10.600 3.329 8.27 14 13 734 3.67838 0.6343 YES B 3 3 10 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design 2.71 3.654 2.25 5 7.095 3.17493 0.6414 YES B 3 3 11 1 M&R-3 Other Non-Design/Dispatch/FL(hours)	B3372										
B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Dispatch/FL(hours) R&B (POTS) excl SB FT 23.86 126,107 16.30 898 29.469 0.98690 7.3539 YES B 3 3 9 1 M&R-3 ZW Analog Loop Non-Design/Dispatch/FL(hours) R&B (POTS) excl SB FT 8.45 56,477 14.62 57 23.045 3.05394 -2.0210 NO B 3 3 10 1 M&R-3 Other Design/Dispatch/FL(hours) Design 10.60 3.329 8.27 14 13.734 3.67838 0.6343 YES B 3 3 10 2 M&R-3 Other Non-Design/Non-Dispatch/FL(hours) Design 2.71 3.654 2.25 5 7.095 3.1443 YES B 3 3 11 1 M&R-3 Other Non-Design/Dispatch/FL(hours) Design 2.350 128,404 26.20 49 29.534 4.21993 -0.6414 YES											
B 3 3 9 2 M&R-3 2W Analog Loop Non-Design/Non-Dispatch/FL(hours) R&B (POTS) excl SB FT 8.45 56,477 14 62 57 23.045 3 05394 -2 0210 NO B 3 3 10 1 M&R-3 Other Design/Dispatch/FL(hours) Design 10.60 3.329 8.27 14 13 734 3.67838 0.6343 YES B 3 3 10 2 M&R-3 Other Design/Non-Dispatch/FL(hours) Design 2.71 3.654 2.25 5 7.095 3.17493 0.1443 YES B 3 3 11 1 M&R-3 Other Non-Design/Dispatch/FL(hours) R&B 23 50 128,404 26 20 49 29 534 4.21993 -0.6414 YES											
B 3 3 10 1 M&R-3 Other Design/Dispatch/FL(hours) Design 10.60 3.329 8.27 14 13 734 3.67838 0.6343 YES B 3 3 10 2 M&R-3 Other Design/Vion-Dispatch/FL(hours) Design 2.71 3,654 2.25 5 7.095 3.17493 0.1443 YES B 3 3 11 1 M&R-3 Other Non-Design/Dispatch/FL(hours) R&B 23 50 128,404 26 20 49 29 534 4.21993 -0.6414 YES											
B 3 3 10 2 M&R-3 Other Design/Non-Dispatch/FL(hours) Design 2.71 3,654 2.25 5 7.095 3.17493 0.1443 YES B 3 3 11 1 M&R-3 Other Design/Non-Dispatch/FL(hours) Design 23 50 128,404 26 20 49 29 534 4.21993 -0.6414 YES											
B 3 3 111 Må R-3 Other Non-Design/Dispatch/FLihours) RåB 23 50 128,404 26 20 49 29 534 4.21993 -0.6414 YES											
B 3 3.11 2 M&R-3 (Other Non-Design/Non-Dispatch/FL(hours) R&B 8.02 67,789 3.89 28 22 024 4 16293 0 9928 YES				and the second se							
	B 3 3.11 2	M&R-3 Other Non-Design/Non-Dispatch/FL(hours)	J 848	8.02	67,789	1 3.89	28	1 22 024	4 16293	0 9928	169

	BellSouth Monthly State Summary									
	Florida, October 2001	Benchmark /	BŜT	9ST	CLEC	CLEC	Standard	Standard		
	······	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		•							·	
B 3 3 12 1	M&R-3 LNP (Standalone)/Dispatch/FL(hours)	R&B (POTS)	23.56 8 08	126,479 66,643			29.458 22.173		·	
B 3.3 12 2	M&R-3 [LNP (Standalone)/Non-Dispatch/FL(hours)	R&B (POTS)	000	00,043			22.00			
	% Repeat Troubles within 30 Days	-								
B.3.4.1 1	M&R-4 Switch Ports/Dispatch/FL(%)	R&B (POTS)	20.66%	128,479					j	I
B 3 4.1 2	M&R-4 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	18.12%	66,643	0.000					YES
B 3.4.2 1	M&R-4 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	33.79% 30.95%	1,030	0.00%	0		0.11679	-1.6309	YES
B 3 4.2.2	M&R-4 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3 R&B	20.59%	128,404	19,19%	1.855		0.00946	1.4764	YES
B 3.4.3 1	M&R-4 Loop + Port Combinations/Dispatch/FL(%) M&R-4 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	18.06%	67,789	17.64%	686		0.01476	0.2878	YES
B 3 4.3.2 B 3 4 4.1	M&R-4 Combo Other/Dispatch/FL(%)	R&B&D - Disp	20.85%	130,168	17.78%	45		0.06056	0 5065	YES
B 3 4.4.2	M&R-4 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	20.85%	130,168	11 43%	35	1	0.06867	1.3713	YES
B 3 4.5 1	M&R-4 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	49.27%	1,638	19.51%	62		0.05657	5.2594	YES
B.345.2	M&R-4 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	43.27%	5,108	13.64%	22		0.10586	2.7990	YES
8.3461	M&R-4 UNE ISDN/Dispatch/FL(%)	ISON - BRI	32.87%	286	29.46%	129		0 04982	0.6844	YES
B 3.4.6.2	M&R-4 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	33.23%	331	19.30%	57		0.06755	2.0628	YES
B 3 4.7.1	M&R-4 Une Sharing/Dispatch/FL(%)	ADSL to Retail	49.27%	1,638	33.33%	33	ł	0.28891 0.08652	0.5515 3.2493	YES
B.3.4 7 2	M&R-4 Une Sharing/Non-Dispatch/FL(%)	ADSL to Retail	43 27%	5,108	15.15% 15.46%	1,177		0.08652	4.3281	YES
8348.1	M&R-4 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp R&B - Disp	20.59%	128,404	12 20%	295		0.01164	3.5573	YES
B3482	M&R-4 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	20.62%	126,107	15.48%	898		0.01355	3.7965	YES
B.3 4 9.1	M&R-4 12W Analog Loop Non-Design/Dispatch/FL(%) M&R-4 12W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	18.08%	56,477	28 07%	57		0.05100	-1.9584	NO
834.92	M&R-4 Other Design/Dispatch/FL(%)	Design	37.85%	3,329	28.57%	14		0.12990	0.7142	YES
B34101 B34.102	M&R-4 Other Design/Non-Dispatch/FL(%)	Design	35.30%	3,654	60 00%	5		0.21388	·1.1547	YES
834111	M&R-4 Other Non-Design/Dispatch/FL(%)	R&B	20.59%	128,404	16 33%	49		0.05777	0 7375	YES
834.112	M&R-4 Other Non-Design/Non-Dispatch/FL(%)	R&B	18.06%	67,789	3.57%	28		0 07272	1.9929	YES
B 3 4 12 1	M&R-4 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	20 66%	126,479						
B 3 4 12 2	M&R-4 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	18.12%	66,643						
	Out of Service > 24 hours									
B3511	M&R-5 [Switch Ports/Dispatch/FL(%)	R&B (POTS)	26.02%	84,303						
83512	M&R-5 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	9.82%	21,578						
B3521	M&R-5 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1.17%	1,030	0.00%	0				YES
B.3522	M&R-5 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0.00%	756	0.00%	16		0.00000		YES
B3531	M&R-5 Loop + Port Combinations/Dispatch/FL(%)	R&B	25.93%	85,662	18.33%	1,271		0.01238	6.1371	YES
B3532	M&R-5 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	9.71%	22,067	1.77%	395		0.01503	5 2797	YES YES
B 3 5 4 1	M&R-5 Combo Other/Dispatch/FL(%)	R&B&D · Disp	25.42%	87,749	8.89%	45 35		0.06492	2.5457	YES
B3542	M&R-5 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp ADSL to Retail	25.42% 56.90%	67,749 1,638	0.00%	<u> </u>		0.07361	9,5004	YES
83551	M&R-5 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	3.54%	5,108	0.00%	22	-	0.03950	0.8971	YES
B3552	M&R-5 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ISDN - BRI	9.47%	285	4 65%	129	·	0.03308	1.5518	YES
B356.1	M&R-5 UNE ISDN/Dispatch/FL(%) M&R-5 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	2.42%	331	3.51%	57		0 02202	-0.4958	YES
B 3 5 6 2 B 3 5 7 1	M&R-5 [Une ISDN/Non-Dispatch/FL(%)	ADSL to Retail	56.90%	1,638	100.00%	1		0 49537	-0.8701	YES
B357.2	Mar-s Line Shang/Non-Dispatch/FL(%)	ADSL to Retail	3.54%	5,108	0.00%	0	1			YES
B3581	M&R-5 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	25.93%	85,662	5.69%	1,177		0.01286	15.7360	YES
B.3.5 8 2	M&R-5 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	25.93%	85,662	1.36%	295		0 02556	9.6149	YES
B.3591	M&R-5 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	26.01%	84,255	25.69%	109		0.04205	0.0766	YES
B.3.5 9 2	M&R-5 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	9.87%	21,347	41.67%	12		0 08611	3 6932	NO
B 3 5.10 1	M&R-5 Other Design/Dispatch/FL(%)	Design	4.03%	3,328	7.14%	14		0.05265	-0 5919	YES
B 3 5 10 2	M&R-5 Other Design/Non-Dispatch/FL(%)	Design	0.99%	3,654	0.00%	5		0 04420	0.2229	YES
B 3 5 11 1	M&R-5 Other Non-Design/Dispatch/FL(%)	R&B	25.93%	85,662	37 84%	37		0 07207	-1.6521	NO
B 3.5.11 2	M&R-5 Other Non-Design/Non-Dispatch/FL(%)	R&B	9.71%	22,067	0 00%	15		0 07647	1.2694	YES
B 3 5 12 1	M&R-5 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	26.02%	84,303	ł				├ ────	↓
B 3 5 12 2	M&R-5 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	9.82%	21,578	<u> </u>				L	
	Unbundled Network Elements - Billing									
	Invoice Accuracy	-								
B41	B-1 FL(%)	BST - State	97 93%	\$492,661,862	99 95%	\$6,194,623		0 00006	-351 8966	YES
	Mean Time to Deliver Involces - CRIS									
842	B-2 Region(business days)	BST - Region	391	1	3 38	1,313				YES
				-						

Florida	October	2001

	Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
	Local Interconnection Trunks - Ordering									
C11	% Rejected Service Requests O-7 [Local Interconnection Trunks/FL(%)	Diagnostic			56.25%	128	1			Diagnostic
C 1 2	Reject Interval O-8 [Local Interconnection Trunks/FL(%)	>= 85% w in 4 days			79.17%	72				NÖ
C13	FOC Timeliness O-9 [Local Interconnection Trunks/FL(%)	> ∞ 95% w in 10 days			95.24%	105				YES
C 1.4	FOC & Reject Response Completeness O-11 Local Interconnection Trunks/FL(%)	>= 95%			89.20%	111				NO
C.1.5	FOC & Reject Response Completeness (Multiple Responses) O-11 [Local Interconnection Trunks/FL(%)	>= 95%								
	Local Interconnection Trunks - Provisioning									
C 2 1	Order Completion Interval P-4 [Local Interconnection Trunks/FL(days)	Parity w Retail	15 35	136	27 66	29	10.442	2.13572	-5.7602	NO I
C 2 2	Heid Orders P-1 [Local Interconnection Trunks/FL(days)	Parity w Retail	0.00	ō	0.00	0				YES
C.2 3	% Jeopardies P-2 [Local Interconnection Trunks/FL(%)	Parity w Retail	0.00%	146	0.00%	33		0 00000 1		YES
C 2 4	Average Jeopardy Notice Interval P-2 [Local Interconnection Trunks/FL(hours)	95% >= 48 h rs								
C.2.5	% Missed Installation Appointments P-3 [Local Interconnection Trunks/FL(%)	Partty w Retail	0 00%	136	0 00%	30		0.00000		YES
C 2 6	% Provisioning Troubles within 30 Deys P-9 JLocal Interconnection Trunks/FL(%)	Parity w Retail	0.05%	1,868	5 41%	1,330		0.00083	-64.5869	NO
C 2 7	Average Completion Notice Interval P-5 [Local Interconnection Trunks/FL(hours)	Parity w Retail	45.37	106	30 66	25	140 524	31.24382	0 4707	YES
C 2 8	Total Service Order Cycle Time P-10 [Local Interconnection Trunks/FL(days)	Diagnostic	2100 A D 24			ander jaar en se ster en Nederlander en se ster en se s		an an the second	Sec. Sec.	
C 2 9	Total Service Order Cycle Time (offered) P-10 [Local Interconnection Trunks/FL(days)	Diagnostic								
C2 01 C2 02	% Completions w/o Notice or < 24 hours P-6 [Local Interconnection Trunks/Dispatch/FL(%) P-6 [Local Interconnection Trunks/Non-Dispatch/FL(%)	Diagnostic Diagnostic			100,00%	29				Diagnostic Diagnostic
C2 111 C2 112 C2 121 C2 122 C2 122	Serv NAccuracy P-11 Local Interconnection Trunks/<10 circuits/Dispatch/FL(%)	>= 95% >= 95% >= 95% >= 95%			100 00% 100.00% 100 00% 94 74%	2 11 3 19				YES YES YES NÖ
	Local Interconnection Trunks - Maintenance and Repair				·····			·		
C311 C312	Missed Repair Appointments M&R-1 [Local Interconnection Trunks/Dispatch/FL(%) M&R-1 [Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail Panty w Retail	0.00%	1 205	0.00%	0 76		0.00000		YES
C 3 2 1 C.3 2 2	Customer Trouble Report Rate M&R-2 [Local Interconnection Trunks/Dispatch/FL(%) M&R-2 [Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail Parity w Retail	0.00% 0.05%	399,191 399,191	0 00%	141,833 141,833		0 00000 0 00007	0 5120	YES

	Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
	Maintenance Average Duration									
C331	M&R-3 Local Interconnection Trunks/Dispatch/FL(hours)	Parity w Retail	3.53	1	0.00	0	0.000			YES
C332	V&R-3 Local Interconnection Trunks/Non-Dispatch/FL(hours)	Parity w Retail	0.30	205	0.06	76	1 480	0.19875	1.2078	YES
	% Repart Troubles within 30 Days									
C 3.4 1	M&R-4 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	1	0.00%	0				YES
C 3.4.2	M&R-4 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.98%	205	94.74%	76		0.01320	71.0305	NO
	Out of Service > 24 hours									
C.3.5.1	M&R-5 Local interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	1	0.00%	0				YES
C 3 5.2	M&R-5 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.00%	205	0.00%	76		0.00000		YES
	Local interconnection Trunks - Billing Invoice Accuracy						_			
C.4.1	(B-1 (F ¹ .(%)	BST - State	97.93%	\$492,661,862	99.57%	\$7,800,975		0.00005	-320.4386	YES
						4.100010.0				
C42	Mean Time to Deliver Involces - CABS IB-2 [Region(calendar days)]	BCT Basis	1 1 24		4.56	3,484				1000
C42	B-2 [Region(calendar days)	BST - Region	4.67		4.90	3,464				YES
	LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING								· · · · · · · · · · · · · · · · · · ·	T
	Trunk Group Performance - Aggregate									
C.5.1	TGP-1 FL	>0 5% dif 2 consec. Hrs			0	i da serie de la companya de la comp				YES

	nchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
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Operations Support Systems - Pre-Ordering

	Oberatious and hour avariance - Lie-Organing						
	% Interface Availability - CLEC						· · · · · · · · · · · · · · · · · · ·
D 1.1 1	OSS-2 EDVRegion(%)	>= 99 5%			100.00%		YES
D112	OSS-2 HAL/Region(%)	>= 99.5%			100.00%		YES
D 1.1 3	OSS-2 LENS/Region(%)	>= 99.5%			99.88%		YES
D 1.14	OSS-2 LEO MAINFRAME/Region(%)	>= 99.5%			99.95%		YES
D.1.1 5	OSS-2 LEO UNIX/Region(%)	>= 99.5%					
D 1.16	OSS-2 LESOG/Region(%)	>= 99.5%			99.98%		YES
D117	OSS-2 TAG/Region(%)	>= 99.5%			99.97%		YES
D117	OSS-2 [PSIMS/Region(%)	>= 99.5%			100.00%		YES
U 1 1.0		 			100.0010	-	
	% Interface Availability - BST & CLEC	_					
D 1.2 1	OSS-2 ATLAS/COFF/Region(%)	>= 99.5%			99.98%		YES
D 1 2.2	OSS-2 BOCRIS/Region(%)	>= 99.5%			99.98%		YES
D.1.2 3	OSS-2 DSAP/Region(%)	>= 99 5%			99.98%		YES
D 1.24	OSS-2 RSAG/Region(%)	>= 99.5%			99.99%		YES
D.1.2 5	OSS-2 SOCS/Region(%)	>= 99 5%			99.98%		YÉS
D 1.26	OSS-2 SONGS/Region(%)	>= 99 5%			99.98%		YES
D.1.27	OSS-2 DOE/Region(%)	>= 99.5%			100.00%		YES
D128	OSS-2 LNP Gateway/Region(%)	>= 99 5%			100.00%		YES
D129	OSS-2 COG/Region(%)	>= 99 5%			100.00%		YES
D 1 2 10	OSS-2 DOM/Region(%)	>= 99.5%			100.00%		YES
D.1 2 11	OSS-2 SOG/Region(%)	>= 99.5%			100.00%		YES
	Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)						
		BINE BEAC IN THIS 2 Per	3.07	3,242,632	1.48	377,756	YES
D1311	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	3.14	4.525	1.40	377,756	YES
D1312	OSS-1 RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.14	9,469,095	1.40	250,778	YES
D1321	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	4.96	509.554	1.37	250,778	YES
D1322	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec			1.37		YES
D1331	OSS-1 ATLAS/Region (seconds)	RNS - ATLAS + 2 Sec	3.30	937,185		95,924	YES
D1332	OSS-1 ATLAS/Region (seconds)	ROS - ATLAS + 2 Sec	2.70	178,644	1.05	95,924	YES
D1341	OSS-1 DSAP/Region (seconds)	RNS - DSAP + 2 Sec	2.92	1,740,226	0.69	2,026	
D 1 3 4.2	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.62	209,174	0.69	2,026	YES
D1351	OSS-1 HAL/CRIS/Region (seconds)	RNS CRSACCTS + 2 Sec	3.68	5,154,749	1.48	1,291,197	YES
D1352	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.25	355,048	1.48	1,291,197	YES
D1361	OSS-1 COFFI/Region (seconds)	RNS - OASISBIG + 2 Sec	4.89	11,479,423	0 93	49,894	YES
D1362	OSS-1 COFFI/Region (seconds)	ROS - OASISBIG + 2 Sec	4.43	440,381	0.93	49,894	YES
D1371	OSS-1 PSIMS/ORB/Region (seconds)	RNS - OASISBIG + 2 Sec	4.89	11,479,423	0.04	100,083	YES
D1372	OSS-1 PSIMS/ORB/Region (seconds)	ROS · OASISBIG + 2 Sec	4.43	440,381	0.04	100,083	YES
	Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)						
D.1 4 1.1	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	3.07	3,242,632	1.65	229,679	YES
D1412	OSS-1 RSAG, by TN/Region (seconds)	ROS RSAG by TN + 2 Sec	3.14	4,525	1 65	229,679	YES
D1421	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	3.21	9,469,095	1 81	45,390	YES
D1422	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	4.96	509,554	1 81	45,390	YES
D1431	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic					Diagnost
D1432	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic					Diagnost
D1441	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			0.99	2	Diagnost
D.1442	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			0.99	2	Diagnost
		RNS - ATLAS - TN + 2 Sec	3.30	937,185	1.95	5.672	YES
D1451	OSS-1 ATLAS - TN/Region (seconds)	ROS - ATLAS - TN + 2 Sec	2.70	178,644	1.00	5.672	YES
D1452	OSS-1 ATLAS TN/Region (seconds)	RNS - DSAP + 2 Sec	2.92	1,740,226	2.05	79,727	YES
D 1 4 6.1	OSS-1 DSAP/Region (seconds)						YES
D1462	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.62	209,174	2.05	79,727	YES
D 1.4 7 1	OSS-1 HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.68	5,154,749	2.46	160,872	
D147.2	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.25	355,048	2.46	160,872	YES
D1481	OSS-1 CRSEINT/Region(seconds)	RNS - CRSACCTS + 2 sec					F 8-1-2001, and D.1.4.7.1
D1482	OSS-1 CRSEINT/Region(seconds)	ROS - CRSOCSR + 2 sec					4-1-2001, and D.1.47.2
D1491	OSS-1 CRSECSRL/Region(seconds)	RNS - CRSACCTS + 2 sec	L	í Plák			r 7-1-2001; eve 0.1.4.7.1
D 1 4 9.2	OSS-1 CRSECSRL/Region(seconds)	ROS - CRSOCSR + 2 sec	L	^. v	- This date in	n applicáble aflei	7-1-2001; per 0.1.4.7.2

-8.2149

-8.2149

8.2149

-0.3229

0.2254

-0.2254

2.0683

-1.6866

1 6866

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0 00732

0.00123

0 00123

YES

YES

YES

YES

YES

YES

NÔ

YES

YES

	BellSouth Monthly State Summary Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
	Operations Support Systems - Maintenance and Repair						-				
	% Interface Availability - BST	-									
l	OSS-3 TAFVRegion(%)	>= 99.5%	100.00%							YES	
	% Interface Availability - CLEC										
	OSS-3 CLEC TAFVRegion(%)	>= 99 5%			100 00%					YES	
	OSS-3 ECTA/Region(%)	>= 99.5%			99.52%					YES	
	% Interface Availability - BST & CLEC										
	OSS-3 [CRIS/Region(%)	>= 99.5%			99.98%					YES	
	OSS-3 LMOS HOST/Region(%)	>= 99.5%			99.97%					YES	
	OSS-3 LNP/Region(%)	>= 99.5%			100.00%					YES	
	OSS-3 MARCH/Region(%)	>= 99.5%			99.96%					YES	
	OSS-3 OSPCM/Region(%)	>= 99.5%			100.00%					YES	
	OSS-3 Predictor/Region(%)	>= 99.5%			100.00%					YES YES	
	OSS-3 SOCS/Region(%)	>= 99 5%			99 98%					TES	
	Average Response Interval	-									
	OSS-4 CRIS/Region(%) <= 4 Seconds	Parity w Retail	95.01%	1,700,672	94.31%	104,309		0.00069	10.1579	ŇÓ	
	OSS-4 CRIS/Region(%) <= 10 Seconds	Parity w Retail	98.75%	1,700,672	99.14%	104,309		0.00035	-10.9687	YES	
	OSS-4 CRIS/Region(%) > 10 Seconds	Parity w Retail	1.25%	1,700,672	0.86%	104,309		0.00035	10 9687	YES	
	OSS-4 DLETH/Region(%) <= 4 Seconds	Parity w Retail Parity w Retail	8 98% 80.11%	50,353 50,353	9.84% 85.70%	874	-	0.00975	-0.8830 -4.1054	YES	
	OSS-4 DLETH/Region(%) <= 10 Seconds	Parity w Retail	19.89%	50,353	14 30%	874		0.01362	4,1054	YES	
	OSS-4 DLETH/Region(%) > 10 Seconds OSS-4 (DLR/Region(%) <= 4 Seconds	Parity w Retail	5.48%	34,853	17.14%	40.374	-	0.00166	-70.0169	YES	
	OSS-4 DLR/Region(%) <= 10 Seconds	Parity w Retail	84.21%	34,853	92.52%	40,374		0.00267	-31.1732	YES	
	OSS-4 DLR/Region(%) > 10 Seconds	Parity w Retail	15.79%	34,853	7.48%	40,374	1	0.00267	31.1732	YES	
	OSS-4 LMOS/Region(%) <= 4 Seconds	Parity w Retail	99.61%	1,700,641	98.08%	105,666		0 00020	77.3774	NO	
	OSS-4 LMOS/Region(%) <= 10 Seconds	Parity w Retail	99.79%	1,700,641	99.45%	105,666		0.00015	22.6894	NO	
	OSS-4 LMOS/Region(%) > 10 Seconds	Parity w Retail	0.21%	1,700,641	0 55%	105,666		0 00015	-22.6894	NO	
	OSS-4 LMOSupd/Region(%) <= 4 Seconds	Parity w Retail	98.15%	1,240,024	94.12%	61,378		0 00056	72.4764	NO	
	OSS-4 LMOSupd/Region(%) <= 10 Seconds	Parity w Retail	99.82%	1,240,024	96.69%	61,378		0.00018	176 4999	NO	
	OSS-4 LMOSupd/Region(%) > 10 Seconds	Parity w Retail	0.18%	1,240,024	3.31%	61,378		0 00018	-176.4999	NO	
	OSS-4 LNP/Region(%) <= 4 Seconds	Parity w Retail Parity w Retail	99.67% 99.84%	119,274	99.52% 99.90%	5,860 5,860		0.00076	1.9612 -1 0275	NO YES	
	OSS-4 LNP/Region(%) <= 10 Seconds	Parity w Retail Parity w Retail	99.84%	119,274	0.10%	5,860		0.00053	1.0275	YES	
	OSS-4 LNP/Region(%) > 10 Seconds OSS-4 MARCH/Region(%) <= 4 Seconds	Parity w Retail	29.62%	7,599	28.03%	585		0.00053	0.8106	YES	
	OSS-4 MARCH/Region(%) <= 10 Seconds	Parity w Retail	29.62%	7,599	28.03%	585		0.01959	0.8106	YES	
	OSS-4 MARCH/Region(%) > 10 Seconds	Parity w Retail	70.38%	7,599	71.97%	585		0.01959	-0.8106	YES	
	OSS-4 OSPCWRegion(%) <= 4 Seconds	Parity w Retail	43.13%	6,611	35 00%	100		0 04990	1.6283	YES	
	OSS-4 OSPCM/Region(%) <= 10 Seconds	Parity w Retail	95.90%	6,611	94.00%	100		0 01998	0 9515	YES	
	OSS-4 OSPCM/Region(%) > 10 Seconds	Parity w Retail	4 10%	6,611	6.00%	100		0 01998	-0.9515	YES	
	OSS 4 Bradistor/Bogson/(4) 4 Seconde	Parity w Botail	18 54%	76 137	23.02%	5 448		0.00545	8 2149	YES	

D 2.4 9 1

D.2.492

D.249.3

D 2.4 10 1

D.2.4.10 2

D 2 4.10 3

D24111

D24112

D.2 4 11 3

OSS-4 Predictor/Region(%) <= 4 Seconds OSS-4 Predictor/Region(%) <= 10 Seconds

OSS-4 Predictor/Region(%) > 10 Seconds OSS-4 SOCS/Region(%) <= 4 Seconds

OSS-4 SOCS/Region(%) <= 10 Seconds

OSS-4 SOCS/Region(%) > 10 Seconds

OSS-4 NIW/Region(%) <= 4 Seconds OSS-4 NIW/Region(%) <= 10 Seconds OSS-4 NIW/Region(%) > 10 Seconds

Parity w Retail

Parity w Retail

Parity w Retail

Parity w Retall

Parity w Retail

18.54%

18.54%

81.46%

99.73%

99.97%

0.03%

72 73%

99.43%

0.57%

76,137

76,137

76,137

249,254

249,254

249,254

69,645

69,645

69.645

23.02%

23.02%

76.98%

99.74%

99 97%

0.03%

71 22%

99.64%

0 36%

5,448

5,448

5,448

16,278

16,278

16,278

3,912

3,912

3,912

YES YES

YES

YES

YES

YES YES

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BellSouth Monthly State Summary

Florida, October 2001	Benchmark / Analog	BST Messure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
									_

Ave	arage Response Time			
C-1		<= 15 days	5	4
C-1		<= 15 days	9	47
C-1		<= 15 days		
Ave	rage Arrangement Time			
C-2	Virtual/FL (calendar days)	<= 60 days	60	1
C-2	Virtual-Augments/FL (calendar days)	<= 45 days	13	5
C-2	Virtual-Augments - Additional Space Required/FL (calendar days)	<= 60 days		
C-2		<= 90 days	48	1
C-2	Physical Caged-Augments/FL (calendar days)	<= 45 days	27	23
C-2	Physical Caged-Augments Additional Space Required/FL (calendar days)	<= 90 days		
C-2		<= 90 days		
C-2	Physical Cageless-Augments/FL (calendar days)	<= 45 days	24	17
C-2	Physical Cageless-Augments Additional Space Required/FL (calendar days)	<= 90 days	52	2
*0	Due Dates Missed			
C-3	Virtual/FL (%)	< 10% missed	0 00%	6
C-3	Physical/FL (%)	< 10% missed	0.00%	43

	Belisouth Monthly State Summary									
	Florida, October 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
	General - Flow Through									· · -
	% Flow Through Service Requests									
F111	O-3 Summary/Region(%)	Diagnostic			85.49%	290,922				Diagnostic
F 1.1.2	O-3 Aggregate/Region(%)	Diagnostic			85.49%	290,922				Diagnostic
F113	O-3 Residence/Region(%)	>= 95%			89.40%	205,200				NO
F114 F1.15	O-3 Business/Region(%) O-3 UNE/Region(%)	>= 90% >= 85%			70.17% 76.74%	7,750	-			NO NO
	% Flow Through Service Requests - Achieved						_			
F121	O-3 Summary/Region(%)	Diagnostic			75.89%	327,738				Diagnostic
F.1.2.2	O-3 Aggregate/Region(%)	Diagnostic			75.89%	327,736				Diagnostic
F 1.2.3	O-3 Residence/Region(%)	Diagnostic			82.01%	223,685				Diagnostic
F 1.2 4	O-3 Business/Region(%)	Diagnostic			48.45%	11,224				Diagnostic
F 1.2 5	0-3 (UNE/Region(%)	Diagnostic			64.46%	92,827				Diagnostic
F13.1	% Flow Through Service Requests - LNP O-3 Summary/Region(%)	>= 85%			89 09%	8,738	1			YES
F132	O-3 Aggregate/Region(%)	>= 85%			89.09%	8,738	-			YES
F133	O-3 Residence/Region(%)	Diagnostic			03.03 /0	0,100	-			Diagnostic
F134	O-3 Business/Region(%)	Diagnostic								Diagnostic
	General - Pre-Ordering		· ·			÷				
	Loop Makeup Inquiry (Menual)					<u> </u>				
F 2 1	PO-1 Loops/FL(%)	>= 95% w in 3 bus days			93,75%	48	1			NO
• = •										
F 2 2	Loop Makeup Inquiry (Electronic) PO-2 Loops/FL(%)	>= 95% w in 1 min			96 61%	1,886				YES
						(1000				
	General - Ordering									
	Service inquiry with Firm Order									
F311	O-10 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% win 5 bus days			99.12%	114	-			YES
F312	O-10 Local Interoffice Transport/FL(%)	>= 95% w in 5 bus days			100.00%	2				YËS
	General - Ordering									
	Average Speed of Answer								-	
F 4 1	O-12 [Region(seconds)	Parity w Retail	148.68	7,044,446	20.48	45,635				YES
	General - Maintenance Center									
F 5 1	Average Answer Time [M&R-6 [Region(seconds)	Parity w Retail	42 30	2,266,972	26.98	90,236				YES
	Man-o [region(seconds)		42.30	2,200,972	20.90	30,230				163
	General - Operator Services (Toll)									
	Average Speed to Answer									
F61	OS-1 FL(seconds)	PBD			4.82					PBD
	% Answered in 30 seconds									
F62	OS-2 [FL (%)	PBD			97 10%					PBD
	General - Directory Assistance					•				
	Average Speed to Answer									
F 7 1	DA-1 FL(seconds)	PBD			5 42					PBD
	% Answered in 20 seconds									

% Answered in 20 seconds

	ISouth Monthly State Summary Ida, October 2001	Bençhmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
DA-2	FL (%)	PBD			94.80%					PBD
Gene	al - E911									
	Interval FL(hours)	PBD			1.32	1,196				PBD
% Ac	IFL(%)	PBD			95.30%	658,490				PBD
% Tin E-1	iniiness FL(%)	PBD			100 00%	1,196				PBD
	ai - Billing									
	a Data Delivery Accuracy Region(%)	Parity w Retail	99.81%	5,319	100.00%	19,326		0.00067	-2.8029	YES
Usag B-5	Pata Delivery Timelinesa Region(%)	Parity w Retail	98 72%	34,666	99.33%	288,233,354		0.00060	-10.1553	YES
Usag B-4	e Dete Delivery Completeness [Region(%)	Parity w Retail	99.62%	34,666	99.80%	288,233,354		0.00033	-5.4681	YES
Moan B-6	Time to Defiver Usage [Region(days)	Parity w Retail	3.44	34,666	2 64	288,233,354				YES
Recu B-7 B-7 B-7	ring Charge Completeness [Resale/FL(%) UNE/FL(%) [Interconnection/FL(%)]	Parity w Retail >= 90% >= 90%	83.76%	\$20,682,954	94.79% 98.16% 92.43%	\$921,426 \$388,821 \$38,486		0.00097	-113.2099	YES
Non-1 B-8 B-8 B-8	Recurring Charge Completeness Ressle/FL(%) UNE/FL(%) Interconnection/FL(%)	Parity w Retail >= 90% >= 90%	89.69%	\$24,890,271	99.00% 97.40% 63.16%	\$891,307 \$1,747,527 \$1,119,157		0.00102	-91 2177	YES YES NÖ
Gane	al - Change Management									
	tware Reloase Notices Sent On Time	>= 98% w in 30 days			50 00%	2				NO
Avera	ge Software Release Notice Delay Days	>= 25 days prior to release			-6	1				NO
% Ch	ange Management Documentation Sent On Time	>= 98% w in 30 days			100.00%	3	_			YES
Aven	FL(%) ge Documentation Release Delay Days				100.0070		-			144
% CL	FL(sverage) EC Interface Outages Sent within 15 Minutes FL(%)	>= 25 days prior to release >= 97% w in 15 min		1 B.H I	100.00%	22				YES
	al - New Business Requests									
% Net	ar - New Dusiness Requests v Business Requests Processed within 30 Business Days [Region(%)	>= 90% w in 30 bus days			100 00%	1				YES
BFR-	Otes Provided within X Business Days XA [Region(%)	>= 90% w in 10 bus days >= 90% w in 30 bus days >= 90% w in 60 bus days								

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BellSouth Monthly State Summary

	Florida, October 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
F 12 1 1 F 12 1 2	Acknowledgement Message Timeliness O-1 EDVRegion(%) O-1 TAG/Region(%)	>= 95% w in 30 min >= 95% w in 30 min			99 93% 100.00%	87,896 195,248	-			YES YES
F 12 2.1 F 12 2 2	Acknowledgement Message Completeness O-2 EDVinegion(%) O-2 TAC/Region(%)	100% 100%			99.98% 100.00%	87,896 195,248				NO NO
F 13.1.1 F 13 1 2 F 13.1 3	General - Database Updates Average Database Update Interval D-1 LIDB/FL(hours) D-1 Directory Listings/FL(hours) D-1 Directory Assistance/FL(hours)	PBD PBD PBD	1.23 0 09 3.84	22 27 27	1.23 0.09 3.84	22 27 27				PBD PBD PBD
F 13.2 1 F 13 2 2 F 13 2 3	% Update Accuracy D-2 LIDB/FL(%) D-2 Directory Listings/FL(%) D-2 Directory Assistance/FL(%) C Virges/FL(%)	>= 95% >= 95% >= 95%			100.00% 96.99% 100.00%	56 266 71				YES YES YES
F 13 3	% NXXs / LRNs Loaded by LERG Effective Date D-3 [Region(%) General - Network Outage Notification	100%		· · ·	93.75%	48				NO
F 14 1	Mean Time to Notify CLEC of Major Network Outages M&R-7 [Region (minutes)	Parity w Retail	189	2	168	2				YES

Flor	ISouth Monthly State Summary ida, October 2001 crate Sevent)	Benchmark /	BST	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
(Geo	orgia Format)	Analog	Measure	volume	WORKUTE	VOIUTIN	Deviation	Effor	Zacore	Equity
0	eation - Collocation				··· ,					
									·····	
Avera IC-1	ge Response Time Virtual/FL (calendar days)	<= 20 days			5	4				YÉŠ
C-1	Physical/FL (calendar days)	<= 30 days			8	19				YES
Avera	ge Arrangement Time									
C-2	Virtua//FL (calendar days)	<= 50 days			21	6			أقتضعهم	YES
C-2	Virtual (Extraordinary)/FL (calendar days)	<= 75 days								
C-2	Physical Caged/FL (calendar days)	<= 90 days			28	24				YES
C-2 C-2	Physical Cageless/FL (calendar days)	<= 60 days			26	18				YES
C-2	Physical Cageless (Extraordinary)/FL (calendar days)	<= 90 days			45	1				YES

E.1 2.5	C-2 Physical Cageless (Extraordinary)/FL (calendar days)	<= 90 days
	% Due Dates Missed	
E131	C-3 Virtual/FL (%)	< 5% missed
E132	C-3 Physical/FL (%)	< 5% missed

28	24	YE
26	18	YE
45	1	YE

.

1	0.00%	6	 YES
1	0.00%	43	YES

E111 E112

E 1.2.1 E.1 2.2 E.1 2 3

E 1.24

	PERCENT ACHIEVED	PERCENT FLOW
	FLOW-THROUGH	THROUGH
CLEC AGGREGATE		
REGION ALL SERVICES	75.89%	85.49%
	FLOW-THROUGH %	
BST AGGREGATE		
REGION		
- RETAIL RESIDENCE	94.00%	
DETAIL DUCINECC*	TBD	
- RETAIL BUSINESS*		cted by the Georgia
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu	g of business retail flow through as dire ntly has no way to measure flow through usiness retail. BellSouth retail reports ca	for the Regional pture all business
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources,	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development
*NOTE: BellSouth is reinstituting the reportin Public Service Commission. BellSouth currer Operating System (ROS) interface used by bu service requests submitted from all sources, of an accurate report and will reflect this mea	g of business retail flow through as dire ntly has no way to measure flow through isiness retail. BellSouth retail reports ca including manually. BellSouth has initial	for the Regional pture all business ted the development

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Exhibit October PM Data Attachment 2E

GREGATE ORDER TYPES						· · · ·			-							
Company Info							OCESSING								FLOWT	HROUGH
	1						ESOG							·		
		M	echanized	Interface L	sed	Manual	Rejects		Validated		Errors					I
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Totai Manuai Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	^l ercent Fic Through
1		0	484	0	484	396	53	8	27	17	15	2	10	2.38%	37.04%	40.00%
2		0	18	0	18	4	0	1	13	5	5	0	8	47.06%	61.54%	61.54%
3		0	130	0	130	75	19	0	36	11	7	4	25	23.36%	69.44%	78.13%
4		0	8	0	8	5	1	0	2	0	0	0	2	28.57%	100.00%	100.00%
5		0	15	0	15	6	0	2	7	6	6	0	1	7.69%	14.29%	14.29%
6		0	214	0	214	168	14	2	30	13	5	8	17	8.95%	56.67%	77.27%
7		0	104	0	104	78	6	1	19	7	3	4	12	12.90%	63.16%	80.00%
8	1	0	75	0	75	2	4	0	69	13	10	3	56	82.35%	81.16%	84.85%
9	<u> </u>	0	7	0	7	3	2	0	2	1	0	1	1	25.00%	50.00%	100.00%
10		0	580	0	580	244	139	7	190	156	97	59	34	9.07%	17.89%	25.95%
11	1	0	13	0	13	3	1	0	9	6	2	4	3	37.50%	33.33%	60.00%
12	11	0	43	0	43	0	18	1	24	3	2	1	21	91.30%	87.50%	91.30%
13		0	5562	0	5562	75	1094	1	4392	1075	871	204	3317	77.81%	75.52%	79.20%
14		Ó	8618	0	8618	154	1383	1	7080	2013	1610	403	5067	74.18%	71 57%	75.89%
15		0	2285	0	2285	331	286	31	1637	655	442	213	982	55.95%	59.99%	68.96%
16][Û	2	0	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%
17		0	50	0	50	12	11	0	27	18	6	12	9	33.33%	33.33%	60.00%
18		0	3313	0	3313	63	479	2	2769	738	605	133	2031	75.25%	73.35%	77.05%
19	[· · · ·]	0	24	0	24	1	6	2	15	10	2	8	5	62.50%	33.33%	71.43%
20		0	3	0	3	0	0	0	3	3	1	2	0	0.00%	0.00%	0.00%
21	 	0	14	0	14	1	2	0	11	5	2	3	6	66.67%	54.55%	75.00%
22	1 1	0	38219	0	38219	2404	4557	63	31195	8618	5934	2684	22577	73.03%	72.37%	79.19%
23		0	1019	0	1019	24	162	1	832	182	72	110	650	87.13%	78.13%	90.03%
24		0	8281	0	8281	20	2480	0	5781	2443	226	2217	3338	93.14%	57.74%	93 66%
25	1 1	0	172	0	172	33	13	2	124	120	80	40	4	3.42%	3.23%	4.76%
26	h	0	33	0	33	3	14	0	16	15	9	6	1	7.69%	6.25%	10.00%
27	1	0	5	0	5	1	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
28	1	0	68	0	68	9	17	1	41	24	18	6	17	38.64%	41.46%	48.57%
29		0	609	0	609	155	73	8	373	227	169	58	146	31.06%	39.14%	46.35%
30	<u>.</u>	0	1	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
31		0	1004	0 0	1004	54	142	3	805	351	288	63	454	57 04%	56.40%	61.19%
32		0	2183	0	2183	161	207	0	1815	313	246	67	1502	78.68%	82.75%	85.93%
33	j i	0	1509	0	1509	209	216	4	1013	407	54	353	673	71.90%	62.31%	92.57%
34	i	0	2	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
35		0	4	0	4	0	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
	, I	·	-7	<u> </u>	+ ł	v				4 115	86	29	131	0.00% 57.46%	53.25%	60.37%

AGGREGATE ORDER TYPES										ļ			+		EL OWER	HROUGH
Company Info							OCESSING								FLOWI	
						L	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Failout	CLEC Caused Failout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
37		0	1	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
38		0	20	0	20	0	18	0	2	0	0	0	2	100.00%	100.00%	100.00%
39		0	49	0	49	5	1	6	37	22	13	9	15	45.45%	40.54%	53.57%
40		0	621	0	621	299	71	20	231	120	76	44	111	22.84%	48.05%	59.36%
41		0	105	0	105	37	12	1	55	31	22	9	24	28.92%	43.64%	52.17%
42		0	1	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
43		0	102	0	102	19	12	0	71	19	6	13	52	67.53%	73.24%	89.66%
44		0	1875	0	1875	119	247	0	1509	916	350	566	593	55.84%	39.30%	62.88%
45		0	15	0	15	2	4	0	9	5	1	4	4	57.14%	44.44%	80.00%
46		0	18	0	18	1	2	0	15	8	6	2	7	50.00%	46.67%	53.85%
47		0	25	0	25	5	2	0	18	13	5	8	5	33.33%	27.78%	50.00%
48		0	188	0	188	12	33	0	143	93	49	44	50	45.05%	34.97%	50.51%
49		0	53	0	53	3	8	0	42	22	19	3	20	47.62%	47.62%	51 28%
50		0	16	0	16	0	1	0	15	8	5	3	7	58.33%	46.67%	58.33%
51		0	8	0	8	1	1	0	6	2	1	1	4	66.67%	66.67%	80.00%
52		0	0	31	31	7	1	1	22	6	6	0	16	55.17%	72.73%	72.73%
53		C	0	647	647	112	91	3	441	161	144	17	280	52.24%	63.49%	66.04%
54		0	0	55	55	14	0	1	40	15	15	0	25	46.30%	62.50%	62.50%
55		0	0	181	181	22	8	1	150	65	50	15	85	54.14%	56.67%	62.96%
56		0	0	436	436	64	29	4	339	108	91	17	231	59.84%	68.14%	71.74%
57		0	0	314	314	73	36	1	204	44	26	18	160	61.78%	78.43%	86.02%
58		0	0	7	7	0	2	0	5	1	0	1	4	100.00%	80 00%	100.00%
59		0	0	1009	1009	35	107	0	867	36	24	12	831	93.37%	95.85%	97.19%
60		0	0	10	10	3	2	0	5	3	3	0	2	25.00%	40.00%	40.00%
61		0	0	17	17	0	13	0	4	0	0	0	4	100.00%	100.00%	100.00%
62		0	0	2361	2361	358	256	27	1720	631	513	118	1089	55 56%	63.31%	67.98%
63		0	0	1215	1215	191	156	6	862	327	266	61	535	53.93%	62.06%	66.79%
64		0	0	2049	2049	396	266	64	1323	485	378	107	838	51.99%	63.34%	68.91%
65		0	0	1013	1013	178	138	12	685	286	239	47	399	48.90%	58.25%	62.54%
66		0	0	1178	1178	185	142	32	819	332	274	58	,487	51.48%	59 46%	63.99%
67		0	0	66	66	8	15	1	42	17	16	1	25	51.02%	59.52%	60.98%
68		0	0	203	203	2	8	0	193	1	0	1	192	98.97%	99.48%	100.00%
69		0	0	75	75	40	3	0	32	5	4	1	27	38.03%	84.38%	87.10%
70		0	0	8	8	2	3	0	3	1	0	1	2	50.00%	66.67%	100.00%
71		0	0	12	12	2	4	0	6	4	2	2	2	33.33%	33.33%	50.00%
72	· · · ·	0	0	1	1	0	1	0	0	0	-	0	0	0.00%	0.00%	0.00%

Company Info						LSR PR	OCESSING								FLOWT	HROUGH
						Ł	ESOG									
		M	echanized	Interface U	sed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Totai System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
73		0	0	578	578	3	24	0	551	2	2	0	549	99.10%	99.64%	99.64%
74		0	0	1564	1564	493	17	115	939	408	361	47	531	38.34%	56.55%	59.53%
75		0	0	138	138	22	25	0	91	29	9	20	62	66.67%	68.13%	87.32%
76		0	0	46	46	11	7	0	28	6	3	3	22	61.11%	78.57%	88.00%
77		0	0	20	20	3	3	2	12	2	2	0	10	66.67%	83.33%	83.33%
78		0	0	28	28	4	11	0	13	0	0	0	13	76.47%	100.00%	100.00%
79		0	0	15	15	1	4	0	10	1	1	0	9	81.82%	90.00%	90.00%
80		0	0	35	35	12	8	1	14	4	3	1	10	40.00%	71.43%	76.92%
81		0	0	25	25	3	1	0	21	5	3	2	16	72.73%	76.19%	84.21%
82		0	0	790	790	8	43	2	737	27	20	7	710	96.21%	96.34%	97.26%
83		0	0	2037	2037	72	50	16	1899	401	344	57	1498	78.27%	78.88%	81.32%
84		0	0	226	226	119	34	0	73	61	36	25	12	7.19%	16.44%	25.00%
85		0	0	6	6	0	3	0	3	0	0	0	3	100.00%	100.00%	100.00%
86		0	0	110	110	25	14	2	69	32	19	13	37	45.68%	53.62%	66.07%
87		0	0	13	13	6	1	0	6	0	0	0	6	50.00%	100.00%	100.00%
88		0	0	3	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
89		0	0	112	112	3	22	0	87	45	16	29	42	68.85%	48.28%	72.41%
90		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
91		0	0	20	20	2	3	1	14	5	3	2	9	64.29%	64.29%	75.00%
92		0	0	131	131	16	17	2	96	45	22	23	51	57.30%	53.13%	69.86%
93		0	0	489	489	180	49	6	254	101	48	53	153	40.16%	60.24%	76.12%
94		0	0	122	122	46	46	0	30	17	11	6	13	18.57%	43.33%	54.17%
95		0	0	139	139	50	25	2	62	34	24	10	28	27.45%	45.16%	53.85%
96		0	0	190	190	33	52	4	101	51	33	18	50	43.10%	49.50%	60.24%
97		0	0	56	56	29	5	0	22	3	3	0	19	37.25%	86.36%	86.36%
98		0	0	43	43	27	5	0	11	3	2	1	8	21.62%	72.73%	80.00%
99		0	0	85	85	23	13	2	47	32	6	26	15	34.09%	31.91%	71.43%
100		0	0	4319	4319	15	621	34	3649	1594	848	746	2055	70.42%	56.32%	70.79%
101		0	0	16	16	1	1	0	14	0	0	0	14	93.33%	100 00%	100.00%
107		0	0	3	3	2	0	0	1	0	0	0	1 1	33.33%	100.00%	100.00%
102		0	0	6	6	2	4	0	0	0	0	0	0	0.00%	0.00%	0.00%
103		0	0	26	26	1	2	4	19	2	1	1	17	89.47%	89.47%	94,44%
104		0	0	480	480	42	32	15	391	90	52	38	301	76.20%	76.98%	85.27%
105		0	0	25	25		15	0	10	4	0	4	6	100.00%	60.00%	100.00%
		0	0	81	23 81	0	39	0	42	4	3	1	38	92.68%	90.48%	92 68%
107		0	0	46	46	1	39	0	42	4	0		43	92.08%	90.48 % 97.73%	100.00%

AGGREGATE ORDER TYPES	1				1]		Î			
Company Info															FLOWT	HROUGH
						L	ESOG									
	- <u> </u>	M	echanized	Interface L	lsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
109		0	0	10268	10268	79	378	11	9800	248	200	48	9552	97.16%	97.47%	97.95%
110		0	0	10	10	9	0	0	1	0	0	0	1	10.00%	100.00%	100.00%
111		0	0	87	87	17	27	3	40	17	10	7	23	46.00%	57.50%	69.70%
112		0	0	283	283	6	7	25	245	176	144	32	69	31.51%	28.16%	32.39%
113		0	0	34	34	3	12	1	18	15	1	14	3	42.86%	16.67%	75.00%
114		0	0	10	10	8	0	0	2	2	0	2	0	0.00%	0.00%	0.00%
115		0	0	96	96	0	33	0	63	1	1	0	62	98.41%	98.41%	98.41%
116		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
117		0	0	8	8	0	3	0	5	0	0	0	5	100.00%	100.00%	100.00%
118		0	0	573	573	3	71	0	499	10	9	1	489	97.60%	98.00%	98.19%
119		0	0	1483	1483	163	104	10	1206	237	172	65	969	74.31%	80.35%	84.93%
120		0	0	1208	1208	17	56	0	1135	13	9	4	1122	97.74%	98.85%	99.20%
121		0	0	233	233	2	36	0	195	0	0	0	195	98.98%	100.00%	100.00%
122									10522	3616	2204	1412	6906	65.35%	65.63%	75.81%
123	[0	0	885	885	378	202	17	288	68	27	41	220	35.20%	76.39%	89.07%
124		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
125		649	0	0	649	18	31	0	600	50	37	13	550	90.91%	91.67%	93.70%
126	[]	15	0	0	15	2	2	0	11	1	1	0	10	76.92%	90.91%	90.91%
127	ן ייין	1940	0	0	1940	205	73	4	1658	105	88	17	1553	84.13%	93.67%	94.64%
128] I	21	0	0	21	8	1	0	12	3	3	0	9	45.00%	75.00%	75.00%
129		741	0	0	741	79	44	4	614	109	75	34	505	76.63%	82.25%	87.07%
130	1	134	0	0	134	4	7	3	120	27	19	8	93	80.17%	77.50%	83.04%
131]	266	0	0	266	35	20	5	206	51	30	21	155	70.45%	75.24%	83.78%
132]]]	832	0	0	832	87	59	14	672	139	96	43	533	74.44%	79.32%	84.74%
133]	379	0	0	379	77	61	0	241	41	23	18	200	66.67%	82.99%	89.69%
134]	33	0	0	33	2	1	2	28	10	5	5	18	72 00%	64.29%	78.26%
135	ļ	173	0	0	173	24	15	0	134	29	26	3	105	67.74%	78.36%	80.15%
136		64	0	0	64	7	3	0	54	13	9	4	41	71 93%	75.93%	82.00%
137] [104	0	0	104	6	15	0	83	35	26	9	48	60.00%	57.83%	64.86%
138] [160	0	0	160	5	7	Э	145	29	6	23	, 116	91.34%	80.00%	95.08%
139	1 1	29	0	0	29	3	0	0	26	6	5	1	20	71.43%	76.92%	80.00%
140		26	0	0	26	2	0	0	24	14	9	5	10	47.62%	41.67%	52.63%
141	1 1	51	0	0	51	1	1	3	46	14	9	5	32	76.19%	69.57%	78.05%
142	1	73	0	0	73	2	3	8	60	53	44	9	7	13.21%	11.67%	13.73%
143	1 1	109	0	0	109	22	17	1	69	51	21	30	18	29.51%	26.09%	46.15%
144	1	70	0	0	70	5	4	0	61	31	9	22	30	68.18%	49.18%	76.92%

Exhibit October PM Data Attachment 2E

AGGREGATE ORDER TYPES				1												
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface (Jsed	Manual	Rejects		Validated		Errors					
Name	IESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flor Through
145		27	0	0	27	5	7	0	15	5	5	0	10	50.00%	66.67%	66.67%
146		149	0	0	149	39	7	2	101	24	14	10	77	59.23%	76.24%	84.62%
147		3	0	0	3	0	0	0	3 .	2	2	0	1	33.33%	33.33%	33.33%
148		15	0	0	15	0	10	0	5	1	1	0	4	80.00%	80.00%	80.00%
149		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
150		3	0	0	3	0	1	0	2	1	1	0	1	50.00%	50.00%	50.00%
151	ļ	36	0	0	36	8	4	1	23	15	9	6	8	32.00%	34.78%	47.06%
152		586	0	0	586	62	57	9	458	103	93	10	355	69.61%	77.51%	79.24%
153		79	0	0	79	6	7	1	65	28	13	15	37	66.07%	56.92%	74.00%
154		63	0	0	63	8	24	0	31	16	9	7	15	46.88%	48.39%	62.50%
155		70	0	0	70	14	18	1	37	6	6	0	31	60.78%	83.78%	83.78%
156		47	0	0	47	0	12	3	32	18	13	5	14	51.85%	43.75%	51.85%
157		27	0	0	27	4	5	0	18	2	2	0	16	72.73%	88.89%	88.89%
158	I	2	0	0	2	1	l 0	1 0	1	0	0	0	1	50.00%	100.00%	100.00%
159		47	0	0	47	7	5	1	34	16	8	8	18	54.55%	52.94%	69.23%
160		208	0	0	208	39	7	1	161	29	25	4	132	67.35%	81.99%	84.08%
161		642	0	0	6 42	90	42	0	510	23	22	1	487	81.30%	95.49%	95.68%
162		103	0	0	103	5	19	0	79	15	9	6	64	82.05%	81.01%	87.67%
163		606	0	0	606	26	46	2	532	73	66	7	459	83.30%	86.28%	87.43%
164		516	0	0	516	61	13	1	441	23	16	7	418	84.44%	94.78%	96.31%
165		538	0	0	538	81	39	4	414	55	45	10	359	74.02%	86.71%	88.86%
166		617	0	0	617	32	33	3	549	49	44	5	500	86.81%	91.07%	91.91%
167		199	0	0	199	7	7	3	182	18	13	5	164	89.13%	90.11%	92.66%
168		215	0	0	215	34	19	0	162	14	12	2	148	76.29%	91.36%	92.50%
169		2449	0	0	2449	286	119	13	2031	218	170	48	1813	79.90%	89.27%	91.43%
170		366	0	0	366	43	24	7	292	38	31	7	254	77.44%	86.99%	89.12%
171		214	0	0	214	1	4	0	209	9	9	0	200	95.24%	95.69%	95.69%
172	ĺ	169	0	0	169	16	7	0	146	6	5	1	140	86 96%	95.89%	96.55%
173		1859	0	0	1859	123	91	1	1644	100	88	12	1544	87.98%	93.92%	94.61%
174		280	0	0	280	65	24	3	188	25	13	12	, 163	67.63%	86.70%	92.61%
175		226	0	0	226	22	9	2	193	28	22	6	165	78.95%	85.49%	88.24%
176		70	0	0	70	5	7	1	57	14	4	10	43	82 69%	75.44%	91 49%
177		2067	0	0	2067	368	300	17	1382	477	404	73	905	53.97%	65.48%	69.14%
178		14	0	0	14	4	5	0	5	4	2	2	1	14.29%	20.00%	33.33%
179	· ·· · · · ·	13	0	0	13	1	2	0	10	1	1	0	9	81.82%	90.00%	90.00%
180		13	0	0	13	1	2	0	10	3	2	1	7	70 00%	70.00%	77.78%

AGGREGATE ORDER TYPES								····								
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
oompany me					1	L	ESOG									
	-	M	echanized	Interface L	lsed	Manual	Rejects		Validated		Errors				<u> </u>	Ì
	RESH / OCN		EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Cau sed Failout	CLEC Caused Fallout	Issued SO's		Ba se Calculation	Percent Flor Through
		1003	0	0	1003	160	49	4	790	36	31	5	754	79.79%	95.44%	96.05%
	1	300	0	0	300	32	20	1	247	9	6	3	238	86.23%	96.36%	97.54%
183		78	0	0	78	13	9	2	54	22	15	7	32	53.33%	59.26%	68.09%
·····		22	0	0	22	1	1	0	20	11	9	2	9	47.37%	45.00%	50.00%
185		11	0	0	11	0	2	0	9	1	1	0	8	88.89%	88.89%	88.89%
186	l	69	0	0	69	8	5	1	55	11	7	4	44	74.58%	80.00%	86.27%
	ļļļ	7	0	0	7	0	3	1	3	3	3	0	0	0.00%	0.00%	0.00%
		14	0	0	14	0	0	0	14	0	0	0	14	100.00%	100.00%	100.00%
] .	18	0	0	18	0	4	0	14	0	0	0	14	100.00%	100.00%	100.00%
	ļļļ	13	0	0	13	0	7	0	6	2	2	0	4	66.67%	66.67%	66.67%
191		10	0	0	10	0	3	0	7	1	1	0	6	85.71%	85.71%	85.71%
		13	0	0	13	0	2	0	11	0	0	0	11	100.00%	100.00%	100.00%
193		6	0	0	6	2	3	0	1	0	0	0	1	33.33%	100.00%	100.00%
194		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
195		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%
196		31232	0	0	31232	2660	2564	60	25948	1720	1481	239	24228	85.40%	93.37%	94.24%
197	[[18	0	0	18	1	1	0	16	2	1	1	14	87.50%	87.50%	93.33%
198		93	0	0	93	7	8	0	78	7	6	1	71	84.52%	91.03%	92.21%
199		1491	0	0	1491	665	110	15	701	168	127	41	533	40.23%	76.03%	80.76%
200		691	0	0	691	96	91	16	488	186	131	55	302	57.09%	61.89%	69.75%
201		70	0	0	70	2	1	1	66	10	6	4	56	87.50%	84.85%	90.32%
202	1 1	100	0	0	100	11	4	0	85	4	2	2	81	86.17%	95.29%	97.59%
203		11	0	0	11	0	1	0	10	9	5	4	1	16.67%	10.00%	16.67%
204	l [159	0	0	159	24	64	3	68	27	9	18	41	55.41%	60.29%	82.00%
205	ļļĮ	4773	0	0	4773	488	483	9	3793	400	315	85	3393	80.86%	89.45%	91.50%
206	Į į	578	0	0	578	14	23	2	539	21	17	4	518	94.35%	96.10%	96.82%
207	<u> </u>	28	0	0	28	0	2	1	25	22	13	9	3	18.75%	12.00%	18.75%
208	<u> </u>]	2	0	0	2	1	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
209	ļ ļ	_1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
210	[]	3654	0	0	3654	122	105	12	3415	196	141	55	, 3219	92.45%	94.26%	95.80%
211	Į į	10071	0	0	10071	281	396	19	9375	377	310	67	8998	93 84%	95.98%	96 67%
212] Ì	2761	0	0	2761	146	256	6	2353	177	139	38	2176	88.42%	92.48%	94.00%
213] 1	1252	0	0	1252	84	110	3	1055	51	36	15	1004	89 32%	95.17%	96.54%
214] 1	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
215	ן ו	202	0	0	202	5	16	0	181	11	7	4	170	93.41%	93.92%	96.05%
216	1 1	354	0	0	354	15	27	2	310	32	17	15	278	89.68%	89.68%	94.24%

AGGREGATE ORDER TYPE	s						1									
Company Info						LSR PF	ROCESSING								FLOWT	HROUGH
						Ĺ	ESOG									
		м	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Totai Manuai Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
217		139	0	0	139	26	8	3	102	27	19	8	75	62.50%	73.53%	79.7 9%
218		6	0	0	6	2	0	1	3	0	0	0	3	60.00%	100.00%	100.00%
219		663	0	0	663	165	65	7	426	143	107	36	283	50.99%	66.43%	72.56%
220	_	106	0	0	106	39	7	0	60	18	13	5	42	44.68%	70.00%	76.36%
221		3	0	0	3	0	0	1	2	2	1	1	0	0.00%	0.00%	0.00%
222		577	0	0	577	32	81	1	463	37	33	4	426	86.76%	92.01%	92.81%
223		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
224		33	0	0	33	4	4	0	25	3	3	0	22	75.86%	88.00%	88.00%
225		2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
226		8	0	0	8	0	0	1	7	3	3	0	4	57.14%	57.14%	57.14%
227		332	0	0	332	0	17	0	315	25	23	2	290	92.65%	92.06%	92.65%
228		6	0	0	6	0	3	0	3	2	2	0	1	33.33%	33 33%	33.33%
229		38	0	0	38	7	0	0	31	4	3	1	27	72.97%	87 10%	90.00%
230		36	0	0	36	3	2	1	30	5	2	3	25	83.33%	83.33%	92.59%
231		193	0	0	193	14	31	1	147	14	11	3	133	84.18%	90.48%	92.36%
232		267	0	0	267	64	18	0	185	60	40	20	125	54.59%	67.57%	75.76%
233		88	0	0	88	4	5	0	79	12	9	3	67	83.75%	84.81%	88.16%
234		109	0	0	109	8	13	4	84	44	25	19	40	54.79%	47.62%	61.54%
235		187	0	0	187	5	18	16	148	118	75	43	30	27.27%	20.27%	28.57%
236		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
237		14	0	0	14	1	0	0	13	1	1	0	12	85.71%	92.31%	92.31%
238		370	0	0	370	41	25	5	299	44	39	5	255	76.12%	85.28%	86.73%
239		3158	0	0	3158	192	192	23	2751	529	340	189	2222	80.68%	80.77%	86.73%
240		168	0	0	168	38	26	3	101	18	14	4	83	61.48%	82.18%	85.57%
241		378	0	0	378	21	42	1	314	23	14	9	291	89.26%	92 68%	95.41%
242		494	0	0	494	43	45	0	406	21	18	3	385	86.32%	94.83%	95.53%
243		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
244		13	0	0	13	0	4	0	9	0	0	0	9	100.00%	100.00%	100.00%
245		377	0	0	377	8	50	4	315	128	93	35	187	64.93%	59.37%	66.79%
246		510	0	0	510	36	33	0	441	22	20	2	, 419	88.21%	95.01%	95.44%
247		11	0	0	11	2	2	0	7	3	3	0	4	44.44%	57.14%	57.14%
248		14	0	0	14	0	2	0	12	0	0	0	12	100.00%	100.00%	100.00%
249		7920	0	0	7920	489	564	12	6855	862	646	216	5993	84.08%	87.43%	90.27%
250		2	0	0	2	0	0	0	2	2	0	2	0	0.00%	0.00%	0.00%
251		12	0	0	12	0	11	0	- 1	0	0	0	1	100.00%	100.00%	100.00%
252	-	3013	0	0	3013	277	212	15	2509	269	238	31	2240	81.31%	89.28%	90.40%

AGGREGATE ORDER TYPES					1									<u> </u>		1
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
	1					L	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	'ercent Flo Through
253		25	0	0	25	2	1	0	22	0	0	0	22	91.67%	100.00%	100.00%
254		41	0	0	41	9	2	0	30	2	1	1	28	73.68%	93.33%	96.55%
255		7	0	0	7	Ó	0	0	7	0	0	0	7	100.00%	100.00%	100.00%
256		34	0	0	34	7	6	0	21	4	3	1	17	62.96%	80.95%	85.00%
257		9	0	0	9	4	0	0	5	0	0	0	5	55.56%	100.00%	100.00%
258		60	0	0	60	14	4	0	42	6	4	2	36	66.67%	85.71%	90.00%
259		14	0	0	14	1	1	0	12	0	0	0	12	92.31%	100.00%	100.00%
260		38	0	0	38	6	1	0	31	1	0	1	30	83.33%	96.77%	100.00%
261		121	0	0	121	43	7	0	71	20	14	6	51	47.22%	71.83%	78.46%
262]]	496	0	0	496	35	17	2	442	27	21	6	415	88.11%	93.89%	95.18%
263		1498	0	0	1498	244	104	8	1142	64	48	16	1078	78.69%	94.40%	95.74%
264		205	0	0	205	33	31	2	139	26	20	6	113	68.07%	81.29%	84.96%
265		192	0	0	192	14	29	1	148	19	15	4	129	81.65%	87.16%	89.58%
266	1	253	0	0	253	44	10	4	195	63	50	13	132	58.41%	67.69%	72.53%
267	1 1	24	0	0	24	0	8	0	16	0	0	0	16	100.00%	100.00%	100.00%
268		40	0	0	40	2	3	3	32	14	12	2	18	56.25%	56.25%	60.00%
269	1	560	0	0	560	380	11	0	169	9	7	2	160	29.25%	94.67%	95.81%
270		907	0	0	907	693	42	1	171	22	5	17	149	17.59%	87.13%	96.75%
271		997	0	0	997	764	11	1	221	18	5	13	203	20.88%	91.86%	97.60%
272		234	0	0	234	9	13	2	210	15	13	2	195	89.86%	92.86%	93.75%
273		1887	0	0	1887	139	108	7	1633	96	69	27	1537	88.08%	94.12%	95.70%
274		68	0	0	68	8	7	2	51	12	9	3	39	69.64%	76.47%	81.25%
275		816	0	0	816	80	28	0	708	41	35	6	667	85.29%	94.21%	95.01%
276		859	0	0	859	71	121	1	666	36	26	10	630	86.66%	94.59%	96.04%
277		2347	0	0	2347	226	239	10	1872	136	121	15	1736	83.34%	92.74%	93.48%
278		410	0	0	410	22	17	0	371	24	17	7	347	89.90%	93.53%	95.33%
279		802	0	0	802	85	91	0	626	46	34	12	580	82.98%	92.65%	94.46%
280		170	0	0	170	2	20	0	148	37	27	10	111	79.29%	75.00%	80.43%
281		118	0	0	118	10	5	0	103	5	5	0	98	86.73%	95.15%	95.15%
282		2	0	0	2	0	0	0	2	1	1	0	, 1	50.00%	50.00%	50.00%
283		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
284		34	0	0	34	4	1	1	28	14	7	7	14	56.00%	50.00%	66.67%
285		511	0	0	511	67	111	2	331	86	56	30	245	66.58%	74,02%	81.40%
286		1724	0	0	1724	175	53	4	1492	98	78	20	1394	84.64%	93.43%	94.70%
286		35	0	0	35	0	3	1	31	3	2	1	28	93.33%	90.32%	93.33%
287		1469	0	0	1469	106	93	8	1262	65	43	22	1197	88.93%	90.32% 94.85%	96.53%

AGGREGATE ORDER TYPES													<u> </u>			
Company Info						LSR PR	OCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
289		105	0	0	105	13	15	1	76	11	10	1	65	73.86%	85.53%	86.67%
290		540	0	0	540	64	44	7	425	58	52	6	367	75.98%	86.35%	87.59%
291		229	0	0	229	19	14	0	196	12	6	6	184	88.04%	93.88%	96.84%
292		1048	0	0	1048	143	49	2	854	62	48	14	792	80.57%	92.74%	94.29%
293		123	0	0	123	13	20	5	85	46	34	12	39	45.35%	45.88%	53.42%
294		3114	0	0	3114	343	247	11	2513	144	110	34	2369	83.95%	94.27%	95.56%
295		62	0	0	62	13	15	1	33	15	2	13	18	54.55%	54.55%	90.00%
296		19	0	0	19	0	7	0	12	0	0	0	12	100.00%	100.00%	100.00%
297		17	0	0	17	t	0	0	16	1	1	0	15	88.24%	93.75%	93.75%
298		13	0	0	13	0	6	0	7	2	1	1	5	83.33%	71.43%	83.33%
299		22	0	0	22	11	4	0	7	3	3	0	4	22.22%	57.14%	57.14%
300		28	0	0	28	1	6	0	21	4	1	3	17	89.47%	80.95%	94.44%
301		113	0	0	113	6	4	0	103	18	10	8	85	84.16%	82.52%	89.47%
302		817	0	0	817	88	35	3	691	98	89	9	593	77.01%	85.82%	86.95%
303		125	0	0	125	16	1	0	108	5	4	1	103	83.74%	95.37%	96.26%
304		113	0	0	113	72	22	0	19	0	0	0	19	20.88%	100.00%	100.00%
305	i i	7	0	0	7	2	1	1	3	2	0	2	1	33.33%	33.33%	100.00%
306		87	0	0	87	1	16	0	70	5	4	1	65	92.86%	92.86%	94.20%
307	İ	42	0	0	42	1	4	1	36	3	1	2	33	94.29%	91.67%	97.06%
308		508	0	0	508	70	39	3	396	43	37	6	353	76.74%	89.14%	90.51%
309		167	0	0	167	26	31	2	108	7	4	3	101	77.10%	93.52%	96.19%
310		2079	0	0	2079	229	234	4	1612	231	180	51	1381	77.15%	85.67%	88.47%
311		19	0	0	19	0	1	0	18	3	3	0	15	83.33%	83.33%	83.33%
312		14	0	0	14	2	2	0	10	6	3	3	4	44.44%	40.00%	57.14%
313	1	3288	0	0	3288	257	403	8	2620	137	108	29	2483	87.18%	94.77%	95.83%
314		5251	0	0	5251	873	450	61	3867	797	620	177	3070	67.28%	79.39%	83.20%
315		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
316		7	0	0	7	0	1	0	6	1	0	1	5	100.00%	83.33%	100.00%
317		12204	0	0	12204	767	548	9	10880	446	362	84	10434	90.24%	95.90%	96.65%
318	/	139	0	0	139	22	4	1	112	10	7	3	, 102	77.86%	91.07%	93.58%
319		106	0	0	106	32	8	0	66	21	16	5	45	48.39%	68.18%	73.77%
320	¦· -· · · · · · · · · · · · · · · · · ·	179	0	0	179	20	51	1	107	7	4	3	100	80.65%	93.46%	96.15%
321		47	0	0	47	15	4	0	28	17	8	9	11	32.35%	39.29%	57.89%
322		12	0	0	12	0	0	2	10	6	1	5	4	80.00%	40.00%	80.00%
323		52	0	0	52	5	12	2	33	10	6	4	23	67.65%	69.70%	79.31%
324	1 I	52 1427	I 0 <u></u>		1427	125	181	12	1109	346	260	86	763	66 46%	68.80%	74.58%

Company Info		[LSR PR	OCESSING								FLOWT	HROUGH
	í						ESOG						1			1
·································		M	echanized	i Interface I	lead	Manuai	Rejects		Validated		Errors			· · · · ·	· ·· <u>-</u> ·	
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Totai System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	'ercent Fie Through
325		6	0	0	6	6	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
326		16	0	0	16	0	0	0	16	1	1	0	15	93.75%	93.75%	93.75%
327		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
328		17	0	0	17	3	0	0	14	5	2	3	9	64.29%	64.29%	81.82%
329		32	0	0	32	4	2	0	26	6	4	2	20	71.43%	76.92%	83.33%
330		5	0	0	5	0	0	0	5	3	3	0	2	40.00%	40.00%	40.00%
331		3	0	0	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33.33%
332		128	0	0	128	25	15	2	86	21	14	7	65	62.50%	75.58%	82.28%
333		1781	0	0	1781	249	251	2	1279	139	89	50	1140	77.13%	89.13%	92.76%
334		2440	0	0	2440	219	199	30	1992	342	229	113	1650	78.65%	82.83%	87.81%
335		363	0	0	363	41	39	3	280	40	22	18	240	79.21%	85.71%	91.60%
336		49	0	0	49	0	25	0	24	2	2	0	22	91.67%	91.67%	91.67%
337	1	389	0	0	389	61	17	1	310	18	15	3	292	79.35%	94.19%	95.11%
338	1 i	103	0	0	103	8	18	0	77	18	13	5	59	73.75%	76.62%	81.94%
339	1	68	0	0	68	31	17	0	20	2	2	0	18	35.29%	90.00%	90.00%
340	1 1	77	0	0	77	13	27	0	37	20	20	0	17	34.00%	45.95%	45.95%
341		2	0	0	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
342		132	0	0	132	19	24	1	88	33	16	17	55	61.11%	62.50%	77.46%
343	1 1	8	0	0	8	0	0	0	8	0	0	0	8	100.00%	100.00%	100.009
344	1	5	0	0 I	5	0	o	0	5	4	2	2	1	33.33%	20.00%	33.33%
345		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
346	1 1	7	0	0	7	1	2	0	4	1	1	0	3	60.00%	75.00%	75.00%
347	· · · · · · · · · · · · · · · · · · ·	79	0	0	79	3	2	0	74	5	3	2	69	92.00%	93.24%	95.83%
348		706	0	0	706	44	37	9	616	380	326	54	236	38.94%	38.31%	41.99%
349	1	42	0	0	42	7	0	6	29	11	4	7	18	62.07%	62.07%	81.82%
350	11	200	0	0	200	2	26	0	172	8	8	0	164	94.25%	95.35%	95.35%
351	1	37	0	0	37	3	15	0	19	2	1	1	17	80.95%	89.47%	94.44%
352	1 1	2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.009
353	1 1	52	0	0	52	4	10	1	37	6	5	1	31	77.50%	83.78%	86.11%
354	1 1	684	0	0	684	56	81	2	545	41	30	11	, 504	85 42%	92.48%	94.38%
355		141	0	0	141	16	17	1	107	39	23	16	68	63.55%	63.55%	74 73%
356	† †	23	0	0	23	18	0	1	4	2	1	1	2	9.52%	50.00%	66.67%
357	-} ···→	73	0	0	73	6	6	0	61	8	6	2	53	81.54%	86.89%	89 839
358	1 1	125	0	0	125	43	12	4	66	34	22	12	32	32.99%	48.48%	59.26%
359	{ {		0	0	55	 	5	4	16	- 34 - 4	4	0	12	24.00%	75.00%	75.00%
359	4 1	<u>55</u>	0	0	1		0	0	0	4 0	4	0	0	0.00%	0.00%	0.00%

GGREGATE ORDER TYPES																
Company Info						LSR PF	ROCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface (Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
361		142	0	0	142	32	14	2	94	36	35	1	58	46.40%	61.70%	62.37%
362		15	0	0	15	4	2	0	9	3	2	1	6	50.00%	66.67%	75.00%
363		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
364		17	0	0	17	4	4	0	9	0	0	0	9	69.23%	100.00%	100.00%
365		61	0	0	61	10	8	0	43	7	4	3	36	72.00%	83.72%	90.00%
366		31	0	0	31	3	3	1	24	3	3	0	21	77.78%	87.50%	87.50%
367		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
368		403	0	0	403	138	39	16	210	114	94	20	96	29.27%	45.71%	50.53%
369		2067	0	0	2067	183	168	31	1685	565	396	169	1120	65.92%	66.47%	73.88%
370		3518	0	0	3518	362	442	30	2684	821	582	239	1863	66.37%	69.41%	76.20%
371		78	0	0	78	0	16	0	62	9	6	3	53	89.83%	85.48%	89.83%
372		1478	0	0	1478	270	253	17	938	521	378	143	417	39.15%	44.46%	52.45%
373		4	0	0	4	0	4	0	0	0	0	0	0	0.00%	0.00%	0.00%
374		440	0	0	440	81	47	1	311	78	49	29	233	64.19%	74.92%	82.62%
375		1746	0	0	1746	342	112	11	1281	397	305	92	884	57.74%	69.01%	74.35%
376		9	0	0	9	0	3	0	6	6	2	4	0	0.00%	0.00%	0.00%
377		25	0	0	25	0	6	0	19	0	0	0	19	100.00%	100.00%	100.00%
378		154	0	0	154	35	13	2	104	29	24	5	75	55.97%	72.12%	75.76%
379		1552	0	0	1552	217	138	13	1184	388	304	84	796	60.44%	67.23%	72.36%
380		36	0	0	36	22	2	1	11	3	3	0	8	24.24%	72 73%	72.73%
381		361	0	0	361	44	58	2	257	103	61	42	154	59.46%	59.92%	71.63%
382		13	0	0	13	0	2	0	11	0	0	0	11	100.00%	100.00%	100.00%
383		3658	0	0	3658	287	149	30	3192	427	325	102	2765	81.88%	86.62%	89.48%
384		668	0	0	668	26	25	0	617	41	30	11	576	91.14%	93.35%	95.05%
385		90	0	0	90	11	5	0	74	13	9	4	61	75.31%	82.43%	87.14%
386		9	0	0	9	1	3	0	5	3	2	1	2	40.00%	40.00%	50.00%
387	1 1	38	0	0	38	21	4	0	13	3	3	0	10	29.41%	76.92%	76.92%
388		1746	0	0	1746	93	89	9	1555	358	269	89	1197	76.78%	76.98%	81.65%
389	1	1127	0	0	1127	84	112	3	928	79	73	6	849	84.39%	91 49%	92.08%
390		4	0	0	4	0	0	0	4	1	1	0	, 3	75.00%	75.00%	75.00%
391	1 1	22	0	0	22	6	8	0	8	3	2	1	5	38.46%	62.50%	71.43%
392	1	295	0	0	295	45	15	0	235	23	12	11	212	78.81%	90.21%	94.64%
393	1 1	114	0	0	114	14	10	0	90	36	33	3	54	53.47%	60.00%	62.07%
394	1 1	2840	0	0	2840	226	186	12	2416	204	137	67	2212	85.90%	91.56%	94.17%
395	++	112	0	0	112	17	9	1	85	28	18	10	57	61.96%	67.06%	76 00%
396	·}	360	0	0	360	32	46	6	276	35	25	10	241	80.87%	87.32%	90 60%

GGREGATE ORDER TYPES					-											<u> </u>
Company Info						LSR PR	OCESSING								FLOWT	HROUGH
						L	ESOG									
		Me	echanized	Interface t	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Totai Manuai Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	issued SO's	Percent Ach ieved Flowthrough	Base Calculation	Percent Flow Through
397		65	0	0	65	11	14	0	40	9	3	6	31	68.89%	77.50%	91.18%
398		435	0	0	435	6	9	1	419	52	44	8	367	88.01%	87.59%	89.29%
399		339	0	0	339	45	12	0	282	20	16	4	262	81.11%	92.91%	94.24%
400		823	0	0	823	141	107	4	571	78	60	18	493	71.04%	86.34%	89.15%
401		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
402		233	0	0	233	29	8	3	193	27	19	8	166	77.57%	86.01%	89.73%
403		620	0	0	620	104	14	0	502	23	20	3	479	79.44%	95.42%	95.99%
404		25	0	0	25	4	2	0	19	7	4	3	12	60.00%	63.16%	75.00%
405		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
406		60	0	0	60	13	10	1	36	0	0	0	36	73.47%	100.00%	100.00%
407		850	0	0	850	61	69	1	719	53	46	7	666	86.16%	92.63%	93.54%
408		79	0	0	79	21	12	1	45	6	6	0	39	59.09%	86.67%	86.67%
409		241	0	0	241	24	6	1	210	28	21	7	182	80.18%	86.67%	89 66%
410		54	0	0	54	2	18	1	33	11	2	9	22	84.62%	66.67%	91.67%
411		493	0	0	493	26	47	5	415	80	47	33	335	82.11%	80.72%	87.70%
412		416	0	0	416	26	40	48	302	214	174	40	88	30 56%	29.14%	33.59%
413		18	0	0	18	0	3	0	15	4	0	4	11	100.00%	73.33%	100.00%
414		46	0	0	46	3	2	0	41	9	8	1	32	74.42%	78.05%	80.00%
415		120	0	0	120	11	13	0	96	9	8	1	87	82.08%	90.63%	91.58%
416		878	0	0	878	83	61	1	733	97	75	22	636	80.10%	86.77%	89 45%
417		86	0	0	86	13	13	0	60	36	27	9	24	37.50%	40.00%	47.06%
418		14	0	0	14	1	5	1	7	4	2	2	3	50 00%	42.86%	60.00%
419		547	0	0	547	33	47	1	466	74	51	23	392	82.35%	84.12%	88.49%
420		1354	0	0	1354	112	127	5	1110	63	50	13	1047	86.60%	94.32%	95.44%
421		375	0	0	375	63	52	12	248	110	87	23	138	47.92%	55.65%	61.33%
422		38	0	0	38	3	19	2	14	10	6	4	4	30.77%	28.57%	40.00%
423		1802	0	0	1802	254	42	17	1489	352	318	34	1137	66 53%	76.36%	78.14%
424		141	0	0	141	16	16	0	109	43	35	8	66	56.41%	60.55%	65.35%
425		65	0	0	65	20	5	2	38	11	6	5	27	50.94%	71.05%	81.82%
426		6	0	0	6	0	2	0	4	3	3	0	, 1	25.00%	25 00%	25.00%
427		43	0	0	43	5	6	0	32	4	3	1	28	77.78%	87.50%	90.32%
428		32	0	0	32	11	2	0	19	18	2	16	1	7.14%	5.26%	33.33%
429		23	0	0	23	13	4	0	6	4	2	2	2	11.76%	33.33%	50.00%
430		408	0	0	408	56	22	1	329	20	7	13	309	83.06%	93.92%	97.78%
431		130	0	0	130	20	78	1	31	11	5	6	20	44.44%	64.52%	80.00%
432		58958	0	0	58958	4866	13676	445	39971	11023	7983	3040	28948	69.26%	72.42%	78 38%

GREGATE ORDER TYPES							<u> </u>								<u> </u>	<u> </u>
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface U	sed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Totai System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flor Through
433		454	0	0	454	30	56	11	357	116	99	17	241	65.14%	67.51%	70.88%
434		38	0	0	38	1	7	1	29	6	4	2	23	82.14%	79.31%	85.19%
435		49	0	0	49	2	6	0	41	13	12	1	28	66.67%	68.29%	70.00%
436		769	0	0	769	66	53	8	642	146	118	28	496	72.94%	77.26%	80.78%
437		18	0	0	18	3	1	1	13	4	4	0	9	56.25%	69.23%	69.23%
438		48	0	0	48	2	3	4	39	4	1	3	35	92.11%	89.74%	97.22%
439		20	0	0	20	4	2	0	14	2	2	0	12	66.67%	85.71%	85.71%
440		41	0	0	41	4	5	0	32	8	6	2	24	70.59%	75.00%	80.00%
441		314	0	0	314	4	9	3	298	23	20	3	275	91.97%	92.28%	93.22%
442		19	0	0	19	3	0	0	16	1	1	0	15	78.95%	93.75%	93.75%
443		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
444		429	0	0	429	76	47	4	302	113	83	30	189	54.31%	62.58%	69.49%
445		3	0	0	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%
446		110	0	0	110	7	4	1	98	5	3	2	93	90.29%	94.90%	96.88%
447		3	0	0	3	0	0	0	3	3	3	0	0	0.00%	0.00%	0.00%
448		677	0	0	677	111	68	5	493	96	57	39	397	70.27%	80.53%	87.44%
449		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
450		23	0	0	23	2	7	0	14	1	1	0	13	81.25%	92.86%	92.86%
451		508	0	0	508	42	9	2	455	27	13	14	428	88.61%	94.07%	97.05%
452		312	0	0	312	37	5	1	269	19	16	3	250	82.51%	92.94%	93.98%
453		561	0	0	561	35	20	5	501	36	28	8	465	88.07%	92.81%	94.32%
454		325	0	0	325	49	34	2	240	52	34	18	188	69.37%	78.33%	84,68%
455		162	0	0	162	29	5	0	128	9	8	1	119	76.28%	92.97%	93.70%
456	<u> </u>	1802	0	0	1802	239	164	13	1386	189	150	39	1197	75.47%	86.36%	88.86%
457		322	0	0	322	52	39	6	225	74	61	13	151	57.20%	67.11%	71.23%
458		26	0	0	26	1	5	0	20	0	0	0	20	95.24%	100.00%	100.00%
459		116	0	0	116	17	6	2	91	28	26	2	63	59.43%	69.23%	70.79%
460		8	0	0	8	0	3	0	5	1	1	0	4	80.00%	80.00%	80.00%
461		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
462	+	2372	0	0	2372	120	243	15	1994	265	222	43	1729	83.49%	86,71%	88.62%
463		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
464		333	0	0	333	20	26	0	287	29	24	5	258	85.43%	89.90%	91.49%
465		61	0	0	61	5	12	5	39	15	10	5	235	61.54%	61.54%	70.59%
466		366	0	0	366	13	37	0	316	33	21	12	283	89.27%	89.56%	93.09%
467		<u> </u>	0	0	96	17	15	3	61	15	11	4	46	62.16%	75.41%	93.09% 80.70%
468	· · · ·	90 41	0	0	90 41	16	10	0	24	5	3	2	19	50.00%	79.17%	86.36%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDi	TAG	Total Mech LSR's	Totai Manuai Fallout	Auto Clarification	Pending Supp s (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
469		2125	0	0	2125	34	135	3	1953	204	118	86	1749	92.00%	89.55%	93.68%
470		1438	0	0	1438	144	15	2	1277	44	34	10	1233	87.38%	96.55%	97.32%
471		42	0	0	42	2	8	0	32	11	4	7	21	77.78%	65.63%	84.00%
472		483	0	0	483	27	35	1	420	16	11	5	404	91.40%	96.19%	97.35%
473		1233	0	0	1233	119	137	9	968	185	124	61	783	76.32%	80.89%	86.33%
474		4	0	0	4	1	0	0	3	2	0	2	1	50.00%	33.33%	100.00%
475		32	0	0	32	7	1	0	24	6	4	2	18	62.07%	75.00%	81.82%
476		16	0	0	16	2	3	0	11	7	4	3	4	40.00%	36.36%	50.00%
477		545	0	0	545	147	85	3	310	63	44	19	247	56.39%	79.68%	84.88%
478		58	0	0	58	9	8	1	40	19	4	15	21	61.76%	52.50%	84.00%
479		13	0	0	13	2	0	0	11	4	3	1	7	58.33%	63.64%	70.00%
480		153	0	0	153	0	36	0	117	2	2	0	115	98.29%	98.29%	98.29%
481		6	0	0	6	0	2	0	4	0	0	0	4	100.00%	100.00%	100.00%
482		762	0	0	762	82	138	6	536	175	143	32	361	61.60%	67.35%	71.63%
483		61	0	0	61	11	11	1	38	14	8	6	24	55.81%	63.16%	75.00%
484		1489	0	0	1489	298	133	30	1028	366	217	149	662	56.24%	64.40%	75.31%
485		15	0	0	15	0	2	0	13	1	1	0	12	92.31%	92.31%	92.31%
486		7	0	0	7	0	0	0	7	4	4	0	3	42.86%	42.86%	42.86%
487		58	0	0	58	12	10	0	36	13	7	6	23	54.76%	63.89%	76.67%
488		47	0	0	47	11	8	1	27	6	3	3	21	60.00%	77.78%	87.50%
489		8	0	0	8	2	2	0	4	2	0	2	2	50.00%	50.00%	100.00%
490		21	0	0	21	7	3	0	11	4	2	2	7	43.75%	63.64%	77.78%
LENS Subtotal		265858	0	0	265858	26516	29946	1531	207865	32352	24078	8274	175513	77.62%	84.44%	87.94%
EDI Subtotal		0	78083	0	78083	5208	11866	172	60837	18839	11426	7413	41998	71.63%	69.03%	78.61%
TAG Subtotal		0	0	54505	54505	5090	7678	593	41144	9940	6703	3237	31204	72.57%	75.84%	82.32%
TOTAL INTERFACES		265858	78083	54505	398446	36814	49490	2296	309846	61131	42207	18924	248715	75.89%	80.27%	85.49%

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AGGREGATE ORDER TYPES																
Company Info	1					LSR PR	OCESSING							F	LOWTHROUG	н
				-		L	ESOG									
		Me	chanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errora		1			
Name	RESH / OCN	LENŜ	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
1		0	75	0	75	2	4	0	69	13	10	3	56	82.35%	81.16%	84.85%
2		0	5562	0	5562	75	1094	1	4392	1075	871	204	3317	77.81%	75.52%	79.20%
3		0	8618	0	8618	154	1383	1	7080	2013	1610	403	5067	74.18%	71.57%	75.89%
4		0	131	0	131	8	26	0	97	37	19	18	60	68.97%	61.86%	75.95%
5		0	3313	0	3313	63	479	2	2769	738	605	133	2031	75.25%	73.35%	77.05%
6		0	3	0	3	0	0	0	3	3	1	2	0	0.00%	0.00%	0.00%
7		0	669	0	669	57	62	0	550	5	1	4	545	90.38%	99.09%	99.82%
8		0	1019	0	1019	24	162	1	832	182	72	110	650	87.13%	78.13%	90.03%
9		0	8280	0	8280	20	2480	0	5780	2443	226	2217	3337	93.13%	57.73%	93.66%
10	<u> </u>	0	5	0	5	0	0	0	5	5	4	1	0	0.00%	0.00%	0.00%
11		0	14	0	14	0	3	0	11	1	1	0	10	90.91%	90.91%	90.91%
12		0	2183	0	2183	161	207	0	1815	313	246	67	1502	78.68%	82.75%	85.93%
13		0	965	0	965	2	170	0	793	312	19	293	481	95.82%	60.66%	96.20%
14		0	4	0	4	0	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
15		0	1	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
16		0	1	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
17		0	8	0	8	0	1	0	7	2	0	2	5	100.00%	71.43%	100.00%
18		0	0	244	244	44	29	1	170	37	21	16	133	67.17%	78.24%	86.36%
19		0	0	1009	1009	35	107	0	867	36	24	12	831	93.37%	95.85%	97.19%
20		0	0	203	203	2	8	0	193	1	0	1	192	98.97%	99.48%	100.00%
21		0	0	63	63	39	0	0	24	3	3	0	21	33.33%	87.50%	87.50%
22		0	0	578	578	3	24	0	551	2	2	0	549	99.10%	99.64%	99.64%
23		0	0	25	25	3	1	0	21	5	3	2	16	72.73%	76.19%	84.21%
24		0	0	790	790	8	43	2	737	27	20	7	710	96.21%	96.34%	97.26%
25		0	0	2037	2037	72	50	16	1899	401	344	57	1498	78.27%	78.88%	81.32%
26		0	0	2	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%
27		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
28		0	0	3	3	3	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
29		0	0	4278	4278	15	608	34	3621	1582	840	742	2039	70.46%	56.31%	70 82%
30		0	0	10268	10268	79	378	11	9800	248	200	48	9552	97.16%	97.47%	97.95%
31		0	0	1	1	1	0	0	0	0	0	0	' 0	0.00%	0.00%	0.00%
32		0	0	80	80	16	25	3	36	15	9	6	21	45.65%	58.33%	70.00%
33		0	0	140	140	0	2	0	138	76	74	2	62	45.59%	44 93%	45.59%
34		0	0	10	10	8	0	0	2	2	0	2	0	0.00%	0.00%	0 00%
35		0	0	573	573	3	71	0	499	10	9	1	489	97.60%	98.00%	98.19%
36		0	0	1208	1208	17	56	0	1135	13	9	4	1122	97.74%	98.85%	99 20%

AGGREGATE ORDER TYPES	·												L			
Company Info							OCESSING							F	LOWTHROUG	н
							ESOG									
		Me	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
37		0	0	1483	1483	163	104	10	1206	237	172	65	969	74.31%	80.35%	84.93%
38		0	0	233	233	2	36	0	195	0	0	0	195	98.98%	100.00%	100.00%
39		0	0	52	52	16	12	3	21	16	2	14	5	21.74%	23.81%	71.43%
40		0	0	745	745	312	176	12	245	44	24	20	201	37.43%	82.04%	89.33%
41		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
42		649	0	0	649	18	31	0	600	50	37	13	550	90.91%	91.67%	93.70%
43		167	0	0	167	16	7	0	144	5	4	1	139	87.42%	96.53%	97.20%
44		13	0	0	13	1	2	0	10	1	1	0	9	81.82%	90.00%	90.00%
45		15	0	0	15	2	2	0	11	1	1	0	10	76.92%	90.91%	90.91%
46		1939	0	0	1939	205	73	4	1657	104	87	17	1553	84.17%	93.72%	94.70%
47		5	0	0	5	0	2	1	2	0	0	0	2	100.00%	100 00%	100.00%
48		242	0	0	242	25	43	0	174	28	16	12	146	78.07%	83.91%	90.12%
49		54	0	0	54	0	1	0	53	15	9	6	38	80.85%	71.70%	80 85%
50		36	0	0	36	8	4	1	23	15	9	6	8	32.00%	34.78%	47.06%
51		328	0	0	328	36	36	2	254	40	35	5	214	75.09%	84.25%	85.94%
52		51	0	0	51	4	16	0	31	6	6	0	25	71.43%	80.65%	80.65%
53		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
54		208	0	0	208	39	7	1	161	29	25	4	132	67.35%	81.99%	84.08%
55		642	0	0	642	90	42	0	510	23	22	1	487	81.30%	95.49%	95.68%
56		594	0	0	594	26	42	2	524	73	66	7	451	83.06%	86.07%	87.23%
57		516	0	0	516	61	13	1	441	23	16	7	418	84.44%	94 78%	96.31%
58		538	0	0	538	81	39	4	414	55	45	10	359	74.02%	86.71%	88.86%
59		617	0	0	617	32	33	3	549	49	44	5	500	86.81%	91.07%	91.91%
60		366	0	0	366	43	24	7	292	38	31	7	254	77.44%	86 99%	89.12%
61		199	0	0	199	7	7	3	182	18	13	5	164	89.13%	90.11%	92 66%
62		215	0	0	215	34	19	0	162	14	12	2	148	76.29%	91.36%	92.50%
63		2425	0	0	2425	282	116	13	2014	211	168	43	1803	80.03%	89.52%	91.48%
64		214	0	0	214	1	4	0	209	9	9	0	200	95.24%	95.69%	95.69%
65		1859	0	0	1859	123	91	1	1644	100	88	12	1544	87.98%	93.92%	94.61%
66		177	0	0	177	30	17	1	129	12	7	5	117	75.97%	90.70%	94.35%
67		226	0	0	226	22	9	2	193	28	22	6	'165	78.95%	85.49%	88.24%
68		45	0	0	45	1	4	0	40	4	1	3	36	94.74%	90.00%	97.30%
69		13	0	0	13	1	2	0	10	3	2	1	7	70.00%	70.00%	77 78%
70		1001	0	0	1001	159	49	4	789	36	31	5	753	79.85%	95.44%	96 05%
71		298	0	0	298	32	20	1	245	9	6	3	236	86.13%	96.33%	97.52%
72	+	20	0	0	20	1	1	0	18	9	8		9	50.00%	50.00%	52.94%

Exhibit October PM Data Attachment 2E

EGATE ORDER TYPE	s										1					
Company Info							OCESSING							F	LOWTHROUG	H
							ESOG									
		Me	chanized	Interface l	Jsed	Manual	Rejects	Valid	ated		Errors			Demonst		Dereent
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
73		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%
74		31232	0	0	31232	2660	2564	60	25948	1720	1481	239	24228	85.40%	93.37%	94.24%
75		18	0	0	18	1	1	0	16	2	1	1	14	87.50%	87.50%	93.33%
76		93	0	0	93	7	8	0	78	7	6	1	71	84.52%	91.03%	92.21%
77		7	0	0	7	0	5	0	2	0	0	0	2	100.00%	100.00%	100.00%
78		142	0	0	142	14	22	2	104	32	19	13	72	68.57%	69.23%	79.12%
79		70	0	0	70	2	1	1	66	10	6	4	56	87.50%	84.85%	90.32%
80		99	0	0	99	11	4	0	84	3	1	2	81	87.10%	96.43%	98.78%
81		134	0	0	134	10	61	3	60	23	7	16	37	68.52%	61.67%	84.09%
82		4773	0	0	4773	488	483	9	3793	400	315	85	3393	80.86%	89.45%	91.50%
83		578	0	0	578	14	23	2	539	21	17	4	518	94.35%	96.10%	96.82%
84		117	0	0	117	10	41	6	60	17	0	17	43	81.13%	71.67%	100.00%
85		10051	0	0	10051	280	387	16	9368	374	309	65	8994	93 85%	96.01%	96.68%
86		2761	0	0	2761	146	256	6	2353	177	139	38	2176	88.42%	92.48%	94.00%
87		1251	0	0	1251	83	110	3	1055	51	36	15	1004	89.40%	95.17%	96.54%
88		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
89		202	0	0	202	5	16	0	181	11	7	4	170	93.41%	93.92%	96.05%
90		354	0	0	354	15	27	2	310	32	17	15	278	89.68%	89.68%	94.24%
91		569	0	0	569	31	80	1	457	35	32	3	422	87.01%	92.34%	92.95%
92		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
93		33	0	0	33	4	4	0	25	3	3	0	22	75.86%	88.00%	88.00%
94		8	0	0	8	0	0	1	7	3	3	0	4	57.14%	57.14%	57.14%
95		38	0	0	38	7	0	0	31	4	3	1	27	72.97%	87.10%	90.00%
96		36	0	0	36	3	2	1	30	5	2	3	25	83.33%	83.33%	92.59%
97		34	0	0	34	0	2	0	32	9	4	5	23	85.19%	71.88%	85.19%
98		88	0	0	88	4	5	0	79	12	9	3	67	83.75%	84.81%	88.16%
99		7	0	0	7	0	4	0	3	1	1	0	2	66.67%	66.67%	66.67%
100		14	0	0	14	1	Ö	0	13	1	1	0	12	85.71%	92.31%	92.31%
101		367	0	0	367	40	24	5	298	43	38	5	255	76.58%	85.57%	87 03%
102		3158	0	0	3158	192	192	23	2751	529	340	189	2222	80 68%	80.77%	86.73%
103		8	0	0	8	0	3	0	5	0	0	0	5	100 00%	100.00%	100.00%
104		378	0	0	378	21	42	1	314	23	14	9	291	89.26%	92.68%	95.41%
105		494	0	0	494	43	45	0	406	21	18	3	385	86.32%	94.83%	95.53%
106		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
107		13	0	0	13	0	4	0	9	0	0	0	9	100 00%	100.00%	100.00%
107		509	0	0	509	36	33	0	440	22	20	2	418	88.19%	95.00%	95.43%

AGGREGATE ORDER TYPES	3															
Company Info	1					LSR PR	OCESSING							F	LOWTHROUG	н
						Li	ESOG									
		Me	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
109		4	0	0	4	2	0	0	2	0	0	0	2	50.00%	100.00%	100.00%
110		14	0	0	14	0	2	0	12	0	0	0	12	100.00%	100.00%	100.00%
111		7920	0	0	7920	489	564	12	6855	862	646	216	5993	84.08%	87.43%	90.27%
112		3013	0	0	3013	277	212	15	2509	269	238	31	2240	81.31%	89.28%	90.40%
113		857	0	0	857	71	121	1	664	36	26	10	628	86.62%	94.58%	96.02%
114		410	0	0	410	22	17	0	⁻ 371	24	17	7	347	89.90%	93.53%	95.33%
115		170	0	0	170	2	20	0	148	37	27	10	111	79.29%	75.00%	80.43%
116		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
117		34	0	0	34	3	3	0	28	3	3	0	25	80.65%	89.29%	89.29%
118		496	0	0	496	35	17	2	442	27	21	6	415	88.11%	93.89%	95.18%
119		1498	0	0	1498	244	104	8	1142	64	48	16	1078	78.69%	94.40%	95.74%
120		6	0	0	6	1	2	0	3	0	0	0	3	75.00%	100.00%	100.00%
121		109	0	0	109	4	16	1	88	5	4	1	83	91.21%	94.32%	95 40%
122		234	0	0	234	9	13	2	210	15	13	2	195	89.86%	92.86%	93.75%
123		1885	0	0	1885	139	108	6	1632	96	69	27	1536	88.07%	94.12%	95.70%
124		68	0	0	68	8	7	2	51	12	9	3	39	69.64%	76.47%	81.25%
125		815	0	0	815	80	28	0	707	40	34	6	667	85.40%	94.34%	95.15%
126		2346	0	0	2346	226	239	10	1871	136	121	15	1735	83.33%	92.73%	93.48%
127		802	0	0	802	85	91	0	626	46	34	12	580	82.98%	92.65%	94.46%
128		118	0	0	118	10	5	0	103	5	5	0	98	86.73%	95.15%	95.15%
129		511	0	0	511	67	111	2	331	86	56	30	245	66.58%	74.02%	81.40%
130		1724	0	0	1724	175	53	4	1492	98	78	20	1394	84.64%	93.43%	94.70%
131		35	0	0	35	0	3	1	31	3	2	1	28	93.33%	90.32%	93.33%
132		1464	0	0	1464	104	93	8	1259	64	42	22	1195	89.11%	94.92%	96.60%
133		104	0	0	104	12	15	1	76	11	10	1	65	74.71%	85.53%	86.67%
134		540	0	0	540	64	44	7	425	58	52	6	367	75.98%	86.35%	87.59%
135		229	0	0	229	19	14	0	196	12	6	6	184	88.04%	93.88%	96.84%
136		1048	0	0	1048	143	49	2	854	62	48	14	792	80.57%	92.74%	94.29%
137		123	0	0	123	13	20	5	85	46	34	12	39	45.35%	45.88%	53 42%
138		3113	0	0	3113	343	247	11	2512	144	110	34	2368	83.94%	94.27%	95.56%
139		113	0	0	113	6	4	0	103	18	10	8	85	84.16%	82.52%	89.47%
140		809	0	0	809	87	35	3	684	98	89	9	586	76.90%	85.67%	86.81%
141	-	124	0	0	124	16	1	0	107	5	4	1	102	83.61%	95.33%	96.23%
142		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
143		5	0	0	5	0	1	1	3	2	0	2	1	100.00%	33.33%	100.00%
144		2	0	0	2	0	1	0	1	1	0		0	0.00%	0.00%	0.00%

AGGREGATE ORDER TYPE	S															
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	esog									
· · · · · · · · · · · · · · · · · · ·		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
145		41	0	0	41	1	4	1	35	3	1	2	32	94.12%	91.43%	96.97%
146		507	0	0	507	69	39	3	396	43	37	6	353	76.91%	89.14%	90.51%
147		117	0	0	117	7	24	1	85	4	4	0	81	88.04%	95.29%	95.2 9%
148		2079	0	0	2079	229	234	4	1612	231	180	51	1381	77.15%	85.67%	88.47%
149		19	0	0	19	0	1	0	18	3	3	0	15	83.33%	83.33%	83.33%
150		3284	0	0	3284	257	403	7	2617	136	107	29	2481	87.21%	94.80%	95.87%
151		525	0	0	525	56	72	5	392	80	56	24	312	73.58%	79.59%	84.78%
152		12204	0	0	12204	767	548	9	10880	446	362	84	10434	90.24%	95.90%	96.65%
153		139	0	0	139	22	4	1	112	10	7	3	102	77.86%	91.07%	93.58%
154		102	0	0	102	32	8	0	62	19	15	4	43	47.78%	69.35%	74.14%
155		147	0	0	147	2	40	0	105	7	4	3	98	94.23%	93.33%	96.08%
156		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
157		17	0	0	17	2	8	1	6	0	0	0	6	75.00%	100.00%	100.00%
158		9	0	0	9	4	3	0	2	0	0	0	2	33.33%	100.00%	100.00%
159		16	0	0	16	0	0	0	16	1	1	0	15	93.75%	93.75%	93.75%
160		5	0	0	5	0	1	0	4	0	0	0	4	100.00%	100.00%	100.00%
161		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
162		95	0	0	95	20	10	1	64	14	8	6	50	64.10%	78.13%	86.21%
163		401	0	0	401	55	89	1	256	37	19	18	219	74.74%	85.55%	92.02%
164		21	0	0	21	5	8	0	8	2	0	2	6	54.55%	75.00%	100.00%
165		312	0	0	312	32	24	1	255	32	22	10	223	80.51%	87.45%	91.02%
166		389	0	0	389	61	17	1	310	18	15	3	292	79.35%	94.19%	95.11%
167		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
168		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
169		79	0	0	79	3	2	0	74	5	3	2	69	92.00%	93.24%	95 83%
170		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
171		200	0	0	200	2	26	0	172	8	8	0	164	94.25%	95.35%	95.35%
172		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
173		52	0	0	52	4	10	1	37	6	5	1	31	77.50%	83.78%	86.11%
174		684	0	0	684	56	81	2	545	41	30	11	504	85.42%	92.48%	94.38%
175		51	0	0	51	2	4	0	45	6	4	2	39	86.67%	86.67%	90.70%
176		9	0	0	9	4	3	0	2	1	1	0	1	16.67%	50 00%	50.00%
177		142	0	0	142	32	14	2	94	36	35	1	58	46.40%	61.70%	62.37%
178		13	0	0	13	3	2	0	8	3	2	1	5	50.00%	62.50%	71.43%
179		46	0	0	46	7	6	0	33	5	3	2	28	73.68%	84.85%	90.32%
180		31	0	0	31	3	3	1	24	3	3	0	21	77.78%	87.50%	87.50%

AGGREGATE ORDER TYPE	S															
Company Info						LSR PF	OCESSING							F	LOWTHROUG	н
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	lated		Errors		l -			<u> </u>
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
181		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
182	-	171	0	0	171	8	18	12	133	78	71	7	55	41.04%	41.35%	43.65%
183		6	0	0	6	0	3	0	3	0	0	0	3	100.00%	100.00%	100.00%
184		9	0	0	9	1	2	1	5	3	1	2	2	50.00%	40.00%	66.67%
185		217	0	0	217	28	39	5	145	62	38	24	83	55.70%	57.24%	68.60%
186		171	0	0	171	9	22	0	140	16	10	6	124	86.71%	88.57%	92.54%
187		4	0	0	4	0	1	1	2	1	0	1	1	100.00%	50.00%	100.00%
188		9	0	0	9	0	3	0	6	6	2	4	0	0.00%	0.00%	0.00%
189		16	0	0	16	3	2	0	11	1	1	0	10	71.43%	90.91%	90.91%
190		5	0	0	5	1	1	0	3	2	2	0	1	25.00%	33.33%	33.33%
191		77	0	0	77	3	14	0	60	16	8	8	44	80.00%	73.33%	84.62%
192		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
193		3658	0	0	3658	287	149	30	3192	427	325	102	2765	81.88%	86.62%	89.48%
194		668	0	0	668	26	25	0	617	41	30	11	576	91.14%	93.35%	95.05%
195		75	0	0	75	9	3	0	63	7	5	2	56	80.00%	88.89%	91.80%
196		6	0	0	6	0	2	0	4	2	2	0	2	50.00%	50.00%	50.00%
197		14	0	0	14	9	0	0	5	1	1	0	4	28.57%	80.00%	80.00%
198	-	1746	0	0	1746	93	89	9	1555	358	269	89	1197	76.78%	76.98%	81.65%
199		1125	0	0	1125	84	112	3	926	78	72	6	848	84.46%	91.58%	92.17%
200		21	0	0	21	6	8	0	7	3	2	1	4	33.33%	57.14%	66.67%
201		291	0	0	291	45	13	0	233	22	11	11	211	79.03%	90.56%	95.05%
202		114	0	0	114	14	10	0	90	36	33	3	54	53.47%	60.00%	62.07%
203		2836	0	0	2836	224	186	12	2414	204	137	67	2210	85.96%	91.55%	94.16%
204		358	0	0	358	32	46	5	275	35	25	10	240	80.81%	87.27%	90.57%
205		65	0	0	65	11	14	0	40	9	3	6	31	68.89%	77.50%	91.18%
206		435	0	0	435	6	9	1	419	52	44	8	367	88.01%	87.59%	89.29%
207		339	0	0	339	45	12	0	282	20	16	4	262	81.11%	92.91%	94.24%
208		823	0	0	823	141	107	4	571	78	60	18	493	71.04%	86.34%	89.15%
209		1	0	0	1	0	0	0	1	1	1	0	0	0 00%	0.00%	0.00%
210		233	0	0	233	29	8	3	193	27	19	8	166	77.57%	86.01%	89.73%
211		620	0	0	620	104	14	0	502	23	20	3	479	79.44%	95.42%	95.99%
212		25	0	0	25	4	2	0	19	7	4	3	12	60.00%	63.16%	75.00%
213		42	0	0	42	5	7	0	30	0	0	0	30	85.71%	100.00%	100.00%
214		850	0	0	850	61	69	1	719	53	46	7	666	86.16%	92.63%	93.54%
215		67	0	0	67	17	10	1	39	4	40	0	35	62.50%	92.03 <i>%</i> 89.74%	89.74%
215		232	0	0	232	24	6	1	201	24	20	4	177	80.09%	88.06%	89.85%

Exhibit October PM Data Attachment 2E

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						LI	ESOG									
		Me	chanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
· · · · · · · · · · · · · · · · · · ·						Total		Pending		Total		CLEC	1	Percent		Percent
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manuai Fallout	Auto Clarification	Supps (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	Flow Through
217		40	0	0	40	2	14	1	23	5	1	4	18	85.71%	78.26%	94.74%
218		444	0	0	444	18	34	5	387	76	45	31	311	83.16%	80.36%	87.36%
219		27	0	0	27	2	16	0	9	1	1	0	8	72.73%	88.89%	88.89%
220		3	0	0	3	0	1	0	2	2	0	2	0	0.00%	0.00%	0.00%
221		44	0	0	44	2	2	0	40	9	8	1	31	75.61%	77.50%	79.49%
222		120	0	0	120	11	13	0	96	9	8	1	87	82.08%	90.63%	91.58%
223		878	0	0	878	83	61	1	733	97	75	22	636	80.10%	86.77%	89.45%
224		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
225		488	0	0	488	29	45	1	413	58	40	18	355	83.73%	85.96%	89.87%
226		1354	0	0	1354	112	127	5	1110	63	50	13	1047	86.60%	94.32%	95.44%
227		81	0	0	81	17	10	1	53	18	13	5	35	53.85%	66.04%	72.92%
228		34	0	0	34	3	18	1	12	8	4	4	4	36.36%	33.33%	50.00%
229		1802	0	0	1802	254	42	17	1489	352	318	34	1137	66.53%	76.36%	78.14%
230		42	0	0	42	8	3	0	31	9	8	1	22	57.89%	70.97%	73.33%
231		38	0	0	38	3	4	0	31	4	3	1	27	81.82%	87.10%	90.00%
232		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
233		405	0	0	405	56	22	1	326	19	7	12	307	82.97%	94.17%	97.77%
234		22	0	0	22	2	10	1	9	0	0	0	9	81.82%	100.00%	100.00%
235		55738	0	0	55738	4398	13119	377	37844	10070	7272	2798	27774	70.41%	73.39%	79.25%
236		23	0	0	23	1	8	3	11	1	1	0	10	83.33%	90.91%	90.91%
237		480	0	0	480	27	35	5	413	49	40	9	364	84.45%	88.14%	90.10%
238		12	0	0	12	2	1	0	9	2	2	0	7	63.64%	77.78%	77.78%
239		329	0	0	329	20	24	0	285	29	24	5	256	85.33%	89.82%	91.43%
240		48	0	0	48	2	3	4	39	4	1	3	35	92.11%	89.74%	97.22%
241		110	0	0	110	7	4	1	98	5	3	2	93	90.29%	94.90%	96.88%
242		3	0	0	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%
243		20	0	0	20	4	2	0	14	2	2	0	12	66.67%	85.71%	85.71%
244		37	0	0	37	2	5	0	30	8	6	2	22	73.33%	73.33%	78.57%
245		314	0	0	314	4	9	3	298	23	20	3	275	91.97%	92.28%	93.22%
246		17	0	0	17	3	0	0	14	1	1	0	13	76.47%	92.86%	92.86%
247		677	0	0	677	111	68	5	493	96	57	39	397	70 27%	80.53%	87.44%
248		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
249		23	0	0	23	2	7	0	14	1	1	0	13	81.25%	92.86%	92.86%
250		508	0	0	508	42	9	2	455	27	13	14	428	88.61%	94.07%	97.05%
251		312	0	0	312	37	5	1	269	19	16	3	250	82.51%	92.94%	93.98%
252		561	0	0	561	35	20	5	501	36	28	8	465	88.07%	92.81%	94.32%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING							F	LOWTHROUG	н
						Ľ	ESOG									
		Mechanized interface Used		Manual Rejects		Validated		Errors								
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supp s (Z Status)	LSR's	Total System Fallout	BST Caused Failout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
253		325	0	0	325	49	34	2	240	52	34	18	188	69.37%	78.33%	84.68%
254		161	0	0	161	29	5	0	127	8	7	1	119	76.77%	93.70%	94.44%
255		13	0	0	13	0	12	0	1	1	0	1	0	0.00%	0.00%	0.00%
256		42	0	0	42	2	8	0	32	14	10	4	18	60.00%	56.25%	64.29%
257		26	0	0	26	1	5	0	20	0	0	0	20	95.24%	100.00%	100.00%
258		49	0	0	49	4	2	0	43	7	6	1	36	78.26%	83.72%	85.71%
259		8	0	0	8	0	3	0	5	1	1	0	4	80.00%	80.00%	80.00%
260		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
261		2351	0	0	2351	118	236	15	1982	262	220	42	1720	83.58%	86.78%	88.66%
262		49	0	0	49	4	11	4	30	11	8	3	19	61.29%	63.33%	70.37%
263		366	0	0	366	13	37	0	316	33	21	12	283	89.27%	89.56%	93.09%
264		55	0	0	55	1	10	1	43	9	7	2	34	80.95%	79.07%	82.93%
265		2125	0	0	2125	34	135	3	1953	204	118	86	1749	92.00%	89.55%	93.68%
266		1402	0	0	1402	134	15	2	1251	40	31	9	1211	88.01%	96.80%	97.50%
267		42	0	0	42	2	8	0	32	11	4	7	21	77.78%	65.63%	84.00%
268		479	0	0	479	25	34	1	419	16	11	5	403	91.80%	96.18%	97.34%
269		16	0	0	16	1	4	0	11	2	0	2	9	90.00%	81.82%	100.00%
270		2	0	0	2	0	0	0	2	2	0	2	0	0.00%	0.00%	0.00%
271		222	0	0	222	15	25	2	180	24	20	4	156	81.68%	86.67%	88.64%
272		10	0	0	10	7	1	0	2	0	0	0	2	22.22%	100.00%	100.00%
273		5	0	0	5	4	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
274		5	0	0	5	4	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
LENS Subtotal		211931	0	0	211931	17077	24494	898	169462	21686	16314	5372	147776	81.57%	87.20%	90.06%
EDi Subtotal		0	30851	0	30851	566	6072	5	24208	7146	3685	3461	17062	80 05%	70.48%	82.24%
TAG Subtotal		0	0	24027	24027	842	1730	92	21363	2756	1756	1000	18607	87.75%	87.10%	91.38%
TOTAL INTERFACES		211931	30851	24027	266809	18485	32296	995	215033	31588	21755	9833	183445	82.01%	85.31%	89.40%

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Exhibit October PM Data Attachment 2E

GGREGATE ORDER TYPES							<u>. </u>									
Company Info							OCESSING							F	LOWTHROUG	H
			<u> </u>				ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects	Valid	ated	Errors						
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Failout	BST Caused Fellout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
1		0	51	0	51	5	14	0	32	8	6	2	24	68.57%	75.00%	80.00%
2		0	7	0	7	3	2	0	2	1	0	1	1	25.00%	50.00%	100.00
3		0	13	0	13	3	1	0	9	6	2	4	3	37.50%	33.33%	60.00%
4		0	428	0	428	86	71	9	262	112	72	40	150	48.70%	57.25%	67.57%
5		0	16	0	16	1	1	0	14	10	1	9	4	66.67%	28.57%	80.00%
6		0	24	0	24	1	6	2	15	10	2	8	5	62.50%	33.33%	71.43%
7		0	1	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
8		0	28	0	28	3	14	0	11	10	5	5	1	11.11%	9.09%	16.67%
9		0	54	0	54	9	14	1	30	23	17	6	7	21.21%	23.33%	29.17%
10		0	61	0	61	36	4	0	21	3	1	2	18	32.73%	85.71%	94.74%
11		0	2	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.009
12		0	0	70	70	29	7	0	34	7	5	2	27	44.26%	79.41%	84.38%
13		0	0	35	35	0	1	0	34	14	4	10	20	83.33%	58.82%	83 33%
14		0	0	16	16	0	0	0	16	11	3	8	5	62.50%	31.25%	62.50%
15		0	0	28	28	0	3	0	25	8	5	3	17	77.27%	68.00%	77.27%
16		0	0	23	23	1	4	0	18	8	2	6	10	76.92%	55.56%	83.33%
17		0	0	8	8	0	0	0	8	4	1	3	4	80.00%	50.00%	80.00%
18		0	0	3	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.009
19		0	0	11	11	1	3	0	7	2	1	1	5	71.43%	71.43%	83.33%
20		0	0	12	12	2	4	0	6	4	2	2	2	33.33%	33.33%	50.00%
21		0	0	5	5	2	2	0	1	1	0	1	0	0.00%	0.00%	0.00%
22		0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
23		0	0	15	15	1	4	0	10	1	1	0	9	81.82%	90.00%	90.00%
24		0	0	4	4	0	4	0	0	0	0	0	0	0.00%	0.00%	0.00%
25		0	0	38	38	10	5	0	23	13	11	2	10	32.26%	43.48%	47.62%
26		0	0	6	6	0	3	0	3	0	0	0	3	100.00%	100.00%	100.00
27		0	0	13	13	6	1	0	6	0	0	0	6	50.00%	100.00%	100.009
28		0	0	3	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
29		0	0	37	37	0	5	0	32	27	6	21	5	45.45%	15.63%	45.45%
30		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.009
31		0	0	10	10	1	1	0	8	2	1	1	' 6	75.00%	75.00%	85.71%
32	1	0	0	29	29	0	5	0	24	17	3	14	7	70.00%	29.17%	70.00%
33	1	0	0	249	249	109	12	0	128	46	17	29	82	39 42%	64.06%	82.83%
34		0	0	4	4	0	2	0	2	2	1	1	0	0.00%	0.00%	0.00%
35	+	0	0	5	5	0	3	0	2	0	0	0	2	100.00%	100.00%	100.00%
36		0	0	4	4	0	1	0	3	3	1	2	0	0.00%	0 00%	0 00%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						LI	ESOG									
		Me	chanized	Interface l	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Suppa (Z Status)	LSR's	Total System Fallout	BST Caused Failout	CLEC Caused Fallout	lasued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
37		0	0	55	55	29	5	0	21	3	3	0	18	36.00%	85.71%	85.71%
38		0	0	26	26	0	4	0	22	17	0	17	5	100.00%	22.73%	100.00%
39		0	0	41	41	0	13	0	28	12	8	4	16	66.67%	57.14%	66.67%
40		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
41		0	0	6	6	2	4	0	0	0	0	0	0	0.00%	0.00%	0.00%
42		0	0	9	9	8	0	0	1	0	0	0	1	11.11%	100.00%	100.00%
43		0	0	7	7	1	2	0	4	2	1	11	2	50.00%	50.00%	66.67%
44		0	0	4	4	0	0	0	4	1	0	1	3	100.00%	75.00%	100.00%
45		0	0	4	4	2	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
46		0	0	100	100	63	6	4	27	9	3	6	18	21.43%	66.67%	85.71%
47		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
48		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
49		40	0	0	40	3	6	0	31	14	3	11	17	73.91%	54.84%	85.00%
50		32	0	0	32	1	0	0	31	9	4	5	22	81.48%	70.97%	84.62%
51		17	0	0	17	0	1	0	16	7	3	4	9	75.00%	56.25%	75.00%
52		51	0	0	51	3	2	0	46	10	2	8	36	87.80%	78.26%	94.74%
53		105	0	0	105	43	7	0	55	11	5	6	44	47.83%	80.00%	89.80%
54		4	0	0	4	1	0	0	3	1	1	0	2	50.00%	66.67%	66.67%
55		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
56		30	0	0	30	6	4	0	20	12	10	2	8	33.33%	40.00%	44.44%
57		29	0	0	29	3	0	0	26	6	5	1	20	71.43%	76.92%	80.00%
58		26	0	0	26	2	0	0	24	14	9	5	10	47.62%	41.67%	52 63%
59		49	0	0	49	1	1	3	44	13	9	4	31	75.61%	70.45%	77.50%
60		10	0	0	10	0	2	1	7	1	0	1	6	100.00%	85.71%	100.00%
61		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
62		68	0	0	68	5	3	0	60	31	9	22	29	67.44%	48.33%	76.32%
63		27	0	0	27	5	7	0	15	5	5	0	10	50.00%	66.67%	66.67%
64		149	0	0	149	39	7	2	101	24	14	10	77	59.23%	76.24%	84.62%
65		258	0	0	258	26	21	7	204	63	58	5	141	62.67%	69.12%	70.85%
66		13	0	0	13	2	0	0	11	1	0	1	10	83.33%	90.91%	100 00%
67		3	0	0	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
68		19	0	0	19	10	2	1	6	0	0	0	6	37.50%	100.00%	100 00%
69		19	0	0	19	4	2	0	13	1	1	0	12	70.59%	92.31%	92.31%
70		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
71		47	0	0	47	7	5	1	34	16	8	8	18	54.55%	52.94%	69.23%
72		103	0	0	103	5	19	0	79	15	9	6	64	82.05%	81.01%	87.67%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	н
·····						LI	ESOG									
		Me	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
73		12	0	0	12	0	4	0	8	0	0	0	8	100.00%	100.00%	100.00%
73		24	0	0	24	4	3	0	17	7	2	5	10	62.50%	58.82%	83.33%
75		103	0	0	103	35	7	2	59	13	6	7	46	52.87%	77.97%	88.46%
76		8	0	0	8	0	3	0	5	10	1	0	40	80.00%	80.00%	80.00%
77		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
78		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
79		2	0	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
80		. <u>-</u> 1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
81		252	0	0	252	208	21	1	22	3	2	1	19	8.30%	86.36%	90.48%
82		529	0	0	529	80	58	12	379	153	111	42	226	54.20%	59.63%	67.06%
83		1	0	0	1	0	0	0	1	135	1	- 42	0	0.00%	0.00%	0.00%
83		11	0	0	11	0	1	0	10	9	5	4	1	16.67%	10.00%	16.67%
85		25	0	0	25	14	3	0	8	4	2	2	4	20.00%	50.00%	66.67%
85 86		20	0	0	23	1	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
87		<u> </u>	0	0	1	0	- 0	0	1	1		0	0	0.00%	0.00%	0.00%
88		1	0	0		0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
88		1	0	0		1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
90		4	0	0	4	0	0	0	4	0	0	0	4	100.00%	100.00%	100.00%
90		4 6	0	0	6	2	0	1	3	0	0	0	3	60.00%	100.00%	100.00%
92		25	0	0	25	4	1	0	20	5	1	4	15	75.00%	75.00%	93.75%
93		13	0	0	13	4	1	0	8	0	0	0	8	66.67%	100.00%	100.00%
94		8	0	0	8	1	1	0	6	2	1	1	4	66.67%	66.67%	80.00%
95		2	0	0	2	0	1	0	t	1	0	1	0	0.00%	0.00%	0.00%
96		22	0	0	22	0	17	0	5	0	0	0	5	100.00%	100.00%	100.00%
97		233	0	0	233	64	16	0	153	51	36	15	102	50.50%	66.67%	73.91%
98		102	0	0	102	8	9	4	81	43	24	19	38	54.29%	46.91%	61.29%
99		2	0	0	2	0	9 0	4	2	43	0	0	2	100.00%	40.91%	100.00%
100		3	0	0	3	1	1	0	1		1	0	0	0.00%	0.00%	0.00%
100			0	0	66	16	8	V	41	7	4	3	34	62.96%	82.93%	89.47%
101	<u> </u>		0	0	1	0	0	0	1	0	4 0	0	1	100.00%	82.93% 100.00%	
102			0	0	4	0	0	0	4	3	3	0	1 1		1 1	100.00%
		4	0	0	4	0	_	0			+ +	-		25.00%	25.00%	25.00%
104		2		0	2 25	2	0	0	2 22	0	0	0	2	100.00%	100.00%	100.00%
105		25	0									0	22	91.67%	100.00%	100.00%
106		41	0	0	41	9	2	0	30	2	1	1	28	73.68%	93.33%	96.55%
107		7	0	0	7	0	0	0	7	0	0	0	7	100.00%	100.00%	100.00%
108		34	0	0	34	7	6	0	21	4	3	1	17	62.96%	80.95%	85.00%

AGGREGATE ORDER TYPES														T		
Company info						LSR PR	OCESSING							F	LOWTHROUG	iH
						LI	ESOG									
		Me	chanized	Interface L	Jsed	Manuai	Rejects	Validated			Errors					
						Total		Pending		Total		CLEC		Percent	_	Percent
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Supps (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	Base Celculation	Flow Through
109		9	0	0	9	4	0	0	5	0	0	0	5	55.56%	100.00%	100.00%
110		59	0	0	59	14	3	0	42	6	4	2	36	66.67%	85.71%	90.00%
111		14	Ó	0	14	1	1	0	12	0	0	0	12	92.31%	100.00%	100.00%
112		38	0	0	38	6	1	0	31	1	0	1	30	83.33%	96.77%	100.00%
113		87	0	0	87	40	4	0	43	17	11	6	26	33.77%	60.47%	70.27%
114		15	0	0	15	6	4	0	5	0	0	0	5	45.45%	100.00%	100.00%
115		72	0	0	72	10	8	0	54	14	11	3	40	65.57%	74.07%	78.43%
116		21	0	0	21	4	3	1	13	7	3	4	6	46.15%	46.15%	66.67%
117		17	0	0	17	0	4	0	13	0	0	0	13	100.00%	100.00%	100.00%
118		40	0	0	40	2	3	3	32	14	12	2	18	56.25%	56.25%	60.00%
119		537	0	0	537	364	10	0	163	7	7	0	156	29.60%	95.71%	95.71%
120		89	0	0	89	51	0	0	38	4	2	2	34	39.08%	89.47%	94.44%
121		5	0	0	5	3	0	0	2	0	0	0	2	40.00%	100.00%	100.00%
122		2	0	0	2	0	0	1	1	0	0	0	1	100.00%	100.00%	100.00%
123		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
124		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
125		34	0	0	34	4	1	1	28	14	7	7	14	56.00%	50.00%	66.67%
126		5	0	0	5	2	0	0	3	1	1	0	2	40.00%	66.67%	66.67%
127		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
128		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
129		4	0	0	4	0	3	0	1	0	0	0	1	100.00%	100.00%	100.00%
130		22	0	0	22	11	4	0	7	3	3	0	4	22.22%	57.14%	57.14%
131		3	0	0	3	t	0	0	2	2	0	2	0	0.00%	0.00%	0.00%
132		8	0	0	8	1	0	0	7	0	0	0	7	87.50%	100.00%	100.00%
133		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
134		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
135		85	0	0	85	1	15	0	69	4	4	0	65	92.86%	94.20%	94.20%
136		1	0	0	1	0	0	0	1	0	0	0	t	100.00%	100.00%	100.00%
137		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0 00%
138		50	0	0	50	19	7	1	23	3	0	3	20	51.28%	86.96%	100.00%
139		14	0	0	14	2	2	0	10	6	3	3	4	44.44%	40.00%	57.14%
140		4	0	0	4	0	0	1	3	1	1	0	2	66.67%	66.67%	66.67%
141		1035	0	0	1035	325	92	18	600	161	113	48	439	50.06%	73.17%	79 53%
142		1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100.00%
143		7	0	0	7	0	1	0	6	1	0	1	5	100.00%	83.33%	100.00%
144		4	0	0	4	0	0	0	4	2	1	1	2	66.67%	50.00%	66.67%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		Me	chanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
					Total Mech	Total Manual	Auto	Pending Suppe		Total System	BST Caused	CLEC Caused		Percent Achieved	Base	Percent Flow
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Through
145		26	0	0	26	11	4	0	11	4	3	1	7	33.33%	63.64%	70.00%
146		10	0	0	10	0	0	2	8	6	1	5	2	66.67%	25.00%	66.67%
147		35	0	0	35	3	4	1	27	10	6	4	17	65.38%	62.96%	73.91%
148		6	0	0	6	6	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
149		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
150		17	0	0	17	3	0	0	14	5	2	3	9	64.29%	64.29%	81.82%
151		27	0	0	27	4	1	0	22	6	4	2	16	66.67%	72.73%	80.00%
152		3	0	0	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33.33%
153		3	0	0	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33.33%
154		33	0	0	33	5	5	1	22	7	6	1	15	57.69%	68.18%	71.43%
155		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
156		20	0	0	20	6	7	0	7	0	0	0	7	53.85%	100.00%	100.00%
157		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0 00%	0 00%
158		9	0	0	9	0	9	0	0	0	0	0	0	0.00%	0.00%	0.00%
159		26	0	0	26	0	26	0	0	0	0	0	0	0.00%	0.00%	0.00%
160		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
161		5	0	0	5	1	1	0	3	1	1	0	2	50.00%	66.67%	66.67%
162		3	0	0	3	2	0	0	1	0	0	0	1	33.33%	100.00%	100.00%
163		17	0	0	17	7	0	0	10	2	2	0	8	47.06%	80.00%	80.00%
164		13	0	0	13	2	2	0	9	5	2	3	4	50.00%	44.44%	66.67%
165		23	0	0	23	18	0	1	4	2	1	1	2	9.52%	50.00%	66.67%
166		22	0	0	22	4	2	0	16	2	2	0	14	70.00%	87.50%	87.50%
167		11	0	0	11	3	1	0	7	5	0	5	2	40.00%	28.57%	100.00%
168		46	0	0	46	30	2	0	14	3	3	0	11	25.00%	78.57%	78.57%
169		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
170		17	0	0	17	4	4	0	9	0	0	0	9	69.23%	100.00%	100.00%
171		15	0	0	15	3	2	0	10	2	1	1	8	66.67%	80 00%	88.89%
172		232	0	0	232	130	21	4	77	36	23	13	41	21.13%	53.25%	64.06%
173		30	0	0	30	5	9	1	15	8	2	6	7	50.00%	46.67%	77.78%
174		47	0	0	47	8	10	1	28	8	0	8	20	71.43%	71.43%	100.00%
175		1209	0	0	1209	235	185	11	778	452	338	114	326	36.26%	41.90%	49.10%
176		262	0	0	262	72	21	1	168	62	39	23	106	48.85%	63.10%	73.10%
177	-	34	0	0	34	6	3	0	25	8	4	4	17	62.96%	68.00%	80.95%
178		134	0	0	134	32	10	1	91	27	23	4	64	53.78%	70 33%	73.56%
179	+	74	0	0	74	18	4	0	52	20	12	8	32	51.61%	61.54%	72.73%
180		31	0	0	31	21	1	1	8	1	1	0	7	24.14%	87.50%	87.50%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		Me	chanized	Interface I	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Failout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
181		172	0	0	172	30	26	1	115	55	30	25	60	50.00%	52.17%	66.67%
182		15	0	0	15	2	2	0	11	6	4	2	5	45.45%	45.45%	55.56%
183		2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
184		24	0	0	24	12	4	0	8	2	2	0	6	30.00%	75.00%	75.00%
185		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
186		4	0	0	4	0	0	0	4	1	1	0	3	75.00%	75.00%	75.00%
187		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
188		4	0	0	4	0	2	0	2	1	1	0	1	50.00%	50.00%	50.00%
189		4	0	0	4	2	0	0	2	0	0	0	2	50.00%	100.00%	100.00%
190		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
191		18	0	0	18	8	3	1	6	0	0	0	6	42.86%	100.00%	100.00%
192		12	0	0	12	4	2	0	6	2	2	0	4	40.00%	66.67%	66.67%
193		9	0	0	9	0	0	0	9	4	1	3	5	83.33%	55.56%	83.33%
194		14	0	0	14	0	4	0	10	6	1	5	4	80.00%	40.00%	80.00%
195		44	0	0	44	8	12	0	24	1	1	0	23	71.88%	95.83%	95.83%
196		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
197		85	0	0	85	13	13	0	59	35	26	9	24	38.10%	40.68%	48.00%
198		59	0	0	59	4	2	0	53	16	11	5	37	71.15%	69.81%	77.08%
199		294	0	0	294	46	42	11	195	92	74	18	103	46.19%	52.82%	58.19%
200		4	0	0	4	0	1	1	2	2	2	0	0	0.00%	0.00%	0.00%
201		99	0	0	99	8	13	0	78	34	27	7	44	55.70%	56.41%	61.97%
202		5	0	0	5	2	2	0	1	0	0	0	1	33.33%	100.00%	100.00%
203		22	0	0	22	13	4	0	5	3	11	2	2	12.50%	40.00%	66.67%
204		3	0	0	3	0	0	0	3	1	0	1	2	100.00%	66.67%	100.00%
205		13	0	0	13	2	7	0	4	0	0	0	4	66.67%	100.00%	100.00%
206		3201	0	0	3201	465	551	67	2118	948	707	241	1170	49.96%	55.24%	62.33%
207		7	0	0	7	0	1	2	4	1	0	1	3	100.00%	75.00%	100.00%
208		285	0	0	285	38	17	2	228	97	78	19	131	53.04%	57.46%	62.68%
209		6	0	0	6	1	0	1	4	2	2	0	2	40.00%	50.00%	50.00%
210		4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%
211		4	0	0	4	2	0	0	2	0	0	0	2	50.00%	100.00%	100.00%
212		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
213		4	0	0	4	1	2	0	1	0	0	0	1	50.00%	100.00%	100.00%
214		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0 00%
215		23	0	0	23	2	8	0	13	4	2	2	9	69.23%	69.23%	81.82%
216		256	0	0	256	40	24	6	186	58	50	8	128	58.72%	68.82%	71.91%

Exhibit October PM Data Attachment 2E

AGGREGATE ORDER TYPES							<u> </u>									
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		Me	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	ÊDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Failout	CLEC Caused Fallout	lssued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
217		67	0	0	67	13	4	2	48	21	20	1	27	45.00%	56.25%	57.45%
218		21	0	0	21	2	7	0	12	3	2	1	9	69.23%	75.00%	81.82%
219		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
220		12	0	0	12	1	1	1	9	4	2	2	5	62.50%	55.56%	71.43%
221		41	0	0	41	16	5	2	18	6	4	2	12	37.50%	66.67%	75.00%
222		27	0	0	27	11	0	0	16	4	2	2	12	48.00%	75.00%	85.71%
223		36	0	0	36	10	0	0	26	4	3	1	22	62.86%	84.62%	88.00%
224		4	0	0	4	2	1	0	1	0	0	0	1	33.33%	100.00%	100.00%
225		1	0	0	1	1	0	0	Ó	0	0	0	0	0.00%	0.00%	0.00%
226		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00
227		32	0	0	32	7	1	0	24	6	4	2	18	62.07%	75.00%	81.82%
228		16	0	0	16	2	3	0	11	7	4	3	4	40.00%	36.36%	50.00%
229		316	0	0	316	132	54	1	129	39	24	15	90	36.59%	69.77%	78.95%
230		58	0	0	58	9	8	1	40	19	4	15	21	61.76%	52.50%	84.00%
231		13	0	0	13	2	0	0	11	4	3	1	7	58.33%	63.64%	70.00%
232		3	0	0	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.009
233		19	0	0	19	0	10	0	9	0	0	0	9	100.00%	100.00%	100.00
LENS Subtotal		12797	0	0	12797	3060	1600	186	7951	3017	2126	891	4934	48.75%	62.06%	69.89%
EDI Subtotal		0	685	0	685	147	127	12	399	183	106	77	216	46.06%	54.14%	67.08%
TAG Subtotal		0	0	885	885	267	111	4	503	215	80	135	288	45.35%	57.26%	78.26%
TOTAL INTERFACES		12797	685	885	14367	3474	1838	202	8853	3415	2312	1103	5438	48.45%	61.43%	70.17%

AGGREGATE ORDER TYPES									••••							
Company Info						LSR PR	OCESSING								FLOWT	IROUGH
						LI	ESOG									
		Me	chanized	Interface (Jsed	Manual	Rejects	Valid	ated		Errors					
	-					Total		Pending		Total		CLEC	Î	Percent		Percent
Name	RESH / OCN	LENS	EDI	TAG	Total Mech	Manual Fallout	Auto Clarification	Supps (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Fallout	issued SO's	Achieved Flowthrough	Base Calculation	Flow Through
Name		0	484	0	484	396	53	8	27	17	15	2	10	2.38%	37.04%	40.00%
2		0	18	0	18	4	0	1	13	5	5	0	8	47.06%	61.54%	61.54%
3	<u>-</u>	0	79	0	79	70	5	0	4	3	1	2	1	1.39%	25.00%	50.00%
4		0	8	0	8	5	1	0	2	0	0	0	2	28.57%	100.00%	100.00%
5		0	15	0	15	6	0	2	7	6	6	0	1	7.69%	14.29%	14.29%
6		0	214	0	214	168	14	2	30	13	5	8	17	8.95%	56.67%	77.27%
7		0	104	0	104	78	6	1	19	7	3	4	12	12.90%	63.16%	80.00%
8	<u> </u>	0	580	0	580	244	139	7	190	156	97	59	34	9.07%	17.89%	25.95%
9		0	43	0	43	0	18	1	24	3	2	1	21	91.30%	87.50%	91.30%
10		ů 0	1726	0	1726	237	189	22	1278	506	351	155	772	56.76%	60.41%	68.74%
11		0	2	0	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%
12		0	34	0	34	11	10	0	13	8	5	3	5	23.81%	38.46%	50.00%
13		0	14	0	14	1	2	0	11	5	2	3	6	66.67%	54.55%	75.00%
14		0	37550	0	37550	2347	4495	63	30645	8613	5933	2680	22032	72.68%	71.89%	78.78%
15		0	172	0	172	33	13	2	124	120	80	40	4	3.42%	3.23%	4.76%
16		0	5	0	5	1	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
17		0	609	0	609	155	73	8	373	227	169	58	146	31.06%	39.14%	46.35%
18		0	1	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
19	<u> </u>	0	1004	0	1004	54	142	3	805	351	288	63	454	57.04%	56.40%	61.19%
20		0	483	0	483	171	42	4	266	92	34	58	174	45.91%	65.41%	83.65%
21		0	315	0	315	11	54	4	246	115	86	29	131	57.46%	53.25%	60.37%
22		0	20	0	20	0	18	0	2	0	0	0	2	100.00%	100.00%	100.00%
23		0	49	0	49	5	1	6	37	22	13	9	15	45.45%	40.54%	53.57%
24		0	621	0	621	299	71	20	231	120	76	44	111	22.84%	48.05%	59.36%
25		0	105	0	105	37	12	1	55	31	22	9	24	28.92%	43.64%	52.17%
26		0	1	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
27		0	101	0	101	19	12	0	70	19	6	13	51	67.11%	72.86%	89.47%
28		0	1867	0	1867	119	246	0	1502	914	350	564	588	55.63%	39.15%	62.69%
29		0	15	0	15	2	4	0	9	5	1	4	4	57.14%	44 44%	80.00%
30		0	18	0	18	1	2	0	15	8	6	2	7	50.00%	46.67%	53.85%
31		0	25	0	25	5	2	0	18	13	5	8	' 5	33.33%	27.78%	50.00%
32		0	188	0	188	12	33	0	143	93	49	44	50	45.05%	34.97%	50.51%
33		0	53	0	53	3	8	0	42	22	19	3	20	47.62%	47.62%	51.28%
34		0	16	0	16	0	1	0	15	8	5	3	7	58.33%	46.67%	58.33%
35		0	8	0	8	1	1	0	6	2	1	1	4	66.67%	66.67%	80.00%
36	1	0	0	31	31	7	1	1	22	6	6	0	16	55.17%	72.73%	72.73%

GGREGATE ORDER TYPE	s		1													
Company Info						LSR PR	OCESSING								FLOWTH	IROUGH
						LI	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
37		0	0	647	647	112	91	3	441	161	144	17	280	52.24%	63.49%	66.04%
38		0	0	55	55	14	0	1	40	15	15	0	25	46.30%	62.50%	62.50%
39		0	0	181	181	22	8	1	150	65	50	15	85	54.14%	56.67%	62.96%
40		0	0	436	436	64	29	4	339	108	91	17	231	59.84%	68.14%	71.74%
41		0	0	7	7	0	2	0	5	1	0	1	4	100.00%	80.00%	100.00%
42		0	0	10	10	3	2	0	5	3	3	0	2	25.00%	40.00%	40.00%
43		0	0	17	17	0	13	0	4	0	0	0	4	100.00%	100.00%	100.00%
44		0	0	2326	2326	358	255	27	1686	617	509	108	1069	55.22%	63.40%	67.74%
45		0	0	1199	1199	191	156	6	846	316	263	53	530	53.86%	62.65%	66.83%
46		0	0	2021	2021	396	263	64	1298	477	373	104	821	51.64%	63.25%	68.76%
47		0	0	990	990	177	134	12	667	278	237	41	389	48.44%	58.32%	62.14%
48		0	0	1170	1170	185	142	32	811	328	273	55	483	51.33%	59.56%	63.89%
49		0	0	63	63	8	15	1	39	17	16	1	22	47.83%	56.41%	57.8 9 %
50		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
51		0	0	8	8	2	3	0	3	1	0	1	2	50.00%	66.67%	100.00%
52		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0 00%
53		0	0	1564	1564	493	17	115	939	408	361	47	531	38.34%	56.55%	59.53%
54		0	0	133	133	20	23	0	90	28	9	19	62	68.13%	68.89%	87.32%
55		0	0	44	44	11	6	0	27	5	2	3	22	62.86%	81.48%	91.67%
56		0	0	20	20	3	3	2	12	2	2	0	10	66.67%	83.33%	83.33%
57		0	0	28	28	4	11	0	13	0	0	0	13	76.47%	100.00%	100.00%
58		0	0	31	31	12	4	1	14	4	3	1	10	40.00%	71.43%	76.92%
59		0	0	188	188	109	29	0	50	48	25	23	2	1.47%	4.00%	7.41%
60		0	0	110	110	25	14	2	69	32	19	13	37	45.68%	53.62%	66.07%
61		0	0	75	75	3	17	0	55	18	10	8	37	74.00%	67.27%	78.72%
62		0	0	10	10	1	2	1	6	3	2	1	3	50.00%	50.00%	60.00%
63		0	0	102	102	16	12	2	72	28	19	9	44	55.70%	61.11%	69.84%
64		0	0	238	238	71	37	6	124	54	31	23	70	40.70%	56.45%	69.31%
65		0	0	118	118	46	44	0	28	15	10	5	13	18.84%	46.43%	56.52%
66		0	0	134	134	50	22	2	60	34	24	10	26	26.00%	43.33%	52.00%
67		0	0	186	186	33	51	4	98	48	32	16	50	43.48%	51.02%	60.98%
68		0	0	43	43	27	5	0	11	3	2	1	8	21.62%	72.73%	80.00%
69		0	0	56	56	20	9	2	25	15	6	9	10	27.78%	40.00%	62.50%
70		0	0	15	15	1	0	0	14	0	0	0	14	93.33%	100.00%	100.00%
71		0	0	3	3	2	0	0	1	0	0	0	1	33.33%	100.00%	100.00%
72		0	0	26	26	1	2	4	19	2	1	1	17	89.47%	89.47%	94.44%

GREGATE ORDER TYPES													ļ		ļ	<u> </u>
Company Info						LSR PR	OCESSING								FLOWTH	HROUGH
						LI	ESOG									
		Me	echanized	Interface I	Jsed	Manual	Rejects	Vaild	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percer Flow Throug
73		0	0	480	480	42	32	15	391	90	52	38	301	76.20%	76.98%	85.27
74		0	0	25	25	0	15	0	10	4	0	4	6	100.00%	60.00%	100.00
75		0	0	81	81	0	39	0	42	4	3	1	38	92.68%	90.48%	92.68
76		0	0	46	46	1	1	0	44	1	0	1	43	97.73%	97.73%	100.0
77	1	0	0	139	139	6	5	25	103	99	70	29	4	5.00%	3.88%	5.41
78		0	0	34	34	3	12	1	18	15	1	14	3	42.86%	16.67%	75.00
79		0	0	96	96	0	33	0	63	1	1	0	62	98.41%	98.41%	98.41
80		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.0
81		0	0	8	8	0	3	0	5	0	0	0	5	100.00%	100.00%	100.0
82		0	0	16356	16356	1439	4254	162	10501	3600	2202	1398	6901	65.46%	65.72%	75.81
83		0	0	40	40	3	20	1	16	15	0	15	1	25.00%	6.25%	100.0
84		21	0	0	21	8	1	0	12	3	3	0	9	45.00%	75.00%	75.00
85		701	0	0	701	76	38	4	583	95	72	23	488	76.73%	83.70%	87.14
86		102	0	0	102	3	7	3	89	18	15	3	71	79.78%	79.78%	82.5
87		249	0	0	249	35	19	5	190	44	27	17	146	70.19%	76.84%	84.3
88		776	0	0	776	84	55	13	624	129	94	35	495	73.55%	79.33%	84.04
89		32	0	0	32	9	11	0	12	2	2	0	10	47.62%	83.33%	83.3
90		33	0	0	33	2	1	2	28	10	5	5	18	72.00%	64.29%	78.2
91		169	0	0	169	23	15	0	131	28	25	3	103	68.21%	78.63%	80.4
92		63	0	0	63	7	2	0	54	13	9	4	41	71.93%	75.93%	82.0
93		20	0	0	20	0	10	0	10	8	7	1	2	22.22%	20.00%	22.2
94		160	0	0	160	5	7	3	145	29	6	23	116	91.34%	80.00%	95.0
95		2	0	0	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.0
96		63	0	0	63	2	1	7	53	52	44	8	1	2.13%	1.89%	2.22
97		108	0	0	108	22	17	1	68	51	21	30	17	28.33%	25.00%	44.7
98		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.0
99		3	0	0	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33.3
100	† İ	15	0	0	15	0	10	0	5	1	1	0	4	80.00%	80.00%	80.0
101		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00
102	1	3	0	0	3	0	1	0	2	1	1	0	1	50.00%	50.00%	50.0
103	-	66	0	0	66	4	7	1	54	27	13	14	27	61.36%	50.00%	67.5
104	1	60	0	0	60	8	21	0	31	16	9	7	15	46.88%	48.39%	62.5
105	+	47	0	0	47	0	12	3	32	18	13	5	14	51.85%	43.75%	51.8
106		6	0	0	6	0	3	0	3	0	0	0	3	100.00%	100.00%	100.0
107		25	0	0	25	4	3	1	17	10	3	7	7	50.00%	41.18%	70.0
108		2059	0	0	2059	368	297	17	1377	476	403	73	901	53.89%	65.43%	69.1

AGGREGATE ORDER TYPES																
Company Info			1			LSR PR	OCESSING								FLOWTH	IROUGH
			1			L	ESOG									
		Me	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC		Percent		Percent
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Supps (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Failout	Issued SO's	Achieved Flowthrough	Base Calculation	Flow Through
109		14	0	0	14	4	5	0	5	4	2	2	1	14.29%	20.00%	33.33%
110		78	0	0	78	13	9	2	54	22	15	7	32	53.33%	59.26%	68.09%
111		11	0	0	11	0	2	0	9	1	1	0	8	88.89%	88.89%	88.89%
112		69	0	0	69	8	5	1	55	11	7	4	44	74.58%	80.00%	86.27%
113		7	0	0	7	0	3	1	3	3	3	0	0	0.00%	0.00%	0.00%
114		14	0	0	14	0	0	0	14	0	0	0	14	100.00%	100.00%	100.00%
115		18	0	0	18	0	4	0	14	0	0	0	14	100.00%	100.00%	100.00%
116		13	0	0	13	0	7	0	6	2	2	0	4	66.67%	66.67%	66.67%
117		10	0	0	10	0	3	0	7	1	1	0	6	85.71%	85.71%	85.71%
118		13	0	0	13	0	2	0	11	0	0	0	11	100.00%	100.00%	100.00%
119		6	0	0	6	2	3	0	1	0	0	0	<u>t</u>	33.33%	100.00%	100.00%
120		1232	0	0	1232	457	84	14	677	165	125	40	512	46.80%	75.63%	80.38%
121		20	0	0	20	2	11	2	5	1	1	0	4	57.14%	80.00%	80.00%
122		28	0	0	28	0	2	1	25	22	13	9	3	18.75%	12.00%	18.75%
123		3537	0	0	3537	112	64	6	3355	179	141	38	3176	92.62%	94.66%	95.75%
124		19	0	0	19	1	9	3	6	3	1	2	3	60.00%	50.00%	75.00%
125		135	0	0	135	26	8	3	98	27	19	8	71	61.21%	72.45%	78.89%
126		638	0	0	638	161	64	7	406	138	106	32	268	50.09%	66.01%	71.66%
127		93	0	0	93	35	6	0	52	18	13	5	34	41.46%	65.38%	72.34%
128		3	0	0	3	0	0	1	2	2	1	1	0	0.00%	0.00%	0.00%
129		332	0	0	332	0	17	0	315	25	23	2	290	92.65%	92.06%	92.65%
130		6	0	0	6	0	3	0	3	2	2	0	1	33.33%	33.33%	33.33%
131		171	0	0	171	14	14	1	142	14	11	3	128	83.66%	90.14%	92.09%
132		185	0	0	185	5	18	16	146	118	75	43	28	25.93%	19.18%	27.18%
133		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
134		94	0	0	94	22	15	2	55	11	10	1	44	57.89%	80.00%	81.48%
135		377	0	0	377	8	50	4	315	128	93	35	187	64.93%	59.37%	66.79%
136		3	0	0	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100.00%
137		2	0	0	2	0	0	0	2	2	0	2	0	0.00%	0.00%	0.00%
138		12	0	0	12	0	11	0	1	0	0	0	1	100.00%	100.00%	100.00%
139		184	0	0	184	26	25	2	131	26	20	6	105	69.54%	80.15%	84.00%
140		11	0	0	11	0	5	0	6	0	0	0	6	100.00%	100.00%	100 00%
141		232	0	0	232	40	7	3	182	56	47	9	126	59.15%	69.23%	72.83%
142		7	0	0	7	0	4	0	3	0	0	0	3	100.00%	100.00%	100.00%
143		23	0	0	23	16	1	0	6	2	0	2	4	20.00%	66.67%	100.00%
144	<u> </u>	818	0	0	818	642	42	1	133	18	3	15	115	15.13%	86.47%	97.46%

GGREGATE ORDER TYPE	s			1									1			
Company Info						LSR PR	OCESSING								FLOWTH	IROUGH
	-					L	ESOG									
,		Me	echanized	Interface	Used	Manual	Rejects	Valid	lated	<u> </u>	Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	lasued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
145		992	0	0	992	761	11	1	219	18	5	13	201	20.79%	91.78%	97.57%
146		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
147		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
148		58	0	0	58	13	12	1	32	15	2	13	17	53.13%	53.13%	89.47%
149		19	0	0	19	0	7	0	12	0	0	0	12	100.00%	100.00%	100.00%
150		17	0	0	17	1	0	0	16	1	1	0	15	88.24%	93.75%	93.75%
151		13	0	0	13	0	6	0	7	2	1	1	5	83.33%	71.43%	83.33%
152		25	0	0	25	0	6	0	19	2	1	1	17	94.44%	89.47%	94.44%
153		112	0	0	112	72	21	0	19	0	0	0	19	20.88%	100.00%	100.00%
154		3691	0	0	3691	492	286	38	2875	556	451	105	2319	71.09%	80.66%	83.72%
155		32	0	0	32	18	11	1	2	0	0	0	2	10.00%	100.00%	100.00%
156		21	0	0	21	4	0	0	17	13	5	8	4	30.77%	23.53%	44.44%
157	-	1418	0	0	1418	121	178	12	1107	346	260	86	761	66.64%	68.74%	74.53%
158		1380	0	0	1380	194	162	1	1023	102	70	32	921	77.72%	90.03%	92.94%
159		2417	0	0	2417	213	191	30	1983	340	229	111	1643	78.80%	82.85%	87.77%
160		31	0	0	31	3	8	2	18	8	0	8	10	76.92%	55.56%	100.00%
161		49	0	0	49	0	25	0	24	2	2	0	22	91.67%	91.67%	91.67%
162		100	0	0	100	8	15	0	77	18	13	5	59	73.75%	76.62%	81. 9 4%
163		59	0	0	59	31	8	0	20	2	2	0	18	35.29%	90.00%	90.00%
164		51	0	0	51	13	1	0	37	20	20	0	17	34.00%	45.95%	45.95%
165		2	0	0	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
166		130	0	0	130	19	23	1	87	33	16	17	54	60.67%	62.07%	77.14%
167		8	0	0	8	0	0	0	8	0	0	0	8	100.00%	100.00%	100.00%
168		5	0	0	5	0	0	0	5	4	2	2	1	33.33%	20.00%	33.33%
169		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
170		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
171		703	0	0	703	42	37	9	615	380	326	54	235	38.97%	38.21%	41.89%
172		23	0	0	23	0	0	6	17	8	1	7	9	90.00%	52.94%	90.00%
173		36	0	0	36	3	14	0	19	2	1	1	17	80.95%	89.47%	94.44%
174		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
175		128	0	0	128	14	15	1	98	34	21	13	64	64.65%	65.31%	75.29%
176		114	0	0	114	40	11	4	59	29	22	7	30	32.61%	50.85%	57.69%
177		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
178		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100 00%	100.00%
179		2031	0	0	2031	178	156	30	1667	557	394	163	1110	65.99%	66.59%	73.80%
180		3462	0	0	3462	353	430	28	2651	810	581	229	1841	66.34%	69.45%	76.01%

GGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWT	HROUGH
						L	ESOG		_							
		Me	echanized	Interface I	Jsed	Manuai	Rejects	Valid	ated		Errors		l i			
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
181		78	0	0	78	0	16	0	62	9	6	3	53	89.83%	85.48%	89.83%
182		52	0	0	52	7	29	1	15	7	2	5	8	47.06%	53.33%	80.00%
183		4	0	0	4	0	4	0	0	0	0	0	0	0.00%	0.00%	0.00%
184		7	0	0	7	0	4	0	3	0	0	0	3	100.00%	100.00%	100.00%
185		1708	0	0	1708	336	108	10	1254	388	301	87	866	57.62%	69.06%	74.21%
186		25	0	0	25	0	6	0	19	0	0	0	19	100.00%	100.00%	100.00%
187		4	0	0	4	0	1	1	2	1	0	1	1	100.00%	50.00%	100.00%
188		1478	0	0	1478	199	134	13	1132	368	292	76	764	60.88%	67.49%	72.35%
189		112	0	0	112	11	18	1	82	32	23	9	50	59.52%	60.98%	68.49%
190		11	0	0	11	0	1	0	10	0	0	0	10	100.00%	100.00%	100.00%
191		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
192		112	0	0	112	17	9	1	85	28	18	10	57	61.96%	67.06%	76.00%
193		2	0	0	2	0	0	1	1	0	0	0	1	100.00%	100.00%	100.00%
194		5	0	0	5	0	1	0	4	3	1	2	1	50.00%	25.00%	50.00%
195		389	0	0	389	24	24	48	293	213	173	40	80	28.88%	27.30%	31.62%
196		15	0	0	15	0	2	0	13	2	0	2	11	100.00%	84.62%	100.00%
197		14	0	0	14	1	5	1	7	4	2	2	3	50.00%	42.86%	60.00%
198		65	0	0	65	20	5	2	38	11	6	5	27	50.94%	71.05%	81.82%
199		6	0	0	6	0	2	0	4	3	3	0	1	25.00%	25.00%	25.00%
200		32	0	0	32	11	2	0	19	18	2	16	1	7.14%	5.26%	33.33%
201		95	0	0	95	16	61	0	18	11	5	6	7	25.00%	38.89%	58.33%
202		19	0	0	19	3	6	1	9	5	4	1	4	36.36%	44.44%	50.00%
203		424	0	0	424	29	47	6	342	114	98	16	228	64.23%	66.67%	69.94%
204		38	0	0	38	1	7	1	29	6	4	2	23	82.14%	79.31%	85.19%
205		49	0	0	49	2	6	0	41	13	12	1	28	66.67%	68.29%	70.00%
206		4	0	0	4	1	1	1	1	0	0	0	1	50.00%	100.00%	100.00%
207		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
208		425	0	0	425	75	45	4	301	113	83	30	188	54.34%	62.46%	69.37%
209		3	0	0	3	0	0	0	3	3	3	0	0	0.00%	0.00%	0.00%
210		1766	0	0	1766	237	144	13	1372	184	148	36	1188	75 52%	86.59%	88.92%
211		24	0	0	24	10	7	0	7	2	1	1	5	31.25%	71.43%	83.33%
212	1	14	0	0	14	5	1	0	8	1	1	0	7	53.85%	87.50%	87.50%
213	1	1216	0	0	1216	117	133	9	957	183	124	59	774	76.26%	80.88%	86.19%
214	1	7	0	0	7	0	6	0	1	0	0	0	1	100.00%	100.00%	100.00%
215	1	153	0	0	153	0	36	0	117	2	2	0	115	98.29%	98.29%	98.29%
216		3	0	0	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100.00%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWTH	ROUGH
						L	ESOG									
		Me	chanized	Interface L	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Totel System Fallout	BST Caused Fallout	CLEC Caused Fellout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
217		743	0	0	743	82	128	6	527	175	143	32	352	61.01%	66.79%	71.11%
218		61	0	0	61	11	11	1	38	14	8	6	24	55.81%	63.16%	75.00%
219		1479	0	0	1479	291	132	30	1026	366	217	149	660	56.51%	64.33%	75.26%
220		15	0	0	15	0	2	0	13	1	1	0	12	92.31%	92.31%	92.31%
221		7	0	0	7	0	0	0	7	4	4	0	3	42.86%	42.86%	42.86%
222		53	0	0	53	8	9	0	36	13	7	6	23	60.53%	63.89%	76.67%
223		42	0	0	42	7	7	1	27	6	3	3	21	67.74%	77.78%	87.50%
224		8	0	0	8	2	2	0	4	2	0	2	2	50.00%	50.00%	100.00%
225		21	0	0	21	7	3	0	11	4	2	2	7	43.75%	63.64%	77.78%
LENS Subtotal		41130	0	0	41130	6379	3852	447	30452	7649	5638	2011	22803	65.49%	74.88%	80.18%
EDI Subtotal		0	46547	0	46547	4495	5667	155	36230	11510	7635	3875	24720	67.08%	68.23%	76.40%
TAG Subtotal		0	0	29593	29593	3981	5837	497	19278	6969	4867	2102	12309	58.18%	63.85%	71.66%
TOTAL INTERFACES		41130	46547	29593	117270	14855	15356	1099	85960	26128	18140	7988	59832	64.46%	69.60%	76.74%

Company Info			
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			FATAL
Name		RESH / OCN	
Nume	1	7602	
			1
	2	1363	
·····	3	947	
	4	2720	2
	5	3094	
	6	3290	
	7	4361	3
· · · · · · · · · · · · · · · · · · ·	8	7039	1
······································	9	2929	
1	0	7131	
1	1	7566	
1	2	8318	2
1	3	8400	
1	4	8478	
1	5	8617	
1	6	8383	
1	7	7774	1
1	8	8839	
	9	2909	
	0	3622	
2	-	8378	4
	2	8414	
	23	5467	
	4	2147	
	4 5		
	-	7970	
	6	8410	
2	-	3188	
······	8	8681	
2	9	7007	
3	0	8776	

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Composed Info			
Company Info			
Name		RESH / OCN	FATAL REJECTS
	31	3730	5
<u> </u>	32	3391	
	33	5358	2
	34	. 4927	1
	35	4698	
· · · · · · · · · · · · · · · · · · ·	36	7710	
	37	3794	
	38	7421	
*#**	39	8300	3
	40	7883	
	41	7818	
	42	1739	5
	43	1897	2
	44	1986	4
	45	2435	2
	46	2441	2
	47	5212	1
	48	8494	38
	49	8657	
	50	7795	18
	51	7796	3
	52	8397	
	53	5984	1
	54	8422	4
	55	6042	1
	56	2896	1
	57	5510	2
	58	7668	4
	59	2661	1
	60	4995	4

Company Info			
		-	FATAL
Name		RESH / OCN	
	61	7446	11
·	62	7440	20
	63	2466	20
			· · · · · · · · · · · · · · · · · · ·
	64	5514	
	65	4740	
	66	4771	
	67	7987	
	68	3181	
· · · · · · · · · · · · · · · · · · ·	69	3712	
	70	7208	
	71	7001	
	72	7002	
	73	1224	
	74	2744	2
	75	7727	36
	76	7029	
	77	7871	5
	78	8547	
	79	4728	4
	80	7667	2
	81	4703	1
	82	8324	
······································	83	8339	5
	84	7260	
	85	7280	2
	86	7490	
	87	7635	
	88	7033	······································
	89		
	69	3047	

Company Info			
Name		RESH / OCN	FATAL REJECTS
	91	8524	
	92	4645	
	93	8479	
	94	4147	
	95	4197	
	96	4198	13
	97	4217	
	98	4101	
	99	1779	
	100	6133	
	101	4641	1
	102	4254	
	103	6131	
	104	5829	
	105	4083	
	106	7904	1
	107	6983	3
	108	4085	7
	109	3608	
	110	8312	
	111	7058	
	112	4888	
	113	373	1
	114	5711	
	115	4110	5
	116	7731	
	117	8337	
	118	4086	
	119	4033	
······································	120		+ · · · · · · · ·

AGGREGATE OF			
company into			
			FATAL
Name		RESH / OCN	
	121		
	121	3496 3591	3
			3
	123	7082	
	124	2988	
	125	7877	
	126	7093	1
	127	8368	6
	128	6043	5
	129	3894	
	130	3291	
	131	7574	
	132	7149	8
	133	7318	
	134	4508	1
	135	7583	1
	136	4389	
	137	4542	
	138	7451	3
	139	7581	7
	140	7582	2
	141	7585	
	142	7637	2
	142	8335	1
	143	2757	'
	144	4664	
	146	6093	30
	147	7560	
	148	8768	
	149	743	1
	150	982	4

AGGREGATE OR	DER ITFES		
Company Info			
Name		RESH / OCN	FATAL REJECTS
	151	3890	
	152	6143	
	153	1940	
	154	3137	
	155	5348	
	156	6104	
	157	7875	
	158	5424	
	159	5425	
	160	7229	360
	161	7170	3
	1 0/1	7562	152
	163	2464	1
	164	8846	
	165	1417	5
	166	5841	1
	167	9011	
	168	610	1
	169	1371	
	170	7050	7
	171	8798	
	172	2579	3
	173	2580	4
	174	4175	
	175	7648	2
	176	8772	2
	177	8773	11
	178	8659	
	179	8660	10
	180	4251	

AGGREGATE ORDE Company Info			
			·
			FATAL
Name		RESH / OCN	REJECTS
	181	7519	8
	182	7771	42
	183	8706	
<u> </u>	184	2626	
	185	7892	20
	186	2708	
	187	2754	
	188	2728	34
	189	8601	
· · · · · · · · · · · · · · · · ·	190	7676	
	191	8750	6
	192	2631	
	193	2617	· · · · · · · · · · · · · · · · · · ·
	194	7938	
	195	8298	
	196	1749	
	197	3109	
	198	2664	1(
	199	2665	2
	200	1392	:
	201	4726	
	202	3469	
1 70-	203	7514	1:
	204	4764	
	205	6233	24
	206	3221	
	207	8962	8
	208	7391	
	209	4398	
	210	7011	

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REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (FATAL REJECTS) REPORT PERIOD: 10/01/2001 - 10/31/2001

Exhibit October PM Data Attachment 2E

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Company Info			
Name		RESH / OCN	FATAL REJECTS
	211	7012	213
	212	5053	
	213	5299	
	214	7518	
	215	1201	
	216	7006	2
	217	7845	54
	218	7884	4
	219	6581	
	220	7125	9
	221	8392	1
	222	5278	
	223	2443	1
	224	8272	1:
	225	3692	
	226	8417	
	227	4225	:
	228	4856	:
	229	4892	16:
	230	7452	2
	231	7086	
	232	2505	64
	233	8672	1.
	234	8631	
	235	4712	
	236	4985	23
	237	6110	
	238	6160	:
	239	2335	49
	240	5677	1

Exhibit October PM Data Attachment 2E

AGGREGATE OR	DER TYPES		
Company Info			
Name		RESH / OCN	FATAL REJECTS
	241	7848	
	242	6100	11
	243	7344	11
	244	8758	2
	245	7065	
	246	8994	
	247	123	
	248	155	85
	249	3327	
	250	3840	
	251	3995	
	252	5468	
	253	5558	1!
Total			1388

GGREGATE	ORDER TY	PES								
RROR DETA	VLS (Auto C	larifications	(A) & Errors (E))	CAUSATION					
			1		[CLEC Cause	đ	BST Caused		
Error Type (by error code)	Count	*	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
1000	20,381	14.86%	14.86%	IF CHGING CLASS OF SERVICE ALL PERTINENT USOCS MUST BE POPULATED IN AND OUT	19,774	97.02%	14.86%	607	2.98%	1.39%
7020	1,035	0.75%	15.61%	NUM= TELNO= TN NOT FOUND IN CRIS	1,033	99.81%	0.75%	2	0.19%	0.00%
7040	1	0.00%	15.62%	LOGON ABORTED/FAILED	0	0.00%	0.00%	1	100.00%	0.00%
7055	2,183	1.59%	17.21%	NUM= TELNO= ACCOUNT IS FINAL	2,182	99.95%	1.59%	1	0.05%	0.00%
7095	7	0.01%	17.21%	INCORRECT RATE ZONE DATA RECEIVED FROM RSAG	2	28.57%	0.01%	5	71.43%	0.01%
7110	5,193	3.79%	21.00%	COFFINOT AVAILABLE	1,699	32.72%	3.79%	3,494	67.28%	8.02%
7115	9	0.01%	21.01%	DSAP TELEPHONE NUMBER NOT ACTIVE/FOUND IN SITE	7	77.78%	0.01%	2	22.22%	0.00%
7150	7	0.01%	21.01%	UNE - ERROR GENERATING ECCKT	7	100.00%	0.01%	0	0.00%	0.00%
7225	1	0 00%	21.01%	USOC= IS MISSING	1	100.00%	0.00%	0	0.00%	0.00%
7235	487	0.36%	21.37%	10 DIGIT TN REQUIRED WITH USOC/FID=ZCRN	330	67.76%	0.36%	157	32.24%	0.36%
7245	663	0.48%	21.85%	NUM= ZCRT FID, DATA, OR DELIMITER IS MISSING	464	69.98%	0.48%	199	30.02%	0.46%
7250	473	0.34%	22.19%	LSR HOUSENUMBER INCORRECT	473	100.00%	0.34%	0	0.00%	0.00%
7267	31	0.02%	22.22%	UNE - LOCBAN MISSING FOR LINP ORDER	31	100.00%	0.02%	0	0.00%	0.00%
7295	8	0.01%	22.22%	LINE CLASS OF SERVICE MISSING, NUM AND TN REQUIRED	5	62.50%	0.01%	3	37.50%	0.01%
7300	6	0.00%	22.23%	UNE - CANNOT GENERATE CLASS OF SERVICE USOC	5	83.33%	0.00%	1	16 67%	0.00%
7315	355	0 26%	22 49%	CANNOT GENERATE BILLING NAME AND ADDRESS FIDS	321	90 42%	0.26%	34	9.58%	0.08%
7375	36	0.03%	22.51%	UNE - BOCABS SCREEN ERROR BOE001 ACCOUNT NUMBER NOT FOUND	35	97.22%	0.03%	1	2.78%	0.00%
7380	131	0.10%	22.61%	UNE - ACTL INVALID	131	100.00%	0.10%	0	0.00%	0.00%
7400	8,085	5 89%	28,50%	CLEC DOES NOT OWN THIS ACCOUNT.	8,083	99.98%	5.89%	2	0.02%	0.00%
7445	44	0 03%	28.53%	UNE - CALL FORWARD TN REQUIRED	44	100.00%	0.03%	0	0.00%	0.00%
7465	1,229	0 90%	29.43%	CANNOT CANCEL ORDER	741	60.29%	0.90%	488	39.71%	1.12%
7495	37	0 03%	29.46%	UNE - DIR LOCATOR PROBLEM	4	10.81%	0.03%	33	89.19%	0.08%
7555	233	0 17%	29.63%	FID MISSING IN FEATURE DETAIL	198	84.98%	0.17%	35	15.02%	0.08%
7570	5	0 00%	29.63%	SEQ1X NOT ALLOWED WITH ZNB	4	80.00%	0.00%	1	20.00%	0.00%
7630	341	0.25%	29 88%	MEMORY CALL SERVICE NOT AVAILABLE IN SWITCH	128	37.54%	0 25%	213	62.46%	0.49%
7640	1	0.00%	29.88%	DUPLICATE CUSTOMERS EXCEED NINE ON CSR	0	0.00%	0.00%	1	100.00%	0.00%
7645	3,232	2 36%	32.24%	MATCH IN CSR SA AND LSR HOUSENUM NOT FOUND	2,000	61.88%	2.36%	1,232	38.12%	2.83%
7660	6	0.00%	32 24%	USOC FUJIX NOT FOR RESALE	6	100.00%	0.00%	0	0.00%	0.00%
7690	45	0.03%	32 27%	UNE - ACTL AND ENDUSER LSO MUST BE THE SAME FOR LOOP/LINP SERVICE	45	100.00%	0.03%	0	0.00%	0.00%
7710	385	0.28%	32.55%	CANNOT CANCEL OR CHANGE DUE DATE ON NON-EXISTENT ORDER	229	59.48%	0.28%	156	40 52%	0.36%
7715	21	0.02%	32.57%	SOCS TIMEOUT/NOT AVAILABLE	16	76.19%	0.02%	5	23.81%	0.01%
7718	2,146	1 56%	34.13%	UNABLE TO RETRIEVE PSO TO PROCESS SUP	861	40.12%	1.56%	1.285	59.88%	2.95%
7725	60	0.04%	34.13%	WAITING PERIOD EQUALS 5 MINUTES	26	43.33%	0.04%	34	56.67%	0.08%
7735	62	0.04%	34.22%	INVALID/MISSING LISTING NAME OR TYPE	62	100.00%	0.04 %	0	0.00%	0.00%
7740	10	0.01%	34.22%	LOCAL CALLING PLUS INDICATOR NOT FOUND	7	70.00%	0.01%	3	30.00%	0.00%
7755	15	0.01%	34.23%	UNE - NPANXX NOT FOUND IN CLLI TABLE	12	80.00%	0.01%	3	20.00%	0.01%

AGGREGATE	ORDER TY	PES								
			(A) & Errors (E		CAUSATION			Í		
						CLEC Cause	4	BST Caused		
Error Type (by error		*	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
7805	136	0.10%	34.34%	SITE COULD NOT BE DETERMINED	67	49.26%	0.10%	69	50.74%	0.16%
7815	64	0.05%	34.39%	FID=RCU INVALID OR MISSING DATA	56	87.50%	0.05%	8	12.50%	0.02%
7860	260	0.19%	34.58%	RSAG - NO EXACT MATCH ON STREET NAME	260	100.00%	0.19%	0	0.00%	0.00%
7890	20	0 01%	34.59%	RSAG - NO EXACT MATCH ON SUPPLEMENTAL ADDRESS	20	100.00%	0.01%	0	0.00%	0.00%
7900	17	0.01%	34.60%	RSAG - NO MATCH ON STREET NAME	17	100.00%	0.01%	0	0.00%	0.00%
7905	3,985	2.91%	37.51%	RSAG - INCORRECT COMMUNITY, INCORRECT ZIP CODE OR INVALID ADDRESS FORMAT	3,980	99.87%	2.91%	5	0.13%	0.01%
7910	1,700	1.24%	38.75%	RSAG - NO MATCH ON EXACT STREET NAME	1.561	91.82%	1.24%	139	8.18%	0.32%
7930	1	0.00%	38.75%	RSAG-STREET FOUND IN DIFFERENT COMMUNITY AND/OR ZIP	1	100.00%	0.00%	0	0.00%	0.00%
7935	22	0.02%	38.77%	RSAG-SIMILAR STREET FOUND IN DIFFERENT COMMUNITY AND/OR ZIP	22	100.00%	0.02%	0	0.00%	0.00%
7945	132	0.10%	38.86%	RSAG SYSTEM ERROR	71	53.79%	0.10%		46.21%	0.14%
8150	59	0.04%	38.91%	ORDER HAS BEEN REQUEUED FOR THE MAXIMUM NUMBER OF OCCURRENCES	22	37.29%	0.04%	37	62.71%	0.08%
8167	64	0.05%	38.95%	INVALID USOC CHARACTER. FORMAT SAE 013 11 CREXI	64	100.00%	0.05%		0.00%	0.00%
8170	376	0.27%	39.23%	USOC MAY ONLY APPEAR ONCE. FORMAT SAE 110 11 CREX1 /TN	375	99.73%	0.27%	1	0.27%	0.00%
8173	57	0.04%	39.27%	INVALID CLASS OF SERVICE. FORMAT IDNT 131 UEPRL=	57	100.00%	0.04%	0	0.00%	0.00%
8175	1	0 00%	39.27%	USOC NOT AVAILABLE IN SWITCH. FORMAT SAE 180N 11 ESXDC	1	100.00%	0.00%	0	0.00%	0.00%
8180	202	0.15%	39 42%	LNUM=00001 TC TO PRIMARY NUMBER MUST BE DIFFERENT FROM NUMBER BEING REFERE	200	99.01%	0.15%	2	0.99%	0.00%
8183	19	0 01%	39.43%	AREA CALLING PLAN USOC MISMATCH. FORMAT 320 LINE UPP :0000000 / LINE ASSIGN :0000	19	100.00%	0.01%	0	0.00%	0.00%
8185	36	0.03%	39.46%	ESC/ESCWT NOT VALID COMBINATION. FORMAT SAE 424 11 ESCWT	36	100.00%	0.03%	0	0.00%	0.00%
8187	2,289	1 67%	41.12%	USOC MAY NOT APPEAR ON REQUEST. FORMAT SAE 431 T1 EMP1S /TN	2,288	99.96%	1.67%	1	0.04%	0.00%
8189	714	0.52%	41.65%	USOC IS NOT VALID ON BST FILE. FORMAT SAE 433 11 CREX6	710	99.44%	0.52%	4	0.56%	0.01%
8190	1,712	1.25%	42.89%	INVALID USOC FOR BASIC CLASS OF SERVICE. FORMAT SAE 434 11 S98CP /TN	1,617	94.45%	1.25%	95	5.55%	0.22%
8193	1	0.00%	42.89%	USOC NOT VALID WITH CALLER ID. FORMAT SAE 473 11 NXMCR /TN	1	100.00%	0.00%	0	0.00%	0.00%
8195	546	0.40%	43.29%	CALL FORWARDING USOC MUST NOT APPEAR. FORMAT SAE 540 11 GCJ /TN	546	100.00%	0.40%	0	0.00%	0.00%
8197	851	0.62%	43.91%	CALL FORWARDING USOC MUST APPEAR. FORMAT SAE 541	851	100.00%	0.62%	0	0.00%	0.00%
8199	88	0 06%	43.98%	GCJRC/GCJ COMBINATION INVALID. FORMAT SAE 560 11 GCJRC /TN	87	98.86%	0.06%	1	1.14%	0.00%
8204	143	0.10%	44.08%	BCR/NSS/NXB INVALID USOC COMBINATION. FORMAT SAE 575 R1 NSS /TN	143	100.00%	0.10%	0	9.00%	0.00%
8207	70	0.05%	44.13%	BRD/NSQ/NX9 INVALID USOC COMBINATION. FORMAT SAE 576 11 NX9 /TN	70	100.00%	0.05%	0	0.00%	0.00%
8209	898	0.65%	44.79%	USOC COMBINATION IS INVALID. FORMAT SAE 587 11 ESXDC /TN	898	100.00%	0.65%	0	0.00%	0.00%
8240	198	0.14%	44.93%	INVALID LINE CLASS OF SVC FOR REQUESTED SERVICE	198	100.00%	0.14%	0	0.00%	0.00%
8250	72	0.05%	44.98%	USOC= NOT APPLICABLE TO PORT LOOP SERVICE	71	98.61%	0.05%	1	1.39%	0 00%
8415	26	0.02%	45 00%	LSF LP ALREADY EXISTS ON ACCOUNT	26	100.00%	0.02%	0	0.00%	0.00%
8430	2	0.00%	45.00%	LSF DOES NOT EXIST ON ACCOUNT	2	100.00%	0.00%	0	0.00%	0.00%
8700	6	0.00%	45.01%	RSAG-INVALID SEARCH AREA	3	50 00%	0 00%	3	50.00%	0.01%
8820	10.890	7.94%	52.95%	SOCS ERROR: LUD BILL 004 ACT CODE NOT FOR THIS ORD TYPE	3.308	30 38%	7.94%	7,582	69.62%	17.40%
8825	22,407	16.34%	69.29%	ORDER ERR:	5,063	22 60%	16.34%	17,344	77.40%	39.80%

Exhibit October PM Data Attachment 2E

AGGREGATE										
ERROR DETA	AILS (Auto C	arifications (A) & Errors (E		CAUSATION					
						CLEC Caused	1	BST Caused		
Error Type (by error code)	Count	%	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BS ⁻ Caused
8830	748	0.55%	69.83%	CLEC ALREADY OWNS THIS ACCOUNT	748	100.00%	0.55%	0	0.00%	0.00%
8850	97	0.07%	69.90%	CFA NOT FOUND, PLEASE VERIFY CFA	96	98.97%	0.07%	1	1.03%	0.00%
8875	1	0.00%	69.90%	ERROR GENERATING BTN	1	100.00%	0.00%	0	0.00%	0.00%
8925	4	0.00%	69.91%	CFN HAS INVALID FORMAT ON COFFI SCREEN	4	100.00%	0.00%	0	0.00%	0.00%
8940	1,615	1.18%	71.08%	CALL FORWARDING NUMBER MISSING OR INVALID	1,614	99.94%	1.18%	1	0.06%	0.00%
8945	29	0.02%	71.10%	LINECLSSVC AND TOS DO NOT MATCH	29	100.00%	0.02%	0	0.00%	0.00%
8970	1.251	0.91%	72.02%	FID RCU WITH TWC FOUND ON SAME LINE AS 3-WAY CALLING USOC	1,250	99.92%	0.91%	1	0.08%	0.00%
9000	25	0.02%	72.03%	LSO/LOCBAN (NPANXX) MISSING OR INVALID	25	100.00%	0.02%	0	0.00%	0.00%
9015	1 .	0.00%	72.04%	SUP FAILED TO UPDATE DUE DATE	1	100.00%	0.00%	0	0.00%	0.00%
9110	2	0.00%	72.04%	TELNO= PIC REQUIRED PER UNIQUE TELEPHONE NUMBER ON A, V, P9 LINE ACTIVITY TYP	2	100.00%	0.00%	0	0.00%	0.00%
9115	2	0.00%	72.04%	TELNO= LPIC REQUIRED PER UNIQUE TELNO ON A, V, P9 LINE ACTIVITY TYPES	2	100.00%	0.00%	0	0.00%	0.00%
9145	2	0.00%	72.04%	ACCOUNT IS DENIED	2	100.00%	0.00%	0	0.00%	0.00%
9155	31	0.02%	72.06%	UNE - PORTED OUT NUMBER	31	100.00%	0.02%	0	0.00%	0.00%
9160	13	0.01%	72.07%	LOCBAN INVALID FOR PORTED NUMBER ACTIVITY	13	100.00%	0.01%	0	0.00%	0.00%
9245	353	0.26%	72.33%	CORRECT ECCKT IS REQUIRED FOR LNA , LNUM	353	100.00%	0.26%	0	0.00%	0.00%
9428	1	0.00%	72.33%	DLNUM=0001 LTN= INVALID NICK DATA	1	100.00%	0.00%	0	0.00%	0.00%
9432	2	0.00%	72.33%	DLNUM=0002 LTN= LTXTY OF CR REQUIRES SEE AS FIRST WORD IN LTEXT	2	100.00%	0.00%	0	0.00%	0.00%
9432 9438	35	0.03%	72.36%	DLNUM=0001 LTN= ACCOUNT ACTIVITY OF N CAN ONLY HAVE AN LACT OF N	- 33	94.29%	0.03%	2	5.71%	0.00%
		0.10%	72.45%	LTN= DISPOSITION OF LISTINGS ON MIGRATED LINES REQUIRED	131	100.00%	0.10%	0	0.00%	0.00%
9439	131		72.45%	DLNUM=0004 LTN=5047388816 ALI VALUE INVALID	2	100.00%	0.00%	0	0.00%	0.00%
9441	2	0.00%	72.97%	DLNUM=0004 LTN=SU7300010 ALL VALUE INVALID	695	99.14%	0.51%	6	0.86%	0.01%
9442	701	0.51%		UNABLE TO DETERMINE BLOCK CHOICE	43	100.00%	0.03%	0	0.00%	0.00%
9466	43	0.03%	73.00%		18	81.82%	0.02%	4	18.18%	0.01%
9471	22	0.02%	73.01%	TOTAL QUANTITY OF VCA AND SCO SHOULD EQUAL IWJQ ACT= ALLOWED ONLY ON SAME LOCNUM SERVICE ADDRESS	329	99.70%	0.02%	1	0.30%	0.00%
9475	330	0.24%	73.25%	IS NOT FOUND ON CSR TO DISCONNECT	49	100.00%	0.24%	0	0.00%	0.00%
9476	49	0.04%	73.29%		150	99.34%	0.04%	1	0.66%	0.00%
9477	151	0.11%	73.40%	LSR LNUM=00002 INVALID LNA, NO RECORDED CHANGE FOR TELEPHONE NUMBER	107	99.34% 99.07%	0.08%	1	0.93%	0.00%
9479	108	0.08%	73.48%	LNUM=00001 FEATURE DOES NOT EXIST ON ACCOUNT TO MODIFY	2,697	99.07% 99.26%	1.98%	20	0.93%	0.05%
9481	2,717	1.98%	75.46%	LNUM=00001 FEATURE DOES NOT EXIST ON ACCOUNT TO DISCONNECT				20	0.74%	0.00%
9484	24	0.02%	75.48%	TNS= FOR LNUM=00001 ALREADY EXIST ON ATN=	24	100.00%	0.02%	0		0.00%
9487	3	0.00%	75.48%	INVALID ACT TYPE FOR FULL MIGRATION	3	100.00%	0.00%		0.00%	0.00%
9488	551	0.40%	75.88%	DISPOSITION OF ALL LINES REQUIRED ON ACT V	551	100.00%	0.40%	0	0.00%	
9495	67	0.05%	75.93%	EATN= MUST EXIST FOR ACT P AND Q	67	100.00%	0.05%	0	0.00%	0.00%
9496	2,464	1.80%	77.73%	TNS= ON LNUM=00004 NOT FOUND ON EATN= FOR ACT=	2,460	99.84%	1.80%	4	0.16%	0.01%
9497	1	0.00%	77.73%	LEATN= ON LNUM=00001 AND EATN= ARE NOT COMPATIBLE	1	100.00%	0.00%	0	0.00%	0.00%
9498	42	0.03%	77.76%	EAN= ON LNUM= AND LEAN= ARE POPULATED	42	100.00%	0.03%	0	0.00%	0.00%

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AGGREGAT	E ORDER TY	PES						Ī		
			(A) & Errors (E		CAUSATION					
	T T		<u> </u>			CLEC Cause	d	BST Caused		
	Count	%	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
	2	0.00%	77.76%	FA OF D AND C ARE DISALLOWED WHEN TNS IS NOT POPULATED FOR A LEATN	2	100.00%	0.00%	0	0.00%	0.00%
9515	1.959	1.43%	79.19%	WKG SVC-INPUT ADL, CONVERSION ORDER OR NOTE ABANDONED STATION	1,951	99.59%	1.43%	8	0.41%	0.02%
9516	24	0.02%	79.20%	WSOP OF V AND ADL NOT ALLOWED ON SAME ATN	24	100.00%	0.02%	0	0.00%	0.00%
9517	23	0.02%	79.22%	UNDC INVALID IF PIC ALREADY EXISTS	23	100.00%	0.02%	0	0.00%	0.00%
9523	3	0.00%	79.22%	LOCNUM=000 HNUM=00001 HT= MIXED NPA(S) ARE NOT ALLOWED FOR HUNTING IN THIS SY	3	100.00%	0.00%	0	0.00%	0.00%
9526	12	0.01%	79.23%	BLOCK CHOICE DOES NOT EXIST ON ACCOUNT	12	100.00%	0.01%	0	0.00%	0.00%
9529	2.067	1.51%	80.74%	CANNOT RESTORE A LINE WHICH IS NOT SUSPENDED/DENIED	2,065	99.90%	1.51%	2	0.10%	0.00%
9543	65	0.05%	80.79%	LOCNUM= HNUM= HT= HT CANNOT BE IN MORE THAN ONE HID	64	98.46%	0.05%	1	1.54%	0.00%
9600	12	0.01%	80.80%	TASKMATE ERROR INCORRECT SCREEN FORMAT ON HOST	5	41.67%	0.01%	7	58.33%	0.02%
9602	5,559	4.05%	84.85%	USOC=NSS ALREADY EXISTS ON CUSTOMER RECORD	5,055	90.93%	4.05%	504	9.07%	1.16%
9604	20	001%	84.86%	TN ON SUP DOES NOT MATCH ORIGINAL TN	12	60.00%	0.01%	8	40.00%	0.02%
9605	166	0.12%	84.98%	USOC NOT FOR RESALE FORMAT SAE 959 T1 PGRAX /ZPGR 1 /RMKR (A)	166	100.00%	0.12%	0	0.00%	0.00%
9606	20	0.01%	85.00%	TNS CANNOT BE REASSIGNED FOR 90 DAYS	20	100.00%	0.01%	0	0.00%	0.00%
9613	14	0.01%	85.01%	EXISTING ACCOUNT TYPE NOT AUTHORIZED FOR MIGRATION YET	14	100.00%	0.01%	0	0.00%	0.00%
9616	40	0.03%	85.04%	YPH INVALID	39	97.50%	0.03%	1	2.50%	0.00%
9623	14	0.01%	85.05%	TOUCHTONE IS INVALID WITH AREA PLUS SERVICE	13	92.86%	0.01%	1	7.14%	0.00%
9626	473	0.34%	85.39%	CLASS OF SERVICE LNPRL NOT ELIGIBLE FOR CONVERSION TO PORT/LOOP	473	100.00%	0.34%	0	0.00%	0.00%
9627	2.569	1.87%	87.27%	ALL CUSTOMER RECORDS ARE FINAL FOR THIS NUMBER	2.568	99.96%	1.87%	1	0.04%	0.00%
9628	320	0.23%	87.50%	REQUEST DOES NOT QUALIFY FOR STAR 98 SERVICE	320	100.00%	0.23%	0	0.00%	0.00%
9629	56	0.04%	87.54%	CALL FORWARDING FID (CFND) AND CFND TN REQUIRED BEHIND USOC S98AF	55	98.21%	0.04%	1	1.79%	0.00%
9639	148	0.11%	87.65%	CATEGORY L USOC MUST APPEAR FOR SAME TN	148	100.00%	0.11%	0	0.00%	0.00%
9641	2,258	1 65%	89.29%	REQUESTED ACTIVITY ALREADY PENDING DM4V32	2,258	100.00%	1.65%	0	0.00%	0.00%
9647	145	0.11%	89.40%	BAN DOES NOT EXIST FOR COMPANY CODE	145	100.00%	0.11%	0	0.00%	0.00%
9654	156	0.11%	89.51%	DIRECTORY DELIVERY ADDRESS IS REQUIRED FOR INDEFINITE OR UNNUMBERED ENDUSE	155	99.36%	0.11%	1	0.64%	0.00%
9656	9	0.01%	89.52%	SLTN NOT FOUND ON CRIS ACCOUNT FOR LNA N, LNUM	9	100.00%	0.01%	0	0.00%	0.00%
9661	27	0.02%	89.54%	LINE SHARE AND ADSL REQUIRED BST VOICE SERVICE	17	62.96%	0.02%	10	37.04%	0.02%
9670	23	0.02%	89.56%	TOUCHTONE USOC REQUIRED INWARD OR RECAPPED - FORMAT SAE 004	23	100.00%	0.02%	0	0.00%	0.00%
9671	125	0.09%	89.65%	TOUCHTNE USOC REQUIRED - FORMAT SAE 245	125	100.00%	0.09%	0	0.00%	0.00%
9673	18	0.01%	89.66%	RINGMASTER USOC REQUIRED - FORMAT SAE 387	18	100.00%	0.01%	0	0.00%	0.00%
9674	30	0.02%	89.68%	INVALID TN/PN DATA - FORMAT SAE 389 11 DRS /TN /PN /RNP B	30	100.00%	• 0.02%	0	0.00%	0.00%
9675	29	0.02%	89 70%	BBC USOC MUST NOT APPEAR - FORMAT SAE 679 11 BBC /TN	29	100.00%	0.02%	0	0.00%	0.00%
9679	15	0.01%	89.72%	FIRST CHARACTER OF LINE NUMBER IS NOT VALID FOR BST IN COFFI	15	100.00%	0.01%	0	0.00%	0.00%
9680	18	0.01%	89.73%	INVALID REQTYP OR TOS FOR LIFELINE	18	100.00%	0.01%	0	0 00%	0.00%
9681	15	0 01%	89.74%	LINKUP DISCOUNT CANNOT BE ADDED TO EXISTING SERVICE	15	100.00%	0.01%	0	0.00%	0.00%
9682	12	0.01%	89.75%	LINKUP DISCOUNT IS ONLY AVAILABLE ON LIFELINE ACCOUNTS	12	100.00%	0.01%	0	0 00%	0.00%

AGGREGAT	E ORDER TY	PES]]			
ERROR DET	AILS (Auto C	larifications	A) & Errors (E))	CAUSATION					
					CLEC Caused		CLEC Caused			
Error Type (by error code)	Count	%	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
9685	12,623	9.20%	98.95%	DUE DATE COULD NOT BE CALCULATED	2,993	23.71%	9.20%	9,630	76.29%	22.10%
9686	16	0.01%	98.96%	RESID NOT VALID IN LFACS	15	93.75%	0.01%	1	6.25%	0.00%
9687	3	0.00%	98.97%	ACT=N/LNA=N IS INVALID WHEN THE REQUESTING CLEC ALREADY HAS A LINESHARE ON T	3	100.00%	0.00%	0	0.00%	0.00%
9691	21	0.02%	98.98%	ACT=C, LNA=N IS INVALID ON A SINGLE LINE ACCOUNT	21	100.00%	0.02%	0	0.00%	0.00%
9700	13	0.01%	98.99%	REQUESTED CIRCUIT NUMBER/ECCKT NOT FOUND	13	100.00%	0.01%	0	0.00%	0.00%
9715	48	0.03%	99.03%	TOS IS INVALID FOR REQUESTED SERVICE	48	100.00%	0.03%	0	0.00%	0.00%
9772	6	0.00%	99.03%	UNE - ECCKT PROHIBITED WITH LINE ACTIVITY OF A	4	66.67%	0.00%	2	33.33%	0.00%
9800	23	0.02%	99.05%	MAIN LISTING REQUIRED FOR NEW ACCOUNT	14	60.87%	0.02%	9	39.13%	0.02%
9850	1	0.00%	99.05%	USOC P25 INVALID WITH USOC AQ3 IN KY	1	100.00%	0.00%	0	0.00%	0.00%
9860	1,307	0.95%	100.00%	UNABLE TO HANDLE REQUEST; ENDUSER ACCOUNT FROZEN	1,307	100.00%	0.95%	0	0.00%	0.00%
	137.153	100.00%			93,573		100.00%	43,580		100.00%

AGGREGATE	ORDER TYP	PES		
ERROR DET	AILS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
1001	1	0.01%	0.01%	CCNA MUST BE 3 ALPHAS
1007	20	0.10%	0.11%	DUPLICATE CC, PON, VER
1012	9	0.05%	0.16%	CANNOT SUPP A PREVIOUSLY CANCELED LSR/PON
1015	3519	18.29%	18.44%	PON DUPLICATE ON INITIAL LSR
1017	1	0.01%	18.45%	PON VALID VALUES ARE UPPER CASE ALPHA A THRU Z, NUMERIC 0 THRU 9, AND SYMBOLS . , - '
1023	34	0.18%	18.62%	NO ORIGINAL LSR FOUND FOR THIS SUP
1025	11	0.06%	18.68%	VER MUST BE GREATER THAN PREVIOUS VERSION
1027	7	0.04%	18.72%	PREVIOUS LSR AGED OFF - (K) STATUS
1030	1294	6.72%	25.44%	VER MUST BE GREATER THAN PREVIOUS VERSION
1035	3	0.02%	25.46%	VER MUST BE TWO NUMERICS - 01 OR GREATER FOR 860
1040	114	0.59%	26.05%	VER MUST BE SPACES OR ZEROES FOR 850
1050	29	0.15%	26.20%	D/SENT - D/SENT CENTURY MUST BE CURRENT OR FUTURE DATE
1055	11	0.06%	26.26%	AN REQUIRED FOR THIS REQTYP/ACT TYPE COMBINATION WHEN ATN IS NOT POPULATED
1060	14	0.07%	26.33%	AN PROHIBITED WHEN ATN IS POPULATED UNLESS REQTYP IS B
1065	22	0.11%	26.44%	AN MUST BE 10 OR 13 ALPHANUMERICS
1070	10	0.05%	26.50%	DDD/DDD-CC MUST BE CURRENT OR FUTURE DATE
1075	11	0.06%	26.55%	ATN REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION WHEN AN IS NOT POPULATED
1080	15	0.08%	26.63%	DDD/DDD-CC MUST BE A VALID DATE
1085	6	0.03%	26.66%	DDDO-CC/DDDO MUST BE CURRENT OR FUTURE DATE
1090	3	0.02%	26.68%	ATN OR AN REQUIRED WHEN EATN IS POPULATED
1100	2	0.01%	26.69%	SERVICE CENTER MUST BE LCSC
1110	191	0.99%	27.68%	INVALID REQTYP - ACCOUNT ACTIVITY TYPE COMBINATION
1125	49	0.25%	27.94%	DDD MUST BE GREATER THAN OR EQUAL TO D/TSENT
1130	1	0.01%	27.94%	DDD MUST BE A VALID DATE
1131	278	1.44%	29.39%	DDD IS LESS THAN CALC DATE ON PRIOR VERSION LSR OR SERVICE ORDER DUE DATE
1135	1	0.01%	29.39%	APPTIME-DDD MUST BE HHMM-HHMM (MILITARY TIME) COVERING A SPAN OF TIME OF ONE HOUR OR GREATER
1140	8	0.04%	29.43%	DDDO REQUIRED WHEN ACT IS T AND REQTYP IS A, E, M, OR N
1145	16	0.08%	29.52%	INTERVAL BETWEEN DDD AND DDDO MUST BE 30 CALENDAR DAYS OR LESS
1154	8	0.04%	29.56%	LSR/PON IS COMPLETED
1155	3	0.02%	29.57%	DFDT MUST BE POPULATED WITH A SINGLE (HHMM) TIME WHEN CHC IS Y
1157	1	0.01%	29.58%	DFDT PROHIBITED FOR THIS REQTYP/LNA COMBINATION
1166	7	0.04%	29.61%	CHC IS PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION

AGGREGATE	ORDER TYP	PES		
ERROR DETA	VLS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
1180	15	0.08%	29.69%	INVALID REQTYP/ACT TYPE COMBINATION (STOP EDIT)
1195	1	0.01%	29.70%	ACTIVITY TYPE VALID ENTRY MUST BE N, C, D, T, R, V, S, B, W, L, Y, P OR Q (STOP EDIT)
1200	114	0.59%	30.29%	SUP REQUIRED WHEN VER IS GREATER THAN 00
1215	65	0.34%	30.63%	ACTL MUST BE 11 ALPHANUMERIC CHARACTERS
1230	2797	14.53%	45.16%	LSO MUST BE 6 NUMERICS
1235	1	0.01%	45.17%	TOS REQUIRED
1270	2	0.01%	45.18%	SECNCI MUST BE A MINIMUM OF 5 ALPHANUMERIC CHARACTERS
1285	10	0.05%	45.23%	ACTL REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1290	11	0.06%	45.29%	ACTL MUST BE 11 ALPHANUMERICS
1325	3	0.02%	45.30%	LST MUST BE 11 ALPHANUMERICS
1335	36	0.19%	45.49%	LSO REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1345	5	0.03%	45.52%	TOS REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION (STOP EDIT)
1360	1	0.01%	45.52%	TOS SECOND CHARACTER MUST BE A, B, C, D, H, J, OR - (HYPHEN) (STOP EDIT)
1390	7	0.04%	45.56%	TOS SECOND CHARACTER MUST BE - (HYPHEN) IF REQTYP IS JB
1392	8	0.04%	45.60%	TOS SECOND CHARACTER OF J IS PROHIBITED ON REQTYP OF A,B,C,F OR J (STOP EDIT)
1430	7	0.04%	45.64%	CIC REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1445	1	0.01%	45.64%	INITIATOR TELEPHONE NUMBER REQUIRED
1453	774	4.02%	49.66%	BAN1 REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1455	80	0.42%	50.08%	BAN1 VALID ENTRY MUST BE VALID BILLING ACCOUNT NUMBER OR E WITH TRAILING BLANKS
1457	47	0.24%	50.32%	BAN1 MUST BE ENTRY OF E IF REQTYPE A-LINE SHARE CO BASED
1490	5	0.03%	50.35%	DRC MUST BE 3 ALPHANUMERICS
1505	1	0.01%	50.35%	INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1510	1	0.01%	50.36%	TEL NO-INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1515	15	0.08%	50.44%	TEL NO-INIT FORMAT MUST BE 10 NUMERICS OR UP TO 15 ALPHANUMERICS
1520	141	0.73%	51.17%	FAX NO-INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1525	6	0.03%	51.20%	FAX NO-INIT MUST BE 10 NUMERICS
1530	13	0.07%	51.27%	IMPCON REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1540	2	0.01%	51.28%	TEL NO IMPCON FORMAT MUST BE 10 NUMERICS IN THE FIRST 10 POSITIONS
1550	19	0.10%	51.38%	TEL NO ALTIMPÇON REQUIRED WHEN ALTIMPCON IS POPULATED
1580	1	0.01%	51.38%	FAX NO-DSGCON MUST BE 10 NUMERICS
1605	231	1.20%	52.58%	REMARKS VIRGULES (/) AND ASTERISKS NOT ALLOWED IN THIS FIELD
1620	3	0.02%	52.60%	BCS REQUIRED WITH REQTYP/ACT TYPE/TOS COMBINATION

AGGREGATE	ORDER TYP	PES		
ERROR DET	AILS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
1630	151	0.78%	53.38%	CANNOT SUP A PREVIOUSLY CANCELED LSR/PON
1635	105	0.55%	53.93%	LSR ORIGINATING SOURCE NOT SAME AS PRIOR VERSION
1640	296	1.54%	55.47%	NO ORIGINAL LSR FOUND FOR THIS SUP
1645	3168	16.46%	71.93%	LSR/PON AGED OFF
1650	753	3.91%	75.84%	LSR/PON COMPLETED
1655	12	0.06%	75.90%	LSR ORIGINATING FORMAT (TCIF) NOT SAME AS ORIGINATING FORMAT
1660	80	0.42%	76.32%	SUP NOT ALLOWED ON THIS ACCOUNT ACTIVITY TYPE
1662	18	0.09%	76.41%	SUP NOT ALLOWED ON RESTORAL WHEN THE REASON WAS DENIED
1664	80	0.42%	76.83%	SUP 03 NOT ALLOWED ON THIS ACCOUNT ACTIVITY TYPE
2000	7	0.04%	76.87%	EU-NAME REQUIRED
2015	11	0.06%	76.92%	EU-STATE REQUIRED
2035	2	0.01%	76.93%	LOCNUM=000 NAME EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2040	25	0.13%	77.06%	LOCNUM=000 SANO PROHIBITED WHEN SASN IS NOT POPULATED AT THIS LOCATION
2055	7	0.04%	77.10%	LOCNUM=000 SASD VALID ENTRY IS E, W. N. S. NE, NW. SE. OR SW AT THIS LOCATION
2060	43	0.22%	77.32%	LOCNUM=000 SASN REQUIRED WITH THIS REQTYP/ACT TYP COMBINATION AT THIS LOCATION
2065	3	0.02%	77.34%	LOCBAN REQUIRED
2080	23	0.12%	77.46%	
2085	36	0.19%	77.64%	
2090	7	0.04%	77.68%	
2095	11	0.06%	77.74%	LOCNUM=000 BLDG-EU MUST NOT BE POPULATED WITH BLDG IN ANY POSITION AT THIS LOCATION
2100	1	0.01%	77.74%	LOCNUM=000 CITY-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2104	4	0.02%	77.76%	LOCNUM=000 STATE-EU REQUIRED WHEN SASN IS POPULATED AT THIS LOCATION
2105	1	0.01%	77.77%	LOCNUM=000 STATE-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2109	20	0.10%	77.87%	LOCNUM=000 ZIP CODE=EU REQUIRED WHEN SASN IS POPULATED AT THIS LOCATION
2110	343	1.78%	79.66%	LOCNUM=000 ZIP CODE-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2115	6	0.03%	79.69%	FBCON-TELNO MUST BE MINIMUM OF 10 NUMERICS
2120	588	3.06%	82.74%	EATN, EAN, ATN OR AN ARE PROHIBITED ON THIS REQTYP/ACT CODE
2125	1	0.01%	82.75%	EAN OR EATN REQUIRED WHEN AN OR ATN IS POPULATED WITH THIS REQTYP/ACT TYPE COMBINATION
2130	24	0.12%	82.87%	LOCNUM=000 TEL NO-LCON MUST BE 10 NUMERICS AT THIS LOCATION
2145	9	0.05%	82.92%	LOCBAN MUST EQUAL EAN OR EATN
2155	2	0.01%	82.93%	ATN MUST BE 10 NUMERICS
2170	1	0.01%	82.93%	IWCON-TEL NO REQUIRED WHEN IWCON IS POPULATED

Error Type (by error code)	Count	%	Σ%	Error Description
2185	1	0.01%	82.94%	EAN MUST BE 10 NUMERICS OR 13 ALPHANUMERICS
2200	5	0.03%	82.97%	EATN MUST BE 10 NUMERICS
2295	1	0.01%	82.97%	DNUM MUST BE GREATER THAN PREVIOUS DNUM
2350	21	0.11%	83.08%	ERL REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
2355	7	0.04%	83.12%	ERL PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
2375	1	0.01%	83.12%	LOCNUM=000 WSOP MUST BE V OR BLANK
3000	2	0.01%	83.13%	LOCNUM=000 LNUM=00001 CABLE ID REQUIRED FOR SERVICE TYPE
3010	16	0.08%	83.22%	REFNUM=0001-TELNO= LINE ACTIVITY MUST BE Y OR L WHEN ACCOUNT ACTIVITY = SS OR RS
3015	2	0.01%	83.23%	REFNUM=0001-TELNO= LNA REQUIRED
3021	13	0.07%	83.29%	REFNUM=0001-TELNO= LNA MUST BE V OR W WHEN AN, ATN, EAN OR EATN IS POPULATED
3035	23	0.12%	83.41%	REFNUM=0001-TELNO= OTN MUST BE 10 NUMERICS
3045	16	0.08%	83.50%	REFNUM=0001 ECCKT MUST BE CLT, CLF OR CLS FORMAT
3047	41	0.21%	83.71%	LNUM=00001 CFA LOC A OR LOC Z CLLI DOES NOT MATCH ACTL
3050	122	0.63%	84.34%	LOCNUM=000 LNUM=00001 CFA FORMAT IS INVALID
3080	1	0.01%	84.35%	LOCNUM=000 · LNUM=00002 CHAN/PAIR REQUIRED FOR SERVICE TYPE
3110	74	0.38%	84.73%	LOCNUM=001 LNUM=00001 TELNO= CKR FORMAT INVALID
3115	33	0.17%	84.90%	LOCNUM=000 LNUM=00002 TELNO= ECCKT IS PROHIBITED WITH REQTYP/ACT/LNA COMBINATION
3120	4	0.02%	84.93%	LOCNUM=000 LNUM=00002 TELNO= ECCKT IS REQUIRED WITH REQTYP/ACT/LNA COMBINATION
3125	10	0.05%	84.98%	LOCNUM=000 LNUM=00001 TELNO= ECCKT FORMAT INVALID
3130	4	0.02%	85.00%	REFNUM=0001-TELNO= TC PER-CC/TC PER-DATE MUST BE CURRENT OR FUTURE DATE
3135	56	0.29%	85.29%	REFNUM=0001-TELNO TC PER-CC/TC PER-DATE REQUIRED WHEN TCTO-PRIMARY FIELD IS POPULATED

AGGREG	ATE ORDER TYPES
ERROR DI	ETAILS - 8825
Error Type (by error code)	Error Description
8825	ORDER ERR: SA LIST 023 LIN STREET NAME FOR SA NOT VALID FOR NPA NXXI
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA
8825	ORDER ERR: CS IDNT 011 LIN USOC FOLLOWING CS IS INCORRECT! OCS 1FR
8825	ORDER ERR: LN LIST 010 LIN RECAPPED LN. NLST OR NP MAY NOT APPEAR! ILN (LNR) CROS
8825	ORDER ERR: DSA IDNT 010 LI DSA PRESENT - NEED CATEGORY LUSOC OR SMV USOC!
8825	ORDER ERR: TN SAE 038 LINE TN OR TLI IS REQUIRED FOR INWARD CATEGORY DUSOCS
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTERI 11 UEAC2 /C
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! 11 UEAC2 /C
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! 11 UEAC2 /C
8825	ORDER ERR: ZLLU SAE 009 LI ZLLU MUST APPEARI
8825	ORDER ERR: TYA BILL 008 LI TYA REQUIRED WITH SIC CODE OF 98XX
8825	ORDER ERR: LCON SAE 007 LI LCON FORMAT INCORRECT! IG2 CKL
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! 11 1FR /TN
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 DRS /TN
8825	ORDER ERR; DSA IDNT 009 LI DSA MUST APPEAR IN IDNT!
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 DRS /TN
8825	ORDER ERR: ZLLU SAE 009 LI ZLLU MUST APPEAR!
8825	ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1 1FB /TN
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! 11 14R /TN
8825	ORDER ERR: CFND SAE 016 LI SEE SOER DOCUMENTATION! T1
8825	ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1 1FB
8825	ORDER ERR: PIC SAE 012 LIN PIC MUST APPEAR ON I AND T ACTION CODED CATEGORY D USOC!
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: FORMAT SAE 389 11 DRS /TN
	ORDER ERR: ZLLU SAE 009 LI ZLLU MUST APPEAR!
8825	ORDER ERR: NLST LIST 013 L. SEE SOER DOCUMENTATION! INLST(NON-LIST) INTERPRINT EQUI
8825	ORDER ERR: LN LIST 010 LIN SEE SOER DOCUMENTATION! ILN
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! 11 14R /
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!

AGGREGATE ORDER TYPES ERROR DETAILS - 8825 Error Type (by error Error Description code) ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT! 8825 ORDER ERR: SS BILL 007 LIN SS DATA FORMAT INCORRECT! ISS 8825 8825 ORDER ERR: SIC LIST 012 LI SIC CODE NOT ON BRIS SIC TABLE! ISIC 3047 ORDER ERR: RESH BILL 023 L USOC BSX++ MAY NOT APPEAR! 8825 ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB) 8825 8825 ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB) ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 8825 ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA 8825 ORDER ERR: FORMAT 374 LINE EUCLC: 0001 RELAY: 0000= 8825 8825 ORDER ERR: ADL SAE 010 LIN ADL MUST APPEAR! 11 ORDER ERR: LOC LIST 019 LI INVALID LAST CHARACTER FOR LEVELS 1-3! ILOC LOT 4 DES (8825 8825 ORDER ERR: SA LIST 023 LIN STREET NAME FOR SA NOT VALID FOR NPA NXX! 8825 ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB) ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB) 8825 ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! 11 UEAC2 /C 8825 ORDER ERR: LCON SAE 007 LI LCON FORMAT INCORRECT! CKL 8825 8825 ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA 8825 ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT! ORDER ERR: ROUT LIST 007 L ROUT INVALID ON THIS ORDER! 8825 8825 ORDER ERR: TYA BILL 008 LI TYA REQUIRED WITH SIC CODE OF 98XX ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1 8825 ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 8825 8825 ORDER ERR: TCP TFC 007 LIN INVALID TCP DATE! TCP 06-13-00 8825 ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT! 8825 ORDER ERR: DSA IDNT 009 LI DSA MUST APPEAR IN IDNT! 8825 ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 8825 ORDER ERR: ADL SAE 010 LIN ADL MUST APPEAR! 11 1FR /TN ORDER ERR: PCA SAE 013 LIN SEE SOER DOCUMENTATION! T1 8825 8825 ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA

AGGREGATI	ORDER TYPES
ERROR DET	
Error Type	
(by error	Ewer Description
code)	Error Description
1000	CLEARED ERR BY ISSUING ORDER MANUALLY
1000	CLEARED SYSTEM ERRORS OSCOL AND UEAMC
1000	CLEARED UP SYSTEM ERRORS
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER#
1000	CORRECTED SYSTEM GENERATED ERRORS FOR ORDER#
1000	CLEANED UP SYSTEM ERRORS
1000	CANCEL PER CLEC.
1000	PUT IN E STATUS TO DROP OFF-ORD CANCELLED BY CLEC
1000	CLEARED ALL SYSTEM ERRORS IN DUE DATE CHANGE BY SYSTEM TO 070700
1000	ORDERDD 06-27-00 WORKED TO CHG LISTING
1000	PLACED IN E-STAT SUP 1 ON VER 1 THANKS
1000	ERR PLACED IN E-STAT SUP 1
1000	ERR CLEARED-ORDER ISS TO PROVIDE 1 LOOP
1000	CORRECT SYSTEM ERRORS
1000	CAN PER CLEC
1000	ERROR TO DROP, PON CANCELLED PER SUP 01
1000	EU NAME IS INCOMPLETE, PLS VERIFY AND RESUBMIT;
1000	CLEAN UP SYSTEM ERROR AND ADD SHELVES TO LOC FLR INFO
	CORRECTED SYSTEM ERRORS FOR ORDER#
1000	CORRECTED ERRORS ON ORDER BY REMOVING OCOSL & UEAMC WHICH SHOULD NOT BE ON LY REQUEST
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER, ORDER #
1000	ERROR TO DROP, UNABLE TO FORCE FOC ON C51RKDT0 CPX 06-08-00
1000	ACCOUNT, SERVICE ORDER, DD 06-30-00
1000	ERROR TO DROP, UNABLE TO FORCE FOC ON
1000	CANCELLED ORDER PER SUP 1 LESOG
1000	CORRECT MAN CODE ON ROUTING ERROR MADE BY SYSTEM
1000	RECVD SUP 1 TO CANCEL
1000	CORRECT SYSTEM ERROS
1000	ERR PLACED IN E-STAT SUP 1 ON VER 1
1000	UPDATE TO CHANGE DUE DATE TO 6-27
1000	ERR PLACED IN E-STAT ORDER COMPLETED
·	CLEARED ERR FOR ORDER # , PON#,

AGGREGATI	E ORDER TYPES
ERROR DET	
Error Type (by error code)	Error Description
1000	CORRECT SYSTEM ERRORS
1000	CORRECT SYSTEM ERRORS
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER #
1000	CLEARED ERROR
1000	CORRECT SVC ORDER BY REMOVING OCOSL & UEAMC-WHCH SHOULD NOT BE ON LY RQST
1000	CORRECT ERRORS
1000	CORRECTED SYSTEM GENERATED ORDERS, ORDER#
1000	CORRECTED SYSTEM GENERATED ORDER #
1000	SENT S STATUS REFERAL FORM 06-20-00.
1000	ISS ORD C509GNJ6 DD 0703 ERR STAT 2 COR FOC-
1000	DD 2000-07-05
1000	ORDER CANCELLED
1000	CLAIMED IN ERROR
1000	ORDER PLACED IN ERROR BUCKET. RECORD ORD CPX B4 FOC WAS SENT.
1000	DD 06-14-00
1000	DD 07-06-00
1000	ORDER NY32B0F8 DOES NOT HAVE PON ON IT
1000	DD 2000-07-05
	CORRECT SYSTEM ERRORS
	CLEAR UP SYSTEM ERRORS
	ERR TO DROP OFF, ORD
	ERR CLEARED-ORDER ISS TO PROVIDE 1 LOOP
	CORRECT SYSTEM ERRORS
	CORRECT SYSTEM PROBLEMS
	CLEARED UP SYSTEM ERRORS
	CLEARED ERRORS FROM ORDER TO FLOW THRU
	CLEAR SYSTEM ERRORS OCOSL AND DFDT
	CORRECT ON ODR NUMBER
1000	ORDER BY PLACING DFDT INFO IN PROPER PLACE AND REMOVING OCOSL (NOT VALID ON LYORDER)

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	PERCENT ACHIEVED FLOW- THROUGH	PERCENT FLOW THROUGH
CLEC AGGREGATE		
REGION ALL SERVICES	50.74%	89.09%

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Exhibit October PM Data Attachment 2E

AGGREGATE ORDER TYPES												 	OWTHROU	
Company Info		LSR PROCESSING												
							14-11-1-1-1	P ^a units and						
· · · · · · · · · · · · · · · · · · ·		Mechan	ized Interfa	ce Used	Manual	Rejects	Validated	Errors			· · · · · · ·			
Name	RESH / OCN	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	lssued SO's	Percent Achieved Flowthrough	Base Calculation	Pecent Flow Through
1	2147	405	0	405	203	55	147	90	65	25	57	17.54%	38.78%	46.72%
2	2505	513	0	513	212	60	241	35	18	17	206	47.25%	85.48%	91.96%
3	2896	50	0	50	12	14	24	7	2	5	17	54.84%	70.83%	89.47%
4	4175	1620	0	1620	334	103	1183	220	100	120	963	68.93%	81.40%	90.59%
5	6100	221	0	221	97	50	74	14	3	11	60	37.50%	81.08%	95.24%
6	7058	88	0	88	63	15	10	2	1	1	8	11.11%	80.00%	88.89%
7	7125	2412	0	2412	699	260	1453	265	146	119	1188	58.44%	81.76%	89.06%
8	7170	2595	0	2595	339	168	2088	154	46	108	1934	83.40%	92.62%	97.68%
9	7280	113	0	113	37	41	35	10	3	7	25	38.46%	71.43%	89.29%
10	7344	181	0	181	69	27	85	23	11	12	62	43.66%	72.94%	84.93%
	7421	135	0	135	122	6	7	3	1	2	4	3.15%	57.14%	80.00%
12	7562	2012	0	2012	254	183	1575	266	65	201	1309	80.41%	83.11%	95.27%
13	7795	684	0	684	301	116	267	111	41	70	156	31.33%	58.43%	79.19%
14	8378	632	0	632	349	59	224	121	60	61	103	20.12%	45.98%	63.19%
15	8660	3	0	3	1	0	2	0	0	0	2	66.67%	100.00%	100.00%
16	8758	83	0	83	49	14	20	13	5	8	7	11.48%	35.00%	58.33%
17	1371	0	95	9 5	23	11	61	20	12	8	41	53.95%	67.21%	77.36%
18	1392	0	9	9	2	5	2	1	0	1	1	33.33%	50.00%	100.00%
19	2028	0	2	2	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
20	2031	0	3	3	2	1	0	0	0	0	0	0.00%	0.00%	0.00%
21	2664	0	8	8	2	2	4	4	0	4	0	0.00%	0.00%	0.00%
22	2665	0	43	43	19	10	14	4	1	3	10	33.33%	71.43%	90.91%
23	3869	0	7	7	6	0	1	1	0	1	0	0.00%	0.00%	0.00%
24	4085	0	2096	2096	780	174	1142	335	172	163	807	45.88%	70.67%	82.43%
25	4542	0	73	73	24	18	31	9	6	3	22	42.31%	70.97%	78.57%
26	6093	0	2089	2089	1959	129	1	0	0	0	1	0.05%	100.00%	100.00%
27	7050	0	1068	1068	353	140	575	160	104	56	415'	47.59%	72.17%	79.96%
28	7149	0	66	66	24	11	31	17	12	5	14	28.00%	45.16%	53.85%
29	7343	0	74	74	51	19	4	4	1	3	0	0.00%	0.00%	0.00%
	7581	0	521	521	184	69	268	58	36	22	210	48.84%	78.36%	85.37%
31	7987	0	261	261	33	20	208	46	41	5	162	68.64%	77.88%	79.80%
32	8378	0	5	5	1	2	2	1	1	0	1	33.33%	50.00%	50.00%

Exhibit October PM Data Attachment 2E

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AGGREGATE ORDER TYPES															
Company Info						LSR PROCESSING							FLOWTHROUGH		
		Mechan	ized Interfa	ce Used	Manual	Rejects	Validated	Errors							
Name	RESH / OCN	EDI	TAG	Totai Mech LSR's	Total Manual Fallout	Auto Clarification	L\$R's	Total System Fallout	BST Caused Failout	CLEC Caused Fallout	lssued SO's	Percent Achieved Flowthrough	Base Calculation	Pecent Flow Through	
33	8717	0	1	1	0	1	0	0	0	0	0	0.00%	0.00%	0.00%	
34	8735	0	1	1	0	1	0	0	0	0	0	0.00%	0.00%	0.00%	
EDI Subtotal		11747	0	11747	3141	1171	7435	1334	567	767	6101	62.20%	82.06%	91.50%	
TAG Subtotal			6422	6422	3465	613	2344	660	386	274	1684	30.42%	71.84%	81.35%	
TOTAL INTERFACES				18169	6606	1784	9779	1994	953	1041	7785	50.74%	79.61%	89.09%	

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Company Info		
Name	RESH / OCN	FATAL Rejects
1	8378	236
2	2147	36
3	7421	9
4	7795	190
5	2896	12
6	7987	12
7	7280	72
8	4085	47
9	7058	25
10	7343	3
11	7149	2
12	4542	13
13	7581	74
14	6093	283
15	7170	62
16	7562	93
17	1371	9
18	7050	171
19	4175	256
20	8660	1
21	2664	2
22	2665	7
23	1392	0
24	2028	0
25	2031	1
26	2664	2
27	3869	0
28	8717	2

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REPORT: PERCENT LNP FLOWTHROUGH SERVICE REQUESTS (FATAL REJECTS BY CLEC) REPORT PERIOD: 10/01/2001 - 10/31/2001

AGGREGATE ORDER TYPES		
Company info		
Name	RESH / OCN	FATAL REJECTS
29	8735	0
30	7125	94
31	2505	83
32	6100	37
33	7344	41
34	8758	27
Total		1902

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UP: 0 UP: 0 <th< th=""><th></th><th></th><th><u></u></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th>Trur</th><th>k Grou</th><th>up Perf</th><th>orman</th><th>ce - Ag</th><th>gregat</th><th>e</th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th><th></th></th<>			<u></u>								Trur	k Grou	up Perf	orman	ce - Ag	gregat	e										
Image: Process of the second	Florida	-		Average t	olocking p	ercentage	by hour			I			· · · · · · · · · · · · · · · · · · ·														
by: by: <th>FIOTION</th> <th></th> <th></th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> <th>5</th> <th>6</th> <th>7</th> <th>8</th> <th>. 9</th> <th>10</th> <th>11</th> <th>12</th> <th>13</th> <th>14</th> <th>15</th> <th>16</th> <th>17</th> <th>18</th> <th>19</th> <th>20</th> <th>21</th> <th>22</th> <th>23</th> <th>24</th>	FIOTION			1	2	3	4	5	6	7	8	. 9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Number Life Consol Consol <th></th> <th>NE</th> <th>RallCouth</th> <th></th> <th>0.0048</th> <th>0.0000</th> <th>0.0000</th> <th>0.0004</th> <th>0.0001</th> <th>0 0188</th> <th>0.0076</th> <th>0.0044</th> <th>0.0036</th> <th>0.0136</th> <th>0.0043</th> <th>Ö 0202</th> <th>0.0347</th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>0.0003</th>		NE	RallCouth		0.0048	0.0000	0.0000	0.0004	0.0001	0 0188	0.0076	0.0044	0.0036	0.0136	0.0043	Ö 0202	0.0347										0.0003
gramma constr constr<	NOV-UU	- Ter							0.0016																		0.0175
## ###20ml 0.0001 0.0001 0.0001 0.0000 0.0000 0.0001 0.0000 <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>0.1080</th>																											0.1080
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Berlingth Description Description <thdescription< th=""> <thdescription< th=""> <</thdescription<></thdescription<>																			-0.0581	-0.0765	-0.1835	-0.1533	-0.0754	-0.1328	-0.2005	-0.1159	0.0067
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Chick Construct Co	Dec-00	NF																									0.0085
Bit Exclore Discrete																						-0.0160	-0.0393	-0.0485			-0.0065
CLC COURT C		QF													0.1635	0.1162											0.0058
Diffuence Outree utree Outree Outre	<u> </u>	- <u> </u>		0.0004	0.0000	0.0008	0.0006																				0.0049
Imr 01 Imr 02 Imr 01 Imr 01<			Difference	0.0021	0.0025	-0 0004	0.0000	-0.0052	-0 0238	-0.0927	-0.1341	-0.1164	0.0146	0.0758	0.0636	0.0066	-0.0042	0.0034]	-0.0180	-0.0602	-0.1701	-0.0027	-0.0311	-0.00001	-0.000kj	-0.0107	0.0000
Barry Difference Control Contro Control Control <t< td=""><td>las Of</td><td>NE</td><td>BaltSouth</td><td>ii 0000</td><td>0.0000</td><td>0.0005</td><td>0 00001</td><td>0.0000</td><td>0.0000</td><td>0.0027</td><td>0.0056</td><td>0.0012</td><td>0.0007</td><td>0.0039</td><td>0.0037</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.0000</td></t<>	las Of	NE	BaltSouth	ii 0000	0.0000	0.0005	0 00001	0.0000	0.0000	0.0027	0.0056	0.0012	0.0007	0.0039	0.0037												0.0000
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B# Discuss Dis			Difference																								0.0013
Otherword 3.0283 6.0010 0.0004 0.0000 0.0001 0.0004 0.0004 0.0004 0.0001 0.0001 0.00000 0.0		SF																					0.2638	0.4444	0.3759	0.0241	0.0259
Feb-01 INF BetlSouth 0.0000<																	0.0156	-0.0402	-0.0654	-0.1380	-0.3378	-0.1422	-0.2337	-0.3945	0.3895	-0.0202	-0.0246
Feb-01 WF BellSouth 0.00000 0.													0.0000		0.0(00)	0.0070	0.0006	0.0164	0.0005	0.0121	0.0078	0.0000	0.0714	0 2478	0.0310	0.0000	0.0010
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BF DesiSouth 0.00001 0.00000 0																		-0.0681	-0.0888	-0.0733	-0.0141	-0.0576	-0.4285	-0.7213			-0.0008
CLEC 0.0006 0.0002 0.0002 0.0017 0.0006 0.0002 0.0127 0.0006 0.0002 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.1700 0.0224 0.0170 0.0224 0.0170 0.0224 0.0007 0.0024 0.0261 0.0027 0.0224 0.0007 0.0122 0.0227 0.0322 0.0261 0.0274 0.0274 0.0007 0.00007 0.00007 0.0007<		SF						0.0000	0.0006																		0.0009
Difference JUDICE UDICE JUDICE JUDI																											-0.0008
Name-01 PL BellSouth 0.00000 0.00000 0.000			Difference	-0.0005	-0.0062	-0.0169	-0.0032	-0 0217	-0.0001	-0.0106	-0.1000	-0.1509	-0.0000	-0.3301	-0.410-	-0.0473	-0.0224	v	0.0100	012011	0.1000	0.11200					
CLEC 0.4914 0.0066 0.0073 0.0072 0.0170 0.1176 0.0287 0.0080 0.0281 0.0080 0.0281 0.0080 0.0281 0.0180 0.0281 0.0180 0.0081 0.0180 0.0081 0.0180 0.0081 0.0180 0.0081 0.0180 0.0083 0.0014 0.0100 0.0002 0.0013 0.0007 0.0281 0.0181 0.0181 0.0181 0.0181 0.0081 0.0081 0.0181 0.0181 <td>Mar-01</td> <td>FL</td> <td>BellSouth</td> <td>0.0001</td> <td>0.0000</td> <td>0.0004</td> <td>0 0000</td> <td>0.0000</td> <td>0.0001</td> <td>0.0027</td> <td>0.0582</td> <td></td> <td>0.0047</td>	Mar-01	FL	BellSouth	0.0001	0.0000	0.0004	0 0000	0.0000	0.0001	0.0027	0.0582																0.0047
Utility ends 0.4/91 0.0006 0.0007 0.0008 0																											0.0209
Apr-01 FL Bellsouth 0.00000 0.00000 0.0000			Difference	-0.4913	-0.0066	-0.0049	-0.0072	-0.0008	-0.0069	-0.0144	0.1093	-0.0287	-0.0137	-0.0769	-0.0333	-0.0444[-0.0180	-0.03001	-0.10-5	-0.1700	-02001	-0.1101	0.1010	0.1010			
CLEC 0.0007 0.0283 0.0007 0.0283 0.0002 0.0024 0.0025 0.0111 0.0119 0.0225 0.0111 0.0125 0.0225 0.0111 0.0125 0.0226 0.0124 0.0024 0.0024 0.0225 0.0113 0.0225 0.0111 0.0112 0.0225 0.0115 0.0022 0.0164 0.0024 0.0264 0.0025 0.0116 0.0022 0.0276 0.0416 0.0026 0.0277 0.0416 0.0023 0.0617 0.0116 0.0026 0.0027 0.0116 0.0026 0.0027 0.0117 0.0006 0.0026 0.0026 <td>Apr-01</td> <td>FL</td> <td>BellSouth</td> <td>0 0008</td> <td>0.0001</td> <td>0.0000</td> <td>0.0053</td> <td>0.0000</td> <td>0.0003</td> <td>0.0011</td> <td>0.0082</td> <td></td> <td>0.0004</td>	Apr-01	FL	BellSouth	0 0008	0.0001	0.0000	0.0053	0.0000	0.0003	0.0011	0.0082																0.0004
Ummerce -0.0000 -0.0007 <t< td=""><td>- -</td><td>1</td><td></td><td>0 0010</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.0047</td></t<>	- -	1		0 0010																							0.0047
Hary-01 FL BellSouth 0.0001 0.00000 0.00000 0.000			Difference	-0.0003	-0.0027	-0.0007	-0.0240	-0.0002	-0.0007	-0.0139	-0.0419	-0.0529	-0.0266	0.0043	-0.0068	-0.0163	0.0002	-0.0197	-0.0430	-0.1001	-0.5116	-0.038.1	-0.0000	-0.0/0/	0.10101	0.007.01	
CiteC 0.0031 0.0428 0.0027 0.0169 0.0218 0.0035 0.0118 0.0118 0.0118 0.0118 0.0021 0.0025 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0018 0.0118 0.0011 0.0011 0.0011 </td <td>Nev-01</td> <td>EI</td> <td>BallSouth</td> <td>0.0001</td> <td>0.0000</td> <td>0.0094</td> <td>0 0000</td> <td>0.0000</td> <td>0 0040</td> <td>0.0029</td> <td>0 1190</td> <td>0.0675</td> <td>0.0055</td> <td>0 0151</td> <td>0.0720</td> <td>0 0076</td> <td>0.1039</td> <td>0 0984</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.0002</td>	Nev-01	EI	BallSouth	0.0001	0.0000	0.0094	0 0000	0.0000	0 0040	0.0029	0 1190	0.0675	0.0055	0 0151	0.0720	0 0076	0.1039	0 0984									0.0002
Difference -0.00000 -0.0000 -0.0000	mayor					0.0027	0 0109	0.0218	0.0075	0.0183																	0.0024
Jun-01 FL BellSouth 0.00002 0.00000 0.			Difference	-0.0030	-0.0428	0.0068	-0.0109	-0.0218	-0 0035	-0.0153	-0.0666	-0.0546	-0.0200	-0.0163	0.0116	-0.0078	0.0706	0.0466	-0.1026	-0.1467	-0.3241	-0.0605	-0.0362	-0.0785	-0.1000	-0.0000	-00021
Juli-VI FL Bellsouth 0.0002 0.0030 0.00680 0.00677 0.00670 0.00640 0.00640 0.00413 0.0222 0.00671 0.00680 0.00271 0.1410 0.3364 0.3193 0.1157 0.0027 Difference -0.1137 -0.0374 -0.0680 -0.0674 -0.0227 0.0228 0.00674 -0.0688 -0.0688 -0.0616 -0.0698 -0.0611 -0.1386 -0.3564 -0.0616 -0.0698 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0618 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.1386 -0.0611 -0.0611 -0.0223 -0.0233 -0.0112 -0.0011 -0.0001 -0.0223 -0.0233 -0.0131 -0.0064 -0.00111 -0.0116	lum O1	100	BallCouth	0.0000	0.0000	0,0000	0.0000	0.0001	0.0004	0.0021	0.0506	0.0686	0 0047	0 0128	0.0172	0.0109	0.0104	0.0071	0.0033	0.0057	0.0117	0.0016	0.0025	0.0132	0.0334		0 0005
Difference -0.1137 -0.0374 -0.0869 -0.0777 -0.0674 -0.0257 0.0210 0.0281 -0.0899 -0.0720 -0.0674 -0.0698 -0.0683 -0.0613 -0.0013 -0.0152 -0.0122 -0.0225 -0.0013 -0.0026 -0.0152 -0.0123 -0.0217 -0.0226 -0.0153 -0.0164 -0.0153 -0.0164 -0.0153 -0.0164 -0.0233 -0.0216 -0.0116 -0.0153 -0.0164 -0.0233 -0.0216 -0.0137 0.0164 -0.0226 -0.0164 -0.0153 -0.0026 -0.0153 <t< td=""><td>JUNHUT</td><td>- FL</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.0405</td><td>0.0946</td><td>0.0648</td><td>0.0646</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.0525</td></t<>	JUNHUT	- FL										0.0405	0.0946	0.0648	0.0646												0.0525
Jul-01 FL BellSouth 0.0000 </td <td></td> <td></td> <td></td> <td></td> <td>-0.0374</td> <td>-0.0890</td> <td>-0.0669</td> <td>-0.0777</td> <td>-0.0674</td> <td>-0.0257</td> <td>0.0210</td> <td>0.0281</td> <td>-0.0899</td> <td>-0.0720</td> <td>-0.0674</td> <td>-0.0303</td> <td>-0.0168</td> <td>-0.0596</td> <td>-0.0683</td> <td>-0.0643</td> <td>-0.0606</td> <td>-0.0611</td> <td>-0.1385</td> <td>-0.3562</td> <td>0.2859</td> <td>-0.1012</td> <td>-0.0521</td>					-0.0374	-0.0890	-0.0669	-0.0777	-0.0674	-0.0257	0.0210	0.0281	-0.0899	-0.0720	-0.0674	-0.0303	-0.0168	-0.0596	-0.0683	-0.0643	-0.0606	-0.0611	-0.1385	-0.3562	0.2859	-0.1012	-0.0521
JuleOI PL BellSouth 0.00000 0.00000 0.0000	1 1 04	100	D-liCault	0.0000	ā.0000	0,000	0,0000	0.0001	0,0000	0.0014	0.0377	0.0173	0.0152	0.0045	0.0222	0 0038	0.0213	0.0068	0.0077	0.0051	0.0119	0.0040	0 0022	0.0025	0 0041	0.0086	0.0026
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Augent FL BellSouth 0.0001 0.0000 0.0000 0.0001 </td <td></td> <td>+</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>-0.0008</td> <td>0.0009</td> <td>0.0368</td> <td>0.0073</td> <td>-0.0013</td> <td>-0.0488</td> <td>-0.0318</td> <td>-0.0150</td> <td>-0.0313</td> <td>-0.0340</td> <td>-0.0264</td> <td>-0.0205</td> <td>-0.0046</td> <td>0.0115</td> <td>-0.0152</td> <td>-0.0193</td> <td>-0.0163</td> <td>-0.0054</td> <td>-0.0119</td>		+							-0.0008	0.0009	0.0368	0.0073	-0.0013	-0.0488	-0.0318	-0.0150	-0.0313	-0.0340	-0.0264	-0.0205	-0.0046	0.0115	-0.0152	-0.0193	-0.0163	-0.0054	-0.0119
Augent FL BellSouth 0.0001 0.0000 0.0000 0.0001 </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.0000</td> <td>0.0000</td> <td>0.0000</td> <td>0.0010</td> <td>0.0005</td> <td>0.0373</td> <td>0024</td> <td>0.0049</td> <td>0.0072</td> <td>0.0176</td> <td>0.0000</td> <td>0.0137</td> <td>0.0109</td> <td>0.0275</td> <td>0.0144</td> <td>0.0052</td> <td>0.0053</td> <td>0 0085</td> <td>0.0044</td> <td>0.0004</td> <td>0 0011</td>							0.0000	0.0000	0.0000	0.0010	0.0005	0.0373	0024	0.0049	0.0072	0.0176	0.0000	0.0137	0.0109	0.0275	0.0144	0.0052	0.0053	0 0085	0.0044	0.0004	0 0011
CLEC 0.0010 0.0000 0.0000 0.0000 0.0000 0.0013 0.0026 0.0020 0.0138 0.0010 0.0020 0.0198 0.00138 0.0002 0.0000 0.0000 0.00031 0.0155 0.0195 0.0195 0.0002 0.00031 0.0138 0.0013 0.00138 0.0020 0.00138 0.0010 0.0002 0.0002 0.00138 0.00138 0.0010 0.0002 0.00031 0.00155 0.0155 0.0155 0.0155 0.0155 0.0155 0.0155 0.0155 0.0166 0.0000 0.0000 0.00031 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.00031 0.0016 0.0001 0.00031 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016 0.0016	Aug-01	FL																						0.0240	0.0239	0.0056	0.0003
Sep-01 FL BellSouth 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0001 0.0000 0.0016 0.0000 0.0001 0.0000 0.0016 0.0000 0.0000 0.0001 0.0001 0.0001 0.0001 0.0001 0.0001 0.0016 0.0016 0.0016 0.0001 0.0001 0.0016 0.0016 0.0001 0.0001 0.0016 0.0016 0.0001 0.0016 </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.0200</td> <td>0.0198</td> <td>-0.0052</td> <td>0.0106</td> <td>-0.0031</td> <td>-0.0169</td> <td>-0.0155</td> <td>-0.0195</td> <td>-0.0053</td> <td>0.0007</td>																		0.0200	0.0198	-0.0052	0.0106	-0.0031	-0.0169	-0.0155	-0.0195	-0.0053	0.0007
Sep-01 FL BellSouth 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0011 0.0111 0.0118 0.0418 0.0419 0.0221 0.0011 CLEC 0.0206 0.0303 0.0482 0.1485 0.0902 0.0624 0.0267 0.0114 0.0251 0.0126 0.0104 0.0095 0.1117 0.0158 0.0281 0.0111 0.0198 0.0418 0.0419 0.0221 0.0111 0.0111 0.0198 0.0418 0.0419 0.0221 0.0114 0.0294 -0.0198 0.01134 0.01134 0.0111 0.0111 0.0198 0.0418 0.0419 0.0221 0.0114 0.0294 -0.0198 -0.0134 0.01134 0.0111 0.0111 -0.0198 -0.0403 -0.0208 -0.0403 -0.0208 -0.0403 -0.0219 -0.0114 -0.0224 -0.0024 -0.0126 0.0012 0.0015 0.0001 0.0001 0.0002 0.0002 0.0002 0.0022 0.0022 <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.000</td><td>ō ceec '</td><td>0 004 -</td><td>0.0000</td><td>0</td><td>0.0000</td><td>0.000</td><td>0.0000</td><td>0.0004</td><td>0.0004</td><td>0.0000</td><td>0.000</td><td>0.0007</td><td>0.0062</td><td>0.0016</td><td>0 0002</td><td>0 0000</td></t<>											0.000	ō ceec '	0 004 -	0.0000	0	0.0000	0.000	0.0000	0.0004	0.0004	0.0000	0.000	0.0007	0.0062	0.0016	0 0002	0 0000
CLEC 0.0206 0.0462 0.0462 0.0462 0.0462 0.0466 0.024 0.024 0.024 0.014 0.0123 0.0164 0.0034 0.0134 0.1113 0.0164 0.0261 0.0111 0.0191 0.0266 0.0021 0.0021 0.0014 0.0014 0.0104 0.0104 0.0134 0.1113 0.0164 0.0111 0.0111 0.0191 0.0266 0.0021 0.0021 0.0011 0.0111 0.0191 0.0021 0.0011 0.0111 0.0191 0.0021 0.0011 0.0111 0.0191 0.0045 0.0021 0.0011 0.0111 0.0191 0.0045 0.0021 0.0011 0.0011 0.0014 0.0021 0.0011 0.0011 0.0014 0.0021 0.0015 0.0011 0.0014 0.0022 0.0005 0.0012 0.0012 0.0015 0.0011 0.0014 0.0021 0.0015 0.0011 0.0014 0.0022 0.0005 0.0012 0.0012 0.0015 0.0011 0.0014 0.0012 0.0011	Sep-01	FL																									0.0173
Oct-01 FL BellSouth 0.0001 0.0000 </td <td></td> <td>+</td> <td></td> <td>-0.0173</td>		+																									-0.0173
Oct-01 FL BellSouth 0.0001 0.0000 </td <td></td> <td></td> <td></td> <td>0.0200</td> <td>0.0000</td> <td></td> <td>0.0005</td> <td>0.0000</td> <td>0.0000</td>				0.0200	0.0000																				0.0005	0.0000	0.0000
	Oci-01	FL																									0 0000
			Unterence	-0.0001	-0 0052	-0.0004	-0.0268	-0 2831	-0.0013	-0,00/0	-0 0023	-0.0301	0.0000	-0.0075	0.0020	-0.000-0	-0.0111	0.0200	0.0021	0.0000	0.0000	.					