## CONFIDENTIAL

## ATTACHMENT C

BellSouth Telecommunications, Inc. FPSC Docket No. 980119-TP Request for Confidential Classification Page 1 of 1 5/26/04

REQUEST FOR CONFIDENTIAL CLASSIFICATION OF BELLSOUTH'S EXHIBIT RMP-2 TO THE REBUTTAL TESTIMONY OF RONALD M. PATE, FILED MAY 26, 2004, IN FLORIDA DOCKET NO. 980119-TP

PROPRIETARY COPY



This confidentiality request was filed by or for a "telco" for DN OLOGY 4-04 No ruling is required unless the material is subject to a request per 119.07, FS, or is admitted in the record per Rule 25-22.006(8)(b), FAC.

DOCUMENT NUMBER-DATE

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FPSC-COMMISSION CLERK®

1 2 3 4 5 6 7				anuary 2004 - Ma Ordering	rch 2004										
3 4 5 6 7			Poinct	Ordering		<del></del>									
4 5 6 7			Painet												
5 6 7		•	Reject Interval - Mechanized												
6 7		(% of CLEC Reject Notification within Intervals)													
7		Numerator indicates total number of CLEC reject notification within interval hours for this disaggregation in the reporting period													
				hmark 97% <=1	<del></del>		C 7-4-1								
8						Supra Total  etric CLEC Numerator CLEC Volume CLEC Me									
	Month 0	Product Group Description	Numerator 197	Volume	<b>Metric</b> 98.99 %	52	CLEC Volume 52								
		Resale Business		199			29	************							
		Resale Business	147 175	148 176	99.32 % 99.43 %	29									
12 IV 13	nar-04	Resale Business	1/5	. 170	99.43 70	33	30°	100.00%							
	an-04	Resale Residence	1432	1476	97.02 %	300	312	96.15%							
		Resale Residence	1140	1144	99.65 %	204	205								
		Resale Residence	1211	1215	99.67 %	197	198								
17	iai o i	T COURT T COURT TO													
	an-04	2W Analog Loop w/LNP - Design	3	3	100.00 %	23	23								
19 F	eb-04	2W Analog Loop w/LNP - Design	3	3	100.00 %	2	2								
20 N	/lar-04	2W Analog Loop w/LNP - Design	1	1	100.00 %	1	1	100,00%							
21							3 (c) (d) (d) (d) (d) (d) (d) (d) (d) (d) (d								
22 J	an-04	2W Analog Loop w/LNP - Non-Design	408	408	100.00 %	291	291	100,00%							
		2W Analog Loop w/LNP - Non-Design	226	227	99.56 %	148									
24 N	/lar-04	2W Analog Loop w/LNP - Non-Design	185	190	97.37 %	58	.58								
25															
		UNE Loop + Port <sup>®</sup> Combinations	15566	16047	97.00 %	7308	1								
		UNE Loop + Port Combinations	14089	14278	98.68 %	5798									
	/lar-04	UNE Loop + Port Combinations	19669	20188	97.43 %	6816	6914	98.58%							
29					07.04.0/			400,000							
		UNE Other Non-Design	1571	1614	97.34 %	3									
		UNE Other Non-Design	1965	1971	99.70 %	2	3								
	//ar-04	UNE Other Non-Design	1857	2022	91.84 %	20	20	100.00%							
33			10	16	100.00 %	8									
	an-04		16 10	11	90.91 %	0		-							
1	eb-04		29	29	100.00 %	3.0000000000000000000000000000000000000	2								
36 N	/lar-04	EELS	29	29	100.00 %			(1003007)							

A	В	C	D	F	FI G	Н						
1	1		January 2004 - Ma		. 1	1						
2 -		1101144, 0	Ordering									
3	Reject Interval - Partial Mechanized											
4	(% of CLEC Reject Notification within Intervals)											
5	Numerator indicates total number of CLEC reject notification within interval hours for this disaggregation in the reporting period											
6	Volume indicates total number of service requests for this disaggregation rejected in the reporting period											
7	Benchmark 95% <= 10 Hours											
8	State of Florida - ALL CLECs Supra Total											
9 Monti	Product Group Description	Numerator	Volume	Metric	<b>CLEC Numerator</b>	CLEC Volume	CLEC Metric					
10 Jan-04	Resale Business	257	272	94.49 %	54	55	98.18%					
11 Feb-04	4 Resale Business	287	296	96.96 %	45		A STATE OF THE PARTY OF THE PAR					
	4 Resale Business	269	274	98.18 %	30	30						
13												
	Resale Residence	887	939	94.46 %	169	1						
	4 Resale Residence	712	761	93.56 %	118							
	4 Resale Residence	700	769	91.03 %	103	109	94,50%					
17							XVXXXXXXX					
	4 2W Analog Loop Non-Design	126	147	85.71 %	_  ************************************	1.00	100.00%					
	4 2W Analog Loop Non-Design	133	164	81.10 %		1						
	4 2W Analog Loop Non-Design	104	125	83.20 %	0	0	N/A					
21												
	1 2W Analog Loop w/LNP - Non-Design	533	614	86.81 %	256		83.12%					
	4 2W Analog Loop w/LNP - Non-Design	485	648	74.85 %	273		, 63.93%					
	4 2W Analog Loop w/LNP - Non-Design	243	265	91.70 %	/100	107	93.46%					
25		11000	10070	07.00.0/	roo	000	00.0000					
	4 UNE Loop + Port Combinations	11902	12270	97.00 %	5984		98.36% 97.77%					
	4 UNE Loop + Port Combinations	11089	11477	96.62 % 95.66 %	5265 4651							
	4 UNE Loop + Port Combinations	11370	11886	95.00 %	4001	34797	80.8076					
29	ALUNIS Other New Design	400	506	98.02 %	22		100,00%					
	4 UNE Other Non-Design	496 520	529	98.30 %	24							
	4 UNE Other Non-Design	492	497	98.99 %	28	The second secon	Anna anna anna anna anna anna anna anna					
	4 UNE Other Non-Design	492	437	3U.33 /0	20	30	33.0370					
33 34 Jan-04	1 [55]	8	8	100.00 %	791	23.44	100/00%					
34 Jan-04 35 Feb-04		11	12	91.67 %		.2	60:00%					
35 Feb-04		12	12	100.00 %		3.3	100.00%					
36 Mar-04	4  CCLS	12	14	100.00 /0								
3/						100000 Section (1000)	IC 10: 92:385.000:392.000.0					