Nancy B. White General Counsel-Florida

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BellSouth Telecommunications, Inc. 150 South Monroe Street Room 400 Tallahassee, Florida 32301 (305) 347-5558

August 30, 2001

Mrs. Blanca S. Bayó Director, Division of Records and Reporting Florida Public Service Commission 2540 Shumard Oak Boulevard Tallahassee, FL 32399-0850

Re: <u>960786-TL (Section 271)</u>

Dear Ms. Bayó:

Enclosed please find the original and fifteen copies of BellSouth Telecommunications, Inc.'s Notice of Filing Affidavit of Alphonso J. Varner with attachments reflecting performance data for the month of June 2001, which we ask that you file in the captioned docket.

A copy of this letter is enclosed. Please mark it to indicate that the original was filed and return the copy to me. Copies have been served to the parties by Facsimile and Federal Express as shown on the attached Certificate of Service.

Sincerely, Nancy B. White Nancy B. White

Enclosures

cc: All Parties of Record Marshall M. Criser III R. Douglas Lackey

> DOCUMENT NUMBER-DATE 10833 AUG 30 = FPSC-COMMISSION CLERK

CERTIFICATE OF SERVICE DOCKET NO. 960786-TL

I HEREBY CERTIFY that a true and correct copy of the foregoing was served by Hand Delivery (*) and Federal Express this 30th day of August, 2001 to the following:

Mr. Brian Sulmonetti (+) LDDS WorldCom Communications Suite 3200 6 Concourse Parkway Atlanta, GA 30328 Tel. No. (770) 284-5493 Fax. No. (770) 284-5488 brian.sulmonetti@wcom.com

Floyd R. Self, Esq. (+) Messer Law Firm 215 South Monroe Street Suite 701 P.O. Box 1876 Tallahassee, FL 32302-1876 Tel. No. (850) 222-0720 Fax. No. (850) 224-4359 Represents LDDS/ACSI fself@lawfla.com

Vicki Gordon Kaufman (+) Joseph A. McGlothlin (+) McWhirter, Reeves, McGlothlin, Davidson, Rief & Bakas, P.A. 117 South Gadsden Street Tallahassee, Florida 32301 Tel. No. (850) 222-2525 Fax. No. (850) 222-2525 Fax. No. (850) 222-5606 Represents FCCA Represents NewSouth Represents NewSouth Represents NuVox Comm. Represents ACCESS Represents XO vkaufman@mac-law.com Charles J. Beck Office of Public Counsel 111 W. Madison Street Suite 812 Tallahassee, FL 32399-1400 Tel. No. (850) 488-9330 Fax No. (850 488-4992 Beck.Charles@leg.state.fl.us

Richard D. Melson (+) Hopping Green Sams & Smith 123 South Calhoun Street P.O. Box 6526 Tallahassee, FL 32314 Tel. No. (850) 222-7500 Fax. No. (850) 224-8551 Represents MCI, Rhythms <u>RMelson@hgss.com</u>

Susan S. Masterton (+) Sprint Communications Co. Post Office Box 2214 (zip 32316-2214) 1313 Blair Stone Road Tallahassee, FL 32301 Tel. (850) 599-1560 Fax (850) 878-0777 susan.masterton@mail.sprint.com

Beth Keating, Staff Counsel (*) Florida Public Service Commission Division of Legal Services 2540 Shumard Oak Boulevard Tallahassee, FL 32399-0850 Tel. No. (850) 413-6212 Fax. No. (850) 413-6250 bkeating@psc.state.fl.us Scott Sapperstein Intermedia Comm., Inc. One Intermedia Way MCFLT-HQ3 Tampa, Florida 33647-1752 Tel. No. (813) 829-4093 Fax. No. (813) 829-4923 Sasapperstein@intermedia.com

Rhonda P. Merritt AT&T 101 North Monroe Street Suite 700 Tallahassee, FL 32301 Tel. No. (850) 425-6342 Fax. No. (850) 425-6361 rpmerritt@ATT.com

Kenneth A. Hoffman, Esq. (+) Rutledge, Ecenia, Underwood, Purnell & Hoffman, P.A. 215 South Monroe Street Suite 420 P.O. Box 551 Tallahassee, FL 32302 Tel No. (850) 681-6788 Fax. No. (850) 681-6515 Represents TCG Represents US LEC <u>Ken@Reuphlaw.com</u>

John R. Marks, III 215 South Monroe Street Suite 130 Tallahassee, FL 32301 Tel. (850) 222-3768 Fax. (850) 561-0397 Represents BellSouth JohnM@KMRlaw.com

Kenneth S. Ruth Florida Director CWA 2180 West State Road 434 Longwood, FL 32779 Tel. (407) 772-0266 Fax. (407) 772-2516 <u>Kruth@cwa-union.org</u> Marilyn H. Ash MGC Communications, Inc. 3301 N. Buffalo Drive Las Vegas, NV 89129 Tel. No. (702) 310-8461 Fax. No. (702) 310-5689

Rodney L. Joyce Shook, Hardy & Bacon, L.L.P. 600 14th Street, N.W. Suite 800 Washington, D.C. 20005-2004 Tel. No. (202) 639-5602 Fax. No. (202) 783-4211 rjoyce@shb.com Represents Network Access Solutions

Michael Gross/Charles Dudley (+) FCTA, Inc. 246 E. 6th Avenue Suite 100 Tallahassee, FL 32303 Tel. No. (850) 681-1990 Fax. No. (850) 681-9676 mgross@fcta.com

Nanette Edwards ITC^DeltaCom 4092 South Memorial Parkway Huntsville, AL 35802 Tel. No. (256) 382-3856 Fax. No. (256) 382-3969 Represented by Hopping Law Firm

Donna McNulty MCI WorldCom 325 John Knox Road Suite 105 Tallahassee, FL 32303-4131 Tel. No. (850) 422-1254 Fax. No. (850) 422-2586 donna.mcnulty@wcom.com

Network Access Solutions Corp. 100 Carpenter Drive Suite 206 Sterling, VA 20164 Tel. No. (703) 742-7700 Fax. No. (703) 742-7706 Represented by Shook, Hardy & Bacon

Karen Camechis (+) Pennington Law Firm 215 South Monroe Street 2nd Floor Tallahassee, FL 32301 Tel. No. (850) 222-3533 Fax. No. (850) 222-2126 Represents Time Warner pete@penningtonlawfirm.com

8

Rhythms Links, Inc. 6933 South Revere Parkway Suite 100 Englewood, CO 80112 Tel. No. (303) 476-4200 Represented by Hopping Law Firm

Benjamin Fincher Sprint/Sprint-Metro 3100 Cumberland Circle #802 Atlanta, GA 30339 Tel. No. (404) 649-5144 Fax. No. (404) 649-5174 Represented by Ervin Law Firm

Carolyn Marek Time Warner Regulatory Affairs, SE Region 233 Bramerton Court Franklin, TN 37069 Tel. No. (615) 376-6404 Fax. No. (615) 376-6405 carolyn.marek@twtelecom.com Represented by Pennington Law Firm Represented by Parker Poe Adams James Falvey ACSI 131 National Business Parkway Annapolis Junction, MD 20701 Represented by Messer Law Firm

Matthew Feil (+) Florida Digital Network, Inc. 390 North Orange Avenue Suite 2000 Orlando, FL 32801 Tel. No. (407) 835-0460 mfeil@floridadigital.net

Michael Sloan (+) Swidler Berlin Shereff Friedman, LLP 3000 K Street, N.W. Suite 300 Washington, D.C. 20007-5116 Tel. No. (202) 295-8458 Fax No. (202) 424-7645 Represents FDN mcsloan@swidlaw.com

Katz, Kutter Law Firm (+) Charles J. Pellegrini/Patrick Wiggins 106 E. College Avenue Tallahassee, FL 32301 Tel. No. 850-224-9634 Fax. No. 850-224-9634 pkwiggins@katzlaw.com

Lori Reese Vice President of Governmental Affairs NewSouth Communications Two Main Street Greenville, South Carolina 29609 Tel. No. (864) 672-5177 Fax. No. (864) 672-5040 Ireese@newsouth.com

Genevieve Morelli Andrew M. Klein Kelley Drye & Warren LLP 1200 19th Street, NW Suite 500 Washington, DC 20036 Represents KMC John D. McLaughlin, Jr. KMC Telecom 1755 North Brown Road Lawrenceville, Georgia 30043

Suzanne F. Summerlin, Esq. 1311-B Paul Russell Road Suite 201 Tallahassee, Florida 32301 Tel. No. (850) 656-2288 Fax. No. (850) 656-5589 Represents IDS Telecom

Henry C. Campen, Jr. (+) Parker, Poe, Adams & Bernstein, LLP P.O. Box 389 First Union Capital Center 150 Fayetteville Street Mall Suite 1400 Raleigh, NC 27602-0389 Tel. No. (919) 890-4145 Fax. No. (919) 834-4564 Represents US LEC of Florida Represents NuVox Comm. Represents XO Represents Time Warner

Catherine F. Boone Covad Communications Company 10 Glenlake Parkway, Suite 650 Atlanta, Georgia 30328-3495 Tel. No. (678) 222-3466 Fax. No. (678) 320-0004 cboone@covad.com

Bruce Culpepper, Esq. Akerman, Senteriftt & Eidson 301 South Bronough Street Suite 200 Post Office Box 10555 Tallahassee, FL 32302-2555 Attys. for AT&T

Mark D. Baxter Stone & Baxter, LLP 557 Mulberry Street Suite 1111 Macon, Georgia 31201-8256 Represents ACCESS Dana Shaffer XO Communications, Inc. 105 Molloy Street, Suite 300 Nashville, Tennessee 37201-2315 Tel. (615) 777-7700 Fax. (615) 345-1564 dana.shaffer@xo.com Represented by Parker Poe Adams

Nancy B White

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Nancy B. White (KA)

(+) Signed Protective Agreement

BEFORE THE FLORIDA PUBLIC SERVICE COMMISSION

)

In Re: Consideration of BellSouth Telecommunications, Inc.'s entry into interLATA services pursuant to Section 271 of the Federal Telecommunications Act of 1996.

Docket No. 960786-TL

Filed: August 30, 2001

BELLSOUTH TELECOMMUNICATIONS, INC.'S NOTICE OF FILING

BellSouth Telecommunications, Inc. ("BellSouth") hereby files the Affidavit of Alphonso J. Varner that attaches BellSouth's performance data reflecting performance for the month of June 2001. The Affidavit and the accompanying attachments describe the performance data and explain the conclusions that can be drawn from it.

Respectfully submitted this 30th day of August 2001.

BELLSOUTH TELECOMMUNICATIONS, INC.

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NANCY B. WHITE (LA) JAMES MEZA III c/o Nancy Sims 150 South Monroe Street, Suite 400 Tallahassee, FL 32301 (305) 347-5561

LISA FOSHEE (EA) FRED MCCALLUM E. EARL EDENFIELD JR. Suite 4300 675 W. Peachtree St., NE Atlanta, GA 30375 (404) 335-0763 Before the Florida Public Service Commission Tallahassee, Florida

AFFIDAVIT OF ALPHONSO J. VARNER ON BEHALF OF BELLSOUTH TELECOMMUNICATIONS, INC. FILED AUGUST 29, 2001

I, Alphonso J. Varner, being of lawful age and duly sworn upon my oath, depose and state:

 My name is Alphonso J. Varner. I am employed by BellSouth as Senior Director in Interconnection Services. My business address is 675 West Peachtree Street, Atlanta, Georgia 30375.

PROFESSIONAL AND EDUCATIONAL BACKGROUND

- 2. I graduated from Florida State University in 1972 with a Bachelor of Engineering Science degree in systems design engineering. I immediately joined Southern Bell in the division of revenues organization with the responsibility for preparation of all Florida investment separations studies for division of revenues and for reviewing interstate settlements.
- 3. Subsequently, I accepted an assignment in the rates and tariffs organization with responsibilities for administering selected rates and tariffs including preparation of tariff filings. In January 1994, I was appointed Senior Director of Pricing for the nine-state region. I was named Senior Director for Regulatory Policy and Planning in August 1994.

In April 1997, I was named Senior Director of Regulatory for the nine-state BellSouth region, and I accepted my current position in March 2001.

II. PURPOSE OF AFFIDAVIT

4. The purpose of my Affidavit is to provide data specific to BellSouth's operations in Florida. This filing reflects performance for the month of June 2001. Exhibit June PM Data and Attachments 1A though 3A that accompany this filing describe the data and explain the conclusions that can be drawn from it.

DISCUSSION OF PERFORMANCE MEASUREMENTS DATA

TABLE OF CONTENTS

2 3 4 5 6		TABLE OF CONTENTS	
7			
8	I. Analysis of Perfor	mance Measurements	2
9	A. Introduction		2
10	B. Checklist Iter	m 1 – Interconnection	5
11	C. Checklist Iter	m 2 – Unbundled Network Elements	10
12	D. Checklist Iter	m 4 – Unbundled Local Loops	44
13	E. Checklist Iter	m 5 – Unbundled Local Transport	61
14	F. Checklist Iter	m 6 – Unbundled Local Switching	62
15	G. Checklist Iter	m 7a – 911 and E911 Services	62
16	H. Checklist Iter	m 7b – Directory Assistance/Operator Services	62
17	I. Checklist Iter	m 10 - Access To Database & Associated Signaling	162
18	J. Checklist Iter	m 11 – Number Portability	63
19	K. Checklist Itel	m 14 – Resale	68
20			~~
21	II. Summary		88
22			
23	Attachments:	lune 2001 Floride Summers Besults	
24		June 2001 Florida Summary Results	
25	28	Flow-Infough Report	
20	3A	Trunk Group Performance Report	
21 00			
20			
29			

1

1	DISCUSSION OF PERFORMANCE MEASUREMENTS DATA
2 3	I. ANALYSIS OF PERFORMANCE MEASUREMENTS
4	
5	A. Introduction
6	
7	BellSouth is currently producing state level results based on the January 12,
8	2001, Georgia Order from Docket 7892-U. While there are some differences
9	from the interim Service Quality Measurement (SQM) Version 3.0 approved
10	by this Commission on July 3, 2001, they are minor and should not cause any
11	difficulty in determining BellSouth's overall performance level.
12	
13	Attachment 1A is the Monthly State Summary (MSS) for Florida for June
14	2001. The MSS contains 2,249 sub-metrics based on the Georgia Public
15	Service Commission (GPSC) Docket 7892-U. The MSS for May 2001 data
16	contained 2,251 sub-metrics. (Two items that were counted as sub-metrics
17	with no CLEC activity in May are no longer included in the sub-metric total
18	with the June 2001 filing.) In June 2001, BellSouth met or exceeded the
19	criteria for 517 of these 637 sub-metrics, or 81% for which there were both
20	established benchmarks/retail analogues and CLEC activity. The remainder
21	(1612) of the 2,249 sub-metrics were either diagnostic (919), had no CLEC
22	activity (563), were parity by design (10), are still under development (2) or
23	are excluded (118) due to data calculation deficiencies. In May, BellSouth
24	met or exceeded the criteria for 499 of the 608 sub-metrics, or 82% for which

there were both established benchmarks/retail analogues and CLEC activity.
 All measures and sub-metrics are included in these calculations except three
 measures that are currently under investigation that have known deficiencies
 in their calculations. They are Average Jeopardy Notice Interval, FOC &
 Reject Completeness, and LNP Disconnect Timeliness.

6

7 Two general issues can impact the degree to which BellSouth's performance 8 data is meaningful. First, the extreme disaggregation of the data in the 9 reports often dilutes the universe size of individual measurements, which in 10 turn reduces the confidence level of each of the individual Z-test results. As a 11 result, there are many performance measurements for which the results are 12 statistically inconclusive due to the small number of observations. Second, in 13 situations in which there are a large number of observations and the 14 difference between the means is very small, the results can be misleading 15 and not indicative of the absolute level of performance that BellSouth 16 provides to CLECs.

17

With respect to the first issue, in many cases, the extensive levels of disaggregation leads to numerous sub-metrics with fewer than 30 observations, which is generally accepted as the smallest number of observations for application of the Z-test. Despite this fact, BellSouth has reported results for all of the measures, even those with statistically inconclusive universe sizes.

З

2 The second issue arises in situations where BellSouth provides very high 3 quality service to both BellSouth's retail units and the CLECs, where there are 4 very large universe sizes, and the difference between the means is very 5 small. This scenario can cause an apparent missed condition from a 6 quantitative viewpoint. For example, in June 2001, the % Missed Installation Appointments (%MIA), for Residence / Non-Dispatch / < 10 Circuits 7 8 (A.2.11.1.1.2) showed that BellSouth retail had 0.11% missed appointments 9 for the 669,560 scheduled orders. The CLEC %MIA for the same period is 10 0.16% missed appointments for 33,424 scheduled orders. While there is very 11 little difference in the results, only five one hundredth of a percentage point, 12 the universe is so large that the Z-test becomes overly sensitive to any 13 difference. As a result, the statistical test shows that the sub-metric missed 14 the standard criteria but BellSouth's actual performance is at a very high level 15 for both the CLECs and BellSouth retail, in this case, greater than 99.8%. 16 From a practical point of view, the CLECs' ability to compete has not been 17 hindered, even though the statistical result does not technically meet the retail 18 analogue.

19

In reviewing the data, the Florida Public Service Commission (Commission) should use the data as a tool in analyzing whether BellSouth has met its commitments. It is not a substitute for the qualitative evaluation of BellSouth's performance. The commission will still need to conduct a

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23 <u>Trunking Reports</u>

1	Attachment 1A, Section C, Items C.1.1 to C.4.2 of the MSS contains data for
2	ordering, provisioning, maintenance and repair, and billing associated with
3	Local Interconnection Trunks.

4

5 In May 2001, BellSouth met 15 of 18 sub-metrics or 83% and in June, met 18 6 of 22 sub-metrics or 82% of the applicable benchmarks/analogues for all local 7 interconnection trunking measures having CLEC activity. The sub-metrics that 8 did not meet the benchmarks/retail analogues for May and June 2001 are as 9 follows:

10

11 FOC Timeliness / Local Interconnection Trunks / (C.1.3) (May)

BellSouth met the standard for 134 of the 144 (93.10%) ASRs received in this sub-metric for May 2001. The 95% benchmark set a requirement of 137 based on the quantity of orders for this sub-metric. Although BellSouth is within 2% of the benchmark for this measure, BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met or exceeded the benchmark for this sub-metric in June 2001.

18

19 % Missed Installation Appointments / Local Interconnection Trunks (C.2.5)

20 <u>(June)</u>

BellSouth missed 4 of the 47 scheduled appointments for this sub-metric in
June 2001. A detailed analysis of the four missed appointments did not
reveal any systemic issues for this sub-metric in June.

24

1 Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits / Non

2 <u>Dispatch (C.2.11.2.2) (June)</u>

BellSouth met the standard for 31 of the 33 orders reviewed in this sub-metric for June 2001. The 95% benchmark set a requirement of 32 based on the quantity of orders for this sub-metric. Although BellSouth is within one order of the benchmark for this measure, BellSouth continues to focus on this measurement in order to improve results to meet the benchmark.

8

9 Customer Trouble Report Rate / Local Interconnection Trunks / Non Dispatch

10 <u>(C.3.2.2) (May)</u>

11 BellSouth provided over 99.95% trouble free service for both retail and the 12 CLECs for this sub-metric for the month of May. When BellSouth provisions high quality service coupled with very large universe sizes, it can cause an 13 14 apparent out of equity condition from a quantitative viewpoint. In these 15 cases, there is very little variation and the universe size is so large that the Z-16 test becomes overly sensitive to any difference. In other words, the statistical 17 test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual performance for 18 both CLECs and its own retail operations is at a very high level - often 98% 19 20 or 99%. From a practical point of view, the CLECs' ability to compete has not 21 been hindered even though the statistical results may technically show that BellSouth failed to meet the benchmark/analogue. BellSouth met or 22 23 exceeded the retail analogue for this sub-metric in June 2001.

Maintenance Average Duration / Local Interconnection Trunks / Non Dispatch (C.3.3.2) (June)

There were a total of 16 trouble reports that averaged 1.52 hours per completion for this sub-metric in June 2001. One of the reports was a translation problem that required 22 hours to repair. The other 15 reports required an average of less than 8 minutes per report to fix. The retail analogue comparison averaged just over 28 minutes per report for June.

8

9 % Repeat Reports in 30 Days / Local Interconnection Trunks / Non Dispatch

10 (C.3.4.2) (May)

A data problem was identified for this sub-metric in May 2001. BellSouth met
or exceeded the retail analogue for this sub-metric in June 2001.

13

14 Invoice Accuracy – Interconnection (C.4.1.1) (June)

15 The CLECs experienced Local Interconnection invoice accuracy rates that 16 were slightly less than the invoices BellSouth sends to its customers during June 2001 (98.46% accuracy for BellSouth versus 94.29% for the CLEC 17 18 invoices). The difference in performance was the result of two adjustments issued to two CLECs in Florida. The first adjustment resulted from usage that 19 was being investigated for possible error conditions. A keying error was made 20 21 and the usage was included on the wrong account, which was subsequently 22 adjusted for the customer. The second situation involved a keying mistake on a billing transaction causing an inaccurate amount to be included on a bill for 23 24 a customer and subsequently corrected. 25

1 <u>Trunk Blockage</u>

2 BellSouth has developed a trunk blocking report that compares BellSouth 3 retail's trunk blockage rates to those of CLECs. The report, Trunk Group 4 Performance Report (TGP), Attachment 3A, displays trunk blocking in a 5 manner that accurately represents the customer experience. The TGP report 6 tabulates actual call blocking as a percentage of call attempts for all 7 comparable trunk groups administered by BellSouth that handle CLEC and 8 BellSouth traffic. Time consistent busy hour blocking data for each trunk 9 group is provided to each CLEC for its trunk groups. In order to ensure that 10 all possible trunks in the network were considered for inclusion and exclusion 11 in the trunk blocking comparison process. BellSouth has analyzed all trunks, 12 their roles in the network according to use and their interconnection 13 arrangements. Additionally, the TGP report provides a direct comparison of 14 hour-by-hour blocking between CLEC and BellSouth trunk groups. The Trunk 15 Group Categories included in the Blocking Comparison are as follows:

- 16
- 17 For Traffic Terminating at CLEC End Offices:
- Category 1 (BellSouth End-Office to BellSouth Access Tandem)
- Category 3 (BellSouth End-Office to CLEC Switch)
- Category 4 (BellSouth Local Tandem to CLEC Switch)
- Category 5 (BellSouth Access Tandem to CLEC Switch)
- Category 10 (BellSouth End-Office to BellSouth Local Tandem)
- Category 16 (BellSouth Inter-Tandem Trunk Groups)

1

2 For Traffic Terminating at BellSouth End Offices:

3

Category 9 (BellSouth End-Office to BellSouth End-Office)

4

5 BellSouth's approach ensures the inclusion of comparative data that will 6 permit a more complete comparative analysis. The new measurement 7 method provides direct and clear comparison of blocking levels for all relevant 8 trunk groups. The interim SQM for OSS Evaluation Version 3.0, approved by 9 this Commission on July 3, 2001, also describes how BellSouth derives and 10 calculates its performance data, including trunk blockage data. In addition, 11 Section C.5.1, TGP (Attachment 3A to this Exhibit) shows the actual blocking 12 percentages by hour. The Self Effectuating Enforcement Mechanism (SEEM) 13 Analogue/Benchmark for the Trunk Group Performance measure is any two 14 hour period in 24 hours where CLEC blockage exceeds BellSouth blockage 15 by more than 0.5%. BellSouth met or exceeded the benchmark for this sub-16 metric in May and June 2001.

17

18

C. CHECKLIST ITEM 2 – UNBUNDLED NETWORK ELEMENTS (UNE)

19

This section addresses the measures associated with UNEs under checklist item 2. Attachment 1A, Sections B1 – B3, provides data that is divided into Ordering, Provisioning and Maintenance & Repair operations. The Ordering function is disaggregated into 17 sub-metrics. The Provisioning function has 9 sub-metrics, and there are 12 sub-metrics for the Maintenance & Repair function. All Ordering measures will be included in this checklist item
because of the overall relationship of the mechanized, partially mechanized
and manual processing of Local Service Requests (LSRs). The Provisioning
and Maintenance & Repair measures for the following products are included
in the checklist item as shown below:

6	Product	Checklist Item:
7	Combo (Loop & Port)	#2 – Unbundled Network Elements
8	Combo (Other)	#2 – Unbundled Network Elements
9	Other Design	#2 – Unbundled Network Elements
10	Other Non-Design	#2 – Unbundled Network Elements
11	xDSL Loop	#4 – Unbundled Local Loops
12	UNE ISDN Loop	#4 – Unbundled Local Loops
13	Line Sharing	#4 - Unbundled Local Loops
14	2w Analog Loop Design	#4 – Unbundled Local Loops
15	2w Analog Loop Non Design	#4 – Unbundled Local Loops
16	2w Analog Loop w/INP Design	#4 – Unbundled Local Loops
17	2w Analog Loop w/INP Non Design	#4 – Unbundled Local Loops
18	2w Analog Loop w/LNP Design	#4 – Unbundled Local Loops
19	2w Analog Loop w/LNP Non Design	#4 – Unbundled Local Loops
20	Digital Loop < DS1	#4 - Unbundled Local Loops
21	Digital Loop => DS1	#4 – Unbundled Local Loops
22	Local Interoffice Transport	#5 – Unbundled Local Transport
23	Switch Ports	#6 – Unbundled Local Switching

.

1	INP Standalone #11 – Local Number Portability
2	LNP Standalone #11 – Local Number Portability
3	
4	An overall review of the UNE sub-metrics for Ordering, Provisioning,
5	Maintenance & Repair and Billing indicates that BellSouth met the
6	benchmark/analogue for 82% of the sub-metrics during the month of May and
7	74% in June 2001.
8	
9	1. UNE Ordering Measures
10	
11	Items B.1.1 - B.1.19 in Attachment 1A show data for Percent Rejected
12	Service Requests, Reject Interval, FOC Timeliness and FOC & Reject
13	Response Completeness. These reports are disaggregated by interface type
14	(electronic, partial electronic and manual), as well as product type.
15	
16	Percent Rejected Service Requests
17	Results for individual CLECs in this measure vary. Some CLECs have few
18	rejected service requests, while some CLECs have many. Of the CLECs
19	submitting LSRs, three of the five CLECs that submitted the largest volumes
20	of fully mechanized LSRs had rejection rates ranging from 2% to 5% in May
21	and June 2001.
22	

1 In order to lower the rejection rate for individual CLECs, BellSouth has 2 developed an action plan template to be used in conjunction with an analysis 3 of the pre-order and order activity of a CLEC who is performing at less than 4 90% on flow-through on mechanically submitted orders and has a clarification 5 rate of 20% or higher. So far, seven CLECs in the BellSouth region have 6 agreed to utilize this template. Five CLECs have had presentations 7 concerning their individual results and are currently reviewing the proposals. Meetings are being scheduled with two additional CLECs and twenty-two 8 9 others are either in the final stages of the action plan preparation or data analyzation. The initial results after implementation indicates a 5% overall 10 11 reduction in clarifications and rejected requests.

12

13 **Reject Interval**

14 Items B.1.4 - B.1.8 in Attachment 1A examine the Reject Interval for the 15 month of June 2001. For orders submitted electronically, the benchmark is 16 97% within one hour. In May, 74% of the rejected service requests were 17 delivered within the one-hour time period. In June 2001, 95% of the rejected 18 service requests were delivered within the one-hour benchmark. (See the 19 write-up below for further discussion concerning electronically submitted 20 orders.)

21

For partially mechanized orders, which are LSRs submitted electronically and requiring service representative intervention, the current benchmark is 85%

•

1	within 18 hours. In May, BellSouth exceeded this benchmark, with over 98%
2	of partially mechanized rejects being returned to the CLECs within the 18-
3	hour time period. In June 2001, BellSouth exceeded the benchmark, with
4	over 93% of the partially mechanized rejects being returned within the 18-
5	hour time period.
6	
7	For manual orders, the current benchmark is 85% within 24 hours. BellSouth
8	also exceeded this requirement, with over 96% of the LSRs submitted
9	manually being returned to the CLECs within the 24-hour time period in May
10	and 97% in June 2001.
11	
12	The following sub-metrics did not meet the established benchmarks in May
13	and/or June 2001:
14	
15	Reject Interval / Local Interoffice Transport / Electronic (B.1.4.2) (June)
16	Reject Interval / Combo (Loop & Port) / Electronic (B.1.4.3) (May/June)
17	Reject Interval / 2w Analog Loop Design / Electronic (B.1.4.8) (May/June)
18	Reject Interval / 2w Analog Loop w/INP Design / Electronic (B.1.4.10) (May)
19	Reject Interval / 2w Analog Loop w/LNP Design / Electronic (B.1.4.12) (June)
20	Reject Interval / 2w Analog Loop w/LNP Non-Design / Electronic (B.1.4.13)
21	(June)
22	<u>Reject Interval / Other Design / Electronic (B.1.4.14) (June)</u>
23	Reject Interval / Other Non-Design / Electronic (B.1.4.15) (May/June)

1 Reject Interval / LNP (Standalone) / Electronic (B.1.4.17) (May/June)

The current benchmark for these sub-metrics is >= 97% within one hour. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems.

7

8 Thus far, the analysis has determined that many of the LSRs that did not 9 meet the one-hour benchmark were issued between 11:00 p.m. and 4:30 a.m. 10 Between these hours the system is unable to process LSRs because of the 11 back-end legacy systems are out of service. Such hours should be excluded 12 from the measurement. BellSouth is currently reviewing the scheduled down 13 time for all systems and how that down time affects the ordering capability of 14 the CLECs.

15

With the implementation of May data BellSouth was directed to change the 16 time stamp identification for the start and complete times of the interval for 17 this measurement from the Local Exchange Ordering (LEO) System to the 18 CLEC ordering interface system (TAG or EDI). With this change BellSouth 19 was unable to identify multiple issues of the same version of the LSRs that 20 21 may be rejected (fatal rejects), which should be excluded from the measurement. If there are multiple issues of the same version, the measure 22 currently calculates the interval from the initial issue to the final issue of the 23

LSR returned to the CLEC, Reject or FOC. Consequently, BellSouth's performance level is inappropriately understated. An initial review indicated 41% of all mechanized rejected LSRs that did not meet the one hour benchmark were submitted after 11:00 p.m. BellSouth is currently working to determine a fix for this issue.

6

With the May update, the data for the UNE Loop & Port Combination is being
included in the UNE Other Non-Design sub-metric. BellSouth is currently
changing the programming to remove the UNE Loop & Port Combination from
the UNE Other Non-Design sub-metric and expects the update to be
complete with the release of August data.

12

13 Reject Interval / 2w Analog Loop w/LNP Design / Partially Electronic

14 (B.1.6.12) (June)

BellSouth met the benchmark for 276 of the 352 LSRs rejected in this submetric for June 2001. On June 2, 2001, an update was loaded in the LNP Gateway software. Due to problems associated with this release, it had to be removed on June 10, 2001. Basically, for the first 10 days of the month this sub-metric met very few of the LSRs that were rejected in 18 hours and almost all for the last 20 days.

- 21
- 22 Reject Interval / LNP (Standalone) / Partially Electronic (B.1.6.17) (June)

BellSouth met the benchmark for 812 of the 982 LSRs rejected in this submetric for June 2001. On June 2, 2001, an update was loaded in the LNP Gateway software. Due to problems associated with this release, it had to be removed on June 10, 2001. Basically, for the first 10 days of the month this sub-metric met very few of the LSRs that were rejected in 18 hours and almost all for the last 20 days.

7

8 FOC Timeliness

9 For LSRs submitted electronically, the benchmark is 95% of the FOCs 10 returned within 3 hours. For partially mechanized LSRs, the benchmark is 11 85% returned within 18 hours. For LSRs submitted manually, the benchmark 12 is 85% returned within 36 hours. In June 2001, BellSouth met the benchmark for 39,801 of the 41,273 (96%) LSRs that received an FOC. In May 2001, 13 14 BellSouth met the benchmark for 44,471 of the 45,368 (98%) LSRs that 15 received an FOC. The sub-metrics that did not meet the benchmark in May 16 and/or June 2001 are as follows:

17

18 FOC Timeliness / xDSL / Electronic (B.1.9.5) (May/June)

BellSouth met the benchmark for 137 of the 153 LSRs that received a FOC in May and 264 of 287 for this sub-metric in June 2001. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are

accessed by the ordering systems. For further information see the
 explanation included with the electronic reject interval measurement, item
 B.1.4.x.

4

5 FOC Timeliness / 2w Analog Loop w/LNP Design / Electronic (B.1.9.12) 6 (May/June)

7 BellSouth met the benchmark for 456 of the 575 LSRs in May and 57 of the 8 79 LSRs in June that received a FOC for this sub-metric. BellSouth is 9 conducting a detailed root cause analysis of the process for electronic 10 ordering. This analysis addresses the ordering systems (EDI, TAG, and 11 LENS) used by the CLECs and the back-end legacy applications, such as 12 SOCS, that are accessed by the ordering systems. For further information 13 see the explanation included with the electronic reject interval measurement, 14 item B.1.4.x.

15

16 <u>FOC Timeliness / 2w Analog Loop w/LNP Non Design / Electronic (B.1.9.13)</u> 17 (May)

BellSouth met the benchmark for 14 of the 90 LSRs for this sub-metric in May 2001. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems. For further information see the explanation included with the electronic reject interval

•

1	measurement, item B.1.4.x. BellSouth met or exceeded the benchmark for
2	this sub-metric in June 2001.
3	
4	FOC Timeliness / LNP (Standalone) / Electronic (B.1.9.17) (June)
5	BellSouth met the benchmark for 2,687 of the 3,173 LSRs confirmed in this
6	sub-metric for June 2001. On June 2, 2001, an update was loaded in the
7	LNP Gateway software. Due to problems associated with this release, it had
8	to be removed on June 10, 2001. Basically, for the first 10 days of the month
9	this sub-metric met very few of the LSRs that were confirmed in 18 hours and
10	almost all for the last 20 days.
11	
12	FOC Timeliness / xDSL / Partially Electronic (B.1.11.5) (May/June)
13	There were only nine orders for May and five in June 2001 in this sub-metric
14	with BellSouth meeting the benchmark for seven and four of them,
15	respectively. Such a small universe does not produce a statistically
16	conclusive benchmark comparison.
17	
18	FOC Timeliness / ISDN Loops / Partially Electronic (B.1.11.6) (June)
19	There were only four orders in June 2001 for this sub-metric with BellSouth
20	meeting the benchmark for three of them. Such a small universe does not
21	produce a statistically conclusive benchmark comparison.
22	

<u>FOC Timeliness / 2w_Analog Loop w/LNP_Design / Partially_Electronic</u> (B.1.11.12) (June)

BellSouth met the benchmark for 556 of the 703 LSRs confirmed in this submetric for June 2001. On June 2, 2001, an update was loaded in the LNP
Gateway software. Due to problems associated with this release, it had to be
removed on June 10, 2001. Basically, for the first 10 days of the month this
sub-metric met very few of the LSRs that were confirmed in 18 hours and
almost all for the last 20 days.

9

10 FOC & Reject Response Completeness

11 This measurement was introduced with the March 2001 data month. The 12 benchmark is 95%. In this sub-metric, BellSouth did not meet the benchmark 13 in May and/or June 2001 for the FOC and Reject Response Completeness 14 metrics listed below:

15

16 FOC & Reject Response Completeness / Local Interoffice Transport /

- 17 <u>Electronic (B.1.14.2) (May/June)</u>
- 18 FOC & Reject Response Completeness / Combo (Loop & Port) / Electronic
- 19 (B.1.14.3) (June)
- 20 FOC & Reject Response Completeness / xDSL / Electronic (B.1.14.5)
- 21 (May/June)
- 22 FOC & Reject Response Completeness / ISDN Loop / Electronic (B.1.14.6)
- 23 <u>(May)</u>

- 1 FOC & Reject Response Completeness / 2w Analog Loop Non Design /
- 2 <u>Electronic (B.1.14.9) (May/June)</u>
- 3 FOC & Reject Response Completeness / Other Design / Electronic
- 4 (B.1.14.14) (May/June)
- 5 FOC & Reject Response Completeness / Other Non-Design / Electronic
- 6 (B.1.14.15) (June)
- 7 FOC & Reject Response Completeness / xDSL / Partial Electronic (B.1.15.5)
- 8 <u>(May)</u>
- 9 FOC & Reject Response Completeness / Combo (Loop & Port) / Manual
- 10 (B.1.16.3) (May)
- 11 FOC & Reject Response Completeness / Line Sharing / Manual (B.1.16.7)
- 12 <u>(June)</u>
- 13 FOC & Reject Response Completeness / 2w Analog Loop Design / Manual
- 14 (B.1.16.8) (June)
- 15 FOC & Reject Response Completeness / 2w Analog Loop Non-Design /
- 16 Manual (B.1.16.9) (May/June)
- 17 FOC & Reject Response Completeness / 2w Analog Loop w/INP Design /
- 18 <u>Manual (B.1.16.10) (May)</u>
- 19 FOC & Reject Response Completeness / Other Non-Design / Manual
- 20 (B.1.16.15) (May)
- 21 FOC & Reject Response Completeness (Multiple Responses) / xDSL /
- 22 Electronic (B.1.17.5) (May)

- 1 FOC & Reject Response Completeness (Multiple Responses) / Local
- 2 Interoffice Transport / Partial Electronic (B.1.18.2) (May/June)
- 3 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
- 4 <u>& Port) / Partial Electronic (B.1.18.3) (May/June)</u>
- 5 FOC & Reject Response Completeness (Multiple Responses) / xDSL / Partial
- 6 Electronic (B.1.18.5) (May)
- 7 FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop /
- 8 Partial Electronic (B.1.18.6) (May)
- 9 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 10 Loop Non Design / Partial Electronic (B.1.18.9) (May/June)
- 11 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 12 / Partial Electronic (B.1.18.14) (May/June)
- 13 FOC & Reject Response Completeness (Multiple Responses) / Other Non-
- 14 Design / Partial Electronic (B.1.18.15) (May/June)
- 15 FOC & Reject Response Completeness (Multiple Responses) / Local
- 16 Interoffice Transport / Manual (B.1.19.2) (May/June)
- 17 FOC & Reject Response Completeness (Multiple Responses) / Combo
- 18 (Loop&Port) / Manual (B.1.19.3) (May)
- 19 FOC & Reject Response Completeness (Multiple Responses) / xDSL /
- 20 Manual (B.1.19.5) (May/June)
- 21 FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop /
- 22 Manual (B.1.19.6) (May/June)

- 1 FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
- 2 Manual (B.1.19.7) (June)
- 3 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 4 Loop Design / Manual (B.1.19.8) (May/June)
- 5 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 6 Loop Non Design / Manual (B.1.19.9) (May/June)
- 7 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 8 Loop w/INP Design / Manual (B.1.19.10) (May/June)
- 9 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 10 / Manual (B.1.19.14) (May/June)
- 11 FOC & Reject Response Completeness (Multiple Responses) / Other Non
- 12 Design / Manual (B.1.19.15) (May)

BellSouth has determined that the coding for the FOC and Reject Completeness measures failed to include rejections that were classified as "auto clarifications." This coding change will impact all FOC and Reject Completeness measures that include auto clarification rejects. The code for this measurement is being rewritten and is projected to be included with the August data, available at the end of September. BellSouth continues to review this measurement in order to improve results to meet the benchmark.

20

21 Flow-Through

Attachment 1A, Items F.1.1 - F.1.3, shows Flow-Through data disaggregated by customer type and for the Summary/Aggregate. Detailed flow-through results for individual CLECs are included in Attachment 2A. The following table shows the Regional Flow-Through results for May and June 2001 as compared with the Interim SQM benchmarks.

- 6
- 7 <u>% Flow-through Service Requests (F.1.1.1 F.1.3.4)</u>
- 8

Customer Type	<u>May 2001</u>	<u>June 2001</u>	<u>Benchmark</u>
Residence	90.25%	92.21%	95%
Business	61.15%	57.26%	90%
UNE	74.80%	78.33%	85%
LNP	90.65%	91.83%	85%

9

10 The table above excludes those LSRs designed to "fall out" for manual 11 handling. Business flow-through rate is well below the 90% objective. 12 Business LSRs are more complex than the typical LSRs and, as a result, 13 there is a greater probability for error. For example, an LSR requesting 10 14 lines with series completion hunting that are located over multiple floors and 15 have a variation of features on the lines presents many more opportunities for 16 system mismatches than one that adds just lines and features.

17

1 BellSouth's flow-through rates will continue to improve. BellSouth has formed 2 a joint BellSouth/CLEC Flow-Through Improvement Task Force to specifically 3 address this issue. The Task Force will operate as a subcommittee of the existing Change Control Process. The first meeting was held on February 28, 4 5 2001. The objective of the Task Force is to work jointly to identify potential 6 enhancements to electronic order flow-through, document those 7 enhancements, and develop an implementation schedule. Fifteen CLECs 8 and BellSouth were represented at the initial meeting.

9

On March 19, 2001, the Flow-Through Improvement Task Force met at the BellSouth Conference Center (BSCC). Fourteen CLECs and BellSouth were represented. The Task Force agreed upon a definition for flow-through for purposes of the Task Force. In addition, the Task Force discussed further the role of the Task Force and status of the existing flow-through changes. BellSouth expects the work of the Task Force to improve the process of flowthrough.

17

The Flow-Through Task Force met on May 24, 2001, with agreement being reached to identify specific areas of concentration for the team. All attendees agreed that the Task Force would be better focused on the areas it was created to examine with this identification. The team prioritized eight items that had previously been identified. Action items were assigned with followup meetings to be scheduled based on status of the prioritized items.

1 BellSouth has established а Flow-Through Improvement Program 2 Management process that includes seven different internal organizations. 3 Ongoing analysis is being done to determine trends and identify flow-through 4 problems. To date, fifteen system enhancements have been identified and 5 are targeted for Encore releases. These releases are being implemented in 6 July and August 2001.

7

8 2. UNE Provisioning Measures

9 BellSouth met 81% of the overall UNE Provisioning measurements in the
10 month of May and 73% in June 2001.

11

12 The following sub-metrics did not meet the applicable retail analogues in the 13 month of May and/or June 2001:

14

15 Order Completion Interval / Combo (Loop & Port) / < 10 Circuits / Non

16 <u>Dispatch (B.2.1.3.1.2) (June)</u>

A root cause analysis for OCI for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same interval as "dispatched" orders. On June 2, 2001, a release was added to the due date calculator software to correct this error. However, due to problems with the software load, it had to be removed. A temporary fix was installed at the end of June, until the final update can be added. In addition to the

.

1	appointment interval issue, OCI is adversely affected by LSRs for which
2	CLECs request intervals beyond the offered interval. When a CLEC requests
3	an interval beyond the available interval offered by BellSouth, an "L" code is
4	entered on the Service Order generated by BellSouth. "L" coded orders are
5	excluded from the OCI metrics.
6	
7	Order Completion Interval / Other Non-Design / < 10 Circuits / Non Dispatch
8	(B.2.1.15.1.2) (June)
9	There were only a total of five orders completed in this sub-metric in June
10	2001. This small universe does not provide a statistically conclusive
11	comparison with the retail analogue.
12	
13	% Jeopardy Notice Interval >= 48 hours / Combo (Loop & Port) / < 10
14	Circuits (B.2.10.3) (May/June)
15	The calculations for this measure have been determined to be incorrect. The
16	coding change in the Service Order Control System (SOCS) is currently
17	scheduled for a September 13, 2001, system load date. Based on this
18	schedule, the October data month will be the first full month that the change
19	will be in effect.
20	
21	% Missed Installation Appointments / Combo (Loop & Port) / < 10 Circuits /
22	Non Dispatch (B.2.18.3.1.2) (May/June)
1 BellSouth missed 25 of the 10,487 scheduled appointments in this sub-metric 2 for May and 28 of the 10,251 appointments for June 2001. BellSouth met over 3 99% of the scheduled appointments for both retail and the CLECs in this sub-4 metric for the May and June. When BellSouth provisions high quality service 5 coupled with very large universe sizes, it can cause an apparent out of equity condition from a quantitative viewpoint. In these cases, there is very little 6 variation and the universe size is so large that the Z-test becomes overly 7 8 sensitive to any difference. In other words, the statistical test shows that the measurement does not meet the fixed critical value when compared with the 9 10 retail analogue, but BellSouth's actual performance for both CLECs and its own retail operations is at a very high level - often 98% or 99%. From a 11 12 practical point of view, the CLECs' ability to compete has not been hindered even though the statistical results may technically show that BellSouth failed 13 14 to meet the benchmark/analogue.

15

16 <u>% Missed Installation Appointments / Combo (Loop & Port) / >= 10 Circuits /</u>
 17 Dispatch (B.2.18.3.2.1) (June)

BellSouth missed 3 of the 14 appointments for this sub-metric in June 2001.
The detailed analysis did not indicate any systemic problems with the three
missed appointments for this sub-metric in June.

21

<u>% Missed Installation Appointments / Other Non-Design / < 10 Circuits / Non</u>
 Dispatch (B.2.18.15.1.2) (June)

.

1	BellSouth missed 2 of the 12 appointments for this sub-metric in June 2001.
2	The detailed analysis did not indicate any systemic problems with the two
3	missed appointments for this sub-metric in June.
4	
5	% Provisioning Troubles w/I 30 Days / Combo (Loop & Port) / < 10 Circuits /
6	Dispatch (B.2.19.3.1.1) (June)
7	BellSouth is currently analyzing the data for this sub-metric. The extremely
8	high number of troubles indicated does not match the overall report rates for
9	June.
10	
11	% Provisioning Troubles w/I 30 Days / Combo (Loop & Port) / >= 10 Circuits /
12	<u>Dispatch (B.2.19.3.2.1) (May)</u>
13	There were four troubles reported for the thirteen orders that completed in the
14	30 days prior to May 2001 for this sub-metric. No systemic problems were
15	identified for this small number of orders in May. BellSouth met or exceeded
16	the retail analogue for this sub-metric in June 2001.
17	
18	% Provisioning Troubles w/I 30 Days / Other Design / < 10 Circuits / Dispatch
19	(B.2.19.14.1.1) (June)
20	There were seven troubles reported for the thirty-four orders that completed in
21	the 30 days prior to June 2001 for this sub-metric. No systemic problems
22	were identified for the seven reports received in June for this sub-metric.

- 1 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits /
- 2 <u>Dispatch (B.2.21.3.1.1) (May/June)</u>
- 3 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits /
- 4 Non-Dispatch (B.2.21.3.1.2) (May/June)
- 5 Average Completion Notice Interval / Combo (Loop & Port) / >= 10 Circuits /
- 6 Dispatch (B.2.21.3.2.1) (May/June)

7 The root cause analysis of these measures indicated that the only differences 8 between the performance between BellSouth retail and CLECs are the 9 mismatches found when the orders are compared with the original LSRs. 10 The start of the completion interval is the point at which the technician 11 completes the order, and the interval ends when the completion notice is 12 sent. Any change to a name, number of items, etc., occurring during the 13 provisioning process will generate inconsistencies with the original LSRs that 14 must be resolved before a final completion notice can be sent. Any time to 15 resolve these inconsistencies with the original LSRs is included in the 16 Because of numerous CLEC changes and order updates, average. 17 mismatches on CLECs orders exceed those for BellSouth retail orders. Combining this with the smaller base for the CLECs' measurement raises the 18 19 average, which results in a miss. Specific Service Representatives within the 20 Work Management Centers have been assigned to resolve any completion 21 issues that are required. Providing specific training and dedicating personnel 22 to this task should reduce the difference between the CLEC and retail 23 analogue results.

1	Service Order Accuracy / Design (Specials) / < 10 Circuits / Non Dispatch
2	(B.2.34.1.1.2) (June)
3	BellSouth met the standard for 40 of the 48 orders reviewed in this sub-metric
4	for June 2001. The 95% benchmark set a requirement of 46 based on the
5	quantity of orders for this sub-metric. BellSouth continues to focus on this
6	measurement in order to improve results to meet the benchmark.
7	
8	Service Order Accuracy / Loops Non-Design / < 10 Circuits / Dispatch
9	(B.2.34.2.1.1) (May)
10	BellSouth met the standard for 11 of the 12 orders reviewed in this sub-metric
11	for May 2001. The 95% benchmark set a requirement of 12 based on the
12	quantity of orders for this sub-metric. Although BellSouth was within one order
13	of the benchmark for this measure, BellSouth continues to focus on this
14	measurement in order to improve results to meet the benchmark. BellSouth
15	met or exceeded the benchmark for this sub-metric in June 2001.
16	
17	Service Order Accuracy / Loops Non-Design / < 10 Circuits / Non-Dispatch
18	(B.2.34.2.1.2) (May)
19	BellSouth met the standard for 168 of the 186 orders reviewed in this sub-
20	metric for May 2001. The 95% benchmark set a requirement of 177 based on
21	the quantity of orders for this sub-metric. BellSouth continues to focus on this
22	measurement in order to improve results to meet the benchmark. BellSouth
23	met or exceeded the benchmark for this sub-metric in June 2001.

2	(B.2.34.2.2.1) (May)
3	There were only two observations in this sub-metric for May 2001. Such a
4	small universe does not produce a statistically conclusive benchmark
5	comparison. BellSouth met or exceeded the benchmark for this sub-metric in
6	June 2001.
7	
8	Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Non-Dispatch
9	(B.2.34.2.2.2) (May/June)
10	BellSouth met the standard for 14 of the 20 orders reviewed in this sub-metric
11	for May and 9 of 17 orders in June 2001. The 95% benchmark set a
12	requirement of 19 and 16 in May and June, respectively based on the quantity
13	of orders for this sub-metric. BellSouth continues to focus on this
14	measurement in order to improve results to meet the benchmark.
15	
16	3. UNE Maintenance and Repair (M&R) Measures
17	BellSouth met the applicable performance standard for 79% in May and 75%
18	in June 2001 of the overall UNE M&R measurements. The sub-metrics that
19	did not meet the fixed critical value for this checklist item in May and/or June
20	are as follows:
21	

Service_Order_Accuracy / Loops_Non-Design / >= 10 Circuits / Dispatch

1

% Missed Repair Appointments / Other Design / Dispatch (B.3.1.10.1) (June) 22

•

1	BellSouth missed 19 of the 269 repair appointments scheduled for this sub-
2	metric in June 2001. No systemic problems were identified for the 19
3	appointments missed in June.
4	
5	% Missed Repair Appointments / Other Design / Non Dispatch (B.3.1.10.2)
6	(June)
7	BellSouth missed 5 of the 118 repair appointments scheduled for this sub-
8	metric in June 2001. No systemic problems were identified for the 5
9	appointments missed in June.
10	
11	% Missed Repair Appointments / Other Non-Design/ Non Dispatch
12	(B.3.1.11.2) (May)
13	BellSouth missed 4 of the 67 repair appointments scheduled for this sub-
14	metric in May 2001. No systemic problems were identified for the four
15	appointments missed in May. BellSouth met or exceeded the retail analogue
16	for this sub-metric in June 2001.
17	
18	Customer Trouble Report Rate / Other Design / Dispatch (B.3.2.10.1)
19	(May/June)
20	The difference between the retail analogue and the CLEC aggregate was less
21	than 3% for this sub-metric in May and June 2001. Both the CLECs and
22	BellSouth retail had greater than 97% trouble free service for all in service
23	lines in this sub-metric in May and June.

.

1	
2	Customer Trouble Report Rate / Other Design / Non Dispatch (B.3.2.10.2)
3	(May/June)
4	The difference between the retail analogue and the CLEC aggregate was less
5	than 1% for this sub-metric in May and June 2001. Both the CLECs and
6	BellSouth retail had greater than 98% trouble free service for all in service
7	lines in this sub-metric in May and June.
8	
9	Customer Trouble Report Rate / Other Non Design / Dispatch (B.3.2.11.1)
10	(May/June)
11	There were a total of 48 troubles reported for the 688 in service lines for this
12	sub-metric in May and 58 troubles reports for the 697 in service lines in June
13	2001. A preliminary analysis indicated that 17% of the troubles were closed
14	out as found OK. Further analysis is underway to determine any systemic
15	issues with this sub-metric.
16	
17	Customer Trouble Report Rate / Other Non Design / Non Dispatch
18	(B.3.2.11.2) (May/June)
19	There were a total of 67 troubles reported for the 688 in service lines for this
20	sub-metric in May and 57 troubles reports for the 697 in service lines in June
21	2001. A preliminary analysis indicated that 48% of the troubles were closed
22	out as found OK or approximately half of the troubles reported had minimal

1	impact	on	the	end-user	customer.	Further	analysis	is	underway	to
2	determi	ne a	any s	ystemic is:	sues with this	sub-metric.				

3

<u>% Repeat Reports in 30 Days / Combo (Loop&Port) / Non Dispatch</u> (B.3.4.3.2) (May/June)

6 There were a total of 898 trouble reports of which 379 were repeats in this 7 sub-metric for May 2001. A detailed analysis has identified 268 of the 379 8 repeats to be from the third party test CLEC. Also, 337 of the 379 repeat 9 reports were closed as Test OK / Found OK or approximately 90% of the troubles had minimal impact on the end-user customer. In June, there were a 10 11 total of 938 trouble reports of which 231 were repeats. A detailed analysis has identified 108 of the 231 repeats to be from the third party test CLEC. 12 13 Also, 189 of the 231 repeat reports were closed as Test OK / Found OK or approximately 82% of the troubles had minimal impact on the end-user 14 customer. The exclusion of the third party tests reports from this sub-metric 15 16 would meet or exceed the retail analogue for May and June.

17

18 Out of Service > 24 Hours / Other Design / Dispatch (B.3.5.10.1) (June)

19 of the 269 repair appointments scheduled for this sub-metric in June 2001
20 were out of service longer than 24 hours. No systemic problems were
21 identified for the 19 appointments in June.

22

23 Out of Service > 24 hours / Other Design / Non Dispatch (B.3.5.10.2) (June)

1	BellSouth missed 5 of the 118 repair appointments scheduled for this sub-						
2	metric in June 2001 were out of service longer than 24 hours. No systemic						
3	problems were identified for the 5 appointments in June.						
4							
5	Invoice Accuracy – UNE (B.4.1.1)						
6	The CLECs experienced UNE invoice accuracy rates that were slightly lower						
7	than the invoices BellSouth sends to its customers during June 2001 (98.46%						
8	accuracy for BellSouth versus 89.32% for the CLEC invoices). The difference						
9	in performance was the result of a single adjustment for one CLEC caused by						
10	an inaccurate rate being used for one type of unbundled switch port. The						
11 12	incorrect rate has been changed and the problem has been resolved.						
13	4. Other UNE Measures						
14							
15	Pre-Ordering						
16	Service Inquiry for xDSL loops (F.3.1.1), Loop Makeup Manual (F.2.1.1) and						
17	Loop Makeup Electronic (F.2.2.1) are included in the Pre-Ordering						
18	measurements. All measures met the established benchmarks for May 2001.						
19	The two of the sub-metrics did not meet the benchmarks in June 2001 are as						
20	follows:						

- 21
- 22 Loop Makeup Inquiry (Manual) (F.2.1.1) (June)

.

1	BellSouth met 129 of the 136 inquiries within the 3 business day benchmark
2	in June 2001 or 94.85%. Normal rounding would indicate that this quantity
3	met the 95% benchmark.
4	
5	Service Inquiry with Firm Order / xDSL (F.3.1.1) (June)
6	BellSouth met 218 of the 234 inquiries within the 5 business day benchmark
7	in June 2001. The 95% benchmark for this quantity of orders requires 222 of
8	them to be met. BellSouth continues to focus on this measurement in order to
9	improve results to meet the benchmark.
10	
11	The remainder of the UNE measurements for which BellSouth did not meet
12	the applicable analogue or benchmark in May and/or June 2001 is as follows:
13	
14	Operations Support Systems
15	The OSS/Preordering measures for which BellSouth did not meet the
16	benchmark/retail analogue in May and June 2001 were:
17	
18	Average Response Interval - CLEC (LENS) / HAL / CRIS / Region / RNS
19	(D.1.3.5.1) (May/June)
20	Average Response Interval - CLEC (LENS) / HAL / CRIS / Region / ROS
21	(D.1.3.5.2) (May/June)

Detailed analysis has identified a problem in the LENS software that deals
 with response times from HAL/CRIS. This was corrected in an update on July
 28, 2001.

4

5

Average Response Interval / CRIS / Region (D.2.4.1.1) (May/June)

6 The average response interval for this sub-metric is measured in three 7 separate disaggregations. The percentage of gueries that are responded to 8 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 9 The average response interval for the CLEC requests did not meet the retail 10 analogue intervals for the less than 4-second disaggregation but exceeded 11 both the less than 10 and greater than 10 seconds responses. In May, the 12 CLEC response interval was 94.25% within 4 seconds as compared with 13 95.65% for the retail analogue. For the less than 10 second response, the CLECs received 99.03% of their responses and the retail analogue received 14 15 98.82%. In June 2001, the response interval was 94.76% within 4 seconds compared with 95.81% for the retail analogue. For the less than 10 second 16 response, the CLECs received 99.13% of their responses and the retail 17 18 analogue received 98.89%. The one percent difference for both of these intervals indicates equivalent service levels for the CLECs and BellSouth 19 20 retail.

- 21
- 22 Average Response Interval / DLETH / Region (D.2.4.2.1) (June)

1 The average response interval for this sub-metric is measured in three 2 separate intervals. The percentage of queries that are responded to in less 3 than 4 seconds, less than 10 seconds and greater than 10 seconds. In June 2001, the average response interval for the CLEC requests did not meet the 4 retail analogue intervals for the less than 4-second disaggregation but 5 6 exceeded both the less than 10 and greater than 10 seconds responses. 7 Average Response Interval / LMOSupd / <= 4 sec. / Region (D.2.4.5.1) 8 9 (May/June) Average Response Interval / LMOSupd / <= 10 sec. / Region (D.2.4.5.2) 10 11 (May/June) Average Response Interval / LMOSupd / > 10 sec. / Region (D.2.4.5.3) 12 13 (May/June) The average response interval for this sub-metric is measured in three 14 separate disaggregations. The percentage of gueries that are responded to 15 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 16 The average response interval for the CLEC requests did not meet the retail 17 analogue intervals for all three of these sub-metrics in May and June 2001. 18 For each of the three sub-metrics, there was less than a 1% difference in the 19 responses received by the CLECs and BellSouth retail. The one percent 20 21 difference for all of these intervals indicates equivalent service levels for both 22 the CLECs and BellSouth retail.

23

1 Average Response Interval / LNP / <= 4 sec. / Region (D.2.4.6.1) (Mav/June) 2 The average response interval for this sub-metric is measured in three 3 separate disaggregations. The percentage of queries that are responded to 4 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 5 The average response interval for the CLEC requests did not meet the retail 6 analogue intervals for the less than 4-second disaggregation but exceeded 7 both the less than 10 and greater than 10 seconds responses. In May 2001, the CLEC response interval was 99.28% within 4 seconds as compared with 8 99.62% for the retail analogue. For the less than 10 second response, the 9 10 CLECs received 99.84% of their responses and the retail analogue received 99.84%. In June 2001, the CLEC response interval was 98.78% within 4 11 12 seconds as compared with 99.35% for the retail analogue. For the less than 10 second response, the CLECs received 99.67% of their responses and the 13 14 retail analogue received 99.67%. The less than one-half percent difference 15 for these intervals indicates equivalent service levels for the CLECs and 16 BellSouth retail.

17

18 General - Change Management

19 % Software Release Notices sent on time (F.10.1) (May)

There were only four releases in this sub-metric for May 2001 with BellSouth meeting the benchmark for three of them. BellSouth missed one release for this sub-metric in May. All personnel with posting responsibility for these notices have been advised of the need to make sure that they meet the 30-

- day requirement of this measure. BellSouth met or exceeded the benchmark
 for this sub-metric in June 2001.
- 3

4 General – Billing

5 Usage Data Delivery Accuracy (F. 9.1) (May)

6 This measure compares the rate at which usage data is sent accurately to 7 CLECs with the same measure for the BellSouth retail analogue. In May 8 2001, a software problem caused an error for one CLEC which dropped the 9 results to 99.99% compared to BellSouth's 100%. Out of approximately 14,000 packs (or groupings) of usage data sent to CLECs in May only one of 10 11 the packs was impacted by the problem. Once the software was fixed, the 12 corrected pack of data was resent to the CLEC. BellSouth met or exceeded 13 the retail analogue for this sub-metric in June 2001.

14

15 Mean Time to Deliver Usage (F.9.4) (May)

This measure compares the average number of days to deliver usage to CLECs with the BellSouth retail analogue. In May, 2001 the CLEC result was 3.76 days compared to BellSouth's 3.73 days. While the CLEC measurement is slightly greater than the BellSouth results, the CLECs are provided with substantially the same opportunity to bill end users as is BellSouth. BellSouth met or exceeded the retail analogue for this sub-metric in June 2001.

- 22
- 23 Recurring Charge Completeness / Interconnection (F.9.5.3) (June)

- 1 Non-Recurring Charge Completeness / Interconnection (F.9.6.3) (June)
- 2 This is the first month for these two sub-metrics. **BellSouth is currently** 3 investigating the data associated with these sub-metrics.
- 4
- 5 General New Business Requests
- 6 <u>% Quotes Provided in 10 Business Days (F.11.2.1) (June)</u>

In June 2001, there were only two requests in sub-metric F.11.2.1. Such a
small universe does not provide a statistically conclusive benchmark
comparison.

10

11 <u>% Quotes Provided Within 60 Business Days (F.11.2.3) (May)</u>

12 The MSS for this item indicates that there were a total of 13 requests for this 13 sub-metric in June 2001 and that one of the 13 requests met the 60 day 14 interval. This was a reporting error in that there were a total of thirteen 15 requests for all intervals, 10, 30 and 60 days. Only one of the requests was in 16 the 60 day interval sub-metric and it was returned in 26 days, thus meeting 17 the benchmark. The results should have indicated one quote with 100% 18 returned on time for this sub-metric, not 13 quotes with 1 returned on time. 19 This has been corrected on a going-forward basis. BellSouth met or 20 exceeded this sub-metric in June 2001.

21

22 General – Ordering

23 <u>% Acknowledgement Message Timeliness / EDI (F.12.1.1) (May)</u>

1 A root cause analysis has identified 8.856 of 10.010 (88%) failed EDI 2 acknowledgements were submitted by the Florida Third Party Test (3PT) 3 CLEC and should have been filtered out of the acknowledgement 4 calculations. During the setup for the 3PT volume tests, a problem was 5 encountered in the EDI system. Since the setup had to be redone, all of the 6 acknowledgements that had been generated for the test were eliminated. With the removal of these test messages, the results would have been 98.8%, 7 8 well above the 90% benchmark for this sub-metric in May 2001. BellSouth 9 met or exceeded the benchmark comparison for this sub-metric in June 2001.

10

% Acknowledgement Message Completeness / EDI (F.12.2.1) (May/June) 11

12 BellSouth experienced EDI outages in May and June that caused less than 3% of the acknowledgement messages to not be returned. A Stability Plan to 13 14 improve EDI availability has been put into effect. This plan includes implementing both a manual application monitoring schedule (24 / 7) and 15 increased mechanized application alarms to more adequately monitor and 16 react to application outages. The database parameters have also been 17 adjusted to allow for maximum processing in the EDI system. 18

- 19
- 20

% Acknowledgement Message Completeness / TAG (F.12.2.2) (May/June)

BellSouth failed to deliver 16 of the 183,966 messages in May 2001 and 51 of 21 the 127,390 messages in June for this sub-metric. Analysis continues to 22

.

1	identify any issues in this process. However, such a small number of failed
2	records have not revealed any systemic process problems
3	
4	D. CHECKLIST ITEM 4 – UNBUNDLED LOCAL LOOPS
5	As discussed in Checklist Item 2, Sections B.2 and B.3 of Attachment 1A
6	provide data for provisioning and maintenance & repair measures for
7	unbundled local loops.
8	
9	For purposes of discussion in this checklist item, the local loop sub-metrics
10	have been separated into two mode-of-entry groups, xDSL and
11	SL1/SL2/Digital. The xDSL group includes xDSL (ADSL, HDSL, UCL), ISDN
12	and Line Sharing sub-metrics. The SL1/SL2/Digital group includes the design
13	and non-design 2-wire analog loops, as well as the 2-wire and 4-wire digital
14	loop sub-metrics.
15	
16	xDSL Group
17	
18	1. Provisioning Measures
19	The xDSL group sub-metrics that did not meet the fixed critical value
20	comparison requirements for May and/or June 2001 are as follows:
21	
22	OCI / xDSL w/o conditioning / < 6 Circuits / Dispatch (B.2.2.2) (May)

There wee a total of 239 orders completed for this sub-metric in May 2001 1 2 that averaged 7.18 days. The benchmark is 7.0 days. A detailed analysis 3 revealed that the CLECs requested extended intervals on 18 orders that should have been excluded from the measure. Also, there were 8 orders that 4 were extended due to customer missed appointments and should have been 5 excluded. The exclusion of these 26 orders would have resulted in a 6.90 6 day average, thus meeting the 7.0 day benchmark. BellSouth met or 7 8 exceeded the retail analogue for this sub-metric in June 2001.

9

10 Order Completion Interval / Line Sharing / < 6 Circuits / Non Dispatch

11 (B.2.1.7.3.2) (June)

A root cause analysis for OCI for Non-Dispatch orders revealed that 12 13 BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same 14 interval as "dispatched" orders. On June 2, 2001, a release was added to the 15 due date calculator software to correct this error. However, due to problems 16 with the software load, it had to be removed. A temporary fix was installed at 17 the end of June, until the final update can be added. In addition to the 18 appointment interval issue, OCI is adversely affected by LSRs for which 19 CLECs request intervals beyond the offered interval. When a CLEC requests 20 an interval beyond the available interval offered by BellSouth, an "L" code is 21 entered on the Service Order generated by BellSouth. "L" coded orders are 22 23 excluded from the OCI metrics.

.

1	<u>% Jeopardies – Mechanized / ISDN Loops (B.2.5.6) (June)</u>
2	There were 88 jeopardies issued for the 250 orders issued in this sub-metric
3	in June 2001. All of these were resolved prior to the due date and the
4	scheduled installations were completed on time.
5	
6	% Missed Installation Appointments / ISDN Loops / < 10 Circuits / Dispatch
7	(B.2.18.6.1.1) (May)
8	There were a total of 58 missed appointments for the 527 scheduled in this
9	sub-metric in May 2001. Thirty-three of the missed appointments were due to
10	a lack of cable facilities. The Work Management Center has implemented a
11	new monitoring system that will allow for a more proactive approach to
12	resolving facility issues. BellSouth met or exceeded the retail analogue for
13	this sub-metric in June 2001.
14	
15	<u>% Missed Installation Appointments / Line Sharing / < 10 Circuits / Non</u>
16	Dispatch (B.2.18.7.1.2) (June)
17	There was only one missed appointment for the 57 scheduled orders in this
18	sub-metric in June 2001. There was no systemic problem identified for the
19	one missed appointment.
20	
21	<u>% Provisioning Troubles within 30 Days / ISDN Loops / < 10 Circuits /</u>
22	Dispatch (B.2.19.6.1.1) (June)

1	There were a total of 55 troubles reported for this sub-metric for the orders
2	that completed in the 30 days prior to June 2001. BellSouth is currently
3	investigating this sub-metric.

- 4
- 5

2. Maintenance & Repair Measures

6 The xDSL group sub-metrics that did not meet the fixed critical value 7 comparison requirements for May and/or June 2001 are as follows:

8

9 % Missed Repair Appointments / xDSL / Non Dispatch (B.3.1.5.2) (May)

10 BellSouth missed one of the twelve scheduled appointments for this sub-11 metric in May 2001. There was no systemic problem found for the missed 12 appointment. BellSouth met or exceeded the retail analogue for this sub-13 metric in June 2001.

14

15 <u>% Missed Repair Appointments / ISDN Loops / Non Dispatch (B.3.1.6.2)</u> 16 (May/June)

BellSouth missed one of the twenty-six scheduled appointments for this submetric in May and three of the twenty-nine in June 2001. There was no
systemic problem found for the missed appointments in May or June.

20

21 % Missed Repair Appointments / Line Sharing / Dispatch (B.3.1.7.1) (June)

.

1	There were only two scheduled appointments for this sub-metric in June
2	2001. Such a small universe does not provide a statistically conclusive
3	comparison with the retail analogue.
4	
5	% Missed Repair Appointments / Line Sharing / Non Dispatch (B.3.1.7.2)
6	(May/June)
7	BellSouth missed one of the twelve scheduled appointments for this sub-
8	metric in May and seven of twenty-eight in June 2001. There was no
9	systemic problem found for the missed appointments.
10	
11	Customer Trouble Report Rate / xDSL Loops / Dispatch (B.3.2.5.1)
12	(May/June)
13	A total of 62 troubles were reported for the 5,870 in service lines for this sub-
14	metric in May and 84 troubles for the 5,674 in service lines in June 2001.
15	Both the CLECs and BellSouth retail had 99% trouble free service for all in
16	service lines in this sub-metric in May and 98% in June.
17	
18	Customer Trouble Report Rate / xDSL Loops / Non Dispatch (B.3.2.5.2)
19	(June)
20	A total of 23 troubles were reported for the 5,674 in service lines for this sub-
21	metric in June 2001. Both the CLECs and BellSouth retail had 99% trouble
22	free service for all in service lines in this sub-metric in June.
23	

•

1	Customer Trouble Report Rate / ISDN Loops / Dispatch (B.3.2.6.1)
2	(May/June)
3	There were a total of 32 troubles reported for the 2,803 in service lines for this
4	sub-metric in May and 50 troubles for the 2,489 in service lines in June 2001.
5	Both the CLECs and BellSouth retail had 99% trouble free service for all in
6	service lines in this sub-metric in May and 98% trouble free service in June.
7	
8	Customer Trouble Report Rate / ISDN Loops / Non Dispatch (B.3.2.6.2)
9	(May)
10	There were a total of 26 troubles reported for the 2,803 in service lines for this
11	sub-metric in May 2001. Both the CLECs and BellSouth retail had greater
12	than 99% trouble free service for all in service lines in this sub-metric in May.
13	BellSouth met or exceeded the retail analogue for this sub-metric in June
14	2001.
15	
16	Customer Trouble Report Rate / Line Sharing / Non Dispatch (B.3.2.7.2)
17	(May/June)
18	There were a total of 12 troubles reported for the 747 in service lines for this
19	sub-metric in May and 28 troubles for the 807 in service lines in June 2001.
20	Both the CLECs and BellSouth retail had greater than 98% trouble free
21	service for all in service lines in this sub-metric in May and 97% in June.
22	

•

1	Maintenance Average Duration / ISDN Loops / Non Dispatch (B.3.3.6.2)
2	(May/June)
3	There were a total of 26 troubles reported for this sub-metric in May and 29
4	troubles in June 2001. There was no systemic problem identified in this sub-
5	metric for May or June.
6	
7	% Repeat Reports in 30 Days / ISDN Loops / Non Dispatch (B.3.4.6.2) (May)
8	Six of the twenty-six reports filed in this sub-metric in May 2001 were repeat
9	reports in the past 30 days. No systemic problems were identified in any of
10	these issues. BellSouth met or exceeded the retail analogue for this sub-
11	metric in June 2001.
12	
13	% Repeat Reports in 30 Days / Line Sharing / Non Dispatch (B.3.4.7.2)
14	(June)
15	Sixteen of the twenty-eight reports filed in this sub-metric in June 2001 were
16	repeat reports in the past 30 days. No systemic problems were identified in
17	any of these issues.
18	
19	<u>% Out of Service > 24 hours / xDSL / Non dispatch (B.3.5.5.2) (May)</u>
20	There was only one trouble report of the twelve reports issued in this sub-
21	metric for May 2001 that was out of service greater than 24 hours. This small
22	universe does not provide a statistically conclusive comparison with the retail

1	analogue.	BellSouth r	net or	exceeded	the	retail	analogue	for this	sub-metric
2	in June 20	01.							

3

4 <u>% Out of Service > 24 hours / ISDN Loops / Non dispatch (B.3.5.6.2)</u>

5 (May/June)

6 There was only one trouble out of the twenty-six reports issued in this sub-7 metric for May 2001 that was out of service greater than 24 hours. In June, 8 there were three troubles out of the twenty-nine reports that were out of 9 service greater than 24 hours. This small universe does not provide a 10 statistically conclusive comparison with the retail analogue.

11

12 SL1/SL2/Digital Loop Group

13 **<u>1. Provisioning Measures</u>**

14 The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed 15 critical value comparison requirements for May and June 2001 are as follows:

16

17 Order Completion Interval (OCI)

A root cause analysis for OCI for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same interval as "dispatched" orders. On June 2, 2001, a release was added to the due date calculator software to correct this error. However, due to problems with the software load, it had to be removed. A temporary fix was installed at

the end of June, until the final update can be added. In addition to the appointment interval issue, OCI is adversely affected by LSRs for which CLECs request intervals beyond the offered interval. When a CLEC requests an interval beyond the available interval offered by BellSouth, an "L" code is entered on the Service Order generated by BellSouth. "L" coded orders are excluded from the OCI metrics.

7

8 Order Completion Interval / 2w Analog Loop Design / < 10 Circuits / Dispatch

9 (B.2.1.8.1.1) (May/June)

10 There were a total of 453 completed orders in this sub-metric in May 2001. A 11 detailed analysis indicated that 211 of the 453 orders had intervals that were 12 longer than the due date calculator system would have assigned and should have been given an "L Code" for extended interval. In June there were 37 of 13 the 340 orders that should have been L coded. When an LSR is received, the 14 15 due date calculator determines what the current available interval for that product is, based on the available resources from Network. If the CLEC 16 requests a longer interval ("extended interval"), the order is given an "L Code" 17 and excluded from the OCI measurement. BellSouth continues to work to 18 lower the interval for this sub-metric to meet the "3 day" interval ordered for 19 the POTS type retail analogue services in Florida. The current standard 20 21 interval for this sub-metric is four days.

22

1 Order Completion Interval / 2w Analog Loop w/LNP Design / < 10 Circuits /

2 <u>Dispatch (B.2.1.12.1.1) (May/June)</u>

3 There were a total of 370 orders that completed for this sub-metric in May and 4 236 in June 2001. A detailed analysis indicated that 40 orders with extended 5 intervals were not "L coded" and should have been excluded in May with 21 6 orders in June. An additional 14 orders that were extended due to customer 7 misses and should have been "L coded" were not. The exclusion of these orders from this sub-metric would have met or exceeded the retail analogue. 8 9 BellSouth continues to work to lower the interval for this sub-metric to meet the "3 day" interval ordered for the POTS type retail analogue services in 10 11 Florida. The current standard interval for this sub-metric is four days.

12

13 Order Completion Interval / 2w Analog Loop w/LNP Non Design / < 10

14 <u>Circuits / Dispatch (B.2.1.13.1.1) (May/June)</u>

There were a total of 103 orders that completed for this sub-metric in May and 178 in June 2001. In May, six of the orders were extended due to customer misses and should have been "L coded." No other systemic problems have been identified for this sub-metric. BellSouth continues to work to lower the interval for this sub-metric to meet the "3 day" interval ordered for the POTS type retail analogue services in Florida. The current standard interval for this sub-metric is four days.

22

1	Order Completion Interval / 2w Analog Loop w/LNP Non Design / < 10
2	Circuits / Dispatch In (B.2.1.13.1.4) (June)
3	BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS
4	orders, but the wholesale non-dispatched orders were receiving the same
5	interval as "dispatched" orders. BellSouth applied a temporary fix at the end
6	of June to correct this issue.
7	
8	Order Completion Interval / 2w Analog Loop w/LNP Non Design / >= 10
9	Circuits / Dispatch In (B.2.1.13.2.4) (June)
10	There were only two orders for this sub-metric in June 2001. This small
11	universe does not provide a statistically conclusive comparison with the retail
12	analogue.
13	
14	The remainder of the provisioning measures that did not meet the retail
15	analogue for provisioning is as follows:
16	
17	Held Orders / 2w Analog Loop Design / < 10 Circuits / Facility (B.2.3.8.1.1)
18	(June)
19	There were a total of three held orders for this sub-metric in June 2001. This
20	small universe does not provide a statistically conclusive comparison with the
21	retail analogue.
22	

Held Orders / 2w Analog Loop w/LNP Design / < 10 Circuits / Facility (B.2.3.12.1.1) (June)

There were a total of two held orders for this sub-metric in June 2001. This small universe does not provide a statistically conclusive comparison with the retail analogue.

6

7 <u>% Jeopardies / 2w Analog Loop Design (B.2.5.8) (May/June)</u>

8 There were a total of 209 jeopardies issued for the 279 orders that were 9 scheduled for this sub-metric in May 2001. While the data indicates that 10 BellSouth placed a higher percentage of CLEC orders in jeopardy status, all 11 but 29 of the orders which were placed in jeopardy were actually worked on 12 time as indicated by the fact that there were only 29 missed installation 13 appointments for this sub-metric in May 2001. Of the 29 missed 14 appointments, only 5 resulted in held orders. All of the five orders were completed within an average of less than 14 days. In June 2001, there were 15 16 a total of 108 jeopardies issued for the 383 orders that were scheduled for 17 this sub-metric. All but 26 of the orders were worked as scheduled, with only 18 2 resulting in held orders that were resolved within an average of less than 28 19 davs.

20

21 <u>% Jeopardies / 2w Analog Loop Non-Design (B.2.5.9) (June)</u>

There were a total of 61 jeopardies issued for the 332 orders that were scheduled for this sub-metric in June 2001. While the data indicates that

1	BellSouth placed a higher percentage of CLEC orders in jeopardy status, all
2	but 10 of the orders which were placed in jeopardy were actually worked on
3	time as indicated by the fact that there were only 10 missed installation
4	appointments for this sub-metric in June 2001. None of the 10 missed
5	appointments in this sub-metric resulted in a held order in June.

6

7 % Jeopardies / 2w Analog Loop w/INP N	Non Design	(B.2.5.11) (May/June)
---	------------	-----------------------

8 There was only one trouble reported out of the twenty-six reports issued in 9 this sub-metric for May and one trouble out of the seven reports in June 2001 10 that was out of service greater than 24 hours. This small universe does not

- 11 provide a statistically conclusive comparison with the retail analogue.
- 12
- 13 % Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/INP Non Design
- 14 (B.2.10.11) (May)
- 15 <u>% Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/LNP Non</u>

16 <u>Design (B.2.10.13) (June)</u>

17 <u>% Jeopardy Notices issued >= 48 Hours / Digital Loop < DS1 (B.2.10.18)</u>

- 18 <u>(May)</u>
- 19 The calculations for this measure have been determined to be incorrect. The
- 20 coding change in the Service Order Control System (SOCS) is currently
- scheduled for a September 13, 2001, system load date. Based on this
- schedule, the October data month will be the first full month that the change

23 will be in effect.

1

2

Circuits / Dispatch (B.2.19.8.2.1) (June)

3 There were only three troubles reported for the nineteen orders that 4 completed in the previous 30 days to June 2001 for this sub-metric. This 5 small universe does not provide a statistically conclusive comparison with the 6 retail analogue.

7

8 <u>% Provisioning Troubles w/I 30 Days / 2w Analog Loop w/INP Design / < 10</u>

9 <u>Circuits / Dispatch (B.2.19.10.1.1) (May)</u>

10 There was only one trouble reported for the five orders that completed in the 11 previous 30 days to May 2001 for this sub-metric. This small universe does 12 not provide a statistically conclusive comparison with the retail analogue. 13 BellSouth met or exceeded the retail analogue for this sub-metric in June 14 2001.

15

<u>% Provisioning Troubles w/I 30 Days / 2w Analog Loop w/LNP Design / < 10</u> Circuits / Dispatch (B.2.19.12.1.1) (May/June)

There were a total of 176 trouble reports for the 1,776 orders that completed in the 30 days prior to May 2001. A detailed analysis indicated that 78 of the reports were closed with no trouble found, which had minimal impact on the end-user customer. In June 2001, there were a total of 153 trouble reports for the 1,548 orders that completed in the 30 days prior to June 2001. A detailed

- analysis indicated that 49 of the reports were closed with no trouble found,
 which had minimal impact on the end-user customer.
- 3

4 % Provisioning Troubles w/I 30 Days / 2w Analog Loop w/LNP Design / >= 10

- 5 Circuits / Dispatch (B.2.19.12.2.1) (May/June)
- 6 There were a total of 8 trouble reports for the 22 orders that completed in the 7 30 days prior to May and three trouble reports for the 16 orders that 8 completed in the 30 days prior to June 2001. No systemic issues have been 9 found for the reports in this sub-metric in May or June.
- 10

11 <u>% Provisioning Troubles within 30 Days / Digital Loops < DS1 / < 10 Circuits /</u>

12 Dispatch (B.2.19.18.1.1) (June)

There were a total of 55 troubles reported for this sub-metric for the 527 orders that completed in the 30 days prior to June 2001. BellSouth is currently investigating this sub-metric. There are no troubles indicated for the retail analogue for this sub-metric in June, which is also being reviewed.

17

18 <u>% Provisioning Troubles within 30 Days / Digital Loops >= DS1 / < 10 Circuits</u>

19 / Dispatch (B.2.19.19.1.1) (June)

There were a total of 57 troubles reported for this sub-metric for the 770 orders that completed in the 30 days prior to June 2001. BellSouth is currently investigating this sub-metric. There are no troubles indicated for the retail analogue for this sub-metric in June, which is also being reviewed.

2 <u>Average Completion Notice Interval / 2w Analog Loop Design / < 10 Circuits /</u>

3 Dispatch (B.2.21.8.1.1) (May/June)

1

- 4 Average Completion Notice Interval / 2w Analog Loop Design / >= 10 Circuits
- 5 / Dispatch (B.2.21.8.2.1) (June)
- 6 <u>Average Completion Notice Interval / 2w Analog Loop w/LNP Design / < 10</u>
- 7 Circuits / Dispatch (B.2.21.12.1.1) (May/June)
- 8 Average Completion Notice Interval / 2w Analog Loop w/LNP Non-Design / <
- 9 <u>10 Circuits / Dispatch (B.2.21.13.1.1) (June)</u>
- 10 Average Completion Notice Interval / 2w Analog Loop w/LNP Non-Design /
- 11 >= 10 Circuits / Dispatch (B.2.21.13.2.1) (June)

12 The root cause analysis of these measures indicated that the only differences 13 between the performance between BellSouth retail and CLECs are the 14 mismatches found when the orders are compared with the original LSRs. 15 The start of the completion interval is the point at which the technician 16 completes the order, and the interval ends when the completion notice is 17 sent. Any change to a name, number of items, etc., occurring during the 18 provisioning process will generate inconsistencies with the original LSRs that 19 must be resolved before a final completion notice can be sent. Any time to 20 resolve these inconsistencies with the original LSRs is included in the 21 Because of numerous CLEC changes and order updates, average. 22 mismatches on CLECs orders exceed those for BellSouth retail orders. 23 Combining this with the smaller base for the CLECs' measurement raises the

average, which results in a miss. Specific Service Representatives within the
 Work Management Centers have been assigned to resolve any completion
 issues that are required. Providing specific training and dedicating personnel
 to this task should reduce the difference between the CLEC and retail
 analogue results.

- 6
- 7

2. Maintenance & Repair Measures

8 The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed 9 critical value comparison requirements for May and/or June 2001 are as 10 follows:

11

12 <u>% Missed Repair Appointments / 2W Analog Loop Non Design / Dispatch</u> 13 (B.3.1.9.1) (May)

There were a total of 72 missed appointments out of the 534 scheduled for this sub-metric in May 2001. Twenty of the appointments were missed due to a damaged cable facility. Removal of these twenty reports would have met or exceeded the retail analogue for this sub-metric in May 2001. BellSouth met or exceeded the retail analogue for this sub-metric in June 2001.

19

20 <u>% Repeat Reports w/I 30 Days / 2W Analog Loop Non Design / Non Dispatch</u>

- 21 (B.3.4.9.2) (May/June)
- There were a total of 63 trouble reports of which 37 were repeats in this submetric for May 2001. A detailed analysis has identified 34 of the 37 repeats to

be from the third party test CLEC. Also, 36 of the 37 repeat reports were closed as Test OK / Found OK. In June 2001, there were a total of 96 troubles with 69 of them being repeat reports. A detailed analysis has identified 63 of the 67 repeats to be from the third party test CLEC. The exclusion of the third party tests reports from this sub-metric would meet or exceed the retail analogue for May and June.

7

8 Out of Service > 24 hours / 2w Analog Loop Non-Design / Non Dispatch

9 (B.3.5.9.2) (June)

10 There were a total of 30 out of service troubles reported for this sub-metric in 11 June 2001 with 5 being longer than 24 hours. No systemic issues were 12 identified for these 5 reports in June.

13

14

E. CHECKLIST ITEM 5 – UNBUNDLED LOCAL TRANSPORT

15

the that BellSouth met The data in these measures indicate 16 benchmark/analogue requirements for all measurements in Checklist Item 5 17 18 for May 2001. The two sub-metrics that did not meet the retail analogue in 19 June 2001 are as follows:

20

21 Maintenance Average Duration / Local Interoffice Transport / Non Dispatch

- 22 (B.3.3.2.2) (June)
- 23 % Repeat Troubles within 30 Days / Local Interoffice Transport / Non
- 24 <u>Dispatch (B.3.4.2.2) (June)</u>

•

1	There were only three troubles (same reports) reported in these two sub-
2	metrics for June 2001. This small universe does not provide a statistically
3	conclusive comparison with the retail analogue.
4	
5	F. CHECKLIST ITEM 6 - UNBUNDLED LOCAL SWITCHING
6	
7	The data in these measures indicate that BellSouth met the
8	benchmark/analogue requirements for all measurements in Checklist Item 6
9	for May and June 2001.
10	
11	G. CHECKLIST ITEM 7a – 911 AND E911 SERVICES
12	H. CHECKLIST ITEM 7b - DIRECTORY ASSISTANCE/OPERATOR
13	SERVICES
13 14	SERVICES
13 14 15	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the
13 14 15 16	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and
13 14 15 16 17	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the
13 14 15 16 17 18	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for
13 14 15 16 17 18 19	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for all users.
13 14 15 16 17 18 19 20	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for all users.
13 14 15 16 17 18 19 20 21	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for all users.
13 14 15 16 17 18 19 20 21 21 22	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for all users. I. CHECKLIST ITEM 10 – ACCESS TO DATABASES AND ASSOCIATED SIGNALING
13 14 15 16 17 18 19 20 21 21 22 23	SERVICES As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the benchmark/analogue requirements of Checklist Items 7a and 7b in May and June 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are designed to provide parity for all users. I. CHECKLIST ITEM 10 – ACCESS TO DATABASES AND ASSOCIATED SIGNALING BellSouth made three of the four sub-metrics associated with this checklist

- in Attachment 1A for further details. The one item that did not meet the
 appropriate benchmark in May 2001 is as follows:
- 3

4 <u>% NXXs / LRNs Loaded by LERG Effective Date (Region) (F.13.3)</u>

The measure indicates that only 21 of the 33 NXXs were loaded by their 5 6 effective date for the entire BellSouth region. Florida met three of the 7 thirteen NXXs that could have loaded for this sub-metric in May 2001. Initially 8 the CLECs in Florida requested 34 NXXs to be loaded for May. Twenty-one 9 of these were rescheduled due to the CLEC requests. Of the ten items that were missed, eight were worked within two days of the due date. BellSouth 10 11 will re-focus its effort to verify all due dates ahead of time and make sure that the loads are done in a timely manner. BellSouth met or exceeded the 12 13 benchmark for this sub-metric in June 2001.

- 14
- 15

I. CHECKLIST ITEM 11 – NUMBER PORTABILITY

16

17 All the measurements in this Checklist Item were met or exceeded for May18 and June 2001 except for the following:

19

20 Order Completion Interval / LNP (Standalone)) / < 10 Circuits / Dispatch

21 (B.2.1.17.1.1) (May)

The unadjusted order completion interval was 13.79 days compared to the retail analogue of 4.16 days. BellSouth is currently investigating this data, as there should not be dispatched LNP standalone orders. This is a change
- within the switching system only and therefore classified as non-dispatched.
 There was no data for this sub-metric in June 2001.
- 3

4 Order Completion Interval / LNP (Standalone)) / < 10 Circuits / Non Dispatch

5 (B.2.1.17.1.2) (May/June)

The unadjusted order completion interval was 1.84 days compared to the 6 7 retail analogue of 1.01 days in May and 1.58 days compared to the retail 8 analogue of 0.85 days in June 2001. A root cause analysis for OCI for non-9 dispatched orders revealed that BellSouth was offering the same interval as 10 "dispatched" orders. An interim solution for this problem, a modification to the due date calculation process was installed at the end of June. In addition to 11 12 the appointment interval issue, OCI is adversely affected by LSRs for which 13 CLECs request intervals beyond the offered interval. When a CLEC requests 14 an interval beyond the available interval offered by BellSouth, an "L" code is 15 entered on the Service Order generated by BellSouth. "L" coded orders are 16 excluded from the OCI metrics.

17

18 Order Completion Interval / LNP (Standalone)) / >=10 Circuits / Non Dispatch

19 (B.2.1.17.2.2) (May/June)

The unadjusted order completion interval was 9.00 days compared to the retail analogue of 3.33 days in May 2001. Three of the eighteen orders included in this sub-metric were "trigger" orders for disconnecting service with extended intervals and should have been excluded. The trigger orders are

completed at the request of the CLEC and should have been excluded from
this sub-metric. In June 2001, there were only seven orders in this submetric. This small universe does not provide a statistically conclusive
comparison with the retail analogue.

5

6 <u>% Missed Installation Appointments / LNP (Standalone) / < 10 Circuits / Non</u> 7 Dispatch (B.2.18.17.1.2) (June)

8 BellSouth missed 16 of the 7,615 scheduled appointments in this sub-metric for June 2001, BellSouth met over 99% of the scheduled appointments for 9 10 both retail and the CLECs in this sub-metric for June. When BellSouth provisions high quality service coupled with very large universe sizes, it can 11 12 cause an apparent out of equity condition from a quantitative viewpoint. In 13 these cases, there is very little variation and the universe size is so large that the Z-test becomes overly sensitive to any difference. In other words, the 14 15 statistical test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual 16 performance for both CLECs and its own retail operations is at a very high 17 level - often 98% or 99%. From a practical point of view, the CLECs' ability 18 to compete has not been hindered even though the statistical results may 19 20 technically show that BellSouth failed to meet the benchmark/analogue.

21

22 Average Completion Notice Interval / LNP(Standalone) / < 10 Circuits / Non-

23 Dispatch (B.2.21.17.1.2) (May/June)

1 The root cause analysis of these measures indicated that the only differences 2 between the performance between BellSouth retail and CLECs are the 3 mismatches found when the orders are compared with the original LSRs. 4 The start of the completion interval is the point at which the technician 5 completes the order, and the interval ends when the completion notice is 6 sent. Any change to a name, number of items, etc., occurring during the 7 provisioning process will generate inconsistencies with the original LSRs that 8 must be resolved before a final completion notice can be sent. Any time to 9 resolve these inconsistencies with the original LSRs is included in the Because of numerous CLEC changes and order updates, 10 average. mismatches on CLECs orders exceed those for BellSouth retail orders. 11 Combining this with the smaller base for the CLECs' measurement raises the 12 13 average, which results in a miss. Specific Service Representatives within the Work Management Centers have been assigned to resolve any completion 14 issues that are required. Providing specific training and dedicating personnel 15 to this task should reduce the difference between the CLEC and retail 16 17 analogue results.

18

19 Disconnect Timeliness / LNP / < 10 Circuits (B.2.31.1) (May/June)

The Disconnect Timeliness measure is supposed to track the time it takes to disconnect a number in the central office switch after the message has been received from the Local Number Portability (LNP) Gateway that it is ready.

- However, this measurement does not track the relevant time to perform this
 function.
- 3

4 On a great majority of LNP orders, BellSouth creates what is referred to as a 5 "trigger" in conjunction with the order. This trigger gives the end user 6 customer the ability to make and receive calls from other customers who are 7 served by the customer's host switch at the time of the LNP activation. This 8 ability is not dependent upon BellSouth working a disconnect order in the 9 central office switch. In other words, when a trigger is involved, an end user 10 customer can receive calls from other customers served by the same host 11 switch before the disconnect order is ever worked.

12

As it currently exists. Performance Measure P-11 does not recognize the 13 14 importance of triggers and their effect on the LNP process. Rather, the current measure calculates the end time of the LNP activity as the processing 15 of the actual disconnect order in the host switch, even though, from a 16 17 customer's perspective, this activity is totally meaningless on most LNP orders. It is the activation of the LNP and the routing function accomplished 18 19 by the LSMS that ultimately determines whether the end user is back in full service and is able to make and receive calls when a trigger is used in porting 20 21 a telephone number. So, while BellSouth may be missing this measure, the actual impact on CLECs and their end users, for a great majority of the orders 22 23 is minimal, or nonexistent.

.

1	
2	This measure needs to be changed to more accurately reflect the LNP
3	process and its impacts on end users.
4	
5	K. CHECKLIST ITEM 14 – RESALE
6	BellSouth has met or exceeded the benchmarks/analogues for 79% of the
7	resale metrics for the month of May and 89% in June 2001. The details are
8	delineated in Attachment 1A, Items A.1.1.1.1 through A.4.2.
9	
10	1. Resale Ordering Measures
11	FOC Timeliness
12	For the month of June 2001, BellSouth processed approximately 45,530
13	Resale LSRs in Florida and met the relevant benchmark on 96% of all FOCs.
14	Of the 45,530 LSRs, 329,724 were fully mechanized with 98% meeting the 3-
15	hour benchmark, clearly exceeding the 95% target. See Attachment 1A,
16	Sections A.1.9 through A.1.13 for further details.
17	
18	Reject Interval
19	During the month of June 2001, there were 11,226 rejected LSRs, either
20	mechanically or manually processed, with 94% meeting the benchmark. The
21	benchmark for electronic rejects is 97% within 1 hour. 52% of all orders were
22	processed electronically, and 96% met the 1-hour benchmark. See

68

Attachment 1A, Items A.1.4 through A.1.8 for further details.

2 The Ordering sub-metrics for which BellSouth did not meet the
3 benchmarks/analogues for May and/or June 2001 were:

4

1

5 Reject Interval / Residence / Electronic (A.1.4.1) (May/June)

6 The current benchmark for this sub-metric is >= 97% within one hour. There 7 were 8,905 LSRs rejected in this sub-metric in May 2001 with 7662 or 95% 8 meeting the one hour benchmark. In June 2001, there were 5,285 LSRs 9 rejected with 5,037 or 95% meeting the one hour benchmark. BellSouth is conducting a detailed root cause analysis of the process for electronic 10 11 ordering. This analysis addresses the ordering systems (EDI, TAG, and 12 LENS) used by the CLECs and the back-end legacy applications, such as 13 SOCS, that are accessed by the ordering systems.

14

Thus far, the analysis has determined that many of the LSRs that did not meet the one-hour benchmark were issued between 11:00 p.m. and 4:30 a.m. Between these hours the system is unable to process LSRs because some of the back-end legacy systems are out of service. Such hours should be excluded from the measurement. BellSouth is currently reviewing the scheduled down time for all systems and how that down time affects the ordering capability of the CLECs.

22

1 With the implementation of May data BellSouth was directed to change the 2 time stamp identification for the start and complete times of the interval for 3 this measurement from the Local Exchange Ordering (LEO) System to the 4 CLEC ordering interface system (TAG or EDI). With this change BellSouth 5 was unable to identify multiple issues of the same version of the LSRs that 6 may be rejected (fatal rejects), which should be excluded from the 7 measurement. If there are multiple issues of the same version, the measure currently calculates the interval from the initial issue to the final issue of the 8 9 LSR returned to the CLEC, Reject or FOC. Consequently, BellSouth's 10 performance level is inappropriately understated. BellSouth is currently 11 working to determine a fix for this issue.

12

13 <u>Reject Interval / Business / Electronic (A.1.4.2) (May)</u>

14 The current benchmark for this sub-metric is >= 97% within one hour. There were 696 LSRs rejected in this sub-metric in May 2001 with 672 or 96.6% 15 16 meeting the one hour benchmark. BellSouth is conducting a detailed root 17 cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs 18 and the back-end legacy applications, such as SOCS, that are accessed by 19 20 For further information see the explanation included the ordering systems. 21 with the electronic reject interval measurement, item A.1.4.1. BellSouth met 22 or exceeded the benchmark for this sub-metric in June 2001.

23

1 Reject Interval / ISDN / Partially Electronic (A.1.6.6) (May)

There were only nine orders in this sub-metric for May 2001 with BellSouth meeting the benchmark for seven of them. Such a small universe does not produce a statistically conclusive benchmark comparison. BellSouth met or exceeded the benchmark for this sub-metric in June 2001.

6

7 Reject Interval / Centrex / Manual (A.1.8.5) (June)

8 There were only two orders in this sub-metric for June 2001 with BellSouth

9 meeting the benchmark for one of them. Such a small universe does not

- 10 produce a statistically conclusive benchmark comparison.
- 11

12 FOC Timeliness / Centrex / Manual (A.1.13.5) (May)

13 There was only one order in this sub-metric for May 2001. Such a small

14 universe does not produce a statistically conclusive benchmark comparison.

15 BellSouth met or exceeded the benchmark for this sub-metric in June 2001.

16

17 FOC Reject & Response Completeness / Business / Electronic (A.1.14.2)

- 18 (May/June)
- 19 FOC Reject & Response Completeness / PBX / Electronic (A.1.14.4) (June)
- 20 FOC Reject & Response Completeness / ISDN / Electronic (A.1.14.6) (May)
- 21 FOC Reject & Response Completeness / Residence / Manual (A.1.16.1)
- 22 <u>(June)</u>

- 1 FOC Reject & Response Completeness / Business / Manual (A.1.16.2)
- 2 (May/June)
- 3 FOC Reject & Response Completeness / Design (Specials) / Manual
- 4 (A.1.16.3) (May/June)
- 5 FOC Reject & Response Completeness / PBX / Manual (A.1.16.4)
- 6 (May/June)
- 7 FOC Reject & Response Completeness / PBX / Manual (A.1.16.6) (June)
- 8 FOC Reject & Response Completeness (Multiple Responses) / Residence /
- 9 Partially Electronic (A.1.18.1) (May)
- 10 FOC Reject & Response Completeness (Multiple Responses) / Business /
- 11 Partially Electronic (A.1.18.2) (May/June)
- 12 FOC Reject & Response Completeness (Multiple Responses) / PBX /
- 13 Partially Electronic (A.1.18.4) (June)
- 14 FOC Reject & Response Completeness (Multiple Responses) / ISDN /
- 15 Partially Electronic (A.1.18.6) (May)
- 16 FOC Reject & Response Completeness (Multiple Responses) / Residence /
- 17 <u>Manual (A.1.19.1) (May/June)</u>
- 18 FOC Reject & Response Completeness (Multiple Responses) / Business /
- 19 <u>Manual (A.1.19.2) (May/June)</u>
- 20 As indicated in Checklist Item 2, BellSouth has determined that the coding for
- 21 the FOC and Reject Completeness measures failed to include rejections that
- 22 were classified as "auto clarifications." This coding change will impact all
- 23 FOC and Reject Completeness measures that include auto clarification

1	rejects. The code for this measurement is being rewritten and is projected to
2	be included with the August data, available at the end of September.
3	BellSouth continues to review this measurement in order to improve results to
4	meet the benchmark.

- 5
- 6

2. Resale Provisioning Mea0sures

7

For the month of May 2001, BellSouth met or exceeded the benchmark or
retail analogue for 73% of all resale provisioning measures. In June 2001,
84% met or exceeded the criteria for all provisioning measures. The details
supporting this percentage are delineated in Items A.2.1.1.1 through
A.2.25.3.2.2 of Attachment 1A.

13

14 Order Completion Interval

As discussed in Checklist Item 4, the failure to properly "L" code appropriate orders and the missed appointments for customer reasons negatively impacts the OCI measurements. The following are the measures for which BellSouth did not meet the retail analogue in May and/or June 2001.

19

A root cause analysis for OCI for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same interval as "dispatched" orders. On June 2, 2001, a release was added to the

1 due date calculator software to correct this error. However, due to problems 2 with the software load, it had to be removed. A temporary fix was installed at 3 the end of July, until the final update can be added. In addition to the 4 appointment interval issue, OCI is adversely affected by LSRs for which 5 CLECs request intervals beyond the offered interval. When a CLEC requests 6 an interval beyond the available interval offered by BellSouth, an "L" code is 7 entered on the Service Order generated by BellSouth. "L" coded orders are 8 excluded from the OCI metrics.

9

10 Order Completion Interval / Residence / < 10 Circuits / Non-Dispatch

11 (A.2.1.1.1.2) (May/June)

The unadjusted order completion interval was 2.17 days compared to the retail analogue of 0.97 days in May 2001. In June 2001, the unadjusted order completion interval was 1.08 days compared to the retail analogue of 0.81 days. As explained in the Order Completion Interval section for Checklist ltem 4, BellSouth has determined that non-dispatched orders were given the dispatched interval in error.

18

19 Order Completion Interval / Business / < 10 Circuits / Dispatch (A.2.1.2.1.1)

20 (May/June)

The unadjusted order completion interval was 4.03 days compared to the retail analogue of 3.32 days in May. In June 2001, the unadjusted order completion interval was 3.70 days compared to the retail analogue of 3.02

days. OCI is adversely affected by LSRs for which CLECs request intervals
beyond the offered interval and do not enter an "L" code on the order. When a
CLEC requests an interval beyond the available interval offered by BellSouth,
an "L" code is entered on the Service Order generated by BellSouth. "L"
coded orders are excluded from the OCI metrics.

6

7 Order Completion Interval / Business / < 10 Circuits / Non Dispatch
8 (A.2.1.2.1.2) (May)

9 The unadjusted order completion interval was 1.77 days compared to the 10 retail analogue of 1.51 days. As explained in the Order Completion Interval 11 section for Checklist Item 4, BellSouth has determined that non-dispatched 12 orders were given the dispatched interval in error. BellSouth met or 13 exceeded the retail analogue for this sub-metric in June 2001.

14

15 Order Completion Interval / Design (Specials) / >= 10 Circuits / Non Dispatch

16 (A.2.1.3.2.2) (June)

17 The unadjusted order completion interval was 8.74 days compared to the 18 retail analogue of 3.61 days. As explained in the Order Completion Interval 19 section for Checklist Item 4, BellSouth has determined that non-dispatched 20 orders were given the dispatched interval in error.

21

22 Order Completion Interval / PBX / >= 10 Circuits / Dispatch (A.2.1.4.2.1)
 23 (May)

1 There were only six orders in this sub-metric for May 2001. The small 2 universe for this measurement does not provide a statistically conclusive 3 comparison to the retail analogue. BellSouth met or exceeded the retail 4 analogue for this sub-metric in June 2001.

5

Order Completion Interval / Centrex / < 10 Circuits / Non-Dispatch (A.2.1.5.1.2) (May/June)

The unadjusted order completion interval was 5.91 days compared to the retail analogue of 1.87 days in May. In June 2001, the unadjusted order completion interval was 2.48 days compared to the retail analogue of 1.51 days. As explained in the Order Completion Interval section for Checklist ltem 4, BellSouth has determined that non-dispatched orders were given the dispatched interval in error.

14

15 Order Completion Interval / Centrex / >= 10 Circuits / Non-Dispatch 16 (A.2.1.5.2.2) (May)

17 There were only eight orders in this sub-metric for May 2001. The small 18 universe for this measurement does not provide a statistically conclusive 19 comparison to the retail analogue. BellSouth met or exceeded the retail 20 analogue for this sub-metric in June 2001.

21

Other resale provisioning sub-metrics for which BellSouth did not meet thebenchmark/retail analogue were:

•

1	
2	<u>% Jeopardy Notice >= 48 hours / Residence / Mechanized (A.2.9.1)</u>
3	(May/June)
4	% Jeopardy Notice >= 48 hours / Business / Mechanized (A.2.9.2)
5	(May/June)
6	The calculations for this measure have been determined to be incorrect. The
7	coding change in the Service Order Control System (SOCS) is currently
8	scheduled for a September 13, 2001, system load date. Based on this
9	schedule, the October data month will be the first full month that the change
10	will be in effect.
11	
12	<u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u>
12 13	<u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u> Dispatch (A.2.11.1.1.2) (May/June)
12 13 14	<u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u> <u>Dispatch (A.2.11.1.1.2) (May/June)</u> BellSouth missed 39 of the 48,383 scheduled appointments for this sub-
12 13 14 15	 <u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u> <u>Dispatch (A.2.11.1.1.2) (May/June)</u> BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the
12 13 14 15 16	 <u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u> <u>Dispatch (A.2.11.1.1.2) (May/June)</u> BellSouth missed 39 of the 48,383 scheduled appointments for this submetric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as
12 13 14 15 16 17	% Missed Installation Appointments / Residence / < 10 Circuits / Non Dispatch (A.2.11.1.1.2) (May/June) BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in May and June.
12 13 14 15 16 17 18	% Missed Installation Appointments / Residence / < 10 Circuits / Non Dispatch (A.2.11.1.1.2) (May/June) BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in May and June.
12 13 14 15 16 17 18 19	% Missed Installation Appointments / Residence / < 10 Circuits / Non Dispatch (A.2.11.1.1.2) (May/June) BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in May and June. % Missed Installation Appointments / Business / < 10 Circuits / Dispatch
12 13 14 15 16 17 18 19 20	% Missed Installation Appointments / Residence / < 10 Circuits / Non Dispatch (A.2.11.1.1.2) (May/June) BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in May and June. % Missed Installation Appointments / Business / < 10 Circuits / Dispatch (A.2.11.2.1.1) (May/June)
12 13 14 15 16 17 18 19 20 21	<u>% Missed Installation Appointments / Residence / < 10 Circuits / Non</u> Dispatch (A.2.11.1.1.2) (May/June) BellSouth missed 39 of the 48,383 scheduled appointments for this sub- metric in May and 53 of the 33,424 appointments in June 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in May and June. <u>% Missed Installation Appointments / Business / < 10 Circuits / Dispatch (A.2.11.2.1.1) (May/June)</u> There were a total of 26 missed appointments out of the 569 scheduled for

•

	August 24, 2001
1	for June 2001. Both BellSouth retail and the CLECs had 95% of all
2	scheduled appointments completed on time in May and June.
3	
4	% Missed Installation Appointments / Design (Specials) / < 10 Circuits / Non
5	Dispatch (A.2.11.3.1.2) (May)
6	There was only one order in this sub-metric for May 2001. The small
7	universe for this measurement does not provide a statistically conclusive
8	comparison with the retail analogue. BellSouth met or exceeded the retail
9	analogue for this sub-metric in June 2001.
10	
11	% Provisioning Troubles w/i 30 days / Residence / < 10 Circuits / Dispatch
12	(A.2.12.1.1.1) (May)
13	There were a total of 168 troubles reported for the 2002 orders that
14	completed in the 30 days prior to May 2001 for this sub-metric. A detailed
15	analysis indicated that 50 of the reports were closed as found OK, which had
16	minimal impact on the end-user customer. The exclusion of these reports for
17	this sub-metric would have met or exceeded the retail analogue in May.
18	BellSouth met or exceeded the retail analogue for this sub-metric in June
19	2001.
20	

21 <u>% Provisioning Troubles w/i 30 days / Residence / < 10 Circuits / Non</u>
 22 <u>Dispatch (A.2.12.1.1.2) (May/June)</u>

1 There were 1.356 troubles reported for the 27.342 orders that completed in 2 the 30 days prior to May 2001 for this sub-metric. 307 of the 1,356 were 3 closed as test OK / found OK ("TOK/FOK"), which means that the end-user 4 customer experienced minimal trouble levels for these reports. There were 5 also 448 closed to facilities issues. In June 2001, there were 1,993 troubles 6 reported for the 48.383 orders that completed in the 30 days prior to June 7 2001. 1216 of the troubles were reported by one CLEC with 535 of the 1216 8 closed as "TOK/FOK."

9

10 <u>% Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Dispatch</u>

11 (A.2.12.2.1.1) (May/June)

12 There were 47 troubles reported for the 758 orders that completed in the 30 13 days prior to May 2001 for this sub-metric. 20 of the 47 were closed as 14 TOK/FOK or the end-user experienced minimal trouble levels for these reports. There were also 17 closed to facilities issues. In June 2001, there 15 16 were 38 troubles reported for the 569 orders that completed in the 30 days prior to June 2001. 12 of the 39 were closed as TOK/FOK with minimal 17 impact on the end-user customer. BellSouth continues to look for ways to 18 reduce these trouble levels for this sub-metric. 19

20

21 <u>% Provisioning Troubles w/i 30 days / PBX / < 10 Circuits / Dispatch</u>
22 (A.2.12.4.1.1) (May)

.

1	There was only one trouble reported for the 10 orders that completed in the
2	30 days prior to May 2001 for this sub-metric. The small universe for this
3	measurement does not provide a statistically conclusive comparison with the
4	retail analogue. BellSouth met or exceeded the retail analogue for this sub-
5	metric in June 2001.
6	
7	% Provisioning Troubles w/i 30 days / PBX / < 10 Circuits / Non Dispatch
8	(A.2.12.4.1.2) (June)
9	There were only three troubles reported for the 34 orders that completed in
10	the 30 days prior to June 2001 for this sub-metric. The small universe for this
11	measurement does not provide a statistically conclusive comparison with the
12	retail analogue.
13	
14	% Provisioning Troubles w/i 30 days / Centrex / >= 10 Circuits / Dispatch
15	(A.2.12.5.2.1) (June)
16	There was only one orders that completed in the 30 days prior to June 2001
17	for this sub-metric. The small universe for this measurement does not provide
18	a statistically conclusive comparison with the retail analogue.
19	
20	Average Completion Notice Interval / Residence / < 10 Circuits / Dispatch /
21	Electronic (A.2.14.1.1.1) (May)
22	Average Completion Notice Interval / Residence / < 10 Circuits / Non
23	Dispatch / Electronic (A.2.14.1.1.2) (May/June)

- 1 <u>Average Completion Notice Interval / Residence / >= 10 Circuits / Dispatch /</u>
- 2 <u>Electronic (A.2.14.1.2.1) (May)</u>
- 3 <u>Average Completion Notice Interval / Business / < 10 Circuits / Dispatch /</u>
- 4 <u>Electronic (A.2.14.2.1.1) (May)</u>
- 5 Average Completion Notice Interval / Business / < 10 Circuits / Non-Dispatch /
- 6 <u>Electronic (A.2.14.2.1.2) (May/June)</u>

7 <u>Average Completion Notice Interval / Business / >= 10 Circuits / Non Dispatch</u>

8 / Electronic (A.2.14.2.2.2) (May)

9 The root cause analysis of this measure indicated that the only differences 10 between the BellSouth retail and CLEC data are the mismatches found when 11 the orders are compared with the original LSRs. Any change to a name, 12 number of items, etc., occurring during the provisioning process will generate 13 inconsistencies with the original LSRs that must be resolved before a final 14 completion notice can be sent. The start of the interval is the point at which 15 the technician completes the order and the interval ends when the completion 16 notice is sent. Any time to resolve these inconsistencies with the original 17 LSRs is included in the average. Because of numerous CLEC changes and 18 order updates, mismatches on CLEC orders exceed those for BellSouth retail orders. Combining this with the smaller base for the CLECs' measurement 19 20 raises the average, which results in a miss. Specific Service Representatives 21 within the Work Management Centers have been assigned to resolve any 22 Providing specific training and completion issues that are required.

.

1	dedicating personnel to this task should reduce the difference between the
2	CLEC and retail analogue results.
3	
4	Service Order Accuracy / Residence / < 10 Circuits / Non Dispatch
5	(A.2.25.1.1.2) (June)
6	BellSouth met the standard for 124 of the 131 orders reviewed in this sub-
7	metric for June 2001. The 95% benchmark set a requirement of 125 based
8	on the quantity of orders for this sub-metric. BellSouth continues to focus on
9	this measurement in order to improve results to meet the benchmark.
10	
11	Service Order Accuracy / Business / < 10 Circuits / Non Dispatch
12	(A.2.25.2.1.2) (June)
13	BellSouth met the standard for 101 of the 121 orders reviewed in this sub-
14	metric for May 2001. The 95% benchmark set a requirement of 115 based on
15	the quantity of orders for this sub-metric. BellSouth continues to focus on this
16	measurement in order to improve results to meet the benchmark.
17	
18	Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch
19	(A.2.25.3.1.1) (May)
20	BellSouth met the standard for 12 of the 17 orders reviewed in this sub-metric
21	for May 2001. The 95% benchmark set a requirement of 16 based on the
22	quantity of orders for this sub-metric. BellSouth continues to focus on this

1	measurement in order to improve results to meet the benchmark. BellSouth
2	met or exceeded the benchmark for this sub-metric in June 2001.
3	
4	Service Order Accuracy / Design (Specials) / < 10 Circuits / Non Dispatch
5	(A.2.25.3.1.2) (May)
6	BellSouth met the standard for 3 of the 4 orders reviewed in this sub-metric
7	for May 2001. The 95% benchmark set a requirement of all 4 based on the
8	quantity of orders for this sub-metric. BellSouth continues to focus on this
9	measurement in order to improve results to meet the benchmark. BellSouth
10	met or exceeded the benchmark for this sub-metric in June 2001.
11	
12	Service Order Accuracy / Design (Specials) / >= 10 Circuits / Non Dispatch
13	(A.2.25.3.2.2) (May/June)
14	There was only one order in this sub-metric for May and three in June 2001.
15	The small universe for this measurement does not provide a statistically
16	conclusive comparison with the retail analogue. BellSouth continues to focus
17	on this measurement in order to improve results to meet the benchmark.
18	
19	3. Resale Maintenance and Repair (M&R) Measures
20	
21	BellSouth met the relevant retail analogues for 85% of all the Resale
22	Maintenance & Repair measurements in May and 96% in June 2001. The
23	sub-metrics for which BellSouth did not meet the retail analogues were:

1

2 Customer Trouble Report Rate / Residence / Dispatch (A.3.2.1.1) (May)

There were 2,635 troubles reported for the approximately 125,000 in service lines for this sub-metric in May 2001. Both the CLECs and BellSouth retail had 98% of the in service lines trouble free in May. There was less than a quarter of one percent difference in the report rates between retail and resale results for this sub-metric in May. BellSouth met or exceeded the retail analogue for this sub-metric in June 2001.

9

10 Customer Trouble Report Rate / Business / Dispatch (A.3.2.2.1) (May)

There were 1,073 troubles reported for the approximately 65,000 in service lines for this sub-metric in May 2001. Both the CLECs and BellSouth retail had 98% of the in service lines trouble free in May. There was less than a quarter of one percent difference in the report rates between retail and resale results for this sub-metric in May. BellSouth met or exceeded the retail analogue for this sub-metric in June 2001.

17

18 Customer Trouble Report Rate / Business / Non Dispatch (A.3.2.2.2) (May)

19 There were 790 troubles reported for the approximately 65,000 in service 20 lines for this sub-metric in May 2001. Both the CLECs and BellSouth retail 21 had 98% of the in service lines trouble free in May. There was less than a 22 third of one percent difference in the report rates between retail and resale

- results for this sub-metric in May. BellSouth met or exceeded the retail
 analogue for this sub-metric in June 2001.
- 3

4 Customer Trouble Report Rate / PBX / Dispatch (A.3.2.4.1) (May)

5 There were only 41 trouble reports for the 4,561 in service lines for this sub-6 metric in May 2001. BellSouth provided over 99% trouble free service for both 7 retail and the CLECs for this sub-metric for the month of May. When 8 BellSouth provisions high quality service coupled with very large universe 9 sizes, it can cause an apparent out of equity condition from a quantitative 10 viewpoint. In these cases, there is very little variation and the universe size 11 is so large that the Z-test becomes overly sensitive to any difference. In other 12 words, the statistical test shows that the measurement does not meet the 13 fixed critical value when compared with the retail analogue, but BellSouth's 14 actual performance for both CLECs and its own retail operations is at a very 15 high level - often 98% or 99%. From a practical point of view, the CLECs' 16 ability to compete has not been hindered even though the statistical results 17 may technically show that BellSouth failed to meet the benchmark/analogue. 18 BellSouth met or exceeded the retail analogue for this sub-metric in June 19 2001.

20

21 Customer Trouble Report Rate / PBX / Non Dispatch (A.3.2.4.2) (May/June)

There were only 12 trouble reports for the 4,561 in service lines for this submetric in May and 24 reports for the 4,278 in service lines in June 2001.

1 BellSouth provided over 99% trouble free service for both retail and the 2 CLECs for this sub-metric for the month of May and June. When BellSouth 3 provisions high quality service coupled with very large universe sizes, it can 4 cause an apparent out of equity condition from a quantitative viewpoint. In 5 these cases, there is very little variation and the universe size is so large that 6 the Z-test becomes overly sensitive to any difference. In other words, the 7 statistical test shows that the measurement does not meet the fixed critical 8 value when compared with the retail analogue, but BellSouth's actual 9 performance for both CLECs and its own retail operations is at a very high 10 level – often 98% or 99%. From a practical point of view, the CLECs' ability 11 to compete has not been hindered even though the statistical results may 12 technically show that BellSouth failed to meet the benchmark/analogue.

13

14 Customer Trouble Report Rate / Centrex / Dispatch (A.3.2.5.1) (May)

15 There were only 33 trouble reports for the 4,167 in service lines for this sub-16 metric in May 2001. BellSouth provided over 99% trouble free service for both 17 retail and the CLECs for this sub-metric for the month of May. When BellSouth provisions high quality service coupled with very large universe 18 19 sizes, it can cause an apparent out of equity condition from a quantitative 20 viewpoint. In these cases, there is very little variation and the universe size 21 is so large that the Z-test becomes overly sensitive to any difference. In other 22 words, the statistical test shows that the measurement does not meet the 23 fixed critical value when compared with the retail analogue, but BellSouth's

actual performance for both CLECs and its own retail operations is at a very
high level – often 98% or 99%. From a practical point of view, the CLECs'
ability to compete has not been hindered even though the statistical results
may technically show that BellSouth failed to meet the benchmark/analogue.
BellSouth met or exceeded the retail analogue for this sub-metric in June
2001.

7

8 % Repeat Troubles in 30 Days / Residence / Non Dispatch (A.3.4.1.2) (May)

9 There were a total of 1,431 trouble reports of which 296 were repeats in this 10 sub-metric for May 2001. A detailed analysis has identified 80 of the 296 11 repeats to be from the third party test CLEC. Also, 258 of the 296 repeat 12 reports were closed as Test OK / Found OK with the end-user customer 13 experiencing minimal trouble levels for these reports. The exclusion of the 14 third party tests reports from this sub-metric would meet or exceed the retail 15 analogue for May. BellSouth met or exceeded the retail analogue for this sub-16 metric in June 2001.

17

18 <u>% Repeat Troubles in 30 Days / Business / Non Dispatch (A.3.4.2.2) (May)</u>

There were a total of 792 trouble reports of which 245 were repeats in this sub-metric for May 2001. A detailed analysis has identified 135 of the 245 repeats to be from the third party test CLEC. Also, 206 of the 245 repeat reports were closed as Test OK / Found OK with the end user customer experiencing minimal trouble levels for these reports. The exclusion of the

•

1	third party tests reports from this sub-metric would meet or exceed the retail
2	analogue for May. BellSouth met or exceeded the retail analogue for this sub-
3	metric in June 2001.
4	
5	% Repeat Troubles in 30 Days / Centrex / Non Dispatch (A.3.4.5.2) (June)
6	There were a total of 8 troubles reported with three of them being repeat
7	reports for this sub-metric in June 2001. The small universe for this
8	measurement does not provide a statistically conclusive comparison with the
9	retail analogue.
10	
11	II. <u>Summary</u>
12	
13	As stated in the Introduction to the Analysis of Performance Measurements
14	section, BellSouth met or exceeded the criteria for 499 of the 608 sub-metrics
15	(82%) for which there was CLEC activity in May and 517 of 637 sub-metrics
16	(81%) in June 2001.

ZScore Jun-01 Equity

BellSouth Monthly Performance Summary

Florida, June 2001

A.1.1.1

A.1.1.2 A.1.1.3 A.1.1.4 A.1.1.5 A.1.1.6 A.1.2.1

A.1.2.2 A.1.2.3 A.1.2.4 A.1.2.5 A.1.2.6 A.1.3.1

A.1.3.2 A.1.3.3 A.1.3.4 A.1.3.5 A.1.3.6

A.1.4.1 A.1.4.2 A.1.4.3 A.1.4.4 A.1.4.5 A.1.4.6

A.1.5.1 A.1.5.2 A.1.5.3 A.1.5.4 A.1.5.5 A.1.5.6 A.1.6.1

A.1.6.2 A.1.6.3 A.1.6.4 A.1.6.5 A.1.6.6 A.1.8.1 A.1.8.1 A.1.8.2 A.1.8.3

A.1.8.4 A.1.8.5 A.1.8.6

A.1.9.1

Resale	e - Ordering						
% Reje	ected Service Requests - Mechanized						
0-7	Residence/FL(%)	Diagnostic		13.97%	37,826		Diagnost
0-7	Business/FL(%)	Diagnostic		23.70%	2,253		Diagnost
D-7	Design (Specials)/FL(%)	Diagnostic					Diagnost
D-7	PBX/FL(%)	Diagnostic		0.00%	2	1	Diagnost
D-7	Centrex/FL(%)	Diagnostic		· ···			Diagnost
D-7	ISDN/FL(%)	Diagnostic					Diagnost
% Reje	ected Service Requests - Partially Mechanized						
<u>)-7</u>	Residence/FL(%)	Diagnostic		29.48%	14.000		Diagnost
5-7	Business/FL(%)	Diagnostic		39,96%	1.827		Diagnost
D-7	Design (Specials)/FL(%)	Diagnostic		0.00%	1	-	Diagnost
D-7	PBX/FL(%)	Diagnostic		43.75%	32	-	Diagnost
2-7	Centrex/FL(%)	Diagnostic					Diagnost
0-7	ISDN/FL(%)	Diagnostic		40.00%	5		Diagnost
% Reir	ected Service Bequests - Non-Mechanized				•		
)-7	Residence/FL(%)	Diagnostic		52.31%	390		Diagnost
)-7	Business/FL(%)	Diagnostic		44.52%	620		Diagnost
)-7	Design (Specials)/FL(%)	Diagnostic		35.96%	89		Diagnost
)-7	PBX/FL(%)	Diagnostic		41.38%	29		Diagnost
)-7	Centrex/FL(%)	Diagnostic		40.00%	5		Diagnosti
)-7	ISDN/FL(%)	Diagnostic		34.78%	23		Diagnost
Reiect	Linterval - Mechanized		····				
)-8	Residence/FL(%)	>= 97% w in 1 hr		95.31%	5,285		NO
)-8	Business/FL(%)	>≈ 97% w in 1 hr		98.31%	534		YES
)-8	Design (Specials)/FL(%)	>= 97% w in 1 hr					
)-8	PBX/FL(%)	>= 97% w in 1 hr					
)-8	Centrex/FL(%)	>= 97% w in 1 hr					
D-8	ISDN/FL(%)	>= 97% w in 1 hr					
Reject	t Interval - Partially Mechanized - 24 hours						
)-8	Residence/FL(%)	>= 85% w in 24 hrs		This det	n not applicable	eller 5-1-2001, see below	· · · ·
D-8	Business/FL(%)	>= 85% w in 24 hrs	111 C. M. 18 A.	This date	a not applicable.	eller 5-1-2001, see balow	
D-8	Design (Specials)/FL(%)	>= 85% w in 24 hrs		This dat	not applicable	alter 5-1-2001, and below	
D-8	PBX/FL(%)	>= 85% w in 24 hrs		This did	a nat applicable.	ofter 5-1-2001, easy bology	
D-8	Centrex/FL(%)	>= 85% w in 24 hrs		This date	and explicable	after 5-1-2001, and below	
D-8	ISDN/FL(%)	>= 85% w in 24 hrs		This dat	a not applicable	eller 5-3-2001, see balow	
Reject	t Interval - Partially Mechanized - 18 hours						
D-8	Residence/FL(%)	>= 85% w in 18 hrs		90.74%	4,127		YES
)-8	Business/FL(%)	>= 85% w in 18 hrs		96.30%	730		YES
)-8	Design (Specials)/FL(%)	>= 85% w in 18 hrs			1		
)-8	PBXFL(%)	>≠ 85% w in 18 hrs		92.86%	14		YES
)-8	Centrex/FL(%)	>= 85% w in 18 hrs			1		
D-8	ISDN/FL(%)	>= 85% w in 18 hrs		100.00%	2		YES
Reject	t Interval - Non-Mechanized						
0-8	Residence/FL(%)	>= 85% w in 24 hrs		99.02%	204		YES
D-8	Business/FL(%)	>= 85% w in 24 hrs		99.64%	276		YES
2-8	Design (Specials//EI (%)	>= 85% w in 24 hrs		96.88%	32		YES
7-8	PRX/FI (%)	>= 85% w in 24 hrs		100.00%	12		YES
)- <u>8</u>	Centrev/EL (%)	>= 85% w in 24 hrs		50.00%	2		NO
	ISDN/FL(%)	>= 85% w in 24 hrs		100.00%	8		YES
	The state of the s				•		
2001	Desidence/FL/%	>= 95% w in 3 bre		98 32%	31 339		VES
<u> </u>	I realization L())	2 00 /0 m a 0 m a		00.0L/0			120

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Benchmark /

Analog

BST

Measure

BST

Volume

CLEC

Measure

CLEC

Volume

Standard Standard

Error

Deviation

BellSouth Monthly Performance Summary Florida, June 2001

	Florida, June 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard Standard			
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
A.1.9.2	0-9	Business/FL(%)	>= 95% w in 3 hrs			99.35%	1,385	-			YES
A.1.9.3	0-9	Design (Specials)/FL(%)	>= 95% w in 3 hrs								
A 9.4	0-9	PBX/FU(%)	>= 95% w in 3 hrs								
A 9.5	0.9		>= 95% w in 3 hrs								
A 9.6	0-9	ISDN/FL(%)	>= 95% w in 3 hrs								
A 10.1	0-9	Residence/FL(%)	>= 85% w in 36 hrs			The date	not applicable	Mer 5-1-2091.	eee below		
A 10.2	0-9	Business/FL(%)	>= 85% w in 36 hrs			This data	no averagedo	nter 5-1-0001,	ees balow		
A 10.3	0-9	Design (Specials)/FL(%)	>= 85% w in 36 hrs			The date	and applicable	Mer 5-1-2001,	see bolow		
A 10.4	0-9	PBX/FL(%)	>= 85% w in 36 hrs			This date	r_{1}	Au 3-1-2001,	een balow		
A 10.5	O-9	Centrex/FL(%)	>= 85% w in 36 hrs			This date		after 6-1-2001.	and feelow	ويتحقق والمحادي	
A.1.10.6	0-9	ISDN/FL(%)	>= 85% w in 36 hrs		The sector		<u>1. ()</u>		ter balar	(14) A 🖓	
	FOC	Timeliness - Partially Mechanized - 18 hours		_							
A.1.11.1	0-9	Residence/FL(%)	>= 85% w in 18 hrs			90.87%	10,753				YES
A.1.11.2	0-9	Business/FL(%)	>= 85% w in 18 hrs			94.73%	1,328				YES
A.1.11.3	0-9	Design (Specials)/FL(%)	>= 85% w in 18 hrs			100.00%	1				YES
A.1.11.4	0-9	PBX/FL(%)	>= 85% w in 18 hrs			<u>9</u> 5.00%	20				YES
A.1.11.5	0-9	Centrex/FL(%)	>= 85% w in 18 hrs								
A.1.11.6	O-9	IISDN/FL(%)	>= 85% w in 18 hrs			100.00%	3				YES
A.1.13.1	O-9	Residence/FL(%)	>= 85% w in 36 hrs			99.09%	220			فتقتصف	YES
A.1.13.2	O-9	Business/FL(%)	>= 85% w in 36 hrs			99.49%	391	1			YES
A.1.13.3	0-9	Design (Specials)/FL(%)	>= 85% w in 36 hrs			96.30%	54	1			YES
A.1.13.4	O-9	PBX/FL(%)	>= 85% w in 36 hrs			100.00%	15				YES
A.1.13.5	O-9	Centrex/FL(%)	>= 85% w in 36 hrs			100.00%	3				YES
A.1.13.6	0-9	ISDN/FL(%)	>= 85% w in 36 hrs			100.00%	18				YES
A 14.1	0-11	Residence/FL(%)	>= 95%			96.76%	37,826				YES
A 14.2	<u>Ö-11</u>	Business/FL(%)	>= 95%			85.18%	2,253				NO
A 14.3	0-11	Design (Specials)/FL(%)	>= 95%								
A 14.4	O-11	PBX/FL(%)	>= 95%			0.00%	2				NO
A 14.5	0-11	Centrex/FL(%)	>= 95%								
A 14.6	0-11	ISDN/FL(%)	>= 95%								
A 15.1	O-11	Residence/FL(%)	>= 95%			100.00%	14,000			ي الم	YES
A 15.2	0-11	Business/FL(%)	>= 95%			100.00%	1,827				YES
A 15.3	0-11	Design (Specials)/FL(%)	>= 95%			100.00%	1				YES
A 15.4	0-11	PBX/FL(%)	>= 95%			100.00%	32				YES
A 15.5	0-11	Centrex/FL(%)	>= 95%								
A 15.6	0-11	ISDN/FL(%)	>= 95%			100.00%	5				YES
	FOC	& Reject Response Completeness - Non-Mechanized									
A 16.1	0-11	Besidence/FI (%)	>= 95%			92 78%	526				NO
A 16.2	0-11	Business/FI (%)	>= 95%	1		94.03%	804				NO
A 16.3	0-11	Design (Specials)/FL(%)	>= 95%			88.99%	109				NO
A 16.0	0.11					82 35%	34				NO
A 165	0.11		>= 05%			100.00%					VEC
A 10.5	0.11		- 25%			86 21%					- TES
A 10.0	0-11					00.2170					
= :	FOC	& Reject Hesponse Completeness (Multiple Responses) - Mechanized				400.000	00				
A 17.1	0-11	Hesidence/FL(%)	>= 95%			100.00%	36,600				YES
A 17.2	0-11	Business/FL(%)	>= 95%			100.00%	1,919				YES
A 17.3	0-11	Design (Specials)/FL(%)	>= 95%								
A 17.4	0-11	PBX/FL(%)	>= 95%								
A 17.5	0-11	Centrex/FL(%)	>= 95%								
A 17.6	0-11	IISDN/FL(%)	>= 95%			L					
	FOC	& Reject Response Completeness (Multiple Responses) - Partially Mechanized									

.

Alisity Heater Volume Result Resul		Florida, June 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard			
A11912 Difference (C) Source (C) </th <th></th> <th></th> <th></th> <th>Analog</th> <th>Measure</th> <th>Volume</th> <th>Measure</th> <th>Volume</th> <th>Deviation</th> <th>Error</th> <th>ZScore</th> <th>Jun-01 Equity</th>				Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity	
A11.01 Control (1) Section (1)				-									
A 118.0 D11 Burnes (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ) \sim 6% $0,75\%$ 1.627 NO A 118.0 C11 Derived (LQ)	A.1.18.1	0-11	Residence/FL(%)	>= 95%			95.30%	14,000				YÉS	
A 118.1 Difference Source Source<	A.1.18.2	0-11	Business/FL(%)	>= 95%			90.75%	1,827				NO	
A118.4 D1 D21 D22/12 D23/12 D23 D23 <thd23< th=""></thd23<>	A.1.18.3	0-11	Design (Specials)/FL(%)	>= 95%			100.00%	1				YES	
A 18.6 Control (b)	A.1.18.4	0-11	PBX/FL(%)	>= 95%			93.75%	32				NO	
A11.05 C11 Extra Control Field Source Source <td>A.1.18.5</td> <td>0-11</td> <td>Centrex/FL(%)</td> <td>>= 95%</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>	A.1.18.5	0-11	Centrex/FL(%)	>= 95%									
At 1910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) NO A11910 Chi Readrong (L) > 6% But control (R) YEE A11911 Chi Readrong (L) > 6% But control (R) YEE A21111 Chi Readrong (L) Particle (R) State (R) State (R) State (R) A21112 Chi Readrong (L) Particle (R) Pee State (R) State (R) State (R) State (R) A21112 Chi Readrong (R) Readrong (R) Readrong (R) State (R)	A.1.18.6	0-11	JISDN/FL(%)	>= 95%			100.00%	5				YES	
A1191 O-II Program OFT (S) 3207 468 NO A1192 O-II Patarase (1, 0, 0) 3207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 9207 766 7766		FI	Re ci seponse										
A1192 D-11 Business (TQ) NO A1193 D-11 Parga (Speculy/F1(Sp) $\gamma \in S$ A1194 D-11 Speculy/F1(Sp) $\gamma \in S$ A1195 D-11 Speculy/F1(Sp) $\gamma \in S$ A1194 D-11 Speculy/F1(Sp) $\gamma \in S$ A1195 D-11 Speculy/F1(Sp) $\gamma \in S$ A1196 D-11 Speculy/F1(Sp) $\gamma \in S$ A1196 D-11 Speculy/F1(Sp) $\gamma \in S$ A1197 Speculy/F1(Sp) $\gamma \in S$ $\gamma \in S$ Constructioning $\gamma \in S$ $\gamma \in S$ $\gamma \in S$ A21111 P-4 Restance/10 caculy/for (Caruly/for (Carul	A.1.19.1	0-11	Residence/FL(%)	>= 95%		-	93.03%	488		· · · · ·		ŃÓ	
A1193 D-11 Description (pecusityFLG) y= 5%, b BE37% F7 YES A1194 D-11 Second (pecusityFLG) y= 5%, b 96.47% 97 YES A1194 D-11 Second (pecusityFLG) y= 5%, b 96.47% 97 YES A1194 D-11 Second (pecusityFLG) y= 5%, b 96.47% 97 YES A1194 D-11 Second (pecusityFLG) y= 5%, b 96.47% 97 YES A1194 D-11 Second (pecusityFLG) y= 5%, b 96.47% 97 YES A2111.12 P-4 Resistemation (pecusityFLG) Pecusity (pecus	A.1.19.2	0-11	Business/FL(%)	>= 95%			92.20%	756				NO	
A.119.4 A.119.3 A.119.5	A.1.19.3	0-11	Design (Specials)/FL(%)	>= 95%			98.97%	97				YES	
A.19.6 O.11 Center (n_{0}) >= 8% 100 00% 3 VES Reade - Provisioning Control (n_{0}) S = 86% 100 00% 3 1755 4.22 1755 4.23 1755 4.27 1755 4.26 1755 4.27 1755 4.27 1755 4.27 1755 4.27 1755 4.27 1755 4.27 1755 4.27 1755 4.27 4.27 1755	A.1.19.4	0-11	PBX/FL(%)	>= 95%			96.43%	28				YES	
A1.10.6 O.11 EDMA(10 >= 6% 100.05% 25 YES Reside - Provisioning Deter competion interval A1.11.2 PA Resider-of.10 circul/Subgat/M1(dayr) Pees 6.33 0.7165 3.30 1.776 4.462 0.12624 3.5794 YES A2.11.2.1 PA Residerock-10 circul/Subgat/M1(dayr) Pees 6.33 0.7 3.75 4.4 6.206 0.05678 3.6558 NO A2.11.2.1 PA Residerock-10 circul/Subgat/M1(dayr) Pees 6.33 77 3.75 4.4 6.206 7 7.5559 NO A2.12.1 PA Baumersc/10 circul/Subgat/M1(dayr) Pees 9.33 1.30 4.4538 1.31 2.200 3.345 0.07667 3.650 YES A2.12.2.1 PA Boumersc/10 circul/Subgat/M1(dayr) Pees 9.23 1.63 0.606 7 0.5568 NO A1.12.2 PA Bounersc/10 circul/Subgat/M1(dayr) Pees </td <td>A.1.19.5</td> <td>0-11</td> <td>Centrex/FL(%)</td> <td>>= 95%</td> <td></td> <td></td> <td>100.00%</td> <td>3</td> <td></td> <td></td> <td></td> <td>YES</td>	A.1.19.5	0-11	Centrex/FL(%)	>= 95%			100.00%	3				YES	
Reside - Provisioning Deter Completion Internal Add to instruct Organization (Logy) Res 6.33 57.165 3.00 1.765 4.92 Add to instruct Organization Cognitic (Logy) Res 6.36 5.00 3.00 3.00 3.00 3.00 3.00 1.00 3.00 3.00 1.00 3.00 <th c<="" td=""><td>A.1.19.6</td><td>0-11</td><td>ISDN/FL(%)</td><td>>= 95%</td><td></td><td></td><td>100.00%</td><td>25</td><td></td><td></td><td></td><td>YES</td></th>	<td>A.1.19.6</td> <td>0-11</td> <td>ISDN/FL(%)</td> <td>>= 95%</td> <td></td> <td></td> <td>100.00%</td> <td>25</td> <td></td> <td></td> <td></td> <td>YES</td>	A.1.19.6	0-11	ISDN/FL(%)	>= 95%			100.00%	25				YES
Network comparison interval A Presidences (1.0 incluit/Displation ¹ (days) Person A Presidences (1.0 incluit/Displation ¹ (days) Res 0.1 incluit/Displation ¹ (days) Sector A Presidences -10 concult/Ron Displation ¹ (days) Res 0.1 incluit/Displation ¹ (days) Sector A Presidences -10 concult/Ron Displation ¹ (days) Res 0.1 incluit/Ron Displation ¹ (days) A Presidences -10 concult/Ron Displation ¹ (days) Res 0.1 incluit/Ron Displation ¹ (days) A Presidences -10 concult/Ron Displation ¹ (days) Res 0.1 incluit/Ron Displation ¹ (days) A Presidences -10 concult/Ron Displation ¹ (days) Bas 1.0 incluit/Ron Displation ¹ (days) A Presidence -10 concult/Ron Displation ¹ (days) Bas 1.0 incluit/Ron Displation ¹ (days) A Presidence -10 concult/Ron Displation ¹ (days) Bas 1.0 incluit 0.0 incluit/Ron Displation ¹ (days) A Presidence -10 concult/Ron Displation ¹ (days) Ba													
Order Completion Interval Section 2011 Press Section 2011 Press Section 2011 Press Pres		Resale	- Provisioning										
A2.11.11 P4 Feasure (10 cross) (10 cr		Order	Completion Interval										
A211.12 P4 Hesseney-(10 encutAbyo-Depatch P(day) Pes 0.61 0.2040 1.061 0.0076 28.508 100 A21.12 P4 Hesseney-(10 encutAbyo-Depatch P(day) Pes 5.50 70 3.75 4 2.046 1061 0.0276 28.508 100 A21.12 P4 Businesy-(10 encutAbyo-Depatch P(day) Pes 5.50 70 3.75 4 2.046 1061 0.0276 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 1.379 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.0756 4.8758 0.07565 4.8758 2.05788 7.057	A21111	P-4	Besidence/<10 circuits/Dispatch/El (days)	Bes	4 33	37 188	390	1 755	4 922	0 12024	2 5 7 9 4	VEC	
A2.11.21 P4 Pessmersion 0 accustors accustors packed (lays) Pessmersion 0 accustors accus	A21.1.12	P-4	Besidence/<10 circuits/Non-Dispatch/El (days)	Bes	0.81	623 649	1.08	30,910	1 161	0.12024	-30 5038	NO	
A21.122 P4 Passemeric-10 cacadbranch-Dispatch/F1 (days) P10	A.2.1.1.2.1	P-4	Residence/>=10 circuits/Dispatch/El (days)	Bes	3.50	70	375	4	2.045	1.05141	-0 2334	VES	
A 2.12.11 P-4 Busness(-10) scutta/Boadorf (49y) Bus 3.02 $22,187$ 3.70 220 6.773 0.3780 1.6775 NO A 2.12.12 P-4 Busness(-10) scuta/Boadorf (4yy) Bus 11.40 3.56 6.500 77 23.347 8.91540 0.0757 VFS A 2.12.22 P-4 Busness(-10) scuta/Boadorf(-10y) Bus 11.40 3.56 6.500 77 23.347 8.91540 0.0757 VFS A 2.13.12 P-4 Business(-10) scuta/Boadorf(-10) Bus 11.41 4.6480 2.03 77 23.367 0.3757 VFS A 2.13.12 P-4 Design (Specially-10) cruta/Dogatorf(-10) Design 12.41 4.6480 2.03 77 2.356 3.311 6.3530 7.56 A 2.14.12 P-4 Design (Specially-10) cruta/Dogatorf(-10) Design 12.41 4.53 8.76 6.35267 7.7853 NO A 2.14.12 P-4 PBX/-10 cruta/Dogatorf(-10) Design (Specially-10) Design (Specially-10) 12.652 773 </td <td>A.2.1.1.2.2</td> <td>P-4</td> <td>Besidence/>=10 circuits/Non-Dispatch/FL (days)</td> <td>Bes</td> <td>211</td> <td>3</td> <td></td> <td>•</td> <td>1 379</td> <td>1.05141</td> <td>-0.2004</td> <td>F</td>	A.2.1.1.2.2	P-4	Besidence/>=10 circuits/Non-Dispatch/FL (days)	Bes	211	3		•	1 379	1.05141	-0.2004	F	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.2.1.1	P-4	Business/<10 circuits/Dispatch/FL(days)	Bus	3.02	32,187	3 70	329	6773	0.37529	-1 8175	NO	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.2.1.2	P-4	Business/<10 circuits/Non-Dispatch/FL(days)	Bus	1.30	44.538	1.31	2 230	3 345	0.07258	-0.0737	VES	
A2.12.22 P4 Buinnesso=10 crouts/hore-Departor/F1(days) Buinnesso=10 crouts/hore-Departor/F1(days) Design (Specially-10 crouts/hore-Departor/F1(days)) Design (Specially-10 crouts/hore-Departor/F1(days)) A2.13.12 P-4 Design (Specially-10 crouts/hore-Departor/F1(days)) Design (Specially-10 crouts/hore-Departor/F1(days)) Design (Specially-10 crouts/hore-Departor/F1(days)) Design (Specially-10 crouts/hore-Departor/F1(days)) A2.13.22 P-4 Design (Specially-10 crouts/hore-Departor/F1(days)) Pix (Specially-10 crouts/hore-Departor/F1(days)) Pix (Specially-10 crouts/hore-Departor/F1(days)) No A2.14.12 P-4 Pix (Specially-10 crouts/hore-Departor/F1(days)) Pix (Specially-10 crouts/hore-Departor/F1(days)) Pix (Specially-10 crouts/hore-Departor/F1(days)) No A2.15.12 P-4 Center (Specially-10 crouts/hore-Departor/F1(days)) Centrec (Specially-10 crouts/hore-Departor/F1(A.2.1.2.2.1	P-4	Business/>=10 circuits/Dispatch/FL (days)	Bus	11.40	335	6.90	7	23.347	8 91594	0.5040	YES	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.2.2.2	P-4	Business/>=10 circuits/Non-Dispatch/FL(days)	Bus	1.14	64	0.93	9	0.926	0.32976	0.6498	YES	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.3.1.1	P-4	Design (Specials)/<10 circuits/Dispatch/FL(days)	Design	19.41	4,589	2.03	77	27.335	3 14116	5 5310	YES	
A2.13.21 P4 Desgn (Specially-10 circuts/Depatch/FL(days) Design (A1.1) P4 Desgn (Specially-10 circuts/Depatch/FL(days) Design (A1.1) P4 P5W (10 circuts/Depatch/FL(days) P5 P4 P5W (10 circuts/Depatch/FL(days) P5 P4 P5W (10 circuts/Depatch/FL(days) P5 P6	A.2.1.3.1.2	P-4	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Design	2.72	3,120	0.90	583	5.606	0.25292	7 2274	YES	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.3.2.1	P-4	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Design	4.55	28			2.472				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.3.2.2	P-4	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Design	3.61	41	8.74	19	2.608	0.72365	-7.0863	NO	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.4.1.1	P-4	PBX/<10 circuits/Dispatch/FL(days)	PBX	14.09	101	4.95	7	15.592	6.09390	1,4995	YES	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.4.1.2	P-4	PBX/<10 circuits/Non-Dispatch/FL(days)	PBX	2.66	337	1.61	26	3.750	0.76332	1.3656	YES	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	A.2.1.4.2.1	P-4	PBX/>=10 circuits/Dispatch/FL(days)	PBX	8.00	3	4.00	2	3.578	3.26598	1.2247	YES	
A2.15.1.1 P-4 Centrex/1.0 ionustr/Dispatch/FL(days) Centrex 7.19 613 5.60 5 11.600 5.24462 0.26466 VES A2.15.12 P-4 Centrex/1.0 ionustr/Dispatch/FL(days) Centrex 8.94 36 6.00 1 10.101 10.24003 0.2875 VES A2.15.12 P-4 Centrex/1.0 ionustr/Biopatch/FL(days) Centrex 8.94 36 6.00 1 10.101 10.24003 0.2875 VES A2.15.12 P-4 Centrex/1.0 ionustr/Biopatch/FL(days) Centrex 8.94 36 6.00 1 10.101 10.24003 0.2875 VES A2.16.11 P-4 ISDM/10 conustr/Biopatch/FL(days) ISDN 4.63 502 5.78 23 13.044 2.79220 0.4139 VES A2.16.21 P-4 ISDM/-10 conustr/Biopatch/FL(days) ISDN 4.63 502 5.78 23 13.041 0.900 VES A2.21.12 P-1 Residence/10 circutsr/Biopatch/FL(days) Res 10.77 60 10.00 1 18.500 0.900 VES	A.2.1.4.2.2	P-4	PBX/>=10 circuits/Non-Dispatch/FL(days)	РВХ	1.40	81	2.50	6	1.899	0.80325	-1.3725	YES	
A2.15.12 P-4 Centrex/-10 circuits/Non-Depatch/FL(days) Centrex 1.51 1.704 2.48 48 2.167 0.31710 3.0406 NO A2.15.2.1 P-4 Centrex/>-10 circuits/Non-Depatch/FL(days) Centrex 2.0 131 4.11 3 2.739 1.58915 1.3298 VES A2.16.12 P-4 Centrex/>Locuts/Non-Depatch/FL(days) ISDN 40.72 1.072 0.33 1 44.150 44.17049 0.9144 VES A2.16.21 P-4 ISDN-10 circuits/Non-Depatch/FL(days) ISDN 4.63 502 5.78 2.3 13.064 2.7820 0.4138 VES A2.16.21 P-4 ISDN-10 circuits/Facily/FL(days) ISDN 4.63 502 5.78 2.3 13.064 2.7820 0.4138 VES A2.16.21 P-4 Residence/-10 circuits/Facily/FL(days) Res 1.077 6.0 1.000 1 1.8100 0.0419 VES A2.21.11 P-1 Residence/-10 circuits/Facily/FL(days) Res 1.077 60 1.000 1 1.8150 0.0419 YE	A.2.1.5.1.1	P-4	Centrex/<10 circuits/Dispatch/FL(days)	Centrex	7.19	613	5.80	5	11.680	5.24462	0.2646	YES	
A2.15.2.1 P-4 Centrex>-10 circuits/Dispatch/FL(days) Centrex 8.94 36 6.00 1 10.101 10.24003 0.2875 YES A2.15.2.1 P-4 Centrex>-10 circuits/Non-Dispatch/FL(days) Centrex 8.94 36 6.00 1 10.101 10.24003 0.2875 YES A2.15.2.1 P-4 ISDN/10 circuits/Non-Dispatch/FL(days) SDN 40.72 1.072 0.33 1 44.150 44.17049 0.9144 YES A2.16.2.2 P-4 ISDN/s-10 circuits/Non-Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.064 2.7920 0.4139 YES A2.16.2.2 P-4 ISDN/s-10 circuits/Non-Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.064 2.7920 0.4139 YES A2.16.2.2 P-4 ISDN/s-10 circuits/Facilty/FL(days) Res ISDN 4.63 502 5.78 23 13.064 2.7920 0.4139 YES A2.21.12 P-1 Residence/s-10 circuits/Facilty/FL(days) Res 10.77 60 10.00 1	A.2.1.5.1.2	P-4	Centrex/<10 circuits/Non-Dispatch/FL(days)	Centrex	1.51	1,704	2.48	48	2.167	0.31710	-3.0406	NO	
A2.15.22 P-4 Centrex, = 10 crouts/hon-Dispatch/E(days) Centrex, = 2.00 313 4.11 3 2.739 1.58016 1.5208 YES A2.15.11 P-4 ISDN410 crouts/bach/E(days) ISDN 40.72 1.072 0.33 1 44.15049 0.9144 YES A2.16.12 P-4 ISDN/10 crouts/bach/E(days) ISDN 40.72 1.072 0.33 1 44.15049 0.9144 YES A2.16.21 P-4 ISDN/510 crouts/bach/E(days) ISDN 4.63 502 5.78 23 13.094 2.7920 0.4139 YES A2.16.21 P-4 ISDN/510 crouts/Facity/FL(days) ISDN 4.63 502 5.78 23 13.094 2.7920 0.4139 YES A2.16.11 P-1 Residence/10 crouts/Facity/FL(days) ISDN ISDN ISDN 1.3 7.050 1.98141 0.690 YES A2.21.13 P-1 Residence/-10 crouts/Facity/FL(days) Res 10.77 60 10.00 1 18.100 8.0103 0.0419 YES A22.1.2 P.1	A.2.1.5.2.1	P-4	Centrex/>=10 circuits/Dispatch/FL(days)	Centrex	8.94	36	6.00	1	10.101	10.24003	0.2875	YES	
A.2.1.6.1.1 P-4 ISDN/:10 circuits/Dispatch/FL(days) ISDN 40.72 1.072 0.33 1 44.150 44.17049 0.9144 YES A.2.1.6.12 P-4 ISDN/:10 circuits/Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.094 2.79220 -0.1139 YES A.2.1.6.2 P-4 ISDN/:10 circuits/Mon-Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.094 2.79220 -0.1139 YES A.2.1.6.12 P-1 Residence/c10 circuits/Facility/FL(days) Res 6.69 485 7.31 13 7.050 1.96141 0.6990 YES A.2.2.1.12 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.150 18.0103 0.0419 YES A.2.2.1.2 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.150 18.0103 0.0419 YES A.2.2.1.2 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.150	A.2.1.5.2.2	P-4	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Centrex	2.00	313	4.11	3	2.739	1.58915	-1.3288	YES	
A.2.16.12 P-4 ISDN/>-10 circuits/Ron-Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.094 2.79220 0.4139 YES A.2.16.21 P-4 ISDN/>=10 circuits/Ron-Dispatch/FL(days) ISDN 4.63 502 5.78 23 13.094 2.79220 0.4139 YES A.2.16.21 P-4 ISDN/>=10 circuits/Ron-Dispatch/FL(days) Res 500 ISDN	A.2.1.6.1.1	P•4	ISDN/<10 circuits/Dispatch/FL(days)	ISDN	40.72	1,072	0.33	1	44.150	44.17049	0.9144	YES	
A2.16.2.1 P-4 (SDN/>-10 circuts/Depatch/FL(days) ISDN A2.16.2.2 P-4 (SDN/>-10 circuts/Rom-Dispatch/FL(days) ISDN Meld Orders ISDN A2.21.1.1 P-1 Residence/-10 circuts/Facility/FL(days) Res 8.69 485 7.31 13 7.050 1.98141 0.6990 YES A2.21.1.2 P-1 Residence/-10 circuts/Facility/FL(days) Res 10.77 60 10.00 1 18.100 0.0419 YES A2.21.2.1 P-1 Residence/-10 circuts/Cher/FL(days) Res 10.77 60 10.00 1 18.100.03 0.0419 YES A2.21.2.2 P-1 Residence/-10 circuts/Cher/FL(days) Res 10.77 60 10.00 1 18.100.03 0.0419 YES A2.21.2.3 P-1 Residence/-10 circuts/Cher/FL(days) Res 10.77 60 10.00 1 17.252 0.1780 YES A2.22.1.2 P-1 Business/c10 circuts/Cher/FL(days) Res 11.89 142 9.80 5 25.769 11.7252 0.1780 YES	A.2.1.6.1.2	P-4	ISDN/<10 circuits/Non-Dispatch/FL(days)	ISDN	4.63	502	5.78	23	13.094	2.79220	-0.4139	YES	
A2.1.6.22 P-4 ISDN>=10 circuits/Non-Dispatch/FL(days) Held Orders ISDN Held Orders Residence/10 circuits/Facility/FL(days) Res 8.69 485 7.31 13 7.050 1.98141 0.6990 YES A.22.1.1.1 P-1 Residence/10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.150 18.30103 0.0419 YES A.22.1.2.1 P-1 Residence/-10 circuits/Equpment/FL(days) Res 10.77 60 10.00 1 18.150 18.30103 0.0419 YES A.22.1.2.2 P-1 Residence/-10 circuits/Equpment/FL(days) Res 10.77 60 10.00 1 18.150 18.30103 0.0419 YES A.22.1.2 P-1 Residence/-10 circuits/Equpment/FL(days) Res 10.77 60 10.00 1 18.150 18.30103 0.0419 YES A.22.1.2 P-1 Residence/-10 circuits/Equpment/FL(days) Bus 11.89 142 9.80 5 25.769 11.72552 0.1780 YES A.22.2.1 P-1	A.2.1.6.2.1	P-4	ISDN/>≈10 circuits/Dispatch/FL(days)	ISDN	1 1			-					
Held Orders A.22.1.1 P-1 Residence/-10 circuits/Facility/FL(days) Res 8.69 485 7.31 13 7.050 1.98141 0.6990 YES A.22.1.2 P-1 Residence/-10 circuits/Facility/FL(days) Res	A.2.1.6.2.2	P-4	ISDN/>=10 circuits/Non-Dispatch/FL(days)	ISDN								·	
A.22.1.1 P-1 Residence/c10 circuits/Facility/FL(days) Res 8.69 485 7.31 13 7.050 1.98141 0.6990 YES A.22.1.2 P-1 Residence/c10 circuits/Equipment/FL(days) Res 10.77 60 10.00 1 18.30103 0.0419 YES A.22.1.2 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.30103 0.0419 YES A.22.1.2.1 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.30103 0.0419 YES A.22.1.2.3 P-1 Residence/c10 circuits/Facility/FL(days) Res 10.77 60 10.00 1 18.30103 0.0419 YES A.22.1.2.3 P-1 Residence/c10 circuits/Facility/FL(days) Res 11.89 142 9.80 5 25.769 11.72552 0.1780 YES A.22.2.1.2 P-1 Business/c10 circuits/Facility/FL(days) Bus 19.88 17 26.856		Held O	rders	_									
A221.12 P-1 Residence/<10 circuits/Figuipment/FL(days)	A.2.2.1.1.1	P-1	Residence/<10 circuits/Facility/FL(days)	Res	8.69	485	7.31	13	7.050	1.98141	0.6990	YES	
A.22.1.3 P-1 Residence/<10 circuits/Cating/FL(days)	A.2.2.1.1.2	P-1	Residence/<10 circuits/Equipment/FL(days)	Res									
A.22.1.21 P-1 Residence/>=10 circuits/Facility/FL(days) Res	A.2.2.1.1.3	P-1	Residence/<10 circuits/Other/FL(days)	Res	10.77	60	10.00	1	18.150	18.30103	0.0419	YES	
A221.22 P-1 Residence/>=10 circuits/Cupment/FL(days) Res Image: Constraint of the c	A.2.2.1.2.1	P-1	Residence/>=10 circuits/Facility/FL(days)	Res									
A.22.1.3 P-1 Residence/>=10 circuits/Chein/FL(days) Res	A.2.2.1.2.2	P-1	Residence/>=10 circuits/Equipment/FL(days)	Res						1			
A.222.1.1 P-1 Business/<10 circuits/Facility/FL(days)	A.2.2.1.2.3	P-1	Residence/>=10 circuits/Other/FL(days)	Res									
A.222.12 P-1 Business/<10 circuits/Equipment/FL(days)	A.2.2.2.1.1	P-1	Business/<10 circuits/Facility/FL(days)	Bus	11.89	142	9.80	5	25.769	11.72552	0.1780	YES	
A.22.2.1.3 P-1 Business/<10 circuits/Other/FL(days)	A.2.2.2.1.2	P-1	Business/<10 circuits/Equipment/FL(days)	Bus									
A.2.2.2.1 P.1 Business/s=10 circuits/Facility/FL(days) Bus 9.33 3 7.572 1 A.2.2.2.2 P.1 Business/s=10 circuits/Facility/FL(days) Bus Image: Control of Contendia of Control of Control of Control of Control of Co	A.2.2.2.1.3	P-1	Business/<10 circuits/Other/FL(days)	Bus	19.88	17			26.856				
A.22.22 P-1 Business/s=10 circuits/Equipment/FL(days) Bus Image: Constraint of the state of the	A.2.2.2.2.1	P-1	Business/>=10 circuits/Facility/FL(days)	Bus	9.33	3			7.572				
A.22.2.3 P-1 Business/>=10 circuits/Other/FL(days) Bus Image: Constant of the circuits/FL(days) Bus Image: Constant of the circuits/FL(days) Emage: Constant of the circuits/F	A.2.2.2.2.2	P-1	Business/>=10 circuits/Equipment/FL(days)	Bus									
A.22.3.1.1 P-1 Design (Specials)/<10 circuits/Facility/FL(days) Design 8.25 4 3.00 2 5.188 4.49303 1.1685 YES A.22.3.1.2 P-1 Design (Specials)/<10 circuits/Facility/FL(days)	A.2.2.2.2.3	P-1	Business/>=10 circuits/Other/FL(days)	Bus									
A.2.2.3.1.2 P-1 Design (Specials)/<10 circuits/Equipment/FL(days) Design	A.2.2.3.1.1	P-1	Design (Specials)/<10 circuits/Facility/FL(days)	Design	8.25	4	3.00	2	5.188	4.49303	1.1685	YES	
A.2.2.3.1.3 P-1 Design (Specials)/<10 circuits/Other/FL(days) Design 47.97 33 76.151 A.2.2.3.2.1 P-1 Design (Specials)/>=10 circuits/Facility/FL(days) Design 0 0 0	A.2.2.3.1.2	P-1	Design (Specials)/<10 circuits/Equipment/FL(days)	Design									
A.2.2.3.2.1 P-1 Design (Specials)/>=10 circuits/Facility/FL(days) Design	A.2.2.3.1.3	P-1	Design (Specials)/<10 circuits/Other/FL(days)	Design	47.97	33			76.151				
	A.2.2.3.2.1	P-1	Design (Specials)/>=10 circuits/Facility/FL(days)	Design									

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ZScore Jun-01 Equity

BellSouth Monthly Performance Summary Florida, June 2001

	·······, ·····	Analog	Measure	Volume	Measure	Volume	Deviation
A00300	P.1 Design (Specials)/>=10 circuits/Equipment/El (days)	Design			r1		
A 22323	P-1 Design (Specials)/>=10 circuits/Other/FL (days)	Design	48.00	1	<u>+</u> +		0.000
A 2 2 4 1 1	P-1 PBX/<10 circuits/Eacility/El (days)	- РВХ		· · ·			0.000
A.2.2.4.1.2	P-1 PBX/<10 circuits/Equipment/FL(days)	PBX					1
A22413	P-1 PBX/<10 circuits/Other/FL(days)	PBX					
A.2.2.4.2.1	P-1 PBX/>=10 circuits/Facility/FL(days)	РВХ					1
A.2.2.4.2.2	P-1 PBX/>=10 circuits/Equipment/FL(days)	PBX					
A.2.2.4.2.3	P-1 PBX/>=10 circuits/Other/FL(days)	PBX					1
A.2.2.5.1.1	P-1 Centrex/<10 circuits/Facility/FL(days)	Centrex	9.73	11			5.968
A.2.2.5.1.2	P-1 Centrex/<10 circuits/Equipment/FL(days)	Centrex					
A.2.2.5.1.3	P-1 Centrex/<10 circuits/Other/FL(days)	Centrex					
A.2.2.5.2.1	P-1 Centrex/>=10 circuits/Facility/FL(days)	Centrex					
A.2.2.5.2.2	P-1 Centrex/>=10 circuits/Equipment/FL(days)	Centrex					1
A.2.2.5.2.3	P-1 Centrex/>=10 circuits/Other/FL(days)	Centrex			t 1		
A.2.2.6.1.1	P-1 ISDN/<10 circuits/Facility/FL(days)	ISDN	21.38	8			18.470
A.2.2.6.1.2	P-1 (SDN/<10 circuits/Equipment/FL(days)	ISDN					
A.2.2.6.1.3	P-1 ISDN/<10 circuits/Other/FL(days)	ISDN					· · · · ·
A.2.2.6.2.1	P-1 ISDN/>=10 circuits/Facility/FL(days)	ISDN			1		
A.2.2.6.2.2	P-1 ISDN/>=10 circuits/Equipment/FL(days)	ISDN					1
A.2.2.6.2.3	P-1 ISDN/>=10 circuits/Other/FL(days)	ISDN					
	% Jeonardies - Mechanized				4		
A 2 4 1	P-2 Residence/FI (%)	Bes	0.62%	721.081	0.44%	34,909	
A242	P-2 Business/EI (%)	Bus	1.87%	79,714	1.16%	3.020	
A 2 4 3	P-2 Design (Specials)/EI (%)	Design	18.56%	9.562	0.66%	457	
A 2 4 4	P-2 PBX/FI (%)	PBX	6.50%	600	2.63%	38	
A 2 4 5	P-2 Centrev/EI (%)	Centrex	4.41%	2.814	0.00%	63	
A246	P-2 ISDN/FL(%)	ISDN	9,97%	2.657	6.25%	32	
/	% Jeonardies - Non-Mechanized						
4251	P-2 Besidence/FI (%)	Diagnostic			1.41%	213	1
A 25 2	P-2 Business/FI (%)	Diagnostic			1.71%	234	
A 2 5 3	P-2 Design (Specials)/El (%)	Diagnostic			1.09%	274	
A 25 A	P-2 PRV/FI (%)	Diagnostic			0.00%	14	
A 25 5	P-2 Centrev/FL/%)	Diagnostic	1		0.00%	16	
A.2.5.6	P-2 USDN/FL(%)	Diagnostic			30.77%	13	
ralloro -	Average legnardy Notice Interval - Mechanized						
4271	P-2 Residence/FL (hours)	>= 48 brs			189.95	152	1
A272	P-2 Business/FI (bours)	>= 48 hrs			216.00	35	
A 273	P-2 Design (Specials)/El (hours)				128.00	3	
A 274	P-2 PBX/FI (hours)	>= 48 hrs			312.00	1	
A 275	P-2 Centrev/El (hours)	~~~ >= 48 hrs					
A276	P-2 ISDN/FL (hours)	>= 48 hrs			324.00	2	- Contraction (1997)
, all to	Aueroan Inconstruct Interval - Non-Manhanizard						
A 2 B 1		Diagnostic			240.00	3	
A.2.0.1	P 2 Pusiness (El (hours)	Diagnostic			294.00	4	
A.2.0.2	P-2 Dusinessin (Choure)	Diagnostic			128.00	3	
A.2.0.3	P-2 Design (Special strict floors)	Diagnostic			123.00		
A.2.0.4	P-2 PBWFL(hours)	Diagnostic					
A.2.0.3	P-2 ISDN/EL (hours)	Diagnostic			468.00	4	~
A.2.6.0		- Chagnosto			100.00	-	
	% Jeopardy Notice >= 48 nours - mechanized	1 05% >= 48 brs			00.019/	11	
A.2.9.1	P-2 [Kesidence/FL(%)	95% >= 40 ms			70 739/	11	
A.2.9.2	P+2 Business/FL(%)	95% >= 48 Mrs			12.13%		
A.2.9.3	P-2 Design (Specials)/FL(%)	95% >= 48 nrs					
A.2.9.4	P-2 PBX/FL(%)	95% >= 48 nrs					
A.2.9.5	P-2 Centrex/FL(%)						
A.2.9.6	P-2 SUN/FL(%)	90% >= 40 firs					
	% Jeopardy Notice >= 48 hours - Non-Mechanized						

Benchmark /

BST

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BST

CLEC

CLEC

Standard Standard

Error

0.00043

0.00251

0.01862

0.04124

0.02615 0.05329 4.3520

2.8166

9.6183

0.9381

1.6854 0.6988 YES

YES

YES

YES

YES

Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic

> YES YES YES YES

Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic

> NO NO

BellSouth Monthly Performance Summary Florida, June 2001

	Flori	da, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
											. ,
A.2.10.1	P-2	Residence/FL(%)	Diagnostic			89.52%	124				Diagnostic
A.2.10.2	P-2	Business/FL(%)	Diagnostic			100.00%	19				Diagnostic
A.2.10.3	P-2	Design (Specials)/FL(%)	Diagnostic			0.00%	1				Diagnostic
A.2.10.4	P-2	PBX/FL(%)	Diagnostic								Diagnostic
A.2.10.5	P-2	Centrex/FL(%)	Diagnostic				_				Diagnostic
A.2.10.6	P-2	ISDN/FL(%)	Diagnostic								Diagnostic
			-								
A.2.11.1.1.1	P-3	Besidence/<10 circuits/Dispatch/EL(%)	Bes	5 53%	47 584	3 74%	1 954		0.00528	3 3003	VES
A2111.1.2	P.3	Besidence/<10 circuits/Non-Dispatch/EL(%)	Res	0.11%	669 560	0.14%	33 424		0.00028	-27242	
A211.1.2.1	P-3	Residence/>=10 circuits/Dispatch/Ft (%)	Bes	4 30%	93	0.00%	4		0.00019	0.4152	VES
A.2.11.1.2.2	P-3	Residence/>=10 circuits/Non-Dispatch/FL(%)	Bes	0.00%	3	0.00 / 0			0.10000	0.4132	
A211211	P-3	Business/<10 circuits/Dispatch/FL(%)	Bus	2,30%	32 933	5 29%	435		0.00724	-4 1 255	NO
A.2.11.2.1.2	P-3	Business/<10 circuits/Non-Dispatch/FL(%)	Bus	0.20%	45 097	0.26%	2 726		0.00724	-0 5931	VES
A211221	P-3	Business/>=10 circuits/Dispatch/El (%)	Bus	6.60%	379	0.00%	13		0.07001	0.9421	VES
A.2.11.2.2.2	P-3	Business/>⇒10 circuits/Non-Dispatch/EI (%)	Bus	0.00%	64	0.00%	19		0.00000	0.0 121	VES
A.2.11.3.1.1	P-3	Design (Specials)/<10 circuits/Dispatch/FL(%)	Desian	5.17%	4.661	4.63%	108		0.02155	0.2510	YES
A.2.11.3.1.2	P-3	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	Design	0.92%	3,165	0.00%	592		0.00427	2.1475	YES
A.2.11.3.2.1	P-3	Design (Specials)/>=10 circuits/Dispatch/FL(%)	Design	3.57%	28						
A.2.11.3.2.2	P-3	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	Design	4.76%	42	0.00%	19		0.05888	0.8088	YES
A.2.11.4.1.1	P-3	PBX/<10 circuits/Dispatch/FL(%)	РВХ	6.60%	106	10.00%	10		0.08216	-0.4134	YES
A.2.11.4.1.2	P-3	PBX/<10 circuits/Non-Dispatch/FL(%)	PBX	1.71%	351	3.03%	33		0.02360	-0.5597	YES
A.2.11.4.2.1	P-3	PBX/>=10 circuits/Dispatch/FL(%)	PBX	0.00%	3	0.00%	2		0.00000		YES
A.2.11.4.2.2	P-3	PBX/>=10 circuits/Non-Dispatch/FL(%)	PBX	0.00%	88	0.00%	8		0.00000		YES
A.2.11.5.1.1	P-3	Centrex/<10 circuits/Dispatch/FL(%)	Centrex	4.81%	645	0.00%	5		0.09603	0.5005	YES
A.2.11.5.1.2	P-3	Centrex/<10 circuits/Non-Dispatch/FL(%)	Centrex	0.17%	1,722	0.00%	68		0.00516	0.3379	YES
A.2.11.5.2.1	P-3	Centrex/>=10 circuits/Dispatch/FL(%)	Centrex	2.27%	44	0.00%	1		0.15072	0.1508	YES
A.2.11.5.2.2	P-3	Centrex/>=10 circuits/Non-Dispatch/FL(%)	Centrex	0.00%	317	0.00%	6		0.00000		YES
A.2.11.6.1.1	P-3	ISDN/<10 circuits/Dispatch/FL(%)	ISDN	5.08%	1,102	9.09%	11		0.06655	-0.6025	YES
A.2.11.6.1.2	P-3	ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN	2.94%	511	0.00%	33		0.03032	0.9682	YES
A.2.11.6.2.1	P-3	ISDN/>=10 circuits/Dispatch/FL(%)	ISDN								
A.2.11.6.2.2	P-3	ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN								
A.2.12.1.1.1	P-9	Residence/<10 circuits/Dispatch/FL(%)	Res	7.97%	48,560	6.93%	3,090		0.00502	2.0777	YES
A.2.12.1.1.2	P-9	Residence/<10 circuits/Non-Dispatch/FL(%)	Res	3.61%	702,968	4.12%	48,383		0.00088	-5.8023	NO
A.2.12.1.2.1	P-9	Residence/>=10 circuits/Dispatch/FL(%)	Res	10.78%	102	0.00%	2		0.22147	0.4869	YES
A.2.12.1.2.2	P-9	Residence/>=10 circuits/Non-Dispatch/FL(%)	Res	0.00%	2						
A.2.12.2.1.1	P-9	Business/<10 circuits/Dispatch/FL(%)	Bus	3.27%	36,009	6.68%	569		0.00751	-4.5440	NO
A.2.12.2.1.2	P-9	Business/<10 circuits/Non-Dispatch/FL(%)	Bus	5.44%	46,498	4.55%	3,585		0.00393	2.2647	YES
A.2.12.2.2.1	P-9	Business/>=10 circuits/Dispatch/FL(%)	Bus	5.83%	343	0.00%	10		0.07517	0.7757	YES
A.2.12.2.2.2	P-9	Business/>=10 circuits/Non-Dispatch/FL(%)	Bus	9.09%	11	0.00%	3		0.18725	0.4855	YES
A.2.12.3.1.1	P-9	Design (Specials)/<10 circuits/Dispatch/FL(%)	Design	4.97%	4,061	0.00%	8		0.07694	0.6465	YES
A.2.12.3.1.2	P•9	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	Design	0.83%	121	0.00%	1		0.09091	0.0909	YES
A.2.12.3.2.1	P-9	Design (Specials)/>=10 circuits/Dispatch/FL(%)	Design	0.00%	5						
A.2.12.3.2.2	P-9	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	Design								
A.2.12.4.1.1	P-9	PBX/<10 circuits/Dispatch/FL(%)	PBX	1.96%	102	0.00%	9		0.04821	0.4067	YES
A.2.12.4.1.2	P-9	PBX/<10 circuits/Non-Dispatch/FL(%)	PBX	2.67%	337	8.82%	34		0.02901	-2.1209	NO
A.2.12.4.2.1	P-9	PBX/>=10 circuits/Dispatch/FL(%)	PBX	0.00%	1	0.00%	8		0.00000		YES
A.2.12.4.2.2	P-9	PBX/>=10 circuits/Non-Dispatch/FL(%)	PBX	2.86%	70	0.00%	20		0.04224	0.6764	YES
A.2.12.5.1.1	P-9	Centrex/<10 circuits/Dispatch/FL(%)	Centrex	1.35%	740	0.00%	6		0.04733	0.2855	YES
A.2.12.5.1.2	P-9	Centrex/<10 circuits/Non-Dispatch/FL(%)	Centrex	1.19%	1,340	1.09%	92		0.01171	0.0915	YES
A.2.12.5.2.1	P-9	Centrex/>=10 circuits/Dispatch/FL(%)	Centrex	1.82%	55	100.00%			0.13482	-7.2826	NO
A.2.12.5.2.2	P-9	Centrex/>=10 circuits/Non-Dispatch/FL(%)	Centrex	0.33%	306	0.00%	13		0.01616	0.2022	YES
A.2.12.6.1.1	P-9	ISDN/<10 circuits/Dispatch/FL(%)	ISDN	0.00%	9/0	0.00%	20		0.00000		YES
A.2.12.6.1.2	P-9	ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN	0.00%	952	0.00%	28		0.00000		YES
A.2.12.6.2.1	P-9	ISDN/>=10 circuits/Dispatch/FL(%)	ISDN								
A.2.12.6.2.2	P-9	JISDN/>=10 circuits/Non-Dispatch/FL(%)	ISUN								
	Averag	e Completion Notice Interval - Mechanized									
A.2.14.1.1.1	P-5	Residence/<10 circuits/Dispatch/FL(hours)	Res	3.99	34,216	4.21	1,865	17.476	0.41556	-0.5398	YES
			-							· · · · · · · · · · · · · · · · · · ·	

	Flori	da, June 2001
1	<u>n :</u>	Conidence (10 curvite Non-Disnatch/EI (hours)
.1.2	r-0	Residence/~10 circuits/Dispatch/El (hours)
	P*0 C E	Residence/>=10 circuits/Non-Dispatch/El (hours)
.2	P-5	Residence/>=10 circuits/NoiPDispatch/FL (bours)
	P-5	Business/<10 circuits/Dispatch/EL (hours)
	P~5	Business/<10 circuits/Non-Dispatch/FL(nours)
	P-5	Business/>=10 circuits/Dispatch/El (bours)
	P-5	Business/>=10 circuits/NOI-Dispatch/FL(hours)
	P-5	Design (Specials)/<10 circuits/Dispatch/FL(Iours)
	P-5	Design (Specials)/(10 circuits/Non-Dispatch/FL(hours)
	P-5	Design (Specials)/>=10 circuits/Dispatch/FL(Iours)
	P-5	Design (Specialsy)=10 circulis/ion-Dispactivi Linouis)
	P-5	PBX/<10 circuits/Dispatch/Pc(nours)
	P-5	PBX/<10 circuits/Non-Dispatch/FL(fours)
	P-5	PBX/>=10 circuits/Dispatch/FL(nours)
	P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)
	P-5	Centrex/<10 circuits/Dispatch/FL(nours)
	P-5	Centrex/<10 circuits/Non-Dispatch/FL(nours)
	P-5	Centrex/>=10 circuits/Dispatch/FL(nours)
1	P-5	Centrex/>=10 circuits/Non-Dispatch/FL(nours)
	P-5	ISDN/<10 circuits/Dispatch/FL(nours)
	P-5	ISDN/<10 circuits/Non-Dispatch/FL(nours)
	P-5	ISDN/>=10 circuits/Dispatch/FL(hours)
	P-5	ISDN/>=10 circuits/Non-Dispatch/=L(nours)
	Avera	ge Completion Notice Interval - Non-Mechanized
.1	P-5	Residence/<10 circuits/Dispatch/FL(hours)
2	P-5	Residence/<10 circuits/Non-Dispatch/FL(hours)
1	P-5	Residence/>=10 circuits/Dispatch/FL(hours)
!	P-5	Residence/>=10 circuits/Non-Dispatch/FL(hours)
1	P-5	Business/<10 circuits/Dispatch/FL(hours)
2	P-5	Business/<10 circuits/Non-Dispatch/FL(hours)
	P-5	Business/>=10 circuits/Dispatch/FL(hours)
2	P-5	Business/>=10 circuits/Non-Dispatch/FL(hours)
1	P-5	Design (Specials)/<10 circuits/Dispatch/FL(hours)
2	P-5	Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)
1	P-5	Design (Specials)/>=10 circuits/Dispatch/FL(hours)
	P-5	Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)
	0.5	DDV(10 -i

M.C. 1J.U.C.C.		Design (Cocomopy to include the second
A.2.15.4.1.1	P-5	PBX/<10 circuits/Dispatch/FL(hours)
A.2.15.4.1.2	P-5	PBX/<10 circuits/Non-Dispatch/FL(hours)
A.2.15.4.2.1	P-5	PBX/>=10 circuits/Dispatch/FL(hours)
A.2.15.4.2.2	P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)
A.2.15.5.1.1	P-5	Centrex/<10 circuits/Dispatch/FL(hours)
A.2.15.5.1.2	P-5	Centrex/<10 circuits/Non-Dispatch/FL(hours)
A.2.15.5.2.1	P-5	Centrex/>=10 circuits/Dispatch/FL(hours)
A.2.15.5.2.2	P-5	Centrex/>=10 circuits/Non-Dispatch/FL(hours)
A.2.15.6.1.1	P-5	ISDN/<10 circuits/Dispatch/FL(hours)
A.2.15.6.1.2	P-5	ISDN/<10 circuits/Non-Dispatch/FL(hours)
A.2.15.6.2.1	P-5	ISDN/>=10 circuits/Dispatch/FL(hours)
A.2.15.6.2.2	P-5	ISDN/>=10 circuits/Non-Dispatch/FL(hours)
	Total	Service Order Cycle Time - Mechanized

A.2.17.1.1.1	P-10	Residence/<10 circuits/Dispatch/FL(days)
A.2.17.1.1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)
A.2.17.1.2.1	P-10	Residence/>=10 circuits/Dispatch/FL(days)
A.2.17.1.2.2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)
A.2.17.2.1.1	P-10	Business/<10 circuits/Dispatch/FL(days)
A.2.17.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)
A 2 17 2 2 1	P-10	Business/>=10 circuits/Dispatch/FL(days)
A 217222	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)
A 2 17.3.1.1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
1.45	521,890	4.53	31,267	7.004	0.04078	-75.5111	NO
3.31	70	0.03	4	8.850	4.54962	0.7210	YES
0.64	2	1 1		0.502			
7.20	9,542	7.57	392	30.652	1.57966	-0.2351	YES
2.57	33,668	14.41	2,130	17.176	0.38375	-30.8679	NO
8.51	242	6.36	9	28.187	9.56868	0.2248	YES
0.74	59			0.320			
136.94	3,252	2.68	21	584.084	127.86834	1.0500	YES
11.35	1,671	0.67	13	71.195	19.82252	0.5388	YES
15.66	24			44.702			
1.12	37			1.680			
29.01	60	44.27	20	78.401	20.24315	-0.7538	YES
9.87	258	0.13	4	56.127	28.28016	0.3443	YES
0.49	2			0.185			
6.77	75			48.176			
7.32	475			20,536			
1.83	1,345	0.57	1	9.237	9.24003	0.1360	YES
19.14	24			62.180			
0.97	265			2.200			
222.12	662	0.07	1	876.657	877.31918	0.2531	YES
12.28	395	0.98	7	51.763	19.73725	0.5727	YES
							I

Benchmark / Analog Res

Res

Res

Bus Bus Bus

Bus Design Design Design Design

PBX PBX PBX

PBX Centrex Centrex

Centrex Centrex ISDN ISDN ISDN ISDN

Diagnostic Diagnostic

Diagnostic Diagnostic

22.97 45 21.94 154	Diagnostic Diagnostic Diagnostic	
21.94 154	Diagnostic Diagnostic	
	Diagnostic	
	Diagnostic	
22.00 44	Diagnostic	
19.96 390	Diagnostic	
26.62 3	Diagnostic	
14.00 16	Diagnostic	
28.70 62	Diagnostic	
19.60 102	Diagnostic	
	Diagnostic	
22.00 3	Diagnostic	
30,96 2	Diagnostic	
22.37 15	Diagnostic	
23.47 1	Diagnostic	
14.00 3	Diagnostic	
15.57 2	Diagnostic	
29.00 40	Diagnostic	
40.90 2	Diagnostic	
14.00 6	Diagnostic	
70.36 15	Diagnostic	
60.58 26	Diagnostic	
	Diagnostic	
	Diagnostic	

	3.91	1,269	Diagnostic
	1.24	21,895	Diagnostic
	3.67	3	Diagnostic
			Diagnostic
	3.24	143	Diagnostic
	1.80	745	Diagnostic
	7.00	1	Diagnostic
			Diagnostic
	·····		Diagnostic

BellSouth Monthly Performance Summary Florida, June 2001

		Design (One sight) -10 eigenite (Ion-Dispatch/E) (days)
A.2.17.3.1.2	P-10	Design (Specials)/< 10 circuits/(Oirpotot/El (days)
A.2.17.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL (days)
A.2.17.3.2.2	P-10	Design (Specials/>=10 circuits/Hon-Dispatch/ Godys)
A.2.17.4.1.1	P-10	PBX/<10 circuits/Dispatch/FL(days)
A.2.17.4.1.2	P-10	PBX/<10 circuits/non-Dispatch/FL(days)
A.2.17.4.2.1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
A.2.17.4.2.2	P-10	PBX/>=10 circuits/non-Dispatch/FL(days)
A.2.17.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A.2.17.5.1.2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A.2.17.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
A.2.17.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A.2.17.6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2.17.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A.2.17.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
A.2.17.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
	Total Se	ervice Order Cycle Time - Partially Mechanized
A 2 18 1 1 1	P-10	Besidence/<10 circuits/Dispatch/FL(days)
A 2 18 1 1 2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)
A 2 18 1 2 1	P-10	Residence/>=10 circuits/Dispatch/FL(days)
A 2 18 1 2 2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)
A 2 19 2 1 1	P-10	Business/<10 circuits/Dispatch/FL(days)
A.2.10.2.1.1	P-10	Business/<10 circuits/Non-Dispatch/Et (days)
A.2.10.2.1.2	P.10	Business/s=10 circuits/Dispatch/FL (days)
A.Z. 10.Z.Z. 1	P-10	Business/>=10 circuits/Non-Dispatch/EI (days)
A.Z. 10.Z.Z.Z	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
A.2.10.3.1.1	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
A.2.10.3.1.2	D-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
A.2.10.3.2.1	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
A.2.10.3.2.2	P-10	PBX/<10 circuits/Dispatch/FL(days)
A 2 18 4 1 2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
A 2 18 4 2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
A 2 18 4 2 2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
A 2 18 5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A 2 18 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A 2 18 5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
A 2 18 5 2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A 2 18 6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2.18.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A 2 18 6 2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
A.2.18.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
-	Tatal C	antino Ordez Cuela Time - Non-Mechanized
	Total S	Envice Order Cycle Title - Northeconamized
A.2.19.1.1,1	P-10	Desidence/ () circuits/Dispatch/EL (days)
A.2.19.1.1.2	P-10	Posidence/s=10 circuits/Dispatch/El (days)
A.2.19.1.2.1	0.10	Desidence/-10 circuite/Non-Dispatch/FL (days)
A.2.19.1.2,2	P-10	Rusineen(cl)=10 circuite(Dispatch/FL(days)
A.2.19.2.1.1	P-10	Business/c10 circuits/Dispatch/El (dave)
A.2.19.2.1.2	P-10	Dusinessk to circuits/non-Dispaten/EL (days)
A.2.19.2.2.1	P-10	Business/>=10 circuits/Dispatch/EL (days)
A.2.19.2.2.2	P-10	Desiness/>= 10 circuits/NOIPDispatch/El (days)
A.2.19.3.1.1	P-10	Design (opecials)/<10 circuits/Dispatch/El (days)
A.2.19.3.1.2	P-10	Design (Specials)/<10 circuits/hor-Dispatch/El (days)
A.2.19.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
A.2.19.3.2.2	P-10	Design (Specials) = 10 circults/non-Dispatch/rejuays)
A.2.19.4.1.1	P-10	PBX/ <tu circuits="" dispatch="" fl(days)<="" td=""></tu>
A.2.19.4.1.2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
A.2.19.4.2.1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
A.2.19.4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
A.2.19.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
rinalog								
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic					i i			Diagnostic
Diagnostic					1			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
								Discussion
Diagnostic			4.03	271				Diagnostic
Diagnostic			2.06	7,776				Diagnostic
Diagnostic			5.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic			4.87	113	1			Diagnostic
Diagnostic			2.71	770				Diagnostic
Diagnostic								Diagnostic
Diagnostic				l				Diagnostic
Diagnostic			2.51	11				Diagnostic
Diagnostic			2.25	12				Diagnostic
Diagnostic								Diagnostic
Diagnostic					-			Diagnostic
Diagnostic			11.00	2	_			Diagnostic
Diagnostic			4.67	3				Diagnostic
Diagnostic			1.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic					-			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic				l				Diagnostic
Diagnostic								Diagnostic
Diagnostic			6.50	2				Diagnostic
Diagnostic				·				Diagnostic
Diagnostic								Diagnostic
Discocto			6.11	36			_	Diagnostic
Diagnostic			4 36	117				Diagnostic
Diagnostic			4.00	l				Diagnostic
Diagnostic			·	<u> </u>				Diagnostic
Diagnostic			6.07	28				Diagnostic
Diagnosiic			4 10	324	-			Diagnostic
Diagnostic			4.15	2	-			Diagnostic
Diagnostic			7.00	2	-			Diagnostic
Diagnostic			6.17	62				Diagnostic
Diagnostic			0.17	109				Diagnostic
Diagnostic			2.45	108				Diagnostic
Diagnostic			7.00	<u> </u>				Diagnostic
Diagnostic			7.00					Diagnostic
Diagnostic			/.00	<u> </u>				Diagnostic
Diagnostic			5.83	12				Diagnostic
Diagnostic			15.00	1				Diagnostic
Diagnostic			5.50	2				Diagnostic
Diagnostic			7.50	2				Diagnostic

ZScore Jun-01 Equity

Diagnostic

BellSouth Monthly Performance Summary Florida, June 2001

A.2.19.5.1.2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A.2.19.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
4.2.19.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A.2.19.6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2.19.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A.2.19.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
4.2.19.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
	Total S	Service Order Cycle Time (offered) - Mechanized
.2.21.1.1.1	₽-10	Residence/<10 circuits/Dispatch/FL(days)
.2.21.1.1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)
.2.21.1.2.1	P-10	Residence/>=10 circuits/Dispatch/FL(days)
.2.21.1.2.2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)
.2.21.2.1.1	P-10	Business/<10 circuits/Dispatch/FL(days)
.2.21.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)
.2.21.2.2.1	P-10	Business/>=10 circuits/Dispatch/FL(days)
.2.21.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)
.2.21.3.1.1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
.2.21.3.1.2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
.2.21.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
2.21.3.2.2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
2.21.4.1.1	P-10	PBX/<10 circuits/Dispatch/FL(days)
.2.21.4.1.2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
.2.21.4.2.1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
.2.21.4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
.2.21.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
2.21.5.1.2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
221521	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
2.21.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
221611	P-10	ISDN/<10 circuits/Dispatch/FL(days)
221612	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
221621	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
12.21.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
	Total S	Service Order Cycle Time (offered) - Partially Mechanized
222111	P-10	Residence/<10 circuits/Dispatch/FL(days)
1222112	P-10	Besidence/<10 circuits/Non-Dispatch/FL(days)
1222121	P-10	Besidence/>=10 circuits/Dispatch/FI (days)
222122	P-10	Besidence/>=10 circuits/Non-Dispatch/FI (days)
12.22.1.2.2	P-10	Business/<10 circuits/Dispatch/El (days)
12.22.2.1.1	P.10	Business/<10 circuits/Non-Dispatch/El (days)
12222	P-10	Business/>=10 circuits/Dispatch/FL(days)
2222222	P-10	Business/>=10 circuits/Non-Dispatch/FL(davs)
1222211	P-10	Design (Specials)/<10 circuits/Dispatch/FI (days)
	P.10	Design (Specials)/<10 circuits/Non-Dispatch/FL (days)
1.2.22.3.1.2	P-10	Decign (Specials)/>-10 circuits/Dispatch/Fl (days)
4.2.22.3.2.1	D 10	Design (Specials)/>=10 circuits/Non-Dispatch/El (days)
4.2.22.3.2.2	P-10	Design (opecials)>= 10 circultor(c) opecial circultor (out) of
1.2.22.4.1.1	P-10	DDV(10 urguite Alon Dispatch/EL (days)
A.2.22.4.1.2	P-10	DDV4 10 eircuite/Dispatch/FL(days)
A.2.22.4.2.1	P-10	PBX/>=10 circuits/Uspatch/FL(days)
A.2.22.4.2.2	P-10	PBN>=10 circuits/Non-Dispatch/FL(days)
A.2.22.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A.2.22.5.1.2	P-10	Gentrex/<10 circuits/Non-Dispatch/FL(days)
A.2.22.5.2.1	P-10	Centrex/>=10 circuits/Jispatch/FL(days)
A.2.22.5.2.2	P-10	Centrex/>=10 circuits/non-Dispatch/FL(days)
A.2.22.6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2.22.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A.2.22.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
A.2.22.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)

Diagnostic 9.00 1 10.00 2 Diagnostic Diagnostic Diagnostic 14.85 13 Diagnostic Diagnostic 3.84 1,142 Diagnostic 1.71 12,673 Diagnostic 3.67 Diagnostic 3 Diagnostic Diagnostic 3.25 142 Diagnostic Diagnostic 1.86 659 Diagnostic 246 Diagnostic 4.05 Diagnostic Diagnostic 2.13 5,576 Diagnostic 5.00 Diagnostic Diagnostic Diagnostic 1 Diagnostic 98 3.73 Diagnostic Diagnostic Diagnostic 2.41 596 Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic 3.33 Diagnostic Diagnostic 5 2.00 6 Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic 6.00 Diagnostic Diagnostic Diagnostic 1 4.50 2 Diagnostic 1.00 Diagnostic 1 Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic

6.50

2

CLEC

Measure

4.74

Benchmark /

Analog

Diagnostic

Diagnostic Diagnostic

Diagnostic

Diagnostic

Diagnostic Diagnostic Diagnostic

Diagnostic

Diagnostic

Diagnostic

Diagnostic

Diagnostic Diagnostic

Diagnostic

Diagnostic

Diagnostic

Diagnostic

Diagnostic

BST

Measure

BST

Volume

CLEC

Volume

19

Standard

Deviation

Standard

Error

Total Service Order Cycle Time (offered) - Non-Mechanized

Diagnostic

Diagnostic

Diagnostic

Diagnostic

Diagnostic

Florida, J	une 2001
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23.1.1.2	P-10	Residence/_10 circuits/Dispatch/El (days)
CV. 1 . I . C	P-10	Residence/ 10 circuits/Non-Dispatch/FI (days)
23121	P.10	Decidence -10 circuite/Dispatch/El (days)
23122	P-10	Desidence >= 10 circuits/Dispatch/EL (days)
23211	P-10	Business/_10 circuits/Disnatch/FL (days)
23.2.1.1	P-10	Business/<10 circuits/Non-Dispatch/FL (days)
20.2.1.2	P-10	Business/<10 circuits/Dispatch/FL (days)
20.2.2.1	P-10	Dusiness/>=10 circuits/Dispatch/r Ludys)
23.2.2.2	D-10	Design (Specials)/<10 circuite/Dispatch/El (days)
23.3.1.1	0.10	Design (Specials)<10 circuits/Dispatch/El (days)
23.3.1.2	P-10	Design (Specials) < 10 circuits/Non-Dispactive El (days)
23.3.2.1	D 10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
23.3.2.2	P-10	DPS/(-10 piceuits//)=10 circuits/Non-Dispatch/-L(days)
23.4.1.1	P-10	PDV(10 circuits/Dispatch/FL(days)
23.4.1.2	P-10	PBX<10 circuits/NOFDispatch/FL(days)
23.4.2.1	0.10	IPDV2- to circuits/Dispatch/FL(days)
23.4.2.2	0.10	PDAV>= To Circuits Moti-Uspatch/FL(days)
23.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
23.5.1.2	P-10	
23.5.2.1	P-10	Uentrex/>= 10 circuits/Uspatch/FL(days)
23.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/=L(days)
23.6.1.1	P-10	ISDIN/<10 circuits/Dispatch/FL(days)
23.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
23.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
23.6.2.2	P-10	ISDN/>=10 circuits/non-Dispatch/FL(days)
	% Соп	pletions w/o Notice or < 24 hours
24.1.1	P-6	Residence/Dispatch/FL(%)
24.1.2	P-6	Residence/Non-Dispatch/FL(%)
24.2.1	P-6	Business/Dispatch/FL(%)
24.2.2	P-6	Business/Non-Dispatch/FL(%)
24.3.1	P-6	Design (Specials)/Dispatch/FL(%)
24.3.2	P-6	Design (Specials)/Non-Dispatch/FL(%)
24.4.1	P-6	PBX/Dispatch/FL(%)
24.4.2	P-6	PBX/Non-Dispatch/FL(%)
24.5.1	P-6	Centrex/Dispatch/FL(%)
	P-6	
24.5.2		Centrex/Non-Dispatch/FL(%)
24.5.2 24.6.1	P-6	Centrex/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2	P-6 P-6	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2	P-6 P-6	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDNNon-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2	P-6 P-6 Servic	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) 9 Order Accuracy Besidence/10 circuite/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1	P-6 P-6 Servic	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/-10 circuits/Dispatch/FL(%) Decidence-10 circuits/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2	P-6 P-6 P-11 P-11 P-11	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/<10 circuits/Non-Dispatch/FL(%) Desidence/<10 circuits/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1	P-6 P-6 P-11 P-11 P-11	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/<10 circuits/Non-Dispatch/FL(%) Residence/<10 circuits/Non-Dispatch/FL(%) Residence/=10 circuits/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.1.2.2 25.1.2.2	P-6 P-6 P-11 P-11 P-11 P-11 P-11	Centrex/Non-Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.1.2.2 25.2.1.1	P-6 P-6 P-11 P-11 P-11 P-11 P-11	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) iSDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.1.2.2 25.2.1.1 25.2.1.2	P-6 P-6 P-11 P-11 P-11 P-11 P-11 P-11 P-	Centrex/Non-Dispatch/FL(%) ISDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.1.2.1 25.1.2.2 25.2.1.1 25.2.1.2 25.2.1.2	P-6 P-6 P-11 P-11 P-11 P-11 P-11 P-11 P-	Centrex/Non-Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.2.1.1 25.2.1.2 25.2.1.1 25.2.2.2	P-6 P-6 P-11 P-11 P-11 P-11 P-11 P-11 P-	Centrex/Non-Dispatch/FL(%) [SDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.2.2 25.1.2.1 25.1.2.1 25.2.1.1 25.2.1.2 25.2.2.1 25.2.2.1 25.2.2.1 25.2.2.2 25.2.2.1	P-6 P-6 P-11 P-11 P-11 P-11 P-11 P-11 P-	Centrex/Non-Dispatch/FL(%) [SDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%) Business/>=10 circuits/Dispatch/FL(%) Business/>=10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.2.2 25.2.1.1 25.2.1.2 25.2.1.1 25.2.2.2 25.2.2.1 25.2.2.1 25.3.1.2	P-6 P-11	Centrex/Non-Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Residence/>=10 circuits/Non-Dispatch/FL(%) Business/<10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%)
24.5.2 24.6.1 24.6.2 25.1.1.1 25.1.1.2 25.1.2.1 25.2.1.1 25.2.1.1 25.2.2.1 25.2.2.1 25.2.2.2 25.3.1.1 25.3.2.1	P-6 P-11 P-11	Centrex/Non-Dispatch/FL(%) [SDN/Dispatch/FL(%) ISDN/Non-Dispatch/FL(%) e Order Accuracy Residence/<10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Dispatch/FL(%) Residence/>=10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%) Business/<10 circuits/Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%) Business/>=10 circuits/Non-Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%) Design (Specials)/<10 circuits/Dispatch/FL(%)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
-								
Diagnostic			6.32	31	-			Diagnostic
Diagnostic			3.91	85				Diagnostic
Diagnostic					-			Diagnostic
Diagnostic								Diagnostic
Diagnostic			6.12	25				Diagnostic
Diagnostic			3.88	249				Diagnostic
Diagnostic			7.00	1				Diagnostic
Diagnostic			6.57	7				Diagnostic
Diagnostic			4.83	40				Diagnostic
Diagnostic			2.60	83				Diagnostic
Diagnostic								Diagnostic
Diagnostic			5.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic			6.75	4				Diagnostic
Diagnostic			15.00	1				Diagnostic
Diagnostic			8.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic			4.67	3				Diagnostic
Diagnostic			9.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
•								
Diagnostic			100.00%	1 759				Diagnostic
Diagnostic			100.00%	30,910				Diagnostic
Diagnostic			100.00%	336				Diagnostic
Diagnostic			100.00%	2 239				Diagnostic
Diagnostic			100.00%	77				Diagnostic
Diagnostic			100.00%	602				Diagnostic
Diagnostic			100.00%	002				Diagnostic
Diagnostic			100.00%	32				Diagnostic
Diagnostic			100.00%	6				Diagnostic
Diagnostic			100.00%	51				Diagnostic
Diagnostic			100.00%					Diagnostic
Diagnostic			100.00%					Diagnostic
Diagnostic			100.00 /0					Diagnostic
>= 95%			100.00%	1				YES
>= 95%			94.66%	131				NO
>= 95%								
>= 95%								
>= 95%			100.00%	6				YES
>= 95%			83.47%	121				NO
>= 95%			100.00%	1				YES
>= 95%			100.00%	2				YES
>= 95%			100.00%	6				YES
>= 95%			100.00%	3				YES
>= 95%								
>= 95%			0.00%	3				NO

										_
A3111	M&R-1 Residence/Dispatch/FL(%)	Res	11.28%	103,511	5.20%	2,944	0.005	91 10.2929	YES	
A3112	M&R-1 Residence/Non-Dispatch/FL(%)	Res	1.60%	58,790	1.24%	1,527	0.003	26 1.1047	YES	_
A3121	M&R-1 Business/Dispatch/FL(%)	Bus	14.09%	20,715	7.29%	1,043	0.011	04 6.1629	YES	_
A3122	M&R-1 Business/Non-Dispatch/FL(%)	Bus	3.39%	11,919	2.98%	571	0.007	75 0.5319	YES	_

	Florida June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	l londa, ballo 2001	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
		,								
43131	M&B-1 Design (Specials)/Dispatch/FLI%)	Design	3.05%	2,751	6.25%	32		0.03059	-1.0449	YES
A3132	M&B-1 Design (Specials)/Non-Dispatch/FL(%)	Design	0.75%	2,784	0.00%	7		0.03274	0.2304	YES
A 3 1 4 1	M&B-1 PBX/Dispatch/FL(%)	PBX	24.18%	364	0.00%	13		0.12085	2.0005	YES
A3142	M&B-1 PBX/Non-Dispatch/EL(%)	PBX	8.40%	238	4.17%	24		0.05942	0.7130	YES
A3151	M&B-1 Centrex/Dispatch/EI (%)	Centrex	22.60%	1,597	8.33%	24		0.08602	1.6591	YES
43152	M&B-1 Centrey/Non-Dispatch/EL (%)	Centrex	8.26%	1,041	0.00%	8		0.09771	0.8455	YES
A3161	M&B-1 ISDN/Dispatch/EL (%)	ISDN	7.86%	458						
A3162	M&R-1 ISDN/Non-Dispatch/FL(%)	ISDN	1.10%	455	0.00%	1		0.10437	0.1053	YES
		8								
	Customer Trouble Report Rate		0.000			100 05 1		0.000.40	0 7740	
A.3.2.1.1	M&R-2 Residence/Dispatch/FL(%)	Hes	2.29%	4,527,990	2.25%	130,654		0.00042	0.7718	YES
A.3.2.1.2	M&R-2 Residence/Non-Dispatch/FL(%)	Hes	1.30%	4,527,990	1.1/%	130,654		0.00032	4.0542	YES
A.3.2.2.1	M&R-2 Business/Dispatch/FL(%)	Bus	1.61%	1,286,196	1.63%	63,892		0.00051	-0.4253	YES
A.3.2.2.2	M&R-2 Business/Non-Dispatch/FL(%)	Bus	0.93%	1,286,196	0.89%	63,892		0.00039	0.8455	YES
A.3.2.3.1	M&R-2 Design (Specials)/Dispatch/FL(%)	Design	0.35%	/8/,1/5	0.21%	15,464		0.00048	2.9695	YES
A.3.2.3.2	M&R-2 Design (Specials)/Non-Dispatch/FL(%)	Design	0.35%	/8/,1/5	0.05%	15,464		0.00048	6.3864	YES
A.3.2.4.1	M&R-2 PBX/Dispatch/FL(%)	PBX	0.26%	141,614	0.30%	4,2/8		0.00079	-0.5954	YES
A.3.2.4.2	M&R-2 PBX/Non-Dispatch/FL(%)	РВХ	0.17%	141,614	0.56%	4,278		0.00064	-6.1/6/	NO
A.3.2.5.1	M&R-2 Centrex/Dispatch/FL(%)	Centrex	0.68%	235,525	0.60%	3,972		0.00132	0.5604	YES
A.3.2.5.2	M&R-2 Centrex/Non-Dispatch/FL(%)	Centrex	0.44%	235,525	0.20%	3,972		0.00106	2.261/	YES
A.3.2.6.1	M&R-2 ISDN/Dispatch/FL(%)	ISDN	1.23%	37,202	0.00%	850		0.00385	3.1986	YES
A.3.2.6.2	M&R-2 ISDN/Non-Dispatch/FL(%)	ISDN	1.22%	37,202	0.12%	850		0.00384	2.8814	YES
	Maintenance Average Duration									
42211	M&B-3 Desidence(Dispatch/E) (hours)	Res	21.60	1 103,511	18.60	2,944	21.477	0.40141	7.4714	YES
A 3 3 1 2	M&B-3 Besidence/Non-Dispatch/FL (hours)	Res	7.70	58,790	5.08	1,527	11.488	0.29778	8.7866	YES
A 3 3 3 1	M&B-3 Business/Disnatch/El (bours)	Bus	16.92	20,715	15.78	1,043	20.673	0.65604	1.7438	YES
A 3 3 3 3 3	M&B-3 Business/Non-Disnatch/El (hours)	Bus	5.75	11,919	4.11	571	11.561	0.49525	3.3151	YES
A.3.3.2.2	M&R-3 Design (Specials)/Dispatch/El (hours)	Desian	6.40	2,751	9.63	32	39.863	7.08764	-0.4559	YES
A3332	M&B-3 Design (Specials)/Non-Disnatch/El (hours)	Design	2.51	2,784	7.24	7	19.226	7.27599	-0.6498	YES
A 3 3 4 1	M&R-3 DBY(Dispatch/EI (hours)	РВХ	20.86	364	14.60	13	24.889	7.02504	0.8906	YES
A3342	M&B-3 (PBX/Non-Dispatch/El/hours)	PBX	8.93	238	3.90	24	14.790	3.16745	1.5873	YES
A 3 3 5 1	M&B-3 Centrey/Dispatch/El (hours)	Centrex	18.20	1,597	14.39	24	21.964	4.51701	0.8431	YES
A 3 3 5 3	M&B-3 Centrex/Mon-Dispatch/FI (hours)	Centrex	5.25	1,041	6.10	8	9.200	3.26509	-0.2598	YES
A3361	MR.P.3 ISDN/Dispatch/EL (bours)	ISDN	10.59	458			17.291			
A3362	M&B-3 (ISDN/Non-Dispatch/E) (hours)	ISDN	3.06	455	3.90	1	4.983	4.98857	-0.1678	YES
					T					
	% Repeat Troubles within 30 Days	1 D	01.000/	100 511	17.000/	2.044		0.00762	4 2005	L VEC
A.3.4.1.1	M&R-4 Residence/Dispatch/FL(%)	nes D	21.09%	T03,311	10.06%	1 5 97	-	0.00703	4.3200	VEC
A.3.4.1.2	M&R-4 Residence/Non-Dispatch/FL(%)	Hes	10.06%	00,790	16 700/	1,042	2	0.01010	1.0710	VEC
A.3.4.2.1	M&R-4 Business/Dispatch/FL(%)	Bus	16.34%	20,715	10.78%	571		0.01228	1.2/10	VEC
A.3.4.2.2	M&R-4 Business/Non-Dispatch/FL(%)	Bus	10.43%	0.751	18.21%	20		0.01567	1.1252	
A.3.4.3.1	M&R-4 Design (Specials)/Dispatch/FL(%)	Design	42.06%	2,751	20.57%	32	1	0.00777	-1.2009	VEG
A,3.4.3.2	M&R-4 Design (Specials)/Non-Dispatch/FL(%)	Design	37.90%	2,764	28.5/70	12		0.16309	0.5079	VEC
A.3.4.4.1	M&R-4 PBX/Dispatch/FL(%)		21.15%	304	1.09%	24		0.11527	1 1010	VEC
A.3.4.4.2	M&R-4 PBX/Non-Dispatch/FL(%)	PBA	11.70%	230	4.17%	24		0.00900	0.0744	VES
A.3.4.5.1	M&R-4 Centrex/Dispatch/FL(%)	Centrex	13.90%	1,597	20.63%			0.07115	1 05 97	NO
A.3.4.5.2	M&R-4 Centrex/Non-Dispatch/FL(%)	Centrex	13.64%	1,041	37.50%	8	-	0.12181	-1.9567	NU.
A.3.4.6.1	M&R-4 ISDN/Dispatch/FL(%)	ISDN	27.73%	458				0.40004	0.5674	VES
A.3.4.6.2	M&R-4 ISDN/Non-Dispatch/FL(%)	ISDN	24.40%	455	0.00%			0.42994	0.5674	1 15
	Out of Service > 24 hours									
43511	M&B-5 Besidence/Dispatch/EL(%)	Res	23.08%	71,718	19.56%	2,065		0.00940	3.7405	YES
A 3 5 1 2	M&B-5 Besidence/Non-Disnatch/El (%)	Res	8.45%	17,644	5.11%	411		0.01388	2.4073	YES
A 36 31	M&B-5 Business/Dispatch/FI (%)	Bus	17.03%	13,461	12.50%	696		0.01461	3.0983	YES
A 3 5 3 3	M&B-5 Business/Non-Dispatch/EI (%)	Bus	4.89%	4,560	3.54%	226		0.01470	0.9189	YES
A 3 5 3 1	M&R-5 Decian (Specials)/Dispatch/FL (%)	Design	3.05%	2,751	6.25%	32		0.03059	-1.0449	YES
A 3 5 3 3	M&P-5 Design (Specials)/Non-Dispatch/EI (%)	Design	0.75%	2.784	0.00%	7		0.03274	0.2304	YES
M.J.D.J.Z	M&D.5 (DDV/Dispatch/El /%)	PBX	20.08%	244	20.00%	10		0.12926	0.0063	YES
A.J.5.4.1	M&R-5 PRV/Mon-Dispatch/EL/%)	PBX	14.05%	121	5,56%	18		0.08779	0.9676	YES
M.J.J.4.2	M&P-5 Control/Dispatch/FI (%)	Centrex	23.35%	1,122	15.38%	13		0.11801	0.6750	YES
A.J.D.D.I	Man-s Ivenuevolspacivre(10)	1	10.00 /0							<u> </u>

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	Florida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
A.3.5.5.2	M&R-5 Centrex/Non-Dispatch/FL(%)	Centrex	3.38%	474	0.00%	5		0.08119	0.4158	YES
A.3.5.6.1	M&R-5 ISDN/Dispatch/FL(%)	ISDN	7.86%	458						
A.3.5.6.2	M&R-5 ISDN/Non-Dispatch/FL(%)	ISDN	1.10%	454	0.00%	1		0.10448	0.1054	YES
	Resale - Billing									
A.4.1	B-1 FL(%)	BST - State	98.46%	\$499,829,886	99.89%	\$10,998,813		0.00004	-379.4199	YES
	Mean Time to Deliver Invoices - CRIS							•		
A.4.2	B-2 [Hegion(Dusiness days)	BS1 - Region	3.72	1	3.18	1,791				YES

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Florida,	June	2001
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Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity

Diagnostic

Diagnostic

Diagnostic

Diagnostic

Inhundled	Network	Fiemente	- Ordering	

% Rejected Service Requests - Mechanized							
B.1.1.1	0-7	Switch Ports/FL(%)					
B.1.1.2	0-7	Local Interoffice Transport/FL(%)					
B.1.1.3	0-7	Loop + Port Combinations/FL(%)					
B.1.1.4	0-7	Combo Other/FL(%)					
B.1.1.5	0-7	xDSL (ADSL, HDSL and UCL)/FL(%)					
B.1.1.6	0-7	ISDN Loop (UDN, UDC)/FL(%)					
B.1.1.7	0-7	Line Sharing/FL(%)					
B.1.1.8	0-7	2W Analog Loop Design/FL(%)					
B.1.1.9	Ö-7	2W Analog Loop Non-Design/FL(%)					
B.1.1.10	0-7	2W Analog Loop w/INP Design/FL(%)					
8.1.1.11	0.7	2W Analog Loop w/NP Non-Design/FL(%)					
B.1.1.12	0-13	2W Analog Loop w/LNP Design/FL(%)					
B.1.1.13	0-13	2W Analog Loop w/LNP Non-Design/FL(%)					
B.1.1.14	Ö -7	Other Design/FL(%)					
B.1.1.15	0-7	Other Non-Design/FL(%)					
B.1.1.16	0-7	INP Standalone/FL(%)					
8 1.17	0-13	LNP (Standalone)/FL(%)					
	% Reio	cted Service Bequests - Partially Mechanized					
B 21	0-7	Switch Ports/El (%)					
B 23	0-7	Local Interoffice Transport/EL (%)					
8 23	0-7	Loon + Port Combinations/FI (%)					
B 2.3	0-7	Combo Other/El (%)					
D 2.4 B 25	0-7	VDSL (ADSL HDSL and LICL)/EL(%)					
B 26	0-7	ISDN Loop (UDN_LIDC)/EL (%)					
B 27	0-7	l ine Sharing/El (%)					
B128	0-7	2W Analog Loop Design/FL(%)					
B129	0-7	2W Analog Loop Non-Design/F1 (%)					
B.1.2.10	0-7	2W Analog Loop w/INP Design/FL(%)					
B.1.2.11	0-7	2W Analog Loop w/INP Non-Design/FL(%)					
B.1.2.12	0-13	2W Analog Loop w/LNP Design/FL(%)					
B.1.2.13	0-13	2W Analog Loop w/LNP Non-Design/FL(%)					
B.1.2.14	0-7	Other Design/FL(%)					
8.1.2.15	0-7	Other Non-Design/FL(%)					
B.1.2.16	0-7	INP Standalone/FL(%)					
B.1.2.17	0-13	LNP (Standalone)/FL(%)					
	% Roio	cted Service Requests - Non-Mechanized					
8131	0-7	Switch Ports/FL(%)					
B132	0-7	Local Interoffice Transport/FL(%)					
B133	0.7	Loop + Port Combinations/FI (%)					
B134	0-7	Combo Other/EL (%)					
B135	0-7	vDSL (ADSL HDSL and LICL VEL(%)					
D.1.3.5	0-7	ISDN Loop (LIDN_LIDC//EL (%)					
D.1.3.0	0-7	I ine Sharing/El (%)					
D.1.3.7	0.7	20/ Analog Loon Design/EL (%)					
D.1.3.0 D.1.3.0	0.7	201/ Analog Loop Non-Design/EL (%)					
D.1.3.9	0.7	201/ Appled Loop with Design/EL (%)					
B.1.3.10	0-7	2VV Analog Loop white Design/EL(%)					
B.1.3.11	0.12	2W Analog Loop with Non-Design/FL(%)					
B.1.3.12	0-13	2VV Analog Loop W/LNP Design/FL(76)					
B.1.3.13	0-13	2W Analog Loop W/LINF NON-Design/FL(76)					
B.1.3.14	0-/						

Diagnostic					Diagnostic
Diagnostic		33.33%	63		Diagnostic
Diagnostic		13.32%	10,206		Diagnostic
Diagnostic					Diagnostic
Diagnostic		6.06%	231		Diagnostic
Diagnostic		0.00%	3		Diagnostic
Diagnostic					Diagnostic
Diagnostic		5.25%	1,811		Diagnostic
Diagnostic		8.58%	303		Diagnostic
Diagnostic					Diagnostic
Diagnostic					Diagnostic
Diagnostic:		39.86%	143		Diagnostic
Diagnostic:		95.56%	45		Diagnostic
Diagnostic:		31.82%	66		Diagnostic
Diagnostic		13.32%	10,206		Diagnostic
Diagnostic					Diagnostic
Diagnostic		4.65%	3,485		Diagnostic
Diagnostic					Diagnostic
Diagnostic		22.92%	48		Diagnostic
Diagnostic		32,45%	5.294		Diagnostic
Diagnostic					Diagnostic
Diagnostic					Diagnostic
Diagnostic		25.00%	4		Diagnostic
Diagnostic					Diagnostic
Diagnostic		21.88%	608		Diagnostic
Diagnostic		17.34%	519		Diagnostic
Diagnostic					Diagnostic
Diagnostic					Diagnostic
Diagnostic		39.42%	893		Diagnostic
Diagnostic	-	28.68%	1,018		Diagnostic
Diagnostic		23.08%	52		Diagnostic
Diagnostic		32.45%	5,294		Diagnostic
Diagnostic					Diagnostic
Diagnostic		46.12%	2,129		Diagnostic
Diagnostic					Diagnostic
Diagnostic		5 70%	158		Diagnostic
Diagnostic	-	34.81%	2172		Diagnostic
Diagnostic		01.0170			Diagnostic
Diagnostic		21,59%	477		Diagnostic
Diagnostic		4.08%	98		Diagnostic
Diagnostic		22.78%	180		Diagnostic
Diagnostic		8.41%	107		Diagnostic
Diagnostic		39.98%	1.806		Diagnostic
Diagnostic		31.82%	22		Diagnostic
Diagnostic		UI.UL /J	Date fictude	d in 8 1 3 19	- Draginoorio
Diagnostic		77.59%	174	T ITT III- FIT- FIF	Diagnostic
Diagnostic		11,001,0	Data instado	d in & 1.3.20	

	Date hicks	led in # 1.3.19	
77.59%	174		Diagnostic
	Data Insta	od in # 1.1.2.20	
5.08%	256		Diagnostic
34.81%	2,172		Diagnostic
			Diagnostic
45 71%	1 015		Diagnostic

B.1.3.15

B.1.3.16

B.1.3.17

0-7

0-7

0-13

Other Non-Design/FL(%)

INP Standalone/FL(%)

LNP Standalone/FL(%)

6.1.3.18	0.7	Loops Non-Design/FL(%)	Diagnostic
B 1 3 19	0.7	Loops Non-Design w/NP/FL(%)	Diagnostic
B.1.3.20	0-13	Loops Non-Design w/LNP/FL(%)	Diagnostic
	Beiect i	Interval - Mechanized	
B141	0-8	Switch Ports/FL(%)	>= 97% w in 1 hr
B142	0-8	Local Interoffice Transport/FL(%)	>= 97% w in 1 hr
8143	0-8	Loop + Port Combinations/FL(%)	>= 97% w in 1 hr
B144	0-8	Combo Other/FL(%)	>= 97% w in 1 hr
8145	0-8	xDSL (ADSL, HDSL and UCLI/FL(%)	>= 97% w in 1 hr
B146	0-8	ISDN Loop (UDN, UDC)/FL(%)	>= 97% w in 1 hr
B147	0-8	Line Sharing/FL(%)	>= 97% w in 1 hr
B148	0-8	2W Analog Loop Design/FL(%)	>= 97% w in 1 hr
8149	0-8	2W Analog Loop Non-Design/FL(%)	>= 97% w in 1 hr
B1410	0-8	2W Analog Loop w/INP Design/FL(%)	>= 97% w in 1 hr
B1411	0-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 97% w in 1 hr
B1412	0-14	2W Analog Loop w/LNP Design/FL(%)	>≕ 97% w in 1 hr
B1413	0-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 97% w in 1 hr
B1414	0-8	Other Design/FL(%)	>= 97% w in 1 hr
B 1 4 15	0-8	Other Non-Design/FL(%)	>= 97% w in 1 hr
B 1 4 16	0-8	INP Standalone/FL(%)	>= 97% w in 1 hr
B1417	0-14	LNP (Standalone)/FL(%)	>= 97% w in 1 hr
D.1.1.17	Reject	Interval - Partially Mechanized - 24 hours	
D161	incjeur.	Switch Bots/El (%)	>= 85% w in 24 hrs
D.1.3.1	0-0	Local Interattice Transport/EL (%)	>= 85% w in 24 hrs
D.1.3.2	0.0	Local Interoffice Transport (1/9)	>= 85% w in 24 hrs
D.1.3.3	0-8	Combo Other/Et (%)	>= 85% w in 24 hrs
D.1.0.4 D 1 E E	0-8	VDSL (ADSL HDSL and HCLVEL(%)	>= 85% w in 24 hrs
D.1.3.5	0.0	ISDN Loop (UDN_LIDC)/EL(%)	>= 85% w in 24 hrs
B.1.3.0	0-8	Line Sharing/FI (%)	>= 85% w in 24 hrs
B.1.5.7	0-0	2004 Analog Loop Design/EL (%)	>= 85% w in 24 hrs
D.1.5.0	0.0	2W Analog Loop Design E(//)	>= 85% w in 24 hrs
D.1.5.9	0-0	2W Analog Loop with Design El (%)	>= 85% w in 24 hrs
D.1.5.10	0-0	2W Analog Loop w/NP Non-Design/FL (%)	>= 85% w in 24 hrs
D.1.3.11	0-14	2W Analog Loop with Posign/El (%)	>≃ 85% w in 24 hrs
B.1.5.12 B.1.5.13	0-14	2W Analog Loop w/LNP Non-Design/EL (%)	>= 85% w in 24 hrs
D.1.5.13	0-14	Other Design/EI (%)	>= 85% w in 24 hrs
D.1.3.14	0-8	Other Non-Design/FL (%)	>= 85% w in 24 hrs
D.1.5.15	0.8	INP Standalone/EI (%)	>= 85% w in 24 hrs
B 1 5 17	0-14	INP (Standalone)/FL(%)	>= 85% w in 24 hrs
0.1.0.17	Painet	Interval - Partially Mechanized - 18 hours	•
0161		Suiteb Borte El (%)	>= 85% win 18 hrs
B.1.0.1	0-0	Switch Pons/FL(70)	>= 85% w in 18 hrs
B.1.0.2	0.0	Local Interonice Hanspoor (%)	>= 85% w in 18 hrs
B.1.0.3	0.0	Loop + Poil Combinations/ C(19)	>= 85% w in 18 hrs
B.1.6.4	0-8	Compo Omer/FL(76)	>= 85% w in 18 hrs
B.1.6.5	0-8		>= 85% w in 18 hrs
B.1.6.6	0-8		>= 85% win 18 hrs
B.1.6.7	0-8	Line Sharing/FL(%)	>= 85% win 18 hrs
B.1.6.8	0-8	2W Analog Loop Design/rL(%)	>= 85% w in 18 hrs
B.1.6.9	0-8	ZW Analog Loop Non-Design/FL(%)	>= 85% win 18 brs
B.1.6.10	0-8	ZW Analog Loop W/INP Design/FL(%)	>= 85% w in 18 hrs
B.1.6.11	0-8	ZVV Analog Loop W/INP Non-Design/FL(%)	$\sim 85\%$ w in 18 hrs
B.1.6.12	0-14	ZW ANAIOG LOOP W/LNP Design/FL(%)	>= 85% win 18 hre
В.1.6.13	0-14	2W Analog Loop w/LNP Non-Design/FL(7%)	>= 85% win 18 hre
B.1.6.14	0-8	Uther Design/FL(%)	~ 85% win 18 hre
B.1.6.15	0-8	Uther Non-Design/FL(%)	~ 85% win 18 hre
B.1.6.16	0-8	INP Standalone/FL(%)	>= 85% win 18 hre
B.1.6.17	0-14	LNP Standalone/FL(%)	J >= 00 /0 W in 10 I//S

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
Diagnostic			38.18%	1,231				Diagnostic
Diagnostic			44.35%	124				Diagnostic
Diagnostic			37.32%	1,428	~~			Diagnostic
>≖ 97% w in 1 hr			 7					
>= 97% win 1 hr			90,48%	21				NO
>= 97% w in 1 hr			96.47%	1,359				NO
>= 97% w in 1 hr								
>= 97% w in 1 hr			100.00%	14				YES
>= 97% w in 1 hr								
>= 97% w in 1 hr								
>= 97% w in 1 hr			91.58%	95				NO
>= 97% w in 1 hr			100.00%	26				YES
>= 97% w in 1 hr								
>= 97% w in 1 hr								
>= 97% w in 1 hr			77.19%	57				NO
>= 97% w in 1 hr			95.35%	43				NO
>= 97% w in 1 hr			90.48%	21				NO
>= 97% w in 1 hr			96.47%	1,359				NO
>= 97% w in 1 hr								
>= 97% w in 1 hr			79.01%	162				NO

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e car parta (aga ta ta ta ta co	This data not applicable after 5-1-2001, see below	
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	This data not applicable after 5-1-2001, see balos	
	This date not applicable after 5-7-2001, see balos	N STATISTICS STRATEGY
	This data not applicable after 5-1-2001, see before	
	This data not applicable after 5-1-2001, see below	
ege i e cultori i	This data not applicable after 8-1-2001, see below	a se su solotion.
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	This date not applicable after 5-1-2001, see below	
	This date not applicable after 5-1-2001, see heloy	e te thios
	This data not applicable after 51-2001, non balan	
	This date not applicable after 5-1-2001, see below	

>= 85% w in 18 hrs				
>= 85% w in 18 hrs		90.91%	11	YES
>= 85% w in 18 hrs		97.90%	1,718	YES
>= 85% w in 18 hrs				
>= 85% w in 18 hrs				
>= 85% w in 18 hrs		100.00%	1	YES
>= 85% w in 18 hrs				
>= 85% w in 18 hrs		90.98%	133	YES
>= 85% w in 18 hrs		92.22%	90	YES
>= 85% w in 18 hrs	-			
>= 85% w in 18 hrs	-			
>= 85% w in 18 hrs		78.41%	352	NO
>= 85% w in 18 hrs		85.96%	292	YES
>= 85% w in 18 hrs		91.67%	12	YES
>= 85% w in 18 hrs		97.90%	1,718	YES
>≃ 85% w in 18 hrs				
>= 85% w in 18 hrs	-	82.69%	982	NO

	Flori	da, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
	Reject	Interval - Non-Mechanized	_								
B.1.8.1	O-8	Switch Ports/FL(%)	>= 85% w in 24 hrs								
B.1.8.2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs			100.00%	9	j			YES
B.1.8.3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs			99.60%	756				YES
B.1.8.4	0-8	Combo Other/FL(%)	>= 85% w in 24 hrs								
B.1.8.5	0-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs			93.20%	103				YES
B.1.8.6	0-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs			100.00%	4				YES
B.1.8.7	0-8	Line Sharing/FL(%)	>= 85% w in 24 hrs			100.00%	41				YES
B.1.8.8	0-8	2VV Analog Loop Design/FL(%)	>= 85% w in 24 hrs			100.00%	9				YES
B.1.8.9	0-8	2W Analog Loop Non-Design/FL(%)	>= 85% Win 24 nrs			93.21%					YES
B.1.8.10	0-8	2W Analog Loop wine Design/FL(%)	>= 85% win 24 nrs			100.00%	/				YES
D.1.0.11	0.14	2W Analog Loop with ND Design FL(%)	>= 60% win 24 nrs	-C	347-94 T. A.C.		Cute Anabude	d ID IR. L. A. 19	1172	مېږي دري د ۹ ا	
D.1.0.12	0-14	2W Analog Loop w/ NP Mes Design/FL(%)	>= 00% win 24 firs			91.11%	135				YES
D.1.0.13	0-14	Other Design/EL (%)	>= 0376 W III 24 III'S		r izeltitele	100.000/		a k a la.20	62.		
D. 1.0.14 B 1 8 15	0-8	Other Non-Design/61 (%)	>= 60 % win 24 hrs			100.00%	756				YES
B 1 8 16	0-0	INP Standelone/EI (%)	>= 85% win 24 hrs			99.00%	/50				YES
B1817	0-14	INP (Standalone)/Ei (%)	>= 85% win 24 hrs			05.01%	464				VEC
B 1 8 18	0-8	Loons Non-Design/FL (%)	>= 85% win 24 hrs			97 45%	404				TES VEC
B 1 8 19	0-8	Loops Non-Design w/NP/FL(%)	>= 85% win 24 hrs			98 18%					VEC
B.1.8.20	0-14	Loops Non-Design w/INP/FL(%)	>= 85% win 24 hrs			93.25%	533				VES
	EOC T	impliance Neckenized	•								
8191	0.9	Switch Ports/FI (%)	>= 95% win 3 hrs								
B192	0-9	I ocal Interoffice Transport/EI (%)	>= 95% w in 3 hrs			100.00%	30				VEG
B.1.9.3	0-9	Loop + Port Combinations/FL(%)	>= 95% w in 3 hrs			99.40%	7.979				YES
B.1.9.4	0-9	Combo Other/FL(%)	>= 95% w in 3 hrs								
B.1.9.5	0-9	xDSL (ADSL, HDSL and UCL//FL(%)	>= 95% w in 3 hrs			91.99%	287				NO
B.1.9.6	0-9	ISDN Loop (UDN, UDC)/FL(%)	>= 95% w in 3 hrs			100.00%	3				YES
B.1.9.7	0-9	Line Sharing/FL(%)	>= 95% w in 3 hrs								
B.1.9.8	0-9	2W Analog Loop Design/FL(%)	>= 95% w in 3 hrs			99.94%	1,648				YES
B.1.9.9	O-9	2W Analog Loop Non-Design/FL(%)	>= 95% w in 3 hrs			97.91%	191				YES
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 95% w in 3 hrs								
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 95% w in 3 hrs								
B.1.9.12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 95% w in 3 hrs			72.15%	79				NO
B.1.9.13	0-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95% w in 3 hrs			100.00%	102				YES
B.1.9.14	0-9	Other Design/FL(%)	>= 95% w in 3 hrs			100.00%	33				YES
B.1.9.15	0-9	Other Non-Design/FL(%)	>= 95% w in 3 hrs			99.40%	7,979				YES
B.1.9.16	0-9	INP Standalone/FL(%)	>= 95% w in 3 hrs								
B.1.9.17	0-15	ILNP Standalone/FL(%)	>= 95% win 3 nrs			84.68%	3,173				NO
	FOCT	imeliness - Partially Mechanized									
B.1.10.1	0-9	Switch Ports/FL(%)	>= 85% w in 36 hrs			This data	not applicable :	Aur 5-1-2001,	nee below	· · · · · · · · · · · · · · · · · · ·	
8.1.10.2	0-9	Local Interoffice Transport/FL(%)	>= 85% win 36 hrs			This data	not applicable i	fter 5-1-2001,	ere below		
B.1.10.3	0-9	Loop + Port Combinations/FL(%)	>= 85% W in 36 hrs			This data	net applicable :	Aur 5-1-2001,	ine balow		
B.1.10.4	0-9	Combo Other/FL(%)	>= 85% Win 36 hrs			The shall	nat applicable :	Rev 5-1-2001 ,	nen belour		
B.1.10.5	0-9	XUSL (AUSL, AUSL and UCL)/TL(76)	>= 65% win 36 nrs				not ap licable i	NW 5-1-2001,	eee kalow		
D.I.IU.0	0-9		>= 0570 W In 30 Mrs	No. Con contraction	مار مورد بعد الم	17th dialo	not applicable s	mer 9-1-2091,	ean fanlan		
B.1.10.7	0-9	Children Land David Children C	>= 65% will 30 fits	a water and the			not apprenting	000 5-1-2001	eee Delow		
D.1.10.0	0.9	201/ Analog Loop Non-Design/EL (%)	>= 00 /0 W I(1 00 I)(S		<u> </u>			new 0-1-2001.	100 DOLDH 100 DOLDH		
B 1 10 10	0.9	23/ Analog Loop WIND Design/EL (%)	> 00 /0 W III 00 IIIS	ning an			and management (1	nee Delow		
D.1.10.10	0.9	2014 Analog Loop with D Non-Design (2) (%)	>= 00 /0 W III 00 IIIS		<u></u>		and any property of the second	1997 P* 1*2007,			
B 1 10 12	0-15	20/ Analog Loop w/LNP Design/FL(%)	>- 00 /0 with 00 mrs	nden sinden			nun approximite i		ree Delow		
B 1 10 13	0-15	2W Analog Loop W/LIT Designin L(10)	>= 85% win 36 hre	ap grade of the	i na statu i d	erren smith	mat muchant	Man 8. 1. 2007.			
8 1 10 14	0-13	Other Design/EL (%)	>= 85% win 36 hrs		<u>ارز بر من من من</u>	State Care	not confinable		sop Delow	······································	Parate 2
B 1 10 16	0-9	Other Non-Design/EL (%)	>= 0.5 /0 W III 30 III'S		at residunt	11789 40000	THAT ANY	Anna K. 4 1007.			
B 1 10 16	0-9	INP Standalone/El (%)	>= 85% win 36 hrs		i senerata	This data	not applicable	Mar 5-1-2004	een Delow		<u>, en a contrata de la contrata de la</u>
B 1 10 17	0-15	INP Standalone/FL(%)	>= 85% w in 36 hrs	1		The gate	net approximate a	Ann 6-1-2007,			
	<u> </u>	1		and a star of the second			A REAL PROPERTY AND	····· · · ·····	~~~ 꾸야야~!!!		10 M A & C &

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	Flori	da, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
	FOC T	meliness - Partially Mechanized - 18 hours									
3.1.11.1	0-9	Switch Ports/FL(%)	>= 85% w in 18 hrs						الانتخاذ		
3.1.11.2	0-9	Local Interoffice Transport/FL(%)	>= 85% w in 18 hrs			100.00%	43				YES
3.1.11.3	0-9	Loop + Port Combinations/FL(%)	>= 85% w in 18 hrs			97.32%	3,918				YES
3.1.11.4	O-9	Combo Other/FL(%)	>= 85% w in 18 hrs					Ĩ			
3.1.11.5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>≖ 85% w in 18 hrs			80.00%	5]			NO
3.1.11.6	0-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 18 hrs			75.00%	4				NO
3.1.11.7	0-9	Line Sharing/FL(%)	>= 85% w in 18 hrs								
3.1.11.8	0-9	2W Analog Loop Design/FL(%)	>= 85% w in 18 hrs			95.16%	537				YES
3.1.11.9	0-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 18 hrs			98.43%	446				YES
3.1.11.10	0-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 18 hrs								
3.1.11.11	0-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 18 hrs								
3.1.11.12	0-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 18 hrs			79.09%	703				NO
3.1.11.13	0-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 18 hrs			96.28%	833				YES
3.1.11.14	0-9	Other Design/-L(%)	>= 85% win 18 hrs			97.87%	47				YES
3.1.11.15	0.9	Other Non-Design/I-L(%)	>= 85% win 18 hrs			97.32%	3,918				YES
3.1.11.16	0-9	INP Standalone/FL(%)	>= 85% Win 18 hrs			07 100/	1.000				
5.1.11.17	0-15	LNP Standarche/FL(76)	>= 85% W IN 18 hrs			87.13%	1,538				YES
	FOC T	meliness - Non-Mechanized									
3.1.13.1	O-9	Switch Ports/FL(%)	>= 85% w in 36 hrs								
3.1.13.2	0-9	Local Interoffice Transport/FL(%)	>= 85% w in 36 hrs			98.57%	140				YES
3.1.13.3	0-9	Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs			99.93%	1,454				YES
3.1.13.4	0-9	Combo Other/FL(%)	>= 85% w in 36 hrs								
3.1.13.5	0-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% win 36 hrs			99.77%	435				YES
3.1.13.6	0-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs			_100.00%	93				YES
3.1.13.7	0-9	Line Sharing/FL(%)	>= 85% w in 36 hrs			_100.00%	134				YES
3.1.13.8	0-9	2W Analog Loop Design/FL(%)	>= 85% win 36 hrs			98.89%	90				YES
3.1.13.9	0-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 36 hrs			99.42%	1,383				YES
3.1.13.10	0-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs			100.00%	29				YES
3.1.13.11	0-9	2W Analog Loop W/INP Non-Design/FL(%)	>= 85% W in 36 hrs								
3.1.13.12	0-15	2W Analog Loop W/LNP Design/FL(%)	>= 85% w in 36 hrs			87.71%	1/9				YES
3.1.13.13	0-15	2W Analog Loop W/LNP Non-Design/FL(%)	>= 65% with 36 hrs			93.30%	1,298				YES
3.1.13.14	0.9	Other Design/FL(%)	>= 05% win 30 hrs			99.14%	233	-			YES
0.1.10.10	0.9	IND Standalono/EL (9()	>= 05 % w in 36 hm			99.93%	1,454				YES
31 13 17	0-15	INP Standalone/FL (%)	>= 85% win 36 hrs			89 12%	855				VES
	0 10					00.12/0					
	FOC &	Reject Response Completeness - Mechanized	7								
3.1.14.1	0-11	Switch Ports/FL(%)	>= 95%								
3.1.14.2	0-11	Local Interoffice Transport/FL(%)	>= 95%			80.95%	63				NO
3.1.14.3	0-11	Loop + Port Combinations/FL(%)	>= 95%			91.50%	10,206	-			NO
5.1.14.4	0.11		>= 95%			<u> </u>		-			
5.1.14.5	0.11	XDSL (ADSL, HDSL and UCL)/FL(76)	>= 50%			02.96%	230	-			NO VEC
0.1.14.0	0-11		- 059/			100.00%	3	-			YES
5.1.14.7	0-11	Line Sharing/FL(%)	>= 5576			00.050/	1 011				
5.1.14.0	0.11	12W Analog Loop Design/FL(%)	>= 95%			90.23%	1,011				YES
0.1.14.0	0.11	2W Analog Loop with D Decign (EL (%)				/1.0270					
2 1 14 11	0-11	200 Analog Loop with Design (170)	- 95%					-			
3 1 14 12	0-11	2W Analog Loop with Therebesignin L(10)				100.00%	248	·			VEC
3 1 14 13	0-11	2W Analog Loop w/ NP Non-Design/El (%)	>= 95%			100.00%	278				VES
3 1 14 14	0-11	Other Design/EI (%)	>= 95%			81 82%	66				
3 1 14 15	0-11	Other Non-Design/EL (%)	>= 95%			91,50%	10,206				
1 14 16	0-11	INP Standalone/FI (%)	>= 95%			000/0					
.1.14.17	0-11	LNP Standalone/FL(%)	>= 95%			100.00%	559				YES
		Defect Deserves Council deserves Destinition of the state	4								·
	FOC &	Hejeci Hesponse Completeness - Parlially Mechanized									
3.1.15.1	0-11	SWICH FORS/FL(%)	~ >= 95%			100.000					
3.1.15.2	0-11	Local Interoffice Transport/FL(%)	>= 95%			100.00%	48				YES

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B.1.15.3	0-11	Loop + Port Combinations/FL(%)
B.1.15.4	0-11	Combo Other/FL(%)
B.1.15.5	0-11	xDSL (ADSL, HDSL and UCL)/FL(%)
B.1.15.6	0-11	ISDN Loop (UDN, UDC)/FL(%)
8.1.15.7	0-11	Line Sharing/FL(%)
8.1.15.8	0-11	2W Analog Loop Design/FL(%)
B.1.15.9	0-11	2W Analog Loop Non-Design/FL(%)
B.1.15.10	0-11	2W Analog Loop w/INP Design/FL(%)
8.1.15.11	0-11	2W Analog Loop w/INP Non-Design/FL(%)
B 1 15 12	0-11	2W Analog Loop w/LNP Design/FL (%)
B 1 15 13	0.11	2W Analog Loop w/LNP Non-Design/FL (%)
B 1 15 14	0-11	Other Design/EI (%)
B 1 15 15	0-11	Other Non-Design/El (%)
B 1 15 16	0.11	INP Standaione/El (%)
B 1 15 17	0.11	INP Standalone/EL(%)
0.1.13.17	<u> </u>	
	FOC &	Reject Response Completeness - Non-Mechanized
B.1.16.1	0-11	Switch Ports/FL(%)
B.1.16.2	0-11	Local Interoffice Transport/FL(%)
B.1.16.3	0-11	Loop + Port Combinations/FL(%)
B.1.16.4	0-11	Combo Other/FL(%)
B.1.16.5	0-11	xDSL (ADSL, HDSL and UCL)/FL(%)
B.1.16.6	0-11	ISDN Loop (UDN, UDC)/FL(%)
B.1.16.7	0-11	Line Sharing/FL(%)
B 1 16 8	0-11	2W Analog Loop Design/FL(%)
B 1 16 9	0-11	2W Analog Loop Non-Design/El (%)
B 1 16 10	0-11	2W Analog Loop w/NP Design/EL(%)
B 1 16 11	0.11	2W Analog Loop w/NP Non-Design/El (%)
D.1.10.11	0.11	2W Analog Loop w/I NP Design/EL (%)
D.1.10.12	0-11	2W Analog Loop w/LIV Design E(1/9)
D.1.10.13	0.11	Other Design/EL (%)
B.1.10.14	0.11	Other Design/FL(%)
B.1.10.15	0.11	Uner Non-Design/FL(76)
B.1.10.10	0-11	INP Standalone/FL(%)
B.1.10.17	0-11	LINP Standalone/FL(76)
	FOC &	Reject Response Completeness (Multiple Responses) - Mechanized
B.1.17.1	0-11	Switch Ports/FL(%)
B.1.17.2	0-11	Local Interoffice Transport/FL(%)
B1173	0-11	Loop + Port Combinations/FL(%)
B.1.17.4	0-11	Combo Other/FL(%)
B 1 17 5	0-11	xDSL (ADSL, HDSL and UCL)/FL(%)
B 1 17 6	0-11	ISDN Loop (UDN, UDC)/FL(%)
B 1 17 7	0-11	Line Sharing/El (%)
81178	0.11	2W Analog Loop Design/EL (%)
B 1 17 0	0.11	20/ Analog Loop Non-Design/EL (%)
D.1.17.0	0-11	2011 Analog Loop wiNP Design/EL (%)
D.1.17.10	0.11	2014 Analog Loop with Design/EL (%)
B.I.17.11	0-11	ZVV Analog Loop w/LNP NOP-Design/FL(%)
B.1.17.12	0-11	Zw Analog Loop w/LNP Design/FL(%)
B.1.17.13	0-11	2W Analog Loop W/LNP Non-Design/FL(%)
В.1.17.14	0-11	Other Design/FL(%)
B.1.17.15	0-11	Uther Non-Design/FL(%)
B.1.17.16	0-11	INP Standalone/FL(%)
B.1.17.17	0-11	LNP Standalone/FL(%)
	FOC A	Reject Response Completeness (Multiple Responses) - Partially Mechanized
B 1 18 1	0-11	Switch Ports/FL(%)
B 1 18 2	0-11	Local Interoffice Transport/EI (%)
B 1 18 3	0-11	Loop + Port Combinations/FI (%)
D.1.10.0	0-11	Combo Other/El (%)
D.1.10.4	0.11	
D.1.16.5	0.11	
B.1.18.6	0-11	

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
>= 95%			100.00%	5,294				YES
>= 95%								
>= 95%								
>= 95%			100.00%	4				YES
>= 95%								
>= 95%			100.00%	608				YES
>= 95%			100.00%	519	_			YES
>= 95%								
>= 95%								
>= 95%			100.00%	636				YES
>= 95%			100.00%	/4/	-			YES
>= 95%			100.00%	52	-			YES
>= 95%			100.00%	5,294	-			TES
>= 30 /a >= 95%			100.00%	4 546				VES
	1		100.0070	4,040		_		120
>= 95%								l
>= 95%			96.58%	146				YES
>= 95%			98.69%	2,284				YES
>= 95%								-
>= 95%			100.00%	19				YES
>= 95%			97.92%	96				YES
>= 95%			93.50%	200	-			NO
>= 95%			93.07%	101	-			NO
>= 95%			93.86%	2,539				NO
>= 90%			100.00%	<u> </u>	-			YES
>= 90%			00 E 49/	210				VES
>= 30 /6 ∖= 959∠			99.54 /6	1 545	-			VES
>- 95%			97 11%	242	-			VES
>= 95%			98.69%	2,284				YES
>= 95%			00.00 /0					
>= 95%			100.00%	1,205				YÉS
>= 95%			400.000/	54	-			1/50
>= 95%			100.00%	51	-			YES
>= 9076			100.00%	9,338				TEO
>- 05%			08 65%	148	-			VES
>- 95%			100.00%	3				VES
>= 95%			100.00 /0		-			120
>= 95%			100.00%	1.743				YES
>= 95%			100.00%	217				YES
>= 95%								
>= 95%								
>= 95%			100.00%	248				YES
>= 95%			98.56%	278				YES
>= 95%			100.00%	54				YES
>= 95%			100.00%	9,338				YES
>= 95%								
>= 95%			100.00%	559				YES
S- 95%								
>= 95%			89 58%	48				NO
>= 95%			94 31%	5.294				NO
>= 95%			04.0170					
>= 95%				· · · ·				
>= 95%			100.00%	4				YES

Flor	rida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
0-11	Line Sharing/FL(%)	>= 95%					_			
0-11	2W Analog Loop Design/FL(%)	>= 95%			06 88%	609	-			1/50
0-11	2W Analog Loop Non-Design/FL(%)	>= 95%			94,00%	<u> </u>				YES
0-11	2W Analog Loop w/INP Design/FL(%)	- 95%			54.55 %	519	-			NO
0-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 05%								
0-11	2W Analog Loop w/I NP Design/FI (%)				100.000/					
0-11	2W Analog Loop w/LNP Non-Design/FL(%)				100.00%	0.30				YES
0-11	Other Design/FL(%)				00 289/	<u>/4/</u>	-			YES
0-11	Other Non-Design/FL(%)				04 319/	52				NO
0-11	INP Standalone/FL(%)	>= 05%			94.31%	5,294				NO
0-11	LNP Standalone/FL(%)				00 74%	A E 46	-			1000
FOC	t Reject Response Completences (Multiple Responses) . Non-Machanized		1		00.7470	4,540				YES
0-11	Switch Ports/FL(%)	>= 95%					_			
0-11	Local Interoffice Transport/FL(%)	>= 95%			Q1 40%	141	-			
0-11	Loop + Port Combinations/FL(%)	>= 95%			05 3/94	2.054				NO
0-11	Combo Other/FL(%)	>= 95%			80.0476	2,204				YES
0-11	xDSL (ADSL, HDSL and UCL//FL(%)	>= 95%			04 74%	10				
0-11	ISDN Loop (UDN, UDCVFL(%)	>= 95%			03.62%					NO
0-11	Line Sharing/FL(%)	>= 95%			03.58%	187				NO
0-11	2W Analog Loop Design/FL(%)	>= 95%			80.36%	04				NO
0-11	2W Analog Loop Non-Design/FL(%)	>= 95%			92 70%	2 282				NO
0-11	2W Analog Loop w/INP Design/FL(%)	>= 95%			75.00%	2,303				NO
0-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%			75.00 /6	0				NO
0-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%			100.00%	210				
0-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%			00.0010	1.541				YES
0-11	Other Design/FL(%)	>= 95%			02 34%	225	-			YES
0-11	Other Non-Design/FL(%)	>= 95%			95 34%	2.05				NU VEO
0-11	INP Standalone/FL(%)	>= 95%			00.0470	2,204				YES
0-11	LNP Standalone/FL(%)	>= 95%			100.00%	1 205				VEC
0-11 0-11 0-11	Other Non-Design/FL(%) INP Standalone/FL(%) LNP Standalone/FL(%) Comparison of the standalone/FL(%)	>= 95% >= 95%			95.34% 95.34% 100.00%	2,254				YE
Order	Completion Interval									
P-4	Switch Ports/<10 circuits/Dispatch/El (days)		<u> </u>	60.075	,					
P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	H&B (POTS)	3.72	69,375	L		5.890			
P-4	Switch Ports (~-10 circuits/Non-Dispatch/FL(days)	H&B (POTS)	0,85	668,187			1.421			
P-4	Switch Ports/	H&B (POIS)	10.03	405			21.457			
D-4	I coal Interative Transport/-10 sizevite/Dispatch/FL(days)		1.18	67			0.965			
F-4	Local Interonce Transport <to circuits="" dispatch="" fl(days)<="" td=""><td>DS1/DS3</td><td></td><td></td><td>20.75</td><td>16</td><td></td><td></td><td></td><td></td></to>	DS1/DS3			20.75	16				

DS1/DS3 DS1/DS3 DS1/DS3 R&B R&B R&B R&B R&B R&B R&B R&B R&B&D - Disp R&B&D - Disp R&B&D - Disp R&B&D - Disp ADSL to Retail ADSL to Retail

Co		owner rous to circuits/Dispatent/ L(days)
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL(days)
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(days)
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL(days)
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(days)
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(days)
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/FL(days)
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/FL(days)
B.2.1.5.3.1	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/FL(days)
B.2.1.5.3.2	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/FL(days)
B.2.1.5.4.1	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/FL(days)
B.2.1.5.4.2	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/FL(days)
B.2.1.5.5.1	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/FL(days)
B.2.1.5.5.2	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/FL (days)

69,375			5.890			
668,187			1.421			
405			21.457			
67			0.965			
	20.75	16				
69,996	1.52	504	5.978	0.26726	8.3246	YES
670,555	1.01	7,008	1.423	0.01708	-9.5064	NO
		Dets includes	In #21.1.1.2			
		Date Included	1 h 82 1.3.1.2		10. 26	
444	3.19	7	20.694	7.88309	0.8552	YES
460	1.42	4	2.433	1.22188	0.2807	YES
		Dete included	h #2.1.3.2.2			· · · · ·
		Data Included	In \$2.1.3.2.2		·····	ngerggangstafterserator Fisiologi militik
75,750			11.759			
472			20.120			
1,800	4.59	162	17.366	1.42449	2.0358	YES
656			1.542			
	69,375 668,187 405 67 67 69,996 670,555 670,555 670,555 670,555 444 460 472 1,800 656	69,375 668,187 405 - 67 20.75 - - 69,996 1.52 670,555 1.01 68,996 1.42 444 3.19 460 1.42 75,750 - 472 - 1,800 4.59 656 -	69,375 668,187 405	69,375 5.890 668,187 1.421 405 21.457 67 0.965 20.75 16 69,996 1.52 504 5.978 670 0.965 20.75 16 69,996 1.52 504 5.978 670.555 1.01 7008 1.423 Deta included in 8.2.1.1.2 904 included in 8.2.1.3.2 914 included in 8.2.1.3.2 915 included in 8.2.1.3.2 916 included in 8.2.1.3.2 917 91 910 91 92 93 9472 9472 9472 9473 <	69,375 5.890 668,187 1.421 405 21.457 67 0.965 20.75 16 69,996 1.52 504 5.978 670 0.26726 671 0.26726 69,996 1.52 504 5.978 670 0.26726 670,555 1.01 7,008 1.423 9 0.01708 9 0.26726 670,555 1.01 7,008 1.423 9 0.01708 9 0.026726 75,750 1.1759 9 11.759 11.759 11.759 11.800 4.59 162 17.366 1.42449 656 1.542	69,375 5.890 668,187 1.421 405 21.457 67 0.965 20.75 16 69,996 1.52 504 5.978 67 0.965 9.996 1.52 504 5.978 67 0.01708 69,996 1.52 504 5.978 0.26726 8.3246 670.555 1.01 7,008 1.423 0.01708 9.5064 Date included in 82.13.12 444 3.19 7 20.694 76,750 11.759 9 11.759 472 20.120 1.800 4.59 162 17.366 1.42449 2.0358 656 1.542

	Florida, June 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	1 1011	a, 02110 2001	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
			-					T 45 800	4 47000	C 0750	
3.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL(days)	ISDN - BRI	18.79	481	10.61	293	15.823	1.17260	6.9759	YES
3.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL(days)	ISUN - BHI	2.80	445			0.450			
3.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL(days)	ISUN - BRI	4.00				0.000			
3.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL(days)	ISUN - BRI								
3.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL(days)	ISUN - BRI								
3.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL(days)	ISUN - BHI	7.40	1 800	2.50		17 266	10 20659	0 2247	VEC
3.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/FL(days)	ADSL to Hetall	7.49	1,000	3.50	2	1 542	0.07118	-2 1757	NO
3.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL(days)	ADSL to Hetall	2.91	050	3.00		1.042	0.27110	-2.1137	
3.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL(days)	ADSL to Retail	· · · ·				+			
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail				·				
3.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL(days)	ADSL to Retail								
3.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/-L(days)		2.74	60.006	6 1 2	240	5.078	0.32501	-7 3081	NO
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Had - Disp	3.74	60,006	0.12	340	5.978	0.02001	-7.0001	
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Hab - Disp	3.74	09,990	8.00	16	20.604	5 26504	0 3668	VES
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Had - Disp	0.03	444	0.00		20.034	5.20004	0.0000	
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Hab - Disp	3.70	60 375	4.45	69	5.890	0.71467	-1.0160	VES
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Hab (POTS) excl SB OF	1.72	234 864	4.40		1 970	0.71407	1.0100	
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(days)	Hab (POTS) excl SB OF	10.02	405	5.00	1	21 457	21 49207	0 2343	VES
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	BAB (POTS) excl SB O	1.05	40.0	5.00		0.966	21.40237	0.2010	100
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch in/FL(days)		2.74	60,006	4.50	2	5 078	4 22743	-0 1786	VES
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)		3.74	60,006	4.50	<u>~</u>	5 978	4.22145	-0.1700	,20
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	BRR - Disp	0.03	444			20.694			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	R&R - Disp	0.00	444		~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	20.694	· · · · · ·		
B.2.1.10.2.2	P-4	2W Analog Loop w/NP Design/>=10 circuits/Non-Dispatch/FL(days)	- B&B (POTS) avel SB Or	3.35	69.375	4 20	5	5.890	2 63436	-0 1822	YES
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	BLB (POTS) excl SB Or	1.36	334 864	4.20		1 870	2.00100	V. TOLL	120
B.2.1.11.1.4	P-4	2W Analog Loop W/INP Non-Design/<10 circuits/Dispatch In/PL(days)	BRB (BOTS) avai SB Or	10.03	405			21 457			
B.2.1.11.2.1	P-4	2W Analog Loop w/NP Non-Design/>=10 circuits/Dispatch/FL[days]	BAB (POTS) excl SB Or	1.45	51			0.966			
B.2.1.11.2.4	P-4	2W Analog Loop W/INP Non-Design/>=10 circuits/Displatch In/FL(days)	B&B - Diep	3.74	300.03	6.29	236	5 978	0.38982	-6.5280	NO
B.2.1.12.1.1	P-4	2W Analog Loop W/LNP Design/<10 circuits/Dispatch/FL(days)	- B&B + Disp	3.74	69,996	0.20		5 978	0.00002	0.02.00	
B.2.1.12.1.2	P-4	2W Analog Loop W/LNP Design/<10 circuits/Non-Dispatch/FL(days)	B&B - Disp	9.93	444	7.29	14	20.694	5.61728	0.4710	YES
B.2.1.12.2.1	P-4	2W Analog Loop W/LNP Design/>=10 circuits/Dispatch/FL(days)	B&B - Disp	9.93	444			20 694			
B.2.1.12.2.2	P-4	2W Analog Loop W/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	B&B (POTS) ercl SB Or	3.72	69.375	4.54	178	5.890	0.44207	-1.8450	NO
B.2.1.13.1.1	P-4	2W Analog Loop W/LNP Non-Design/<10 circuits/Dispatch/r-(days)	BAB (POTS) excl SB Or	1.36	334,864	3.95	76	1.870	0.21457	-12.0655	NO
B.2.1.13.1.4	P-4	2W Analog Loop W/LNP Non-Design/<10 circuits/Dispatch II/I-L(days)	BAB (POTS) excl SB Or	10.03	405	6.29	14	21,457	5.83276	0.6426	YES
B.2.1.13.2.1	P-4	2W Analog Loop W/LNP Non-Design/>=10 circuits/Dispatch/ E(days)	B&B (POTS) excl SB Or	1.45	51	6.00	2	0.966	0.69611	-6.5349	NO
B.2.1.13.2.4	P-4	2W Analog Loop W/LNF Noi-Design/2-To chedusionspation nin Elouys)	Design	23.37	5,754	11.10	31	32.085	5.77819	2.1246	YES
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/FL(days)	- Design	3 30	3,295			7.562			
8.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/El (days)	Design	4.55	28			2.472			
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL(days)	Design	3.69	42			2.628	· · · ·		
B.2.1.14.2.2	P-4	Other Design/>= To Circuits/Non-Dispatch/FL (days)	BAB	3.74	69,996	1.67	4	5,978	2.98929	0.6958	YES
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/El (days)	BAB	0.85	670,555	2.07	5	1.423	0.63616	-1.9152	NO
B.2.1.15.1.2	P-4	Other Non-Design/x=10 circuits/Non-Dispatch/in L(days)	- R&B	9.93	444	3.00	1	20.694	20.71749	0.3346	YES
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL(days)	BAB	1.76	460			2.433			
B.2.1.15.2.2	P-4	UND (Clandelone)/<10 circuits/Dispatch/FL (days)	B&B (POTS)	3.72	69,375	1		5.890			
B.2.1.16.1.1	P-4	INP (Standalone)/-10 circuits/Non-Dispatch/EL (days)	R&B (POTS)	0.85	668,187	1		1.421			
B.2.1.16.1.2	P-4	INP (Standalone)/- =10 circuits/Dispatch/EL (days)	BAB (POTS)	10.03	405	1		21.457			
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/El (days)	BAB (POTS)	1.18	67	1		0.965	1		
B.2.1.10.2.2	P-4	INP (Standalone)/-10 circuits/Non Dispatch/El (days)	B&B (POTS)	3.72	69.375			5.890			
B.2.1.17.1.1	P-4	LNP (Standalone)/ 10 circuits/Dispatch/ Cludys/	BAB (POTS)	0.85	668,187	1.58	1.842	1.421	0.03315	-22,1593	NO
B.2.1.17.1.2	P-4	ENP (Standalone)/<10 circuits/NoiPolspatch/ E(days)	BAB (POTS)	10.03	405			21,457			
B.2.1.17.2.1	P-4	LINE (Standalona)/ =10 circuits/Dispatch/Fi (days)	BAB (POTS)	1.18	67	8.86	7	0.965	0.38332	-20.0271	NO
B.2.1.17.2.2	1-4	Digital Loop = DC1/210 circuits/hon-to-spatch/r (Joays)	Digital Loop < DS1	20.15	550	10.61	293	16.100	1.16445	8.1950	YES
B.2.1.18.1.1	P-4	Digital Loop < D51/<10 circuits/Dispatch/FL(days)	- Digital Loop < DS1	12.50	8	1	1	10.542	1	h	
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispaton/n Loops)	Digital Loop < DS1			1	1	+	1		
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatc///FL(Udys)	Digital Loop < DS1		<u>+</u>	1	t	1	1		1
8.2.1.18.2.2	P-4	Digital Loop < DS1/s10 circuits/Non-Dispatch/EL (days)	- Digital Loop >= DSt	20.44	72	5,29	248	22.812	3.05377	4.9637	YES
B.2.1.19.1.1	<u>r-4</u>	Digital Loop >= DS1/<10 circuits/Dispatch/FL(uays)	Digital Loop >= DS1	16.94	16	t	1	8.136	1		t1
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/hoir-Dispatch/FL(days)	Digital Loop >= DS1		t	1	t	1	1		
B.2.1.19.2.1	P-4	Indital roob >= D91/>=10 clicnics/Disbarch/Lifeaves/		L	L	L			L		

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	Florida, June 2001		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
B211922	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/EI (days)	Digital Loop >= DS1			r		<u> </u>			
D.L. WIGLL	Ľ			h-management and		ļ					
0.001	Diaer	Longietion interval within X days	14 days			E 00 1	1				VEC
B.2.2.1	P-4	XDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/El (days)	7 days			4.58	161				VES
0.2.2.2	Ľ <u>~</u>	TYPOE (MDOE, MDOE and OOE) EOOP WO CONtainening to an outside platent equality	, uuju			4.55					120
	Held C							1 10 777			· · · · · · · · · · · · · · · · · · ·
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/FL(days)	PIR (POTS)	9.42	02/			13.777			
B.2.3.1.1.2		Switch Polis/<10 circuits/Equipment/FL(days)	P&B (POTS)	12.78	77	╂────┤		20.544			
B.2.3.1.1.3	P-1	Switch Ports/~10 circuits/Enclit//EL(days)	BAB (POTS)	9.33	3	<u> </u>		7 572			
B23122	P.1	Switch Ports/>=10 circuits/Fauinment/El (days)	BAB (POTS)					1.572		· · · ·	
0.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Other/FL (days)	B&B (POTS)							· · · · · ·	
823211	P-1	l ocal Interoffice Transport/<10 circuits/Facility/FI (days)	DS1/DS3 - Interoffice	}							
B23212	P-1	Local Interoffice Transport/<10 circuits/Equipment/EL (days)	DS1/DS3 - Interoffice			18.00	1				
B23213	P-1	Local Interoffice Transport/<10 circuits/Other/FL (days)	DS1/DS3 - Interoffice				i				
823221	P-1	Local Interdifice Transport/s=10 circuits/Facility/FI (days)	DS1/DS3 - Interoffice			1 1					
823222	P-1	l ocal Interoffice Transport/>=10 circuits/Equipment/FL(days)	DS1/DS3 - Interoffice								
B23223	P-1	Local Interoffice Transport/>=10 circuits/Other/FI (days)	DS1/DS3 - Interoffice			1					
823311	P-1	Loop + Port Combinations/<10 circuits/Facility/FL(days)	R&B	9.42	636			13.690			
B23312	P-1	Loop + Port Combinations/<10 circuits/Equipment/El (days)	R&B								
823313	P-1	Loop + Port Combinations/<10 circuits/Other/FL(days)	R&B	12.78	77	7.00	2	20.544	14,71447	0.3928	YES
B23321	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL(days)	R&B	9.33	3			7.572			
B23322	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL(days)	R&B								
B23323	P-1	Loop + Port Combinations/>=10 circuits/Other/FL(days)	R&B				-	1			i
B 2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/FL(days)	R&B&D - Disp	9.56	650			13.754			
B23412	P-1	Combo Other/<10 circuits/Equipment/FL(days)	R&B&D - Disp								
B23413	P-1	Combo Other/<10 circuits/Other/FL(days)	R&B&D - Disp	23.34	110			47.721	·		
B23421	P-1	Combo Other/>=10 circuits/Facility/FL(days)	R&B&D - Disp	9.33	3			7.572			
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/FL(days)	R&B&D - Disp					1			
8.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/FL(days)	R&B&D - Disp	48.00	1			0.000			
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL(days)	ADSL to Retail	37.27	848			37.668			
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL(days)	ADSL to Retail								
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL(days)	ADSL to Retail	20.84	43			40.064			
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL(days)	ADSL to Retail								
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL(days)	ADSL to Retail								
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL(days)	ADSL to Retail								
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/FL(days)	ISDN - BRI	24.14	7	35.00	2	18.069	14.48719	-0.7494	YES
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/FL(days)	ISDN - BRI								
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/FL(days)	ISDN - BRI			18.00	1				
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/FL(days)	ISDN - BRI								
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/FL(days)	ISDN - BRI								
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/FL(days)	ISDN - BRI								
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/FL(days)	ADSL to Retail	37.27	848			37.668			
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/FL(days)	ADSL to Retail								
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/FL(days)	ADSL to Retail	20.84	43			40.064			
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/FL(days)	ADSL to Retail								
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/FL(days)	ADSL to Retail								
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/FL(days)	ADSL to Retail								
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL(days)	R&B - Disp	9.42	636	27.67	3	13.690	7.92226	-2.3041	NO
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL(days)	R&B - Disp								
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/FL(days)	R&B - Disp	12.78	77	L		20.544			
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL(days)	R&B - Disp	9.33	3	↓ ↓		7.572			
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL(days)	R&B - Disp	II		ļļ					
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL(days)	R&B - Disp								
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.42	627			13.777			
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or			I					
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	12.78				20.544			
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.33	3	9.00	1	7.572	8.74328	0.0381	YES
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or								L

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	Florida, June 2001		Benchmark /	BST	BSŤ	CLEC	CLEC	Standard	Standard		
	riori	<i>a, oune 2001</i>	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
						· · · · · · · · · · · · · · · · · · ·		T			
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL(days)	R&B (POIS) excl SB Or	9.42	636			13,690			
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL(days)	R&B - Disp	5.42	0.00			10.000			
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL(days)	B&B - Disp	12 78	77			20.544			
B.2.3.10.1.3	P-1	2W Analog Loop W/NP Design/<10 circuits/Other/FL(days)	B&B - Disp	9.33	3	1 1		7.572			
8.2.3.10.2.1	P-1	2W Analog Loop W/INP Design/>=10 circuits/Faciny/i L(days)	B&B - Disp					1			
B.2.3.10.2.2	P-1	2W Analog Loop w/NP Design/s=10 circuits/Equipment/ Edu/s)	B&B - Disp					1			
B.2.3.10.2.3	P-1	2W Analog Loop w/NP Design/>10 circuits/Eacility/El (days)	R&B (POTS) excl SB Or	9.42	627			13.777			
B.Z.J.11.1.1	0.1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/El (days)	R&B (POTS) excl SB Or			1					
8.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Cither/El (days)	R&B (POTS) excl SB Or	12.78	77			20.544	· · · · ·		
B.Z.J.11.1.J	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.33	3			7.572			
B.Z.J.11.Z.1	0.1	2W Analog Loop w/NP Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or			1					
B.2.3.11.2.2	0.1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/EL (days)	R&B (POTS) excl SB Or					1			
B.2.3.11.2.3	0.1	2W Analog Loop w/I NP Design/z10 circuits/Eacility/EL (days)	R&B - Disp	9.42	636	26.50	2	13.690	9.69515	-1.7621	NO
D.2.3.12.1.1	P.1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/EL(days)	R&B - Disp								
D.2.3.12.1.2	P.1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL(days)	R&B - Disp	12.78	77			20.544			
0.2.0.12.1.0	D .1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL(days)	R&B - Disp	9.33	3			7.572			
0.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL(days)	R&B - Disp								
0.2.0.12.2.2	P.1	2W Analog Loop w/ NP Design/>=10 circuits/Other/FL(days)	R&B - Disp								
0.2.3.12.2.3	P.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.42	627	6.33	3	13.777	7.97287	0.3867	YES
0.2.3.13.1.3	P-1	2W Analog Loop w/I NP Non-Design/<10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or							l	
B 2 3 13 1 3	P-1	2W Analog Loop w/I NP Non-Design/<10 circuits/Other/FL(days)	R&B (POTS) excl SB Or	12.78	77	2.00	2	20.544	14.71447	0.7326	YES
B 2 3 13 2 1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL(days)	R&B (POTS) excl SB Or	9.33	3	6.00	1	7.572	8.74328	0.3812	YES
8 2 3 1 3 2 2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL(days)	R&B (POTS) excl SB Or								
B 2 3 13 2 3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL(days)	R&B (POTS) excl SB Or								
8231411	P-1	Other Design/<10 circuits/Facility/FL(days)	Design	16.14	14			15.571			
B 2 3 14 1 2	P-1	Other Design/<10 circuits/Equipment/FL(days)	Design								
B 2 3 14 1 3	P-1	Other Design/<10 circuits/Other/FL(days)	Design	47.97	33			76.151			
B 2 3 14 2 1	P-1	Other Design/>=10 circuits/Facility/FL(days)	Design						ļ		
B 2 3 14 2 2	P-1	Other Design/>=10 circuits/Equipment/FL(days)	Design		ļ				Į		
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/FL(days)	Design	48.00	1			0.000			
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/FL(days)	R&B	9.42	636			13.690	.	L	
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/FL(days)	R&B					00.544	· · · · · · · · · · · · · · · · · · ·	I	
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/FL(days)	R&B	12.78				20.544		 	
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/FL(days)	R&B	9.33	3			7.5/2	ļ	 	
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/FL(days)	R&B								
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/FL(days)	R&B	L		+		10 777			
B.2.3.16.1.1	P+1	INP (Standalone)/<10 circuits/Facility/FL(days)	H&B (POIS)	9.42	62/			13.777	+		<u> </u>
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/FL(days)	H&B (POIS)	40.70				20 544		ł	
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/FL(days)	H&B (POIS)	12.78		1		7 572			
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/FL(days)	H&B (POTS)	9.33			·	1.512			
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/FL(days)	HAB (PUIS)					+	1		
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/FL(days)	HAB (PUIS)	0.40	607			13 777		l	
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/FL(days)	HAB (PUTS)	9.42	021			13.777	1	<u> </u>	
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/FL(days)	Hab (PUIS)	12.79	77			20 544	+		
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/FL(days)	HAB (POTS)	12.70				7 572		l	
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/FL(days)	HAB (PUIS)	9.30	<u>↓</u>		↓	1.572		1	
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/FL(days)	HAB (PUIS)						+·		
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/FL(days)	HAB (PUIS)	20.50		26.00		30.406	30 40560	-0 1480	VES
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL(days)	Digital Loop < US1	30.50	<u> </u>		<u> </u>	30.400	00.40500	0.1400	
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL(days)	Digital Loop < US1	00.00	1	18.00	1	0.000	0.00000		YES
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/FL(days)	Digital Loop < DST	90.00	<u> </u>	10.00	<u> </u>		1-0.0000	<u> · · · · · · · · · · · · · · · · · · ·</u>	
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/FL(days)	Digital Loop < UST	1	<u> </u>		 		+ · · · ·	+	1
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/FL(days)	Digital Loop < US1			+		+	1	+	
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/FL(days)	Digital Loop < US1	26.00	1	+	l	0.000			
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/FL(days)		20.00	<u> </u>	+	ł	0.000	1	<u>+</u>	<u> </u>
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/FL(days)	Digital Loop >= DST		ł	+	·····	+	+		
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/FL(days)	Digital Loop >= DS1	1		+				<u> </u>	1
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/FL(days)	Ugital Loop >= UST	L	J	1	L	- I	1		

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			Analog
B.2.3.19.2.2	P-1	Digital Loco >= DS1/>=10 circuits/Equipment/FL(days)	Digital Loop >=
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/FL(days)	Digital Loop >=
	% i o/	nordine - Nechanized	
8251	P-2	Switch Ports/El (%)	BAB (POTS
B 252	P-2	Local Interoffice Transport/EL (%)	DS1/DS3 - Inter
B253	P-2	Loop + Port Combinations/EL(%)	R&B
B.2.5.4	P-2	Combo Other/FL(%)	R&B&D - Dis
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL//FL(%)	ADSL to Reta
B.2.5.6	P-2	UNE ISDN/FL(%)	ISDN - BRI
B.2.5.7	P-2	Line Sharing/FL(%)	ADSL to Reta
B.2.5.8	P-2	2W Analog Loop Design/FL(%)	R&B - Disp
B.2.5.9	P-2	2W Analog Loop Non-Design/FL(%)	R&B (POTS) excl
B.2.5.10	P-2	2W Analog Loop w/INP Design/FL(%)	R&B - Disp
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/FL(%)	R&B (POTS) excl
B.2.5.12	P-2	2W Analog Loop w/LNP Design/FL(%)	R&B • Disp
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)	R&B (POTS) excl
B.2.5.14	P-2	Other Design/FL(%)	Design
8.2.5.15	P-2	Other Non-Design/FL(%)	H&B
B.2.5.16	P-2	INP (Standalone)/FL(%)	H&B (POIS
B.2.5.17	P-2	LNP (Standalone)/FL(%)	H&B (POIS
B.2.5.18	P-2	Digital Loop < DS1/FL(%)	Digital Loop <
8.2.5.19	P-2	Digital Loop >= DS1/FL(%)	Digitai Loop >≠
	% Je c	opardies - Non-Mechanized	
B.2.6.1	P-2	Switch Ports/FL(%)	Diagnostic
B.2.6.2	P-2	Local Interoffice Transport/FL(%)	Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/FL(%)	Diagnostic
B.2.6.4	P-2	Combo Other/FL(%)	Diagnostic
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic
B.2.6.6	P-2	UNE ISDN/FL(%)	Diagnostic
B.2.6.7	P-2	Line Sharing/FL(%)	Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/FL(%)	Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/FL(%)	Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/FL(%)	Diagnostic
8.2.6.11	P-2	2W Analog Loop WINP Non-Design/FL(%)	Diagnostic
B.2.6.12	P-2	2W Analog Loop W/LNP Design/FL(%)	Diagnostic
8.2.6.13	P-2	2W Analog Loop W/LNP Non-Design/FL(%)	Diagnostic
B.2.6.14	<u></u>	Other Design/FL(%)	Diagnostic
B.2.6.15	<u></u>	Nine Non-Design/FL(%)	Diagnostic
B.2.0.10	D .2	INP (Standalone)/FL(%)	Diagnostic
D.2.0.17	P.2	Digital Loop < DS1/EL (%)	Diagnostic
B 2 6 19	P-2	Digital Loop >= DS1/EL(%)	Diagnostic
0.2.0.10			
	Avera	age Jeopardy Notice Interval - Mechanized	40 h
B.2.8.1	P-2	Switch Ports/FL(hours)	>= 48 hrs
B.2.8.2	P-2	Local Interoffice Transport/FL(hours)	>= 46 IIIS
8.2.8.3	P-2	Loop + Port Combinations/FL(nours)	>= 40 hits
B.2.8.4	P-2		
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL/FL(nours)	>= 40 hirs
8.2.8.0 B 2 8 7	P-2	Une SUN/FL(ROUS)	
0.2.0./	P-2	CIM Analog Loop Decim/EL (bours)	>= 46 Nrs
0.2.0.0	<u></u>	Zw Analog Loop Design/EL(hours)	
0.2.8.9	P-2	2W Analog Loop Won-Design/FL(Hours)	>= 48 MIS
D.2.8.10	<u>n-2</u>	2W Analog Loop wilking Non-Design/EL (hours)	
0.2.8.11	P-2	200 Analog Loop within Non-Design/EL(hours)	
0.2.0.12	<u>1-2</u>	200 Analog Loop w/LNP Design/EL(DUIS)	
D.2.0.13		Other Design/El (hours)	
D.2.0.14	P-2	Other Non-Design/EL (hours)	
0.2.0.10	r**2		

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
onital Loop >= DS1			1	1				1
gital Loop >= DS1			*****	L				
3					· · · · · ·			
	0.75			· · · · · ·				
H&B (POIS)	0.75%	800,795						
1/DS3 - Interoffice	0.7001		0.00%	10				
R&B	0.76%	804,366	0.15%	8,219		0.00096	6.3128	YES
H&B&D - Disp	1.00%	816,428		070				
ADSL to Hetail	23,13%	18,307	7.51%	3/3		0.02206	7.0852	YES
ISDN - BHI	18.36%	1,024	35.20%			0.02731	-0.1001	NU
ADSL to Hetall	0.769/	16,307	00.000/	202		0.00442	61.0752	
HODE WISP	1 289/	466 997	20.20%	303		0.00443	-01.9753	NO
B&B - Dien	0.76%	904 366	0.00%			0.00017	0.1746	VEC
RICITS) AVALSE OF	1 28%	466 887	14 20%	7	ł	0.04352	-3.0602	NO
B&R + Dien	0.76%	804 366	0.00%	1.835		0.00202	3 7354	VES
(POTS) excl SB Or	1,28%	466.887	0.00%	2,229		0.00239	5 3645	YES
Design	17.40%	12.062	16.44%	73		0.04451	0.2165	YES
B&B	0.76%	804.366	0.00%	20		0.01937	0.3904	YES
R&B (POTS)	0.75%	800,795			1			
R&B (POTS)	0.75%	800,795	0.00%	6,847		0.00104	7.1469	YES
igital Loop < DS1	31.66%	676	35.20%	250		0.03443	-1.0291	YES
gital Loop >= DS1	49.14%	116	24.25%	466		0.05187	4.7980	YES
Disgnostia			· · · · · · · · · · · · · · · · · · ·					Disessotia
Diagnostic			0.00%	10				Diagnostic
Diagnostic			0.00%	1.065				Diagnostic
Diagnostic			0.20 /6	1,005				Diagnostic
Diagnostic			11 63%	172				Diagnostic
Diagnostic			35 78%	218				Diagnostic
Diagnostic			5.00%	61				Diagnostic
Diagnostic			31.80%	239				Diagnostic
Diagnostic			13.87%	137				Diagnostic
Diagnostic			0.00%	1				Diagnostic
Diagnostic			14.29%	7				Diagnostic
Diagnostic			6.00%	465				Diagnostic
Diagnostic			1.00%	3,932				Diagnostic
Diagnostic			45.45%	22				Diagnostic
Diagnostic			30.77%	13				Diagnostic
Diagnostic								Diagnostic
Diagnostic			0.00%	1,020				Diagnostic
Diagnostic			35.78%	218				Diagnostic
Diagnostic			29.46%	258				Diagnostic
>= 48 hrs								
>= 48 hrs								
>= 48 hrs			218.00	12				YES
>= 48 hrs								
>= 48 hrs			241.71	28				YES
>= 48 hrs			431.18	88				YES
>= 48 hrs								
>= 48 hrs			277.11	108				YES
>= 48 hrs			239.21	61				YES
>= 48 hrs								
>= 48 hrs			336.00	1				YES
>= 48 hrs								
>= 48 hrs			146.82	1				YES
>= 48 hrs			564.00	12				YES
40 L								

			5
B 2 8 16	P-2	INP (Standaione)/EL(hours)	>= 48 hrs
B2817	P-2	INP (Standalone)/EL (hours)	>= 48 hrs
D.2.0.17	P.2	Digital Loop < DS1/EL (hours)	>= 48 hr
B 2 8 19	P-2	Digital Loop >= DS1/FL(hours)	>= 48 hr
0.2.0.10	<u> </u>		
	Avera	nge Jeopardy Notice Interval - Non-Mechanized	Diagnost
B.2.9.1	P-2	Switch Polis/FL(nours)	Diagnost
B.2.9.2	P-2	Local Interoffice Transport/FE(nours)	Diagnost
B.2.9.3	P-2	Combo Other/El (hours)	Diagnost
B.2.9.4	P-2	Composition Children Children (CLIVEL (hours)	Diagnost
B.2.9.5	P-2	UNE (CON/E) (hours)	Diagnost
B.2.9.0	0.0	Une Sharing/El (bours)	Diagnost
B.2.9./	<u></u>	2W Apalog Loop Design/EL (hours)	Diagnost
D.2.3.0	0.2	2W Analog Loop Non-Design/El (hours)	Diagnost
D.2.9.9	D.2	2W Analog Loop w/NP Design/El (hours)	Diagnost
D.2.3.10	0.2	2W Analog Loop w/NP Non-Design/El (hours)	Diagnost
82012	P.2	2W Analog Loop w/LNP Design/FL(hours)	Diagnost
B.2.3.12 B.2.0.13	P.2	2W Analog Loop w/LNP Non-Design/EL(hours)	Diagnost
B.2.9.14	P-2	Other Design/Ei (hours)	Diagnost
B 2 0 15	P.2	Other Non-Design/El (hours)	Diagnost
0.2.3.15	P-2	INP (Standalone)/El (hours)	Diagnost
B.2.3.10 B.2.9.17	P-2	INP (Standalone)/El (hours)	Diagnost
B 2 9 18	P-2	Digital Loop < DS1/FL (hours)	Diagnost
B.2.9.19	P-2	Digital Loop >= DS1/FL(hours)	Diagnost
	~ ~ ~		
0.010.1	76 000	Switch Borts/EL (%)	95% >= 48
B.2.10.1	P-2	Local Interoffice Transport/EI (%)	95% >= 48
B.2.10.2 B 2 10 3	P.2	Loop + Port Combinations/FI (%)	95% >= 48
B.2.10.3	P.2	Combo Other/EL (%)	95% >= 48
B 2 10 5	P-2	VDSL (ADSL HDSL and UCL//EL(%)	95% >= 48
B 2 10 6	P-2	UNE ISDN/FL(%)	95% >= 48
B 2 10 7	P-2	Line Sharing/FL(%)	95% >= 48
B.2.10.8	P-2	2W Analog Loop Design/FL(%)	95% >= 48
8.2.10.9	P-2	2W Analog Loop Non-Design/FL(%)	95% >= 48
B.2.10.10	P-2	2W Analog Loop w/INP Design/FL(%)	95% >= 48
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/FL(%)	95% >= 48
B.2.10.12	P-2	2W Analog Loop w/LNP Design/FL(%)	95% >= 48
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)	95% >= 48
B.2.10.14	P-2	Other Design/FL(%)	95% >= 48
B.2.10.15	P-2	Other Non-Design/FL(%)	95% >= 48
B.2.10.16	P-2	INP (Standalone)/FL(%)	95% >= 48
B.2.10.17	P-2	LNP (Standalone)/FL(%)	95 % >= 40
B.2.10.18	P-2	Digital Loop < DS1/FL(%)	95% >= 49
B.2.10.19	P-2	Digital Loop >= DS1/rL(%)	000074 40
	% Je	opardy Notice >= 48 hours - Non-Mechanized	
B.2.11.1	P-2	Switch Ports/FL(%)	Diagnos
8.2.11.2	P-2	Local Interoffice Transport/FL(%)	Diagnos
B.2.11.3	P-2	Loop + Port Combinations/FL(%)	Diagnos
B.2.11.4	P-2	Combo Other/FL(%)	Diagnos
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnos
B.2.11.6	P-2	UNE ISDN/FL(%)	
B.2.11.7	P-2	Line Sharing/FL(%)	Diagnos
B.2.11.8	P-2	2W Analog Loop Design/FL(%)	Diagnos
B.2.11.9	P-2	2W Analog Loop Non-Design/FL(%)	Diagnos
B.2.11.10	P-2	2W Analog Loop w/INP Design/FL(%)	Diagnos
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/FL(%)	Diagnos
B.2.11.12	P-2	2W Analog Loop w/LNP Design/FL(%)	Diagnos
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnos

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
> = 49 brc								
>= 40 ms								
>= 48 hre			431 18	88				YES
>= 48 hrs			422.87	113				YES
>= 10 113								
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			144.00	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic			170.40	20				Diagnostic
Diagnostic			290.15	78				Diagnostic
Diagnostic			122.00	3				Diagnostic
Diagnostic			214.11	76				Diagnostic
Diagnostic			218.53	19				Diagnostic
Diagnostic								Diagnostic
Diagnostic			192.00	1				Diagnostic
Diagnostic			76.57	28				Diagnostic
Diagnostic			86.64	46				Diagnostic
Diagnostic			470.40	10				Diagnostic
Diagnostic			228.00	4				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			290.15	78				Diagnostic
Diagnostic			214.11	76				Diagnostic
059/ - 49 bro								
95% >= 40 hrs								
05% >= 40 hrs			80.00%	5				NO
95% > 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs	1							
95% >= 48 hrs								
95% >= 48 hrs			100.00%	21				YES
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs			0.00%	1				NO
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs								
95% >= 48 hrs			100.00%	19				YES
Diagnostic				l				Diagnostic
Diagnostic				·····	-			Diagnostic
Diagnostic			100.00%	6				Diagnostic
Diagnostic								Diagnostic
Diagnostic			80.00%	5				Diagnostic
Diagnostic								Diagnostic
Diagnostic			0.00%	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic			95.65%	23				Diagnostic
Diagnostic			00.00 /0	 				Diagnostic
Diagnostic				1				Diagnostic
Diagnostic			64,00%	28				Diagnostic
Diagnostic			46.00%	46				Diagnostic

Diagnostic

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	Flori	la, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
D 0 11 14	100		Disgnostig	_		100.009/	6				Discussion
B.2.11.14	-2	Other Design/FL(%)				100.00%	0				Diagnostic
8.2.11.15	P-2	Other Non-Design/FL(%)	Diagnostic								Diagnostic
B.2.11.16	1-2	INP (Standalone)/FL(%)	Diagnostic								Diagnostic
B.2.11.17	P-2	LNP (Standalone)/FL(%)	Diagnostic								Diagnostic
8.2.11.18	P-2	Digital Loop < DS1/FL(%)	Diagnostic			100.00%	10				Diagnostic
B.2.11.19	P-2	Digital Loop >= DS1/FL(%)	Diagnostic			95.24%	21			.	Diagnostic
	Coordi	nated Customers Conversions									
B.2.12.1	P-7	Loops with INP/FL(%)	>= 95% w in 15 min								
B.2.12.2	P-7	Loops with LNP/FL(%)	>= 95% w in 15 min			99.64%	6,063				YES
B.2.13.1	P-7A	Time-Specific SL1/FL(%)	<= 5%			0.98%	508				YES
B.2.13.2	P-7A	Time-Specific SL2/FL(%)	<= 5%			0.30%	332				YES
B.2.13.3	P-7A	Non-Time Specific SL1/FL(%)	<= 5%			0.00%	28				YES
B.2.13.4	P-7A	Non-Time Specific SL2/FL(%)	<= 5%			0.00%	729				YES
	1		059/ in 15 mm			47 AEA	600				1150
B.2.14.1	P-7A	Time-Specific SL1/FL(%)	>= 95% W IN 15 mill			97.05%	508	-			YES
B.2.14.2	P-/A	Time-Specific SL2/FL(%)	>= 95% W in 15 min			98.80%	332				YES
B.2.14.3	P-7A	Non-Time Specific SL1/FL(%)	>= 95% w in 15 min			100.00%	28				YES
B.2.14.4	P-7A	Non-Time Specific SL2/FL(%)	>= 95% w in 15 min			99.59%	729				YES
D 0 15 1	0.74	10				1.079/	500			_	1/50
B.2.15.1	P-/A	Time-Specific SL1/FL(%)				1.97%	508	-			YES
8.2.15.2	P-/A	Time-Specific SL2/FL(%)	<= 5%			0.90%	332				YES
B.2.15.3	P-7A	Non-Time Specific SL1/FL(%)	<= 5%			0.00%	28				YES
B.2.15.4	P-/A	Non-Time Specific SL2/FL(%)	<= 5%			0.41%	/29				YES
B.2.16.1	P		Diagnostic			298.25	9				Diagnostic
D.2.10.2	Ľ										Dingitoonia
	% Prov	isioning Troubles within 7 Days - Hot Cuts	- 59/			1 000/	E 01E				N/FO
B.2.17.1.1	P-70	UNE Loop Design/Dispatch/FL(%)				1.96%	5,315				YES
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/FL(%)	<= 5%			0.500/	4 700				
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/FL(%)	<= 5%			0.53%	1,708				YES
B.2.17.2.2	P-7C	JUNE Loop Non-Design/Non-Dispatch/I-L(%)	<= 5%			0.63%	1,114				YES
	% Miss	ed Installation Appointments		1	00.547			·			
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/FL(%)		4.21%	80,517	ł					
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/FL(%)		0.12%	/14,65/						
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/FL(%)		6.14%	4/2						
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	6/						
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3			0.00%	19				
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3								
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3								
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3								
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	4.21%	81,168	4.22%	688		0.00769	-0.0117	YES
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0.12%	717,054	0.27%	10,251		0.00034	-4.3918	NO
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B		ي د در ور به د ميروند. در در ور به د ميروند در د	PA LUNA DA	Data included	h B.2. 18. 1 1.2	· · · · · ·		C. W. L. Rade Torder Last
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	R&B	California (California)		linger and a second	Data Included	m B2 18.3.1.2	1940 - Yoshi Sila		. A MARINE P
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	5.78%	519	21.43%	14		0.06321	-2.4759	NO
B218322	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FI (%)	- B&B	0.00%	471	0.00%	6		0.00000		YES
R218323	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL/%	HAB	a is starting a set			Data monuted	in B 2.18.3 2 3			
B 2 18 3 2 4	P.3	Loop + Port Combinations/s=10 circuits/Dispatch In/FL/%		A THE LADE			Data fer bertant	h #2.18 120		 	1 11 11 11 11 11 11 11 11 11 11 11 11 1
D.2.10.3.2.4	p.3	Combo Other/<10 circuits/Disnatch/EI (%)	B&B&D - Disp	4 28%	87.031				T		
0.2.10.4.1.1	P-3	Combo Other/<10 circuits/Dispatch /n/El /%)			01,001	t			⊢		
0.2.10.4.1.4	P-3	Combo Other/<10 circuits/Dispatch III/FL(%)		5.67%	547	 					
B.2.18.4.2.1	P-3	Compo Utrer/>=10 circuits/Dispatch/FL(%)		5.0776	34/	 					
В.2.18.4.2.4	P-3	Combo Otner/>=10 circuits/Dispatch in/FL(%)		7.010/	10 774	2.150/	476		0.01100	2.0402	VEC
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Hetall	7.01%	10,771	3.15%	4/6		0.01186	3.2492	TES
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0.00%	8/1	1					L

	Florio	a June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	1 10114	a, ounc 2001	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
B 2 18 5 2 1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0.00%	4						
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail								
B 2 18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	9.78%	491	8.98%	401		0.01999	0.3994	YES
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	2.91%	446						
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/FL(%)	ISDN - BRI								
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI								
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7.01%	16,771	0.00%	2		0.18050	0.3882	YES
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0.00%	871	1.75%	57		0.00000		NO
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0.00%	4						
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail							5 05 05	100
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4.21%	81,168	1.64%	1,587		0.00509	5.0505	YES
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4.21%	81,168						
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5.78%	519	0.00%	27		0.04607	1.2548	YES
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5.78%	519						
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4.21%	80,517	2.31%	432		0.00969	1.9555	YES
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.22%	381,045	0.00%	2		0.03328	0.0667	YES
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.14%	472	25.00%	4		0.12058	-1.5638	YES
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	51						
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4.21%	81,168	0.00%	5		0.08982	0.4688	YES
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4.21%	81,168						
B.2.18.10.2.1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5.78%	519						
B.2.18.10.2.2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5.78%	519						
B.2.18.11.1.1	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4.21%	80,517	0.00%	13		0.05570	0.7557	YES
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.22%	381,045	0.00%	1		0.04707	0.04/2	YES
B.2.18.11.2.1	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.14%	472						
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	51					4 7000	
B.2.18.12.1.1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4.21%	81,168	1.34%	1,116		0.00605	4.7362	YES
B.2.18.12.1.2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4.21%	81,168	0.000	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~		0.05000	4 4070	VEC
B.2.18.12.2.1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5.78%	519	0.00%			0.05080	1.13/9	YES
B.2.18.12.2.2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5.78%	519	4 7404	700		0.00700	0.0700	VEC
B.2.18.13.1.1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4.21%	80,517	1./1%	/00		0.00762	3.2730	YES
B.2.18.13.1.4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.22%	381,045	0.55%	362		0.00247	-1.3352	YES
B.2.18.13.2.1	P-12	2W Anatog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.14%	4/2	1.85%	54	-	0.03450	1.2442	TES VEC
B.2.18.13.2.4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POIS) excl SB Or	0.00%	51	0.00%	4		0.00000	0.0700	YES
B.2.18.14.1.1	P-3	Other Design/<10 circuits/Dispatch/FL(%)	Design	5.22%	5,863	5.88%	65		0.02450	-0.2729	169
B.2.18.14.1.2	P-3	Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	1.43%	3,352						
B.2.18.14.2.1	P-3	Other Design/>=10 circuits/Dispatch/FL(%)	Design	3.57%	- 28						
B.2.18.14.2.2	P-3	Other Design/>=10 circuits/Non-Dispatch/FL(%)	Design	4.65%	43	0.000/	10		0.0007.74	0.7550	VED
B.2.18.15.1.1	P-3	Other Non-Design/<10 circuits/Dispatch/FL(%)	H&B	4.21%	81,168	0.00%	13		0.05571	0.7559	TES NO
8.2.18.15.1.2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)	H&B	0.12%	/17,054	10.07%	12		0.00990	1 1 261	
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/Dispatch/FL(%)	H&B	5.78%	519	10.0776	0		0.09062	-1.1301	
B.2.18.15.2.2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)	H&B	0.00%	4/1			-			
B.2.18.16.1.1	P-3	INP (Standalone)/<10 circuits/Dispatch/FL(%)	HAB (POIS)	4.21%	80,517						
B.2.18.16.1.2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	H&B (POIS)	0.12%	/14,05/			-			
B.2.18.16.2.1	P-3	INP (Standatone)/>=10 circuits/Dispatch/FL(%)	H&B (PUIS)	0.14%	4/2		·····				· · · · · · · · · · · · · · · · · · ·
B.2.18.16.2.2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	H&B (POIS)	0.00%	0/		······				
B.2.18.17.1.1	P-12	LNP (Standalone)/<10 circuits/Dispatch/FL(%)	H&B (POIS)	4.21%	80,517	0.018/	7.615		0.00040	0.0022	NO
B.2.18.17.1.2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	HAB (POIS)	0.12%	/14,65/	0.21%	7,015		0.00040	-2.2300	
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6.14%	4/2	0.000/	10		0.00000		VEC
B.2.18.17.2.2	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	H&B (POTS)	0.00%	6/	0.00%	18		0.00000	1.0026	VES
B.2.18.18.1.1	P-3	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)	Digital Loop < DS1	11.20%	598	8.98%	401		0.02036	1.0930	160
B.2.18.18.1.2	P-3	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	11.11%	9						
B.2.18.18.2.1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop < DS1			l					
B.2.18.18.2.2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	-					0.00714	0.0005	VEC
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	5.26%	/6	5.99%	634		0.02/11	-0.2090	
B 2 18 19 1 2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	6.25%	16						d
B.2.18.19.21	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	L	L						
B.2.18.19.2.2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	L	<u> </u>	I	l				ļ

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% Provisioning Troubles within 30 Days

	Florid	la June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	1 10/10		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
			3								
B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	5.97%	84,569						
B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3.73%	749,466						
B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6.97%	445						
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	7.69%	13						
B.2.19.2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3			0.00%	13				
B.2.19.2.1.2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3								
B.2.19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3								
B.2.19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3					-			
B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	5.93%	85,305	48.91%	321	-	0.01321	-32.5448	NO
B.2.19.3.1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	3.72%	751,896	2.47%	10,487		0.00186	6.7025	<u>I YES</u>
B.2.19.3.1.3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B			11,13% - V	Date Included	h R2 18.3.1,2	alining on the second		
B.2.19.3.1.4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	H&B				Cale Included	an A.Z. 19.3.1.2	0.07000	0.4606	<u> </u>
B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	H&B	6.39%	501	10.00%	10	-	0.07809	-0.4626	YES
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	H&B	1.03%	389	0.00%	1		0.10101	0.1018	<u>YES</u>
B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)	H&B			a a a a a a a a a a a a a a a a a a a		B: M.Z. TE.J.Y.Z			
B.2.19.3.2.4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	HAB Dian	E 000/	00.440		the district for some	A 62 17 32	<u>, 10 % 6 / 8 %</u>	CORGENER-PLICE	nanger and an and a state of the second s
B.2.19.4.1.1	P-9	Combo Other/<10 circuits/Dispatch/FL(%)	HabaD - Disp	5.62%	90,442						
B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/FL(%)	RABAD - Disp	6 209/	EDE			-			
B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/FL(%)	Right - Disp	0.32%	500						
B.2.19.4.2.4	P-9	Combo Other/>=10 circuits/Dispatch in/+1(%)	ADSt to Batail	7 1 3%	10.087	5 07%	626		0.01045	1 7741	YES
B.2.19.5.1.1	P-9	XDSL (ADSL, HDSL and UCLI/<10 circuits/Dispatch/FL(%)	ADSL to Retail	6 74%	1 351	5.2170	020		0.01040		
8.2.19.5.1.2	P-9	XDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	5.26%	19						
8.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0.20%	10		••••				
B.2.19.5.2.2	P-9	XDSL (ADSL, HDSL and OCL)/>=10 circuits/Non-Dispatch/r L(//)	ISDN - BBI	0.00%	610	10.44%	527		0.00000		NO
B.2.19.0.1.1	P-9	UNE ISDN/<10 circuits/Dispatch/FL/%	ISDN - BRI	0.00%	906						
B.2.19.0.1.2	P-9	UNE ISDN/<10 circuits/10/PDispatch/F1 (%)	ISDN - BRI								
D.2.19.0.2.1	0.0	UNE ISDN/S-10 circuits/Non-Dispatch/FL (%)	ISDN - BRI								
B 2 19 7 1 1	P.9	Line Sharing/<10 circuits/Dispatch/Ft (%)	ADSL to Retail	7.13%	19,087						
B219712	P-9	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	6.74%	1,351	0.00%	46		0.03758	1.7924	YES
B219721	P-9	l ine Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	5.26%	19						
B 2 19 7 2 2	P-9	ine Sharing/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail								
B 2 19 8 1 1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	5.93%	85,305	1.87%	1,921		0.00545	7.4499	YES
B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5.93%	85,305						
B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	6.39%	501	15.79%	19		0.05715	-1.6452	NO
B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	6.39%	501						
B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5.97%	84,569	0.00%	94		0.02444	2.4409	YES
B.2.19.9.1.4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3.48%	394,158						
B.2.19.9.2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	6.97%	445	0.00%	3		0.14748	0.4724	YES
B.2.19.9.2.4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	9.09%	11						
B.2.19.10.1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	5.93%	85,305	0.00%	4		0.11809	0.5021	YES
B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5.93%	85,305						
B.2.19.10.2.1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	6.39%	501						
B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	H&B · Disp	6.39%	501	0.000/			0.10070	0.4000	VEC
B.2.19.11.1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	H&B (POTS) excl SB Or	5.97%	84,569	0.00%	3	-	0.13676	0.4303	TEO
B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	HAB (PUTS) excl SB OF	3.48%	394,158	· · · · · · · · · · · · · · · · · · ·		-			
B.2.19.11.2.1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	H&B (POTS) excise Or	6.97%	445						
B.2.19.11.2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	H&B (POTS) excission	9.09%	11	0.000/	1 549	-	0.00606	6 5 200	NO.
B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	H&B - Disp	5.93%	65,305	9.00%	1,540	-	0.00000	-0.5200	
B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	H&B - Disp	5.93%	65,305	10 750/	16	-	0.06010	1 0009	NO.
B.2.19.12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	6.39%	501	18.75%	10	-	0.00210	-1.9906	
B.2.19.12.2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	H&B - DISP	0.39%	94 660	0.00%	374		0.01228	4 8608	VES
B.2.19.13.1.1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)	HaB (POTS) excl SB OF	3.97%	204,209	0.00%	3/4		0.01220	4.0000	' ^{L3}
B.2.19.13.1.4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch in/FL(%)	PAR (POTS) EXCISE OF	5.4070 6.07%	445	0.00%	24		0.05335	1 3058	YES
B.2.19.13.2.1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	HAD (PUIS) EXCLOBUT	0.97%	11	0.00%			0.030.0	1.0000	<u> </u>
B.2.19.13.2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	Hab (PUIS) exclise Or	3,03%	6 127	20 50%	34		0.03344	-4 9804	NO
B.2.19.14.1.1	P-9	Other Design/<10 circuits/Dispatch/FL(%)	Design	0.63%	320	20.03 %	l		0.00014	7,0004	<u>```</u>
B.2.19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	0.03%	520	 	ł			<u> </u>	<u>├────</u> ┤
B.2.19.14.2.1	P-9	Other Design/>=10 circuits/Dispatch/FL(%)	Design	0.00%	ə	1	L			L	L

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	Flori	da. June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Меавиге	Volume	Deviation	Error	ZScore	Jun-01 Equity
				······					·		
B.2.19.14.2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/FL(%)	Design						0.40700	0.0550	VEC
B.2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/FL(%)	R&B	5.93%	85,305	0.00%	2		0.18/00	0.3050	VES
B.2.19.15.1.2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)	R&B	3.72%	751,896	0.00%			0.16917	0.1905	169
B.2.19.15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/FL(%)	R&B	6.39%	501						
B.2.19.15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)	H&B	1.03%	389						
B.2.19.16.1.1	P-9	INP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	5.97%	84,569	0.000/			0.19020	0.1067	VEC
B.2.19.16.1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3.73%	/49,466	0.00%			0.16939	0.1907	123
B.2.19.16.2.1	P-9	INP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6.9/%	445						
B.2.19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	7.69%	13	0.000/			0.10070	0.4262	VEC
B.2.19.17.1.1	P-9	LNP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	5.97%	84,569	0.00%			0.13676	0.4363	YES
B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3.73%	/49,466						
B.2.19.17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6.97%	445						
B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	7.69%	13	10.110			0.00000		NO
B.2.19.18.1.1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)	Digital Loop < DS1	0.00%	648	10.44%	527		0.00000		NO
B.2.19.18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	0.00%	6						
B.2.19.18.2.1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop < DS1			L					
B.2.19.18.2.2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1			7 4001			0.00000		NO
B.2.19.19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	0.00%	83	7.40%	//0		0.00000		NU
B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	0.00%	1/						
B.2.19.19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	0.000/							
B.2.19.19.2.2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DST	0.00%		1					L
	Avera	e Completion Notice Interval - Mechanized									
B 2 21 1 1 1	P-5	Switch Ports/c10 circuits/Dispatch/FL(hours)	R&B (POTS)	4.69	43,758			21.106			
B 2 21 1 1 2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	1.52	555,558			8.002			
0.2.21.1.1.2	P-5	Switch Ports/>=10 circuits/Dispatch/FL (hours)	R&B (POTS)	7.34	312			25.262			
D.2.21.1.2.1	0.6	Switch Ports/>=10 circuits/Non-Dispatch/EI (hours)	R&B (POTS)	0.74	61			0.325			
D.Z.ZI.I.Z.Z	P-5	Local Interoffice Transport/<10 circuits/Dispatch/FL(hours)	DS1/DS3 - Interoffice								
B.2.21.2.1.1	P-5	Local Interoffice Transport<10 circuits/Non-Dispatch/FL(hours)	DS1/DS3 - Interoffice					I			
B.2.21.2.1.2	P-6	Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)	DS1/DS3 - Interoffice								
B.2.21.2.2.1	P.5	t ocal Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)	DS1/DS3 - Interoffice								
B 2 21 3 1 1	P.5	Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)	R&B	4.71	44,245	8.26	408	21.129	1.05087	-3.3736	NO
0.2.21.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)	R&B	1.52	557,442	4.24	8,570	8.019	0.08728	-31.2023	NO
B 2 21 3 1 3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)	R&B				Date brokeled	m #2.21.1.1.2	<u>), " " (</u>	, i i i i i i i i i i i i i i i i i i i	
B 2 21 3 1 4	P-5	I cop + Port Combinations/<10 circuits/Dispatch In/FL(hours)	R&B				Cash Included	H 227131 3			
8221321	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)	R&B	8.14	338	23.40	18	29.457	7.12563	-2.1414	NO
B 2 21 3 2 2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)	R&B	2.02	401	0.38	2	20.980	14.87184	0.1101	YES
B 2 21 3 2 3	P-5	I con + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)	R&B		Cirvia A		Date Inchedad	h #2.21.1.2.2			
B 2 21 3 2 4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)	R&B	1111222-3.20			Outs included	h #2.21.1.2.5			
B 2 21 4 1 1	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)	R&B&D - Disp	16.65	48,207			188.837			
B 2 21 4 1 4	P.5	Combo Other/<10 circuits/Dispatch In/FL(hours)	R&B&D - Disp								
8221421	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)	R&B&D - Disp	8.64	362			30.717			
B 2 21 4 2 4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)	R&B&D - Disp								
B 2 21 5 1 1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)	ADSL to Retail	9.35	13,401			28.436	L		·
B 2 21 5 1 2	P-5	VDSL (ADSL HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	1.26	744			6.574			
B 2 21 5 2 1	P-5	VDSL (ADSL_HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)	ADSL to Retail	42.25	4			29.608			
D.2.21.3.2.1	P.5	VDSL (ADSL_HDSL_and UCL)/>=10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail								
D.2.21.3.2.2	P.6	LINE ISDN/<10 circuits/Dispatch/EL (hours)	ISDN - BRI	44.82	364	0.52	5	72.179	32.50019	1.3630	YES
D.2.21.0.1.1	0.5	UNE ISDN/<10 circuits/Non-Disnatch/El (hours)	ISDN - BRI	5.54	361			25.425			
D.2.21.0.1.2	P-5	UNE ISDN/s-10 circuits/Dispatch/El (hours)	ISDN - BRI								
D.2.21.0.2.1	0.6	UNE ISDN/>-10 circuits/Non-Dispatch/EI (hours)	ISDN - BRI								
B.2.21.0.2.2 B.0.01711	P-5	Line Sharing/-10 circuits/Dispatch/El (hours)	ADSL to Retail	9.35	13,401			28.436			
D.2.21.7.1.1	D.5	Line Sharing/<10 circuits/Non-Dispatch/FL (hours)	ADSL to Retail	1.26	744			6.574			L
D.2.21./.1.2	0.5	Line Sharing/ To circuits/Dispatch/Fl (hours)	ADSL to Retail	42.25	4			29.608			
B.2.21.7.2.1		Line Sharing/>=10 circuite/Non-Dispatch/FI (hours)	ADSL to Retail			1					
B.2.21.7.2.2	P-5	CM Analog Loop Design/s10 circuits/Dispatch/FL (hours)	R&B - Disp	4.71	44,245	24.11	356	21.129	1.12435	-17.2502	NO
B.2.21.8.1.1	P-5	Zvv Analog Loop Design/<10 exertic/Non-Dispatch/El (hours)	B&B - Disp	4.71	44,245	1		21.129			
B.2.21.8.1.2	H-5	2vv Analog Loop Design/< to circuits/nonPolspatch/r Litours)	B&B - Disp	8.14	338	186.74	2	29.457	20.89099	-8.5491	NO
B.2.21.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL (hours)	B&B - Disp	8,14	338			29.457	1		
B.2.21.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)	B&B (POTS) excl SB Or	4.69	43,758	1.11	215	21.106	1.44294	2.4784	YES
B.2.21.9.1.1	P-5	ZW Analog Loop Non-Design/<10 circulis/Dispaction Enrorsy	,,	· · · · ·	· · · · · · · · · · · · · · · · · · ·						

	Florid	la. June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		•••••	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
											-
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	1.20	270,753	0.08	3	6.196	3.57740	0.3119	YES
B.2.21.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7.34	312	0.12	1	25.262	25.30215	0.2855	YES
B.2.21.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.75	46			0.351			
B.2.21.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	4.71	44,245	1		21.129	-		
B.2.21.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4.71	44,245			21.129			L
B 2 21 10 2 1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	8.14	338			29.457			1
B 2 21 10 2 2	P-5	2W Analog Loop w/NP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	8.14	338			29.457			
82211111	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4.69	43,758			21.106			1
82211114	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	1.20	270,753			6.196			
B 2 21 11 2 1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7.34	312			25.262			
B 2 21 11 2 4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.75	46	1		0.351			
B 2 21 12 1 1	P-5	2W Analog Loop w/I NP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	4.71	44,245	25.90	394	21.129	1.06921	-19.8140	NO
B 2 21 12 1 2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4.71	44,245			21.129			
0.2.21.12.1.2	P-6	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	8.14	338	8.20	11	29.457	9.02508	-0.0065	YES
D.2.21.12.2.1	P-5	2W Analog Loop with P Design/>=10 circuits/Non-Dispatch/FI (hours)	R&B - Disp	8.14	338	1	l .	29.457	i	I	İ
D.2.21.12.2.2	0.6	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/El (hours)	R&B (POTS) excl SB Or	4.69	43,758	14.41	187	21.106	1.54671	-6.2845	NO
D.2.21.13.1.1	D.5	2W Analog Loop w/LNT Non-Design/<10 circuits/Dispatch In/El (hours)	R&B (POTS) excl SB Or	1.20	270,753	1		6.196			
D.2.21.13.1.4	0.0	2W Analog Loop w/ NOn-Design/~T0 diseater/Dispatch/Ri (hours)	R&B (POTS) excl SB Or	7.34	312	26.83	16	25.262	6.47533	-3.0092	NO
D.2.21.13.2.1	0.5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/El (hours)	R&B (POTS) excl SB Or	0.75	46			0.351			
B.2.21.13.2.4	P-5	2VV Analog Loop W/LIVF NOIPDEsign/>= 70 circuits/Disputch min Linding	Design	149.98	3,962			639.981			
B.2.21.14.1.1	P-5	Other Design/<10 circuits/Dispatch/r Lindurs)	Design	14.15	1.785		·	75.311			
B.2.21.14.1.2	0.5	Other Design (< 10 circuits/10/1-Dispatch/E) (hours)	Design	15.66	24			44.702	1		1
B.2.21.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/El (bours)	Design	1.12	37	1		1.680	1		1
B.2.21.14.2.2	P-5	Other Design/s=10 circuits/HoirDispatch/El (hours)	B&B	4.71	44,245		······	21.129			1
B.2.21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/El (hours)		1.52	557,442	1		8.019	1		1
B.2.21.15.1.2	P*5	Other Non-Design (~10 circuits/Non-Dispatch/El (bours)	B&B	8.14	338			29.457	1		
B.2.21.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/FL(Indus)	B&B	2.02	401	1		20.980	1		1
B.2.21.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/r L(hours)	BAB (POTS)	4 69	43,758			21,106		1	
B.2.21.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/FL(nouis)	B&B (POTS)	1.52	555 558			8.002			
B.2.21.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	BAB (POTS)	7.34	312	+		25,262	1	·	1
B.2.21.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/FL(nours)	BAB (POTS)	0.74	61	<u>+</u> ─ ~	<u> </u>	0.325	1		
B.2.21.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(nours)	B&B (POTS)	4.69	43 758	4 19	34	21.106	3.62104	0.1380	YES
8.2.21.17.1.1	P-5	LNP (Standaloney<10 circuits/Dispatch/FL(hours)	BAB (POTS)	1.52	555 558	74.56	5.082	8.002	0.11276	-647.7682	NO
B.2.21.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(nours)	B&B (POTS)	7.34	312		_,	25,262			
B.2.21.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/FL(nours)	- BIR (POTS)	0.74	61	+		0.325			<u> </u>
B.2.21.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(nours)	Digital Loop < DS1	121.69	386	0.52	5	234.883	105 72092	1.1461	YES
B.2.21.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(nours)	Digital Loop < DS1	88.25	6			102 504	1001/LOOL		
B.2.21.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(nours)	- Digital Loop < DS1	00.20	<u> </u>		1	100.001		<u> </u>	4
B.2.21.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/FL(nours)	Digital Loop < DS1					1	1		
B.2.21.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(nours)	Digital Loop >= DS1	175 74	49	23.64	68	330,988	62 02295	2,4523	YES
B.2.21.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(nours)	Digital Loop >= DS1	72.61	45	20.04		98 508	OL. OLLOU	2.1020	
B.2.21.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(nours)	Digital Loop >= DS1	72.01	· · · · ·		<u></u>	00.000	<u> </u>	!	1
B.2.21.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)	Digital Loop >= DS1		I		1				
B.2.21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(nours)							L	ļ	
	Averan	e Completion Notice Interval - Non-Mechanized									
8222111	P-5	Switch Ports/<10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
B 2 22 1 1 2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
0.2.22.1.1.2	0.6	Switch Ports/>=10 circuits/Dispatch/El (hours)	Diagnostic								Diagnostic
D.2.22.1.2.1	6.6	Switch Ports/-10 circuits/Non-Dispatch/El (hours)	Diagnostic								Diagnostic
D.2.22.1.2.2	<u> </u>	Local Interoffice Transport/<10 circuits/Dispatch/El (hours)	Diagnostic			23.07	12				Diagnostic
B.2.22.2.1.1	P-5	Local Interolice Transport/10 erguide/Dop-Dispatch/El (bours)	Diagnostic								Diagnostic
B.2.22.2.1.2	P-5	Local Interonice Transport -10 dirouteDispatch	Diagnostic				1				Diagnostic
B.2.22.2.2.1	<u><u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u></u></u>	Local meronice transport/>= to circuits/Dispatch/FL(hours)	Diagnostic				1				Diagnostic
B.2.22.2.2.2	P-5	Local Interoffice transport/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			37.16	290				Diagnostic
B.2.22.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/FL(nours)	Disgnostic			21.96	943				Diagnostic
B.2.22.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(nours)	Diagnostia			21.00	Data basheda	(h B 2 22 7 1	3		
B.2.22.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)	Diamonatio				Contra granding		N 04	*******	- Angle
B.2.22.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/FL(hours)				13.00	1 1				Diagnostic
B.2.22.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)	Diagnostic			13.90	<u>↓ </u>				Diagnostic
B.2.22.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic	olay, oge stander" .		Ļ	Data bata ta	(in 19 1 mil 0 -			
B.2.22.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)	Diagnostic	b frage states			LANA REPUBLIC	1 1 H. R. T. T. A. Z.	5		

Benchmark / Analog Diagnostic
8222324	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)
B 2 22 4.1.1	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)
B 2 22.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/FL(hours)
B 2 22 4 2 1	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)
B 2 22.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)
B222511	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)
B222512	P-5	xDSL (ADSL, HDSL and UCLV<10 circuits/Non-Dispatch/FL(hours)
8222521	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)
B 2 22 5 2 2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)
B 2 22 6 1 1	P-5	UNF ISDN/<10 circuits/Dispatch/FL(hours)
8222612	P-5	UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)
B 2 22 6 2 1	P-5	UNF ISDN/>=10 circuits/Dispatch/FL(hours)
B 2 22 6 2 2	P-5	UNF ISDN/>=10 circuits/Non-Dispatch/FL(hours)
B 2 22 7 1 1	P-5	l ine Sharing/<10 circuits/Dispatch/FL(hours)
8222712	P-5	tine Sharing/<10 circuits/Non-Dispatch/FL(hours)
B 2 22 7 2 1	P-5	ine Sharing/>=10 circuits/Dispatch/FL(hours)
82222.7.2.1	P-5	line Sharing/>=10 circuits/Non-Dispatch/FL(hours)
D.2.22.7.2.2	P.5	2W Analog Loop Design/<10 circuits/Dispatch/FL(hours)
D.2.22.0.1.1	P.5	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)
B 2 22 8 2 1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)
B 2 22 8 2 2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)
B 2 22 0 1 1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)
B 2 22 0 1 1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)
0.2.22.0.1.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)
0.2.22.9.2.1	P-6	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)
D.2.22.3.2.4	P-6	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)
B.2.22.10.1.1	P-5	2W Analog Loop w/NP Design/<10 circuits/Non-Dispatch/FL(hours)
D.2.22.10.1.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)
B.2.22.10.2.1	D-5	2W Analog Loop w/NP Design/>=10 circuits/Non-Dispatch/FL(hours)
D.2.22.10.2.2	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)
D.2.22.11.1.1	P-6	2W Analog Loop w/NP Non-Design/<10 circuits/Dispatch In/FL(hours)
D.2.22.11.1.4	D-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)
B.2.22.11.2.1	P-5	2W Analog Loop w/NP Non-Design/>=10 circuits/Dispatch In/FL(hours)
D.2.22.11.2.4	P-6	2W Analog Loop w/I NP Design/<10 circuits/Dispatch/FL(hours)
B.2.22.12.1.1	0.5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)
D.Z.ZZ.12.1.Z	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)
D.2.22.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.12.2.2	0.5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)
D.2.22.13.1.1	D-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)
B.Z.ZZ. 13.1.4	0.5	2W Analog Loop w/LNF Non-Design/>=10 circuits/Dispatch/FL(hours)
B.Z.ZZ. 13.Z.1	n-5	2W Analog Loop w/LNP Non-Design/s=10 circuits/Dispatch In/FL(hours)
B.2.22.13.2.4	D 5	Other Design/-10 circuits/Dispatch/El (hours)
B.2.22.14.1.1	F-0	Other Design/<10 circuits/Non-Dispatch/FL (hours)
D.2.22.14.1.2	P-5	Other Design/S=10 circuits/Dispatch/FI (hours)
D.Z.ZZ.14.Z.1	P -5	Other Design/>=10 circuits/Non-Dispatch/El (hours)
B.2.22.14.2.2	P-5	Other Non-Design/-10 circuits/Dispatch/F) (hours)
B.2.22.15.1.1	1-5	Other Non-Design/<10 circuits/Dispatch/r circuits/
B.2.22.15.1.2	P-5	Other Non-Design/s 10 circuits/Non-Dispatch/El (hours)
B.2.22.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/r L(nouis)
B.2.22.15.2.2	P-5	Uner Non-Design/>=10 circuits/Non-Dispatch/ Enforms/
B.2.22.16.1.1	P-5	INP (Standaloney<10 circuits/Dispatch/FL(hours)
B.2.22.16.1.2	P-5	INP (Standaioney<10 circuits/Non-Dispatch/EL(hours)
B.2.22.16.2.1	P-5	INP (Standaloney>=10 circuits/Dispatch/FL(hours)
B.2.22.16.2.2	P-5	INP (Standaloney>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/FL(nours)
B.2.22.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatcn/FL(nours)
B.2.22.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/FL(hours)
B.2.22.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(nours)
B.2.22.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)
	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(nours)

	BST	BST	CLEC	CLEC	Standard	Standard		
	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
	an kunia			Date Included	h 8272322			
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			50.62	368				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			52.88	353				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			61.22	1				Diagnostic
			22.81	51	-			Diagnostic
					-			Diagnostic
			27.10	2006				Diagnostic
			37.18	230				Diagnostic
			18.90	1	-			Diagnostic
			10.00	·	-			Diagnostic
			28.27	206	-			Diagnostic
			20.27		-			Diagnostic
			11 44	8				Diagnostic
								Diagnostic
			18.91	3				Diagnostic
								Diagnostic
					-			Diagnostic
								Diagnostic
			31.60	11				Diagnostic
			14.57	1				Diagnostic
								Diagnostic
								Diagnostic
			24.80	387				Diagnostic
					_			Diagnostic
			14.73	4				Diagnostic
			·					Diagnostic
			20.12	442	_			Diagnostic
			20.94	305				Diagnostic
			24.86	21	-			Diagnostic
			21.22	<u> </u>				Diagnostic
			29.52	19				Diagnostic
				ł				Diagnostic
				l				Diagnostic
			05.00	<u> </u>				Diagnostic
			20.90					Diagnostic
			10.51	3	-			Diagnostic
					-			Diagnostic
				ł				Diagnostic
				·				Diagnostic
								Diagnostic
								Diagnostic
			17.10	26	-			Diagnostic
			25.42	878				Diagnostic
			<i>2</i> 0.42					Diagnostic
			17.24	5				Diagnostic
			52.88	353				Diagnostic
			02.00	<u>↓ ~~~</u>				Diagnostic

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Diagnostic Diagnostic

P-5	Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)
P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)
P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)
P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)
P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)
P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)
Total S	Service Order Cycle Time - Mechanized
P-10	Switch Ports/c10 circuits/Dispatch/F1 (days)
P-10	Switch Ports/<10 circuits/Non-Dispatch/FL (days)
P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
P-10	Combo Other/<10 circuits/Dispatch/FL(days)
P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
P-10	Line Sharing/>=10 circuits/Dispatch/FL(days)
P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop W/INP Design/<10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
P-10	ZW Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
P-14	ZW Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
	P-5 P-5 P-5 P-5 P-5 P-5 P-5 P-10 P-10 P-10 P-10

	BST	BST	CLEC	CLEC	Standard	Standard		
	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
1								Diagnostic
								Diagnostic
			52.31	460				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			2.61	70				Diagnostic
			3.01	2 027				Diagnostic
			12.00	3,63/				Diagnostic
			12.00					Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			11.00	2				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			19.80	5				Diagnostic
								Diagnostic
								Diagnostic
				-				Diagnostic
			5.67	3				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
			7.58	26				Diagnostic
								Diagnostic
			9.00	1				Diagnostic
								Diagnostic
								Diagnostic
								Diagnostic
				1	. دفع			Diagnostic
								Diagnostic
					1000			

B.2.24.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.24.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
B.2.24.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
B.2.24.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.24.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.24.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.24.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.24.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.24.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
8.2.24.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.24.17.2.2	₽-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B.2.24.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.24.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2.24.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2.24.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.24.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
B.2.24.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)
	Total S	ervice Order Cycle Time - Partially Mechanized
B 2 25 1 1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B225112	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B 2 25 1 2 1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B225122	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B225211	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B.2.25.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2.25.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.25.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.25.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.25.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.25.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.25.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.25.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2.25.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
B.2.25.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B.2.25.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
B.2.25.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
B.2.25.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
B.2.25.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
B.2.25.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
8.2.25.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/FL(days)
B.2.25.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.8.1.2		
B.2.25.8.1.2 B.2.25.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2.25.8.1.2 B.2.25.8.2.1 B.2.25.8.2.2	P-10 P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days) 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.8.1.2 B.2.25.8.2.1 B.2.25.8.2.2 B.2.25.8.2.2 B.2.25.9.1.1	P-10 P-10 P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days) 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			-					Diagnostic
Diagnostic								Diagnostic
Diagnostic					1			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic	j							Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic					1			Diagnostic
Diagnostic			44.50	4				Diagnostic
Diagnostic			7.22	3,243	1			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			11.00	2				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			6.50	4				Diagnostic
Diagnostic				-				Diagnostic
Diagnostic				· · · · ·				Diagnostic
Diagnostic								Diagnostic
-								
Discocto								Disgootia
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			4 32	99				Diagnostic
Diagnostic			2.14	1 787	•			Diagnostic
Diagnostic			5.60	5				Diagnostic
Diagnostic			3.11	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic					-			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			7.67	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic				t				Diagnostic
Diagnostic			8.25	32				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			26.50	2				Diagnostic
Diagnostic								Diagnostic
								· · · · · · · · · · · · · · · · · · ·

B.2.25.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
8.2.25.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
8.2.25.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B.2.25.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2.25.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.25.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B.2.25.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B.2.25.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.25.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.25.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
B.2.25.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.25.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.25.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.25.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
8.2.25.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.25.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.25.17.2.2	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/FL(days)
B.2.25.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
8.2.25.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Noil-Dispatch/FL(days)
B.2.25.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Disparch/FL(days)
B.2.25.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Displacin/FL(days)
B.2.25.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.25.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)
D.2.25.19.2.2	P-10	Digital Loop >= D311>=10 circultanton displation Equality
	Total S	ervice Order Cycle Time - Non-Mechanized
B.2.26.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B.2.26.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.26.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B.2.26.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B.2.26.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2.26.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.26.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.26.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.26.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.26.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.26.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.26.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
	D-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Anaiog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic]			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			8.54	149				Diagnostic
Diagnostic								Diagnostic
Diagnostic			9.90	10				Diagnostic
Diagnostic								Diagnostic
Diagnostic			9.24	17	_			Diagnostic
Diagnostic			8.00	4				Diagnostic
Diagnostic			11.17	6				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic					-			Diagnostic
Diagnostic					-			Diagnostic
Diagnostic			19.59	41	-			Diagnostic
Diagnostic			12.19	1,752	-			Diagnostic
Diagnostic			10.67					Diagnostic
Diagnostic			10.67	3				Diagnostic
Diagnostic			1.0/	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic					-			Diagnostic
Diagnostic			0.80	5				Diagnostic
Diagnostic			9.00		-			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnosiic				L				
Diagnostic			-					Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic

		Diagnostic
		Diagnostic
		Diagnostic
		Diagnostic
26.29	7	Diagnostic
		Diagnostic
		Diagnostic
		Diagnostic
4.64	260	Diagnostic
2.56	864	Diagnostic

Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic Diagnostic

Diagnostic

228.5.12 P-10 ZDSI (ADSI, HDSI, and UCI (2-10 creuts/Non-Dispatch/FL(days) 228.5.21 P-10 ADSI, (ADSI, HDSI, and UCI (2-10 creuts/Non-Dispatch/FL(days) 32.86.5.21 P-10 UNE ISDN/-10 creuts/Non-Dispatch/FL(days) 32.86.5.11 P-10 UNE ISDN/-10 creuts/Non-Dispatch/FL(days) 32.86.5.21 P-10 UNE ISDN/-10 creuts/Non-Dispatch/FL(days) 32.86.5.21 P-10 UNE ISDN/-10 creuts/Non-Dispatch/FL(days) 32.86.7.21 P-10 Line Sharing/-10 creuts/Non-Dispatch/FL(days) 32.86.7.21 P-10 Line Sharing/-10 creuts/Non-Dispatch/FL(days) 32.86.8.11 P-10 ZW Analog Loop Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop Non-Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop Non-Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop Non-Design/-10 creuts/Non-Dispatch/FL(days) 32.86.8.12 P-10 ZW Analog Loop NNP Non-Design/-10 creuts/Non-Dis	B 2 26 5 1 1	P-10	vDSL (ADSL_HDSL and UCLV<10 circuits/Dispatch/FL(days)
228.5.21 P10 DSI: (ADSI:, HDSI: and UCI (2>-10 circuits/Nicpatch/FL(days) 328.6.1.2 P10 UNE: ISDN/-10 circuits/Dispatch/FL(days) 32.8.6.1.2 P10 UNE: ISDN/-50 circuits/Dispatch/FL(days) 32.8.6.7.1.2 P10 Line: Sharing/-510 circuits/Dispatch/FL(days) 32.8.6.7.2 P10 Line: Sharing/-510 circuits/Dispatch/FL(days) 32.8.6.8.1 P10 ZW Analog Loop Design/-510 circuits/Dispatch/FL(days) 32.8.6.8.2 P10 ZW Analog Loop Design/-510 circuits/Non-Dispatch/FL(days) 32.8.6.8.2 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.8.6.8.2 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.8.6.8.2 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.8.6.1.1 P10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days) 32.8.6.1.2 P10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days)	B 2 26 5 1 2	P-10	vDSL (ADSL_HDSL_and LICL)/<10 circuits/Non-Dispatch/FL (days)
228.5.22 P10 IDS: [ADS: [AD	8 2 26 5 2 1	P-10	vDSL (ADSL HDSL and UCL)/s=10 circuits/Dispatch/FL(days)
2228.6.1.1 P10 UNE ISDN/-10 circuits/Displat/hFL(days) 228.6.1.2 P10 UNE ISDN/-10 circuits/Displat/hFL(days) 228.6.2.2 P10 UNE ISDN/-10 circuits/Displat/hFL(days) 228.6.2.1 P10 UNE ISDN/-10 circuits/Displat/hFL(days) 228.6.2.2 P10 Line Sharing/-10 circuits/Displat/hFL(days) 228.7.1.2 P10 Line Sharing/-10 circuits/Displat/hFL(days) 228.7.2 P10 Line Sharing/-510 circuits/Displat/hFL(days) 228.8.1.1 P10 ZW Analog Loop Design/-510 circuits/Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop Design/-510 circuits/Non-Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop Non-Design/-510 circuits/Non-Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop WINP Design/-510 circuits/Non-Displat/hFL(days) 228.8.2.1 P10 ZW Analog Loop WINP Design/-510 circuits/Non-Displat/hFL(days) 228.8.10.2 P10 ZW Analog Loop WINP Design/-510 circuits/Non-Displat/hFL(days)	8.2.20.3.2.1	D.10	vDSL (ADSL_HDSL_and LICL Vs=10 circuits/Non-Dispatch/EL (days)
2226.6.12 P-10 UNE ISDN/-10 circuits/Non-Dispatch/FL(days) 2226.6.21 P-10 UNE ISDN/10 circuits/Dispatch/FL(days) 2226.7.11 P-10 Une Sharing/-10 circuits/Dispatch/FL(days) 2226.7.12 P-10 Une Sharing/-10 circuits/Dispatch/FL(days) 2267.12 P-10 Line Sharing/-50 circuits/Non-Dispatch/FL(days) 32268.22 P-10 Line Sharing/-50 circuits/Non-Dispatch/FL(days) 32268.21 P-10 ZW Analog Loop Design/-10 circuits/Non-Dispatch/FL(days) 32268.21 P-10 ZW Analog Loop Design/-10 circuits/Non-Dispatch/FL(days) 32268.21 P-10 ZW Analog Loop Non-Design/-10 circuits/Non-Dispatch/FL(days) 32268.21 P-10 ZW Analog Loop Non-Design/-10 circuits/Non-Dispatch/FL(days) 32268.22 P-10 ZW Analog Loop Non-Design/-10 circuits/Non-Dispatch/FL(days) 32268.12 P-10 ZW Analog Loop wiNP Design/-10 circuits/Non-Dispatch/FL(days) 32268.12 P-10 ZW Analog Loop wiNP Design/-10 circuits/Dispatch/FL(days) 32261.11 P-10 ZW Analog Loop wiNP Design/-10 circuits/Dispatch/FL(days) 32261.12 P-10 ZW Analog Loop wiNP Design/-10 circuits/Dispatch/FL(da	B 2 26 6 1 1	P-10	UNE ISDN/<10 circuits/Dispatch/FI (days)
22.26.6.21 P-10 UNE ISDN/S-10 circuls/Displat/hF1[days] 32.26.6.22 P-10 UNE ISDN/S-10 circuls/Displat/hF1[days] 32.26.7.1.1 P-10 Line Sharing/s-10 circuls/Displat/hF1[days] 32.26.7.2.1 P-10 Line Sharing/s-10 circuls/Displat/hF1[days] 32.26.7.2.1 P-10 Line Sharing/s-10 circuls/Displat/hF1[days] 32.26.7.2.1 P-10 ZW Analog Loop Design/s-10 circuls/Displat/hF1[days] 32.26.8.1.1 P-10 ZW Analog Loop Design/s-10 circuls/Displat/hF1[days] 32.26.8.2.1 P-10 ZW Analog Loop Design/s-10 circuls/Non-Displat/hF1[days] 32.26.8.1.1 P-10 ZW Analog Loop Non-Design/s-10 circuls/Non-Displat/hF1[days] 32.26.8.2.1 P-10 ZW Analog Loop Non-Design/s-10 circuls/Non-Displat/hF1[days] 32.26.8.1.1 P-10 ZW Analog Loop Non-Design/s-10 circuls/Non-Displat/hF1[days] 32.26.8.1.2 P-10 ZW Analog Loop WINP Design/s-10 circuls/Non-Displat/hF1[days] 32.26.8.1.2 P-10 ZW Analog Loop WINP Design/s-10 circuls/Non-Displat/hF1[days] 32.26.10.1.2 P-10 ZW Analog Loop WINP Design/s-10 circuls/Non-Displat/hF1[days] 32.26.11.2 P-10 ZW Analog Loop WINP Non-Design/s-10 circuls/Non-Displat/hF1[days]	B.2.20.0.1.1	P-10	UNE ISDN/<10 circuits/Non-Dispatch/El (days)
22.86.6.22 P-10 UNE ISDN2-10 circuls/Non-Dispatch/FL(days) 32.86.7.1.1 P-10 Line Sharing/<10 circuls/Non-Dispatch/FL(days)	B 2 26 6 2 1	P-10	INE ISDN/s-10 circuits/Dispatch/EL (days)
22.867.1.1 P-10 Line Sharing-C10 circuits/Dispatch/FL(days) 32.867.1.2 P-10 Line Sharing-C10 circuits/Dispatch/FL(days) 32.867.2 P-10 Line Sharing-S-10 circuits/Dispatch/FL(days) 32.86.1.2 P-10 ZWn Sharing/S-10 circuits/Dispatch/FL(days) 32.86.1.2 P-10 ZWn Analog Loop Design/C-10 circuits/Dispatch/FL(days) 32.86.1.2 P-10 ZWn Analog Loop Design/C-10 circuits/Dispatch/FL(days) 32.86.1.2 P-10 ZWn Analog Loop Design/C-10 circuits/Dispatch/FL(days) 32.86.1.2 P-10 ZWn Analog Loop Non-Design/C-10 circuits/Dispatch/FL(days) 32.86.9.1 P-10 ZWn Analog Loop Non-Design/C-10 circuits/Dispatch/FL(days) 32.86.9.2 P-10 ZWn Analog Loop Non-Design/C-10 circuits/Non-Dispatch/FL(days) 32.86.10.1 P-10 ZWn Analog Loop wINP Design/C-10 circuits/Non-Dispatch/FL(days) 32.86.10.2 P-10 ZWn Analog Loop wINP Design/C-10 circuits/Non-Dispatch/FL(days) 32.86.10.2 P-10 ZWn Analog Loop wINP Non-Design/C-10 circuits/Non-Dispatch/FL(days) 32.86.10.2 P-10 ZWn Analog Loop wINP Non-Design/C-10 circuits/Non-Dispatch/FL(days) 32.86.11.2 P-10 ZWn Analog Loop wINP Non-Design/C-10 circuits/Dispatch/FL(days)	B 2 26 6 2 2	P-10	UNE ISDNA-10 circuits/Non-Dispatch/El (days)
22.26.7.12 P-10 Line Sharing/-10 circuits/Non-Dispatch/FL(days) 32.26.7.21 P-10 Line Sharing/-510 circuits/Non-Dispatch/FL(days) 32.26.7.22 P-10 ZW Analog Loop Design/-510 circuits/Non-Dispatch/FL(days) 32.26.7.22 P-10 ZW Analog Loop Design/-510 circuits/Non-Dispatch/FL(days) 32.26.8.1 P-10 ZW Analog Loop Design/-510 circuits/Non-Dispatch/FL(days) 32.26.8.2 P-10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.26.8.1 P-10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.26.8.2 P-10 ZW Analog Loop Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.26.8.0.1 P-10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days) 32.26.10.1 P-10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days) 32.26.10.2 P-10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days) 32.26.11.1 P-10 ZW Analog Loop wiNP Design/-510 circuits/Non-Dispatch/FL(days) 32.26.11.2 P-10 ZW Analog Loop wiNP Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.26.11.2 P-10 ZW Analog Loop wiNP Non-Design/-510 circuits/Non-Dispatch/FL(days) 32.26.11.2 P-10 ZW Analog	B 2 26 7 1 1	P-10	line Sharing/~10 circuits/Dispatch/FI (days)
22.87.2.1 P-10 Line Sharing/>=10 crcuits/Dispatch/FL(days) 3.2.86.7.2 P-10 Line Sharing/>=10 crcuits/Non-Dispatch/FL(days) 3.2.86.1.1 P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	B 2 26 7 1 2	P-10	ine Sharing/<10 circuits/Non-Dispatch/El (days)
Action Design Action Displation PL days 22:26:7:22 P10 Line Shaingy 2:=0 circuitsNon-Displation/FL(days) 22:26:8:12 P10 2W Analog Loop Design/<10 circuitsNon-Displation/FL(days)	B 2 26 7 2 1	P-10	line Sharing/>=10 circuits/Dispatch/El (days)
3.286.8.1. P-10 2W Analog Loop Design/<10 circuits/Depatch/FL(days)	B 2 26 7 2 2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/EL (days)
3.2.26.8.12 P-10 2W Analog Loop Design/=10 circuits/Non-Dispatch/FL(days) 3.2.26.8.2.1 P-10 2W Analog Loop Design/=10 circuits/Non-Dispatch/FL(days) 3.2.26.8.2.2 P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	B 2 26 8 1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
3.2.26.8.2.1 P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days) 3.2.26.8.1.1 P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	8226812	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
3.2.26.8.2.2 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.9.1.1 P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 8 2 1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
3.2.26.9.1.1 P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 8 2 2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
3.2 26.9.1.2 P-10 2W Analog Loop Non-Design/>10 circuits/Non-Dispatch/FL(days) 3.2 26.9.2.1 P-10 2W Analog Loop Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.9.2.1 P-10 2W Analog Loop Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.10.1.1 P-10 2W Analog Loop wiNP Design/>10 circuits/Dispatch/FL(days) 3.2 26.10.2.2 P-10 2W Analog Loop wiNP Design/>10 circuits/Dispatch/FL(days) 3.2 26.10.2.1 P-10 2W Analog Loop wiNP Design/>10 circuits/Dispatch/FL(days) 3.2 26.11.2 P-10 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.11.2 P-10 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.11.2 P-10 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.12.1.2 P-14 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.12.1.2 P-14 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.12.1.2 P-14 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.12.1.2 P-14 2W Analog Loop wiNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2 26.12.1.2 P-14 2W Analog Loop wiNP Non-Design/>10 circ	B 2 26 9 1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
3.2.26.9.2.1 P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.01.1 P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.01.1 P-10 2W Analog Loop WINP Design/<10 circuits/Non-Dispatch/FL(days)	B 2 26 9 1 2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
3.2.26.9.2.2 P-10 ZW Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.10.1.1 P-10 ZW Analog Loop wiNP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.10.2.1 P-10 ZW Analog Loop wiNP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.10.2.2 P-10 ZW Analog Loop wiNP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.11.2.1 P-10 ZW Analog Loop wiNP Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 9 2 1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
3.226.10.1.1 P-10 ZW Analog Loop wINP Design/>10 circuits/Dispatch/FL(days) 3.226.10.1.2 P-10 ZW Analog Loop wINP Design/>10 circuits/Dispatch/FL(days) 3.226.10.2.1 P-10 ZW Analog Loop wINP Design/>10 circuits/Dispatch/FL(days) 3.226.10.2.2 P-10 ZW Analog Loop wINP Design/>10 circuits/Non-Dispatch/FL(days) 3.226.11.1 P-10 ZW Analog Loop wINP Non-Design/<10 circuits/Non-Dispatch/FL(days)	B226922	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
3.2.26.10.1.2 P-10 2W Analog Loop w/INP Design/>10 circuits/Non-Dispatch/FL(days) 3.2.26.10.2.1 P-10 2W Analog Loop w/INP Design/>10 circuits/Dispatch/FL(days) 3.2.26.11.1.2 P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	B 2 26 10 1 1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
226.10.2.2 P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days) 3.2.26.10.2.2 P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days) 3.2.26.11.1.2 P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.11.2 P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.11.2 P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.12.1 P-14 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.13.1.2 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.13.1.2 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.1 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.1 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.2 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	B 2 26 10 1 2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
P-10 PM Analog Loop wINP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.10.2. P-10 2W Analog Loop wINP Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 10 2 1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
3.2.26 11.1.1 P-10 ZW Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 10 2 2	P-10	2W Analog Loop w/NP Design/>=10 circuits/Non-Dispatch/FL(days)
9.1.0.0 ZW Analog Loop w/INP Non-Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.11.1.2 P-10 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.11.2.2 P-10 ZW Analog Loop w/INP Non-Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 ZW Analog Loop w/INP Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 ZW Analog Loop w/INP Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.12.1 P-14 ZW Analog Loop w/INP Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Non-Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.13.2 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/~10 circuits/Dispatch/FL(days) 3.2.26.14.2 P-10 Other Design/~10 circuits/Dispatch/FL(days) 3.2.26.14.1 P-10 Other Design/~10 circuits/Dispatch/FL(days)	B 2 26 11 1 1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)
2.26.11.1.2 P-10 ZW Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.11.2.1 P-10 ZW Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.11.2 P-14 ZW Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days) 3.2.26.12.1 P-14 ZW Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.12.2 P-14 ZW Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.12.2 P-14 ZW Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.13.1 P-14 ZW Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.13.2 P-14 ZW Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.1 P-10 Other Design/>=10 circuits/Dispatch/FL(days) 3.2.26.14.1.1 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.2 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.15.1 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.15.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	8 2 26 11 1 2	P-10	2W Analog Loop w/NP Non-Design/<10 circuits/Non-Dispatch/FL(days)
Az.26.11.2.2 P-10 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.11.2.2 P-14 ZW Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	B 2 26 11 2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
3.2.26112.11 P-14 ZW Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	B 2 26 11 2 2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
3.2.26, 12.1.2 P-14 2W Analog Loop w/LNP Design/>10 circuits/Non-Dispatch/FL(days) 3.2.26, 12.2.1 P-14 2W Analog Loop w/LNP Design/>10 circuits/Dispatch/FL(days) 3.2.26, 12.2.2 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2.26, 13.1.1 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2.26, 13.1.2 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Dispatch/FL(days) 3.2.26, 13.1.2 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Non-Dispatch/FL(days) 3.2.26, 13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Non-Dispatch/FL(days) 3.2.26, 14.1.1 P-10 Other Design/<10 circuits/Dispatch/FL(days)	B 2 26 12 1 1	P-14	2W Analog Loop w/I NP Design/<10 circuits/Dispatch/FL(days)
3.2.26(12.2.1) P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days) 3.2.26(12.2.2) P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days) 3.2.26(13.1.1) P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26(13.1.2) P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(13.2.2) P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26(13.2.2) P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26(14.1.2) P-10 Other Design/<>=10 circuits/Dispatch/FL(days) 3.2.26(14.1.2) P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(14.2.2) P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(14.2.2) P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(14.2.2) P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(15.1) P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(15.2.1) P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(16.1.1) P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26(16.2.1) P-10 INP (B 2 26 12 1 2	P-14	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
3.2.26 1.2.2 P-14 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26 1.2.2 P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	82261221	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
3.2.26(13.1.1) P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	B 2 26 12 2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
3.2.26 (13.1.2) P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26 (13.2.1) P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26 (13.2.2) P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26 (14.1.1) P-10 Other Design/<10 circuits/Dispatch/FL(days)	B 2 26 13 1 1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
3.2.26.13.2.1 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 3.2.26.14.1.1 P-10 Other Design/<10 circuits/Dispatch/FL(days)	8.2.26.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
3.2.26.13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) 3.2.26.14.1.1 P-10 Other Design/<10 circuits/Dispatch/FL(days)	B.2.26.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
3.2.26.14.1.1 P-10 Other Design/<10 circuits/Dispatch/FL(days)	B.2.26.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
$\begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.26.16.2.2 P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days) B.2.26.17.1.1 P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	B.2.26.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.26.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.17.1.2 P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days) B.2.26.17.2.1 P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days) B.2.26.17.2.2 P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days) B.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	B.2.26.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.26.17.2.1 P-14 LNP (Standalone)>=10 circuits/Dispatch/FL(days) B.2.26.17.2.2 P-14 LNP (Standalone)>=10 circuits/Dispatch/FL(days) B.2.26.17.2.2 P-14 LNP (Standalone)>=10 circuits/Dispatch/FL(days) B.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	B.2.26.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.26.17.2.2 P-14 LNP (Standalone)>=10 circuits/Non-Dispatch/FL(days) B.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	B.2.26.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) B.2.26.18.1.2 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	B.2.26.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.18.1.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2.26.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) B.2.26.18.2.2 P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days) B.2.26.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days) B.2.26.19.1.1 P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	B.2.26.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B.2.26.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) B.2.26.18.2.2 P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days) B.2.26.19.1.1 P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	B.2.26.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.26.18.2.2 P-10 Digital Loop < DS1/>= 10 circuits/Non-Dispatch/FL(days) B.2.26.19.1.1 P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	B.2.26.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2.26.19.1.1 P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days) B.2.26.19.1.2 P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	B.2.26.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.19.1.2 P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days) B.2.26.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	B.2.26.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2.26.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	B.2.26.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
	B.2.26.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
Diagnostic			7.36	118				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Dlagnostic
Diagnostic			12.06	255				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			1.00	1				Diagnostic
Diagnostic			1.00	21				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			11.18	82				Diagnostic
Diagnostic								Diagnostic
Diagnostic			8.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic			7.10	52				Diagnostic
Diagnostic			7.60	5				Diagnostic
Diagnostic			8.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic			7.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			7.50	4				Diagnostic
Diagnostic			4.00	1				Diagnostic
Diagnostic								Diagnostic
Diagnostic				45				Diagnostic
Diagnostic			9.84	45				Diagnostic
Diagnostic			10.00					Diagnostic
Diagnostic			13.00	1				Diagnostic
Diagnostic			0.44	454				Diagnostic
Diagnostic			9.41	151				Diagnostic
Diagnostic			9.54					Diagnostic
Diagnostic			8.57					Diagnostic
Diagnostic			10.17	12				Diagnostic
Diagnostic			19.17	12				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			4.00	1				Diagnostic
Diagnostic			6.50	2				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			18.56	39				Diagnostic
Diagnostic			1030.32	614				Diagnostic
Diagnostic								Diagnostic
Diagnostic			5,20	5				Diagnostic
Diagnostic			12.06	255				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			7.87	175				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic

ZScore Jun-01 Equity

Diagnostic

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BellSouth Monthly Performance Summary Florida, June 2001

	11011		Analog	Measure	Volume	Measure	Volume	Deviation
B.2.26.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
	Total S	ervice Order Cycle Time (offered) - Mechanized						
B 2 28 1 1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic					
D.2.20.1.1.1	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL/days)	Diagnostic					
B 2 28 1 2 1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic					
B228122	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B 2 28 2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			3.39	62	
B.2.28.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.48	2,593	
B.2.28.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic			12.00		
B.2.28.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/I-L(days)	Diagnostic					
B.2.28.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.5.2.2	P-10	XDSL (ADSL, HDSL and UCL/>=10 circulis/NOI-Dispatch/FL(days)	Diagnostic			11.00	2	
B.2.28.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.6.1.2	P-10	UNE ISDN/<10 circuits/NotPospatch/E(days)	Diagnostic					
B.2.28.6.2.1	0.10	LINE ISDN/>=10 circuits/Non-Dispatch/El (days)	Diagnostic					
B.2.28.0.2.2	P-10	Line Sharing/-10 circuits/Dispatch/El (days)	Diagnostic					1
D.2.20.7.1.1	P-10	Line Sharing/<10 circuits/Non-Dispatch/FI (days)	Diagnostic					
D.2.20.7.1.2	P-10	Line Sharing/>=10 circuits/Dispatch/FL (days)	Diagnostic					
B 2 28 7 2 2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B 2 28 8 1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			19.80	5	
B 2 28 8 1 2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic				<u> </u>	
B.2.28.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			5.67	3	
B.2.28.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				<u> </u>	
B.2.28.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic				·	-
B.2.28.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				i	-
B.2.28.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic					-
B.2.28.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic					-
B.2.28.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				<u> </u>	
B.2.28.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic					
B.2.28.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			·····	<u> </u>	
B.2.28.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/+L(days)	Diagnostic					
B.2.28.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/rL(days)	Diagnostic			7.58	26	
B.2.28.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.00		
B.2.28.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			9.00	1	
B.2.28.12.2.1	P+14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			0.00		
B.2.28.12.2.2	P-14	2W Analog Loop W/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic					
B.2.28.13.1.1	P-14	Zvv Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/EL(days)	Diagnostic			· · · · · · · · · · · · · · · · · · ·	·	
8.2.28.13.1.2	P-14	2W Analog Loop W/LNP Non-Design/s to circuits/Non-Dispatch/FL(days)	Diagnostic					
B.2.28.13.2.1	10.14	CW Androy Loop w/ NP Non-Design/s-10 circuits/Non-Dispatch/El (days)	Diagnostic					
B.2.28.13.2.2	0.10	Other Design/-10 circuits/Dispatch/El (dave)	Diagnostic					
B.2.28.14.1.1	P-10	Other Design/<10 circuits/Dispatch/El (days)	Diagnostic					
B.2.28, 14, 1.2 D.0.08, 14, 2.1	P-10	Other Design/s-10 circuits/Dispatch/El (days)	Diagnostic			······		
D.2.20.14.2.1	P-10	Other Design/>-10 circuits/Non-Disnatch/FI (days)	Diagnostic					
B 2 28 15 1 1	P.10	Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic					
9.2.20.13.1.1								

BST

Benchmark /

BST

CLEC

CLEC

Standard Standard

Error

1

	Florida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
		-								
B.2.28.15.1.2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.15.2.1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.15.2.2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.16.1.1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.16.1.2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			· · · ·					Diagnostic
B.2.28.16.2.1	P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.26.10.2.2	P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/El (days)	Diagnostia			44.50					Diagnostic
B.2.20.17.1.1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic			44.50	4				Diagnostic
B 2 28 17 2 1	P-14 INP (Standalone)/S=10 circuits/Dispatch/El (days)	Diagnostic			1.21	3,242				Diagnostic
B 2 28 17 2 2	P-14 INP (Standalone)/>=10 circuits/Non-Dispatch/F) (days)	Diagnostic								Diagnostic
B.2.28.18.1.1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			11.00	2				Diagnostic
B.2.28.18.1.2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28,18.2.1	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.18.2.2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.19.1.1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			6.50	4				Diagnostic
B.2.28.19.1.2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.19.2.1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.28.19.2.2	P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
	Total Service Order Cycle Time (offered) - Partially Mechanized									-
B.2.29.1.1.1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic			· · · ·					Diagnostic
B.2.29.1.1.2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.1.2.1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.1.2.2	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.2.1.1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.2.1.2	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.2.2.1	P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
8.2.29.2.2.2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.3.1.1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic			4.51	81				Diagnostic
B.2.29.3.1.2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.07	1,518				Diagnostic
B.2.29.3.2.1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic			6.25	4				Diagnostic
B.2.29.3.2.2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.11	3				Diagnostic
B.2.29.4.1.1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.4.1.2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.4.2.1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.4.2.2	P-10 Combo Utner/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic				<u> </u>				Diagnostic
D.2.29.5.1.1	P-10 RUSE (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B 2 20 5 2 1	P-10 xDSL (ADSL, HDSL and UCL)(>=10 circuits/NOIPDispatch/EL(days)	Diagnostic								Diagnostic
B 2 29 5 2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/El (days)	Diagnostic								Diagnostic
8229611	P-10 UNE ISDN/<10 circuits/Dispatch/EL (days)	Diagnostic			7.67	3				Diagnostic
B.2.29612	P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				-				Diagnostic
B.2.29.6.2.1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.6.2.2	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.7.1.1	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.7.1.2	P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.7.2.1	P-10 Line Sharing/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.7.2.2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.8.1.1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			8.61	28				Diagnostic
B.2.29.8.1.2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.8.2.1	P-10 2W Analog Loop Design/>≈10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.8.2.2	P-10 2W Analog Loop Design/>≈10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.9.1.1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			26.50	2				Diagnostic
B.2.29.9.1.2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.9.2.1	P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.9.2.2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.10.1.1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.10.1.2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.29.10.2.1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			L					Diagnostic

Analog Diagnostic
B.2 29.10.22 P-10 ZW Analog Loop wINP Design/>=10 circuits/Non-Dispatch/El(days) B.2.29.11.12 P-10 ZW Analog Loop wINP Non-Design/>=10 circuits/Dispatch/El(days) B.2.29.11.21 P-10 ZW Analog Loop wINP Non-Design/>=10 circuits/Dispatch/El(days) B.2.29.11.21 P-10 ZW Analog Loop wINP Non-Design/>=10 circuits/Dispatch/El(days) B.2.29.12.11 P-10 ZW Analog Loop wINP Design/>=10 circuits/Non-Dispatch/El(days) B.2.29.12.12 P-14 ZW Analog Loop wINP Design/=10 circuits/Non-Dispatch/El(days) B.2.29.12.21 P-14 ZW Analog Loop wINP Design/=10 circuits/Non-Dispatch/El(days) B.2.29.12.21 P-14 ZW Analog Loop wINP Design/=10 circuits/Non-Dispatch/El(days) B.2.29.12.21 P-14 ZW Analog Loop wINP Design/=10 circuits/Non-Dispatch/El(days) B.2.29.13.12 P-14 ZW Analog Loop wINP Non-Design/=10 circuits/Non-Dispatch/El(days) B.2.29.13.12 P-14 ZW Analog Loop wINP Non-Design/==10 circuits/Non-Dispatch/El(days) B.2.29.13.12 P-14 ZW Analog Loop wINP Non-Design/==10 circuits/Non-Dispatch/El(days) B.2.29.13.12 P-14 ZW Analog Loop wINP Non-Design/==10 circuits/Non-Dispatch/El(days) B.2.29.14.12 P-10 Other Design/==10 circuits/Non-Dispatch/El(days) <t< th=""><th></th><th></th><th></th></t<>			
B.2.29, 11.1.1 P-10 ZW Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 11.2.2 P-10 ZW Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 11.2.2 P-10 ZW Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 11.2.2 P-14 ZW Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 12.1.1 P-14 ZW Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 12.2 P-14 ZW Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 12.2 P-14 ZW Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days) B.2.29, 12.2 P-14 ZW Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 12.2 P-14 ZW Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 13.1 P-14 ZW Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2 P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2 P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2 P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2 P-14 ZW Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 14.1 P-10 Other Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 14.1 P-10 Other Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 14.2 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 15.1 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 15.1 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 15.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(B.2.29.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
$\begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
$\begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.29.11.2.2 P-10 2W Analog Loop w/LNP Design/>10 circuits/Dispatch/FL(days) B.2.29.12.1.1 P-14 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	B.2.29.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.29,12.1, P.14 2W Analog Loop wLNP Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,12.2, P.14 2W Analog Loop wLNP Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,12.2, P.14 2W Analog Loop wLNP Design/~10 circuits/Dispatch/FL(days) B.2.29,12.2, P.14 2W Analog Loop wLNP Design/~10 circuits/Dispatch/FL(days) B.2.29,13.1, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Dispatch/FL(days) B.2.29,13.1, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Dispatch/FL(days) B.2.29,13.1, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Dispatch/FL(days) B.2.29,13.2, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Dispatch/FL(days) B.2.29,13.2, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,13.2, P.14 2W Analog Loop wLNP Non-Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,14.2, P.10 Other Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,14.1, P.10 Other Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,14.1, P.10 Other Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,14.2, P.10 Other Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,14.2, P.10 Other Non-Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,15.1, P.10 Other Non-Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,15.2, P.10 Other Non-Design/~10 circuits/Non-Dispatch/FL(days) B.2.29,15.2, P.10 Other Non-Design/~10 circuits/Dispatch/FL(days) B.2.29,16.1, P.10 INP (Standalone)/~10 circuits/Dispatch/FL(days) B.2.29,16.1, P.10 INP (Standalone)/~10 circuits/Dispatch/FL(days) B.2.29,16.2, P.10 INP (Standalone)/~10 circuits/Dispatch/FL(days) B.2.29,16.2, P.10 INP (Standalone)/~10 circuits/Dispatch/FL(days) B.2.29,17.2, P.14 LNP (Standalone)/~10 circuits/Dispatch/FL(d	B.2.29.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B.229,122.1 P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days) B.229,122.2 P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	B.2.29.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B.2.29, 13.1.1 P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2.1 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 13.2.2 P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 14.1.2 P-10 Other Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 14.1.2 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 14.2.1 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 14.2.2 P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 14.2.2 P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 15.1.2 P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days) B.2.29, 15.2.1 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.1 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.2 P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.2 P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.2 P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 17.2.1 P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 18.2.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) B.2.29, 18.2.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2.29, 18.2.1 P-10 Digital Loop <	B.2.29.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.29, 13.2.1 P-14 2W Analog Loop wLNP Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.1.1 P-10 Other Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.1.1 P-10 Other Design/<=10 circuits/Dispatch/FL(days) B.2.29, 14.1.2 P-10 Other Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.2.1 P-10 Other Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.2.2 P-10 Other Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.2.2 P-10 Other Design/>=10 circuits/Dispatch/FL(days) B.2.29, 14.2.2 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.1.1 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.1.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days) B.2.29, 15.2.2 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.2 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 15.2.2 P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days) B.2.29, 16.1.2 P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days) B.2.29, 16.1.2 P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days) B.2.29, 16.1.2 P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 16.2.2 P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 17.1.1 P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 17.1.1 P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 17.2.1 P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days) B.2.29, 18.1.2 P-10 Digital Loop < DS1/<=10 circuits/Dispatch/FL(days) B.2.29, 18.2.1 P-10 Digital Loop < DS1/<=10 circuits/Dispatch/FL(days) B.2.29, 18.2.2 P-10 Digital Loop < DS1/<=10 circuits/Dispatch/FL(days) B.2.29, 18.2.2 P-10 Digital Loop > DS1/<=10 circuits/Dispatch/FL	B.2.29.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29,13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
$\begin{array}{l c c c c c c c c c c c c c c c c c c c$	B.2.29.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
$\begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{l c c c c c c c c c c c c c c c c c c c$	B.2.29.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{l c c c c c c c c c c c c c c c c c c c$	B.2.29.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	B.2.29.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
$ \begin{array}{l lllllllllllllllllllllllllllllllllll$	B.2.29.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.29,18.1.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2.29,18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) B.2.29,18.2.2 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) B.2.29,19.1.1 P-10 Digital Loop > DS1/>=10 circuits/Dispatch/FL(days) B.2.29,19.1.2 P-10 Digital Loop >> DS1/<10 circuits/Dispatch/FL(days)	B.2.29.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B.2.29.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days) B.2.29.18.2.2 P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days) B.2.29.19.1.1 P-10 Digital Loop >> DS1/<10 circuits/Non-Dispatch/FL(days)	B.2.29.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.29.18.2.2 P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days) B.2.29.19.1.1 P-10 Digital Loop >> DS1/<10 circuits/Dispatch/FL(days)	B.2.29.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2.29.19.1.1 P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days) B.2.29.19.1.2 P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	B.2.29.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.19.1.2 P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days) B.2.29.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days) B.2.29.19.2.2 P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	B.2.29.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2.29.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days) B.2.29.19.2.2 P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	8.2.29.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.29.19.2.2 P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	B.2.29.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
	B.2.29.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)

Tetel Convine Order	Cuele Time	(offered)	Non-Mechani	7ed
Total Service Orner	CVCIP LIMP	(onereu) -	WOII-WECHAIIIA	cu.

	Total Se	Avice Order Cycle Time (oncred) Ron meenanies
B.2.30.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B.2.30.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.30.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B.2.30.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B.2.30.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2.30.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.30.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.30.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.30.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.30.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.30.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.30.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2.30.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
B.2.30.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2.30.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B.2.30.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
Diagnostic								Diagnostic
Diagnostic					1			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			8.71	126				Diagnostic
Diagnostic								Diagnostic
Diagnostic			9.89	9				Diagnostic
Diagnostic					1			Diagnostic
Diagnostic			8.92	13				Diagnostic
Diagnostic			8.00	4				Diagnostic
Diagnostic			9.50	2				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			_					Diagnostic
Diagnostic				l				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			_					Diagnostic
Diagnostic			20.52	21				Diagnostic
Diagnostic			11.77	1,496				Diagnostic
Diagnostic								Diagnostic
Diagnostic			_					Diagnostic
Diagnostic			7.67	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			9.80	5				Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic

1			Diagnostic
			Diagnostic
			Diagnostic
		· · · · ·	Diagnostic
	26.29	7	Diagnostic
			Diagnostic
			Diagnostic
			Diagnostic
	4.80	164	Diagnostic
	2.54	702	Diagnostic
			Diagnostic
	7.40	105	Diagnostic
		Ì	Diagnostic
		1	Diagnostic
			Diagnostic
	12.24	238	Diagnostic

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	Florida June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
						·····				Diamatian di A
B.2.30.6.1.2	P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B.2.30.6.2.1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B.2.30.6.2.2	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.00					Diagnostic
B.2.30.7.1.1	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)	Diagnostic			1.00					Diagnostic
B.2.30.7.1.2	P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.7.2.1	P-10 Line Sharing/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.7.2.2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			10.00	60	-			Diagnostic
B.2.30.8.1.1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	Diagnostic			10.69	65	-			Diagnostic
B.2.30.8.1.2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			8.00		-			Diagnostic
8.2.30.8.2.1	P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			8.00	I				Diagnostic
B.2.30.8.2.2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.01	40	-			Diagnostic
B.2.30.9.1.1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.21	42	-			Diagnostic
B.2.30.9.1.2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.60	5	-			Diagnostic
B.2.30.9.2.1	P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
B.2.30.9.2.2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			7.00					Diagnostic
B.2.30.10.1.1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			7.00	1				Diagnostic
B.2.30.10.1.2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
B.2.30.10.2.1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.10.2.2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			0.00					Diagnostic
B.2.30.11.1.1	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			8.33	3				Diagnostic
B.2.30.11.1.2	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			4.00	1				Diagnostic
B.2.30.11.2.1	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			·					Diagnostic
B.2.30.11.2.2	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.12.1.1	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic			9.24	37				Diagnostic
B.2.30.12.1.2	P-14 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.12.2.1	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			13.00	1				Diagnostic
B.2.30.12.2.2	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.13.1.1	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			9.83	132				Diagnostic
B.2.30.13.1.2	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			6.37	214				Diagnostic
B.2.30.13.2.1	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic			7.60	5				Diagnostic
B.2.30.13.2.2	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			8.00	1				Diagnostic
B.2.30.14.1.1	P-10 Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic			19.17	12				Diagnostic
B.2.30.14.1.2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.14.2.1	P-10 Other Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.14.2.2	P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.15.1.1	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic			4.00	1				Diagnostic
B.2.30.15.1.2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			6.50	2				Diagnostic
B.2.30.15.2.1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.15.2.2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.16.1.1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.16.1.2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.16.2.1	P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.16.2.2	P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.17.1.1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic			6.71	7	-			Diagnostic
B.2.30.17.1.2	P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1491.64	422				Diagnostic
B.2.30.17.2.1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
8.2.30.17.2.2	P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			5.20	5				Diagnostic
B.2.30.18.1.1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			12.24	238				Diagnostic
B.2.30.18.1.2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				ļ				Diagnostic
B.2.30.18.2.1	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.18.2.2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.19.1.1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic			7.83	148				Diagnostic
B.2.30.19.1.2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.19.21	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
B.2.30.19.2.2	P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
		-								
	Disconnect Timeliness	- 05% win 15 mm			1 0 2 %	13 315				NO
B 2 31 1	IP-13 ILNP/FL(%)) >= 95% win ismin			1.9270	10,010				

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B.2.31.1

% Completions w/o Notice or < 24 hours

P-13 LNP/FL(%)

	Flori	da, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
			_								
B.2.32.1.1	P-6	Switch Ports/Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.1.2	P-6	Switch Ports/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.2.1	P-6	Local Interoffice Transport/Dispatch/FL(%)	Diagnostic			100.00%	16				Diagnostic
B.2.32.2.2	P-6	Local Interoffice Transport/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.3.1	P-6	Loop + Port Combinations/Dispatch/FL(%)	Diagnostic			100.00%	511				Diagnostic
8.2.32.3.2	P-6	Loop + Port Combinations/Non-Dispatch/FL(%)	Diagnostic			100.00%	4,044				Diagnostic
B.2.32.4.1	P-6	Combo Other/Dispatch/FL(%)	Diagnostic					1			Diagnostic
B.2.32.4.2	P-6	Combo Other/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.5.1	P-6	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	Diagnostic			100.00%	162				Diagnostic
B.2.32.5.2	P-6	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.6.1	P-6	UNE ISDN/Dispatch/FL(%)	Diagnostic			100.00%	293				Diagnostic
B.2.32.6.2	P-6	UNE ISDN/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.7.1	P-6	Line Sharing/Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.7.2	P-6	Line Sharing/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.8.1	P-6	2W Analog Loop Design/Dispatch/FL(%)	Diagnostic			100.00%	86				Diagnostic
B.2.32.8.2	P-6	2W Analog Loop Design/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.9.1	P-6	2W Analog Loop Non-Design/Dispatch/FL(%)	Diagnostic			100.00%	69				Diagnostic
8,2,32,9,2	P-6	2W Analog Loop Non-Design/Non-Dispatch/FL(%)	Diagnostic			100.00%	5				Diagnostic
B 2 32 10 1	P-6	2W Analog Loop w/INP Design/Dispatch/FL(%)	Diagnostic			100.00%	2				Diagnostic
8232102	P-6	2W Analog Loop w/INP Design/Non-Dispatch/FL(%)	Diagnostic			100.0070					Diagnostic
B 2 32 11 1	P-6	2W Analog Loop w/INP Non-Design/Dispatch/FI (%)	Diagnostic			100.00%	5				Diagnostic
B 2 32 11 2	P-6	2W Analog Loop w/NP Non-Design/Non-Dispatch/FL (%)	Diagnostic			100.00%	1				Diagnostic
B 2 32 12 1	P-6	2W Analog Loop w/I NP Design/Dispatch/EL (%)	Diagnostic			100.00%	250				Diagnostic
B 2 32 12 1	P-6	2W Analog Loop with Design/Dispatch/EL (%)	Diagnostic			100.00 /6	230				Diagnostic
D.2.02.12.2	D-6	201/ Analog Loop w/LNP Non-Design/Dispatch/FL (%)	Diagnostic			100.00%	102				Diagnostic
D.2.02.10.1	0.6	2W Analog Loop w/LNP Non-Design/Non-Dispatch/EL (%)	Diagnostic			100.00%	152				Diagnostic
D.2.02.10.2	0.6	Other Design Dispatch EL (%)	Diagnostic			100.00%	107				Diagnostic
D.2.32.14.1	0.0	Other Design/Dispatchir L(19)	Diagnostic			100.00%	- 31				Diagnostic
D.2.32.14.2	P-0	Other New Design Dispatch (%)	Diagnostic			100.009/	F				Diagnostic
D.2.02.10.1	0.6	Other Non-Design/Dispatch/Fi (%)	Diagnostic			100.00%	5				Diagnostic
B.2.32.15.2	P-0	UNE (Standalana) (Sianalah (E) (%)	Diagnostic			100.00%	5				Diagnostic
D.2.32.10.1	D 6	INP (Standalone)/Non-Dispatch/EL/94	Diagnostic								Diagnostic
B.2.32.16.2	P-0	INP (Standalone)Non-Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.17.1	0-9	LNP (Standalone)/Dispatch/FL(%)	Diagnostic								Diagnostic
B.2.32.17.2	P-6	Line (Standalone)/Non-Dispatch/FL(%)	Diagnostic			400.000/	000				Diagnostic
B.2.32.18.1	P-6	Digital Loop < DST/Dispatch/FL(%)	Diagnostic			100.00%	293				Diagnostic
B.2.32.18.2	P-6	Digital Loop < DS1/Non-Dispatch/FL(%)	Diagnostic			400.000/					Diagnostic
B.2.32.19.1	P+6	Digital Loop >= DS1/Dispatch/FL(%)	Diagnostic			100.00%	248				Diagnostic
B.2.32.19.2	P-6	Digital Loop >= DS1/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
	% Coo	perative Test Attempts for xDSL									
B.2.33.1	P-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% of requests			98.91%	367				YES
B.2.33.2	P-8	xDSL Other/FL(%)	>= 95% of requests								
	Conda	Order Assurem	-								
	Servic	e Order Accuracy	1			400.000/					
8.2.34.1.1.1	P-11	Design (Specials)/<10 circuits/Dispatch/FL(%)	>= 95%			100.00%	3				YES
B.2.34.1.1.2	P-11	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95%			83.33%	48				NO
B.2.34.1.2.1	P-11	Design (Specials)/>=10 circuits/Dispatch/FL(%)	>= 95%								
B.2.34.1.2.2	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	>≃ 95%			100.00%	4				YES
B.2.34.2.1.1	P-11	Loops Non-Design/<10 circuits/Dispatch/FL(%)	>= 95%			100.00%	2				YES
B.2.34.2.1.2	P-11	Loops Non-Design/<10 circuits/Non-Dispatch/FL(%)	>= 95%			96.05%	76				YES
B.2.34.2.2.1	P-11	Loops Non-Design/>=10 circuits/Dispatch/FL(%)	>= 95%			100.00%	4				YES
B.2.34.2.2.2	P-11	Loops Non-Design/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			52.94%	17				NO
	Unbun	dled Network Elements - Maintenance and Repair									
8.3.1.1.1	M& B-1	Switch Ports/Dispatch/FL(%)	R&B (POTS)	11.75%	124,226				T	······	

8.3.1.1.1	M&R-1 Switch Ports/Dispatch/FL(%)	R&B (POTS)	11./5%	124,226						
B.3.1.1.2	M&R-1 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	1.90%	70,709						
B.3.1.2.1	M&R-1 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1.99%	1,104	0.00%	4	0.	07000	0.2847	YÉS
B.3.1.2.2	M&R-1 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0.71%	700	0.00%	3	0.	04872	0.1466	YES

	Florida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
		_								
B.3.1.3.1	M&R-1 Loop + Port Combinations/Dispatch/FL(%)	R&B	11.93%	126,187	10.23%	1,515		0.00838	2.0226	YES
B.3.1.3.2	M&R-1 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	2.02%	71,989	2.24%	938		0.00462	-0.4770	YES
8.3.1.4.1	M&R-1 Combo Other/Dispatch/FL(%)	R&B&D - Disp	11.72%	129,396						
B.3.1.4.2	M&R-1 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	11.72%	129,396					L	
B.3.1.5.1	M&R-1 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	20.23%	1,320	13.10%	84		0.04520	1.5778	YES
B.3.1.5.2	M&R-1 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	0.73%	274	0.00%	23		0.01848	0.3950	YES
B.3.1.6.1	M&R-1 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	9.51%	347	11.54%	52		0.04362	-0.4650	YES
B.3.1.6.2	M&R-1 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1.55%	323	10.34%	29		0.02393	-3.6759	NO
B.3.1.7.1	M&R-1 Line Sharing/Dispatch/FL(%)	ADSL to Retail	20.23%	1,320	100.00%	2		0.28426	-2.8064	NO
B.3.1.7.2	M&R-1 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	0.73%	274	25.00%	28		0.01689	-14.3705	NO
B.3.1.8.1	M&R-1 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	11.93%	126,187	9.12%	1,239		0.00925	3.0316	YES
B.3.1.8.2	M&R-1 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	11.93%	126,187	2.86%	315		0.01828	4.9598	YES
B.3.1.9.1	M&R-1 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	11.73%	123,882	11.22%	704		0.01216	0.4198	YES
B.3.1.9.2	M&R-1 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	1.58%	59,519	3.13%	96		0.01275	-1.2079	YES
B.3.1.10.1	M&R-1 Other Design/Dispatch/FL(%)	Design	3.74%	3,209	7.06%	269		0.01204	-2.7599	NÖ
B.3.1.10.2	M&R-1 Other Design/Non-Dispatch/FL(%)	Design	0.80%	3,238	4.24%	118		0.00836	-4.1059	NO
B 3.1.11.1	M&R-1 Other Non-Design/Dispatch/FL(%)	R&B	11.93%	126,187	13.79%	58		0.04256	-0.4389	YES
8.3.1.11.2	M&R-1 Other Non-Design/Non-Dispatch/FL(%)	R&B	2.02%	71,989	0.00%	57		0.01863	1.0832	YES
831121	M&R-1 INP (Standalone)/Dispatch/FL(%)	R&B (POTS)	11.75%	124,226						
8.3.1.12.2	M&R-1 [LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	1.90%	70,709						
										•
	Customer Trouble Report Rate		· · · · · · · · · · · · · · · · · · ·							
B.3.2.1.1	M&R-2 Switch Ports/Dispatch/FL(%)	H&B (POTS)	2.14%	5,814,186	0.00%	2		0.10336	0.2067	YES
B.3.2.1.2	M&R-2 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	1.22%	5,814,186	0.00%	2		0.07798	0.1560	YES
B.3.2.2.1	M&R-2 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	2.22%	49,698	0.36%	1,116		0.00451	4.1296	YES
B.3.2.2.2	M&R-2 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	1.41%	49,698	0.27%	1,116		0.00359	3.1726	YES
B.3.2.3.1	M&R-2 Loop + Port Combinations/Dispatch/FL(%)	R&B	2.04%	6,191,325	1.55%	97,895		0.00046	10.6670	YES
B.3.2.3.2	M&R-2 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	1.16%	6,191,325	0.96%	97,895		0.00035	5.8895	YES
B.3.2.4.1	M&R-2 Combo Other/Dispatch/FL(%)	R&B&D - Disp	1.84%	7,015,702					L	L
B.3.2.4.2	M&R-2 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	1.84%	7,015,702						
B.3.2.5.1	M&R-2 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	0.69%	191,512	1.48%	5,674		0.00112	-7.0745	NO
B.3.2.5.2	M&R-2 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	0.14%	191,512	0.41%	5,674		0.00051	-5.1476	NO
B.3.2.6.1	M&R-2 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	1.37%	25,343	2.09%	2,489		0.00246	-2.9292	NO
B.3.2.6.2	M&R-2 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1.27%	25,343	1.17%	2,489		0.00237	0.4613	YES
B.3.2.7.1	M&R-2 Line Sharing/Dispatch/FL(%)	ADSL to Retail	0.69%	191,512	0.25%	807		0.00293	1.5073	YES
B.3.2.7.2	M&R-2 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	0.14%	191,512	3.47%	807		0.00133	-24.9312	NO
B.3.2.8.1	M&R-2 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	2.04%	6,191,325	1.70%	72,812		0.00053	6.3228	YES
B.3.2.8.2	M&R-2 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	2.04%	6,191,325	0.43%	72,812		0.00053	30.1689	YES
B.3.2.9.1	M&R-2 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	2.13%	5,814,186	2.03%	34,656		0.00079	1.2626	YES
B.3.2.9.2	M&R-2 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	1.02%	5,814,186	0.28%	34,656		0.00055	13.6977	YES
B.3.2.10.1	M&R-2 Other Design/Dispatch/FL(%)	Design	0.39%	824,377	2.86%	9,392		0.00065	-38.2253	NO
B.3.2.10.2	M&R-2 Other Design/Non-Dispatch/FL(%)	Design	0.39%	824,377	1.26%	9,392		0.00065	-13.2788	NO
B.3.2.11.1	M&R-2 Other Non-Design/Dispatch/FL(%)	R&B	2.04%	6,191,325	8.32%	697		0.00541	-11.6188	NO
B.3.2.11.2	M&R-2 Other Non-Design/Non-Dispatch/FL(%)	R&B	1.16%	6,191,325	8.18%	697	}	0.00408	-17.1747	NO
B.3.2.12.1	M&R-2 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	2.14%	5,814,186						
R32122	M&R-2 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	1.22%	5,814,186						
		-								
	Maintenance Average Duration			101000			01.440		·	
B.3.3.1.1	M&R-3 Switch Ports/Dispatch/FL(hours)	HAB (PUIS)	20.82	124,226			21.418		↓ '	
B.3.3.1.2	M&R-3 Switch Ports/Non-Dispatch/FL(hours)	HALB (POTS)	1.3/	/0,/09			11.527			
B.3.3.2.1	M&R-3 Local Interoffice Transport/Dispatch/FL(hours)	DS1/DS3	5.36	1,104	4.43	4	9.680	4.84886	0.1925	YES
B.3.3.2.2	M&R-3 Local Interoffice Transport/Non-Dispatch/FL(hours)	DS1/DS3	2.38	700	7.12	3	3.789	2.19197	-2.1597	NO
B.3.3.3.1	M&R-3 Loop + Port Combinations/Dispatch/FL(hours)	R&B	20.79	126,187	17.03	1,515	21.438	0.55407	6.7731	YES
B.3.3.3.2	M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours)	R&B	7.34	71,989	3.88	938	11.511	0.37829	9.1524	YES
B.3.3.4.1	M&R-3 Combo Other/Dispatch/FL(hours)	R&B&D - Disp	20.44	129,396			22.218		L	L
B.3.3.4.2	M&R-3 Combo Other/Non-Dispatch/FL(hours)	R&B&D - Disp	20.44	129,396			22.218			
B.3.3.5.1	M&R-3 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours)	ADSL to Retail	58.89	1,320	10.52	84	44.133	4.96616	9.7388	YES
B.3.3.5.2	M&R-3 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(hours)	ADSL to Retail	14.12	274	2.34	23	25.177	5.46558	2.1560	YES
B.3.3.6.1	M&R-3 UNE ISDN/Dispatch/FL(hours)	ISDN - BRI	11.84	347	11.37	52	18.683	2.77815	0.1685	YES
B.3.3.6.2	M&R-3 UNE ISDN/Non-Dispatch/FL(hours)	ISDN - BRI	3.24	323	8.52	29	5.392	1.04533	-5.0510	NO

		Denote and d	007	DOT	0.50	01 50	Oton doud	Chanderd		
	Florida, June 2001	Benchmark /	BSI	BST	CLEC	CLEC	Standard	Standard	7Score	.lun-01 Fauity
		Analog	MCabure	Volume	Weasure	Volume	Deviation	LIIO	200010	oun-or Equity
B.3.3.7.1	M&R-3 Line Sharing/Dispatch/FL(hours)	ADSL to Retail	58.89	1,320	49.94	2	44.133	31.23038	0.2865	YES
B.3.3.7.2	M&R-3 Line Sharing/Non-Dispatch/FL(hours)	ADSL to Retail	14.12	274	21.52	28	25.177	4.99512	-1.4806	YES
B.3.3.8.1	M&R-3 2W Analog Loop Design/Dispatch/FL(hours)	R&B - Disp	20.79	126,187	8.92	1,239	21.438	0.61202	19.3945	YES
B.3.3.8.2	M&R-3 2W Analog Loop Design/Non-Dispatch/FL(hours)	R&B - Disp	20.79	126,187	4.83	315	21.438	1.20940	13.1890	YES
B.3.3.9.1	M&R-3 2W Analog Loop Non-Design/Dispatch/FL(hours)	R&B (POTS) excl SB FT	20.81	123,882	14.93	704	21.413	0.80933	7.2552	YES
B.3.3.9.2	M&R-3 2W Analog Loop Non-Design/Non-Dispatch/FL(hours)	R&B (POTS) excl SB FT	7.50	59,519	2.77	96	11.471	1.17166	4.0411	YES
B.3.3.10.1	M&R-3 Other Design/Dispatch/FL(hours)	Design	7.00	3,209	8.58	269	38.297	2.43088	-0.6513	YES
B.3.3.10.2	M&R-3 Other Design/Non-Dispatch/FL(hours)	Design	2.59	3,238	5.27	118	18.510	1.73474	-1.5459	YES
B.3.3.11.1	M&R-3 Other Non-Design/Dispatch/FL(hours)	R&B	20.79	126,187	17.98	58	21.438	2.81558	0.9961	YES
B.3.3.11.2	M&R-3 Other Non-Design/Non-Dispatch/FL(hours)	R&B	7.34	71,989	4.11	57	11.511	1.52527	2.1220	YES
B.3.3.12.1	M&R-3 LNP (Standalone)/Dispatch/FL(hours)	R&B (POTS)	20.82	124,226			21.418			
B.3.3.12.2	M&R-3 LNP (Standalone)/Non-Dispatch/FL(hours)	R&B (POTS)	7.37	70,709			11.527			1
	% Repeat Troubles within 30 Days									
B.3.4.1.1	M&R-4 Switch Ports/Dispatch/FL(%)	R&B (POTS)	20.63%	124,226			_			
B.3.4.1.2	M&R-4 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	18.30%	70,709						
B.3.4.2.1	M&R-4 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	33.97%	1,104	50.00%	4		0.23723	-0.6758	YES
B.3.4.2.2	M&R-4 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	28.71%	700	100.00%	3		0.26177	-2.7232	NO
B.3.4.3.1	M&R-4 Loop + Port Combinations/Dispatch/FL(%)	R&B	20.55%	126,187	15.51%	1,515		0.01044	4.8250	YES
B.3.4.3.2	M&R-4 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	18.21%	71,989	24.63%	938		0.01268	-5.0598	NO
B.3.4.4.1	M&R-4 Combo Other/Dispatch/FL(%)	R&B&D - Disp	21.03%	129,396						
B.3.4.4.2	M&R-4 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	21.03%	129,396						
B.3.4.5.1	M&R-4 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	26.59%	1,320	20.24%	84		0.04972	1.2778	YES
B.3.4.5.2	M&R-4 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	29.93%	274	8.70%	23		0.09941	2.1357	YES
B.3.4.6.1	M&R-4 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	30.55%	347	23.08%	52		0.06849	1.0907	YES
B.3.4.6.2	M&R-4 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	25.70%	323	31.03%	29		0.08471	-0.6302	YES
B.3.4.7.1	M&R-4 Line Sharing/Dispatch/FL(%)	ADSL to Retail	26.59%	1,320	50.00%	2		0.31265	-0.7487	YES
B.3.4.7.2	M&R-4 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	29.93%	274	57.14%	28		0.09086	-2.9955	NO
B.3.4.8.1	M&R-4 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	20.55%	126,187	18.32%	1,239		0.01154	1.9325	YES
B.3.4.8.2	M&R-4 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	20.55%	126,187	17.46%	315	-	0.02280	1.3556	YES
B.3.4.9.1	M&R-4 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	20.60%	123,882	18.04%	704		0.01529	1.6746	YES
B.3.4.9.2	M&R-4 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	17.92%	59,519	71.88%	96	-	0.03917	-13.7738	NO
B.3.4.10.1	M&R-4 Other Design/Dispatch/FL(%)	Design	40.01%	3,209	31.60%	269		0.03110	2.7056	YES
B.3.4.10.2	M&R-4 Other Design/Non-Dispatch/FL(%)	Design	36.01%	3,238	29.66%	118		0.04499	1.4112	YES
B.3.4.11.1	M&R-4 Other Non-Design/Dispatch/FL(%)	R&B	20.55%	126,187	18.97%	58		0.05307	0.2987	YES
B.3.4.11.2	M&R-4 Other Non-Design/Non-Dispatch/FL(%)	R&B	18.21%	71,989	7.02%	57	_	0.05114	2.1887	YES
B.3.4.12.1	M&R-4 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	20.63%	124,226			_			
B.3.4.12.2	M&R-4 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	18.30%	70,709	1					
	Out of Service > 24 hours	_								
B.3.5.1.1	M&R-5 Switch Ports/Dispatch/FL(%)	R&B (POTS)	22.13%	85,179						
B.3.5.1.2	M&R-5 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	7.72%	22,204						
B.3.5.2.1	M&R-5 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1.99%	1,104	0.00%	4		0.07000	0.2847	YES
B.3.5.2.2	M&R-5 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0.71%	700	0.00%	3		0.04872	0.1466	YES
B.3.5.3.1	M&R-5 Loop + Port Combinations/Dispatch/FL(%)	R&B	22.14%	86,545	13.98%	1,037		0.01297	6.2862	YES
B.3.5.3.2	M&R-5 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	7.66%	22,799	5.63%	320		0.01497	1.3608	YES
B.3.5.4.1	M&R-5 Combo Other/Dispatch/FL(%)	R&B&D - Disp	21.48%	89,754						
B.3.5.4.2	M&R-5 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	21.48%	89,754			_			L
B.3.5.5.1	M&R-5 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	75.00%	8	13.10%	84		0.16022	3.8638	YES
B.3.5.5.2	M&R-5 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	50.00%	2	0.00%	23		0.36860	1.3565	YES
B.3.5.6.1	M&R-5 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	9.51%	347	11.54%	52		0.04362	-0.4650	YES
B.3.5.6.2	M&R-5 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1.55%	322	10.34%	29		0.02397	-3.6678	NO
B.3.5.7.1	M&R-5 Line Sharing/Dispatch/FL(%)	ADSL to Retail	75.00%	8						
B.3.5.7.2	M&R-5 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	50.00%	2	100.00%	2		0.50000	-1.0000	YES
B.3.5.8.1	M&R-5 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	22.14%	86,545	9.12%	1,239		0.01188	10.9567	YES
B.3.5.8.2	M&R-5 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	22.14%	86,545	2.86%	315		0.02343	8.2266	YES
B.3.5.9.1	M&R-5 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	22.12%	85,149	13.50%	526		0.01815	4.7512	YES
83592	M&B-5 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	7.69%	22,116	16.67%	30		0.04867	-1.8452	NO
B 3 5 10 1	M&B-5 Other Design/Dispatch/EI (%)	Design	3.74%	3,209	7.06%	269		0.01204	-2.7599	NO
B 3 5 10 2	M&R-5 (Other Design/Non-Dispatch/FI (%)	Design	0.80%	3,238	4.24%	118		0.00836	-4.1059	NO
2.0.0.10.2	mutri o Joner Deagration Diapatoria Liva			· · · · · ·						

	Florida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
B.3.5.11.1	M&R-5 Other Non-Design/Dispatch/FL(%)	R&B	22.14%	86,545	31.25%	32		0.07340	-1.2417	YES
B.3.5.11.2	M&R-5 Other Non-Design/Non-Dispatch/FL(%)	R&B	7.66%	22,799	4.35%	23		0.05549	0.5973	YES
B.3.5.12.1	M&R-5 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	22.13%	85,179						
B.3.5.12.2	M&R-5 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	7.72%	22,204						
	Unbundled Network Elements - Billing									
D 4 1		BCT - State	00 460/ 1	8400 000 000	1 80 900/ 1	P1 645 555	1	A AAAAA	4405 4407	
D.4.1		DOI - State	90.40%	\$499,029,000	09.3276	\$3,913,225		0.00006 .	1465.1137	<u>NO</u>
	Mean Time to Deliver Invoices - CRIS									
B.4.2	B-2 Region(business days)	BST - Region	3.72	1	3.21	1,248				YES

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Florida, June 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	2 Score	Jun-01 Equity
Local Interconnection Trunks - Ordering					. <u></u> .				
% Rejected Service Requests	Outraportio	-		70.110/					
T Eccar interconnection (Tonks/FE/%)	Diagnostic		_	12.1176	147				Diagnostic
Reject Interval O-8 Local Interconnection Trunks/FL(%)	>= 85% w in 4 days			95 .28%	101				YES
FOC Timeliness -9 ILocal Interconnection Trunks/FL(%)	>= 95% w in 10 days			99.20%	131				VES
FOC & Reject Response Completeness				00.2070					120
-11 Local Interconnection Trunks/FL(%)	>= 95%	_		97.50%	122				YES
FOC & Reject Response Completences (Multiple Responses) -11 [Local Interconnection Trunks/FL(%)	>= 95%								
Local Interconnection Trunks - Provisioning									
Order Completion Interval			-						
P-4 [Local Interconnection Trunks/FL(days)	Parity w Retail	23.53	131	16.83	41	25.816	4.61977	1.4514	YES
Held Orders I Local Interconnection Trunks/FL(days)	Parity w Retail		"a'a"	1.2 I.A. 1911	Not Amilian	le for Trunks		·····	
/ Jeopardies				<u></u>		ын <u>ала таан ша</u> т		<u></u>	s sitti dan da a
P-2 Local Interconnection Trunks/FL(%)	Parity w Retail				Hot Applica	te for Trunke	şa şe	111.452.13	
Iverage Jeopardy Notice Interval 5-2 [Local Interconnection Trunks/FL(hours)	95% >≃ 48 hrs				Not Applica	ile for Trunks			
6 Missed Installation Appointments	Dente un Deteil								
3 Local Interconnection Trunks/FL(%)	Parity w Retail	0./6%	131	8.51%	4/		0.01480	-5.2351	NO
2-9 [Local Interconnection Trunks/FL(%)	Parity w Retail	0.00%	3,853	0.00%	2,281		0.00000		YES
Average Completion Notice Interval P-5 II ocal Interconnection Trunks/EI (hours)	Parity w Betail	141.45	93	5939		322 560	67 70973	1 1 2116	VES
Total Service Order Cycle Time		141.40				322.303	01.12013	1.2110	TES
2-10 [Local Interconnection Trunks/FL(days)	Diagnostic				😑 Under de	valopmant			
Total Service Order Cycle Time (offered) -10 [Local Interconnection Trunks/FL(days)	Diagnostic	veliet, st		1 2041		elopment			
% Completions w/o Notice or < 24 hours									
Cocal Interconnection Trunks/Dispatch/FL(%)	Diagnostic Diagnostic			100.00%	41				Diagnostic
	Diagnootic				· · · · ·			<u> </u>	Diagnostic
P-11 [Local Interconnection Trunks/<10 circuits/Dispatch/FL(%)	>= 95%			100.00%	5				YES
P-11 Local Interconnection Trunks/<10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	5				YES
P-11 Local Interconnection Trunks/>=10 circuits/Dispatch/FL(%)	>= 95%			00.049/					
	>= 90%	_		93.94%	- 33				NO
Local Interconnection Trunks - Maintenance and Repair									
Missed Repair Appointments	Dorty w Dotail	0.000/		1 0 000/			0.00000		Vra
M&R-1 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.16%	643	0.00%	16		0.00000	0.1559	YES
							0.00001	0.1000	120

Customer Trouble Report Rate

	Florida, June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
C.3.2.1	M&R-2 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	397,511	0.00%	133,526		0.00001	-0.3464	YÉS
C.3.2.2	M&R-2 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.16%	397,511	0.01%	133,526		0.00013	11.7734	YES
	Maintenance Average Duration									
C.3.3.1	M&R-3 Local Interconnection Trunks/Dispatch/FL(hours)	Parity w Retail	1.59	2	1.28	1	1.607	1.96804	0.1567	YES
C.3.3.2	M&R-3 Local Interconnection Trunks/Non-Dispatch/FL(hours)	Parity w Retail	0.47	643	1.52	16	1.275	0.32267	-3.2448	NO
	% Repeat Troubles within 30 Days									
C.3.4.1	M&R-4 [Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	2	0.00%	1		0.00000		YES
C.3.4.2	M&R-4 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	8.24%	643	0.00%	16		0.06960,	1.1842	YES
	Out of Service > 24 hours									
C.3.5.1	M&R-5 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	2	0.00%	1		0.00000		YES
C.3.5.2	M&R-5 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.16%	643	0.00%	16		0.00997	0.1559	YES
	Local Interconnection Trunks - Billing								i.	
	Invoice Accuracy									
C.4.1	B-1 [FL(%)	BST - State	98.46%	\$499,829,886	94.29%	\$10,033,056		0.00004	1064.6675	NO
	Mean Time to Deliver Invoices - CABS									
C.4.2	B-2 [Region(calendar days)	BST - Region	4.29	1	3.73	3,213				YES
	LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING	·····								
	Trunk Group Performance - Aggregate	- 0.59(dit 0 appears Hiro								VES
C.5.1	IGP-1 JFL	>0.3% Qil 2 COnsec. His			U					162

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ZScore Jun-01 Equity

BellSouth Monthly Performance Summary

Florida, June 2001

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Open	ations Support Systems - Pre-Ordering	····						<u></u>
0000	2 EDifDagion/%)	N= 00 F9/			100 000			
055	2 IEDI/Region(%)	>= 99,5%			100.00%			·
055-	2 (HAL/Hegion(%)	>= 99,5%			100.00%			
055-	Z LENS/Region(%)	>= 99.5%			99.94%			
OSS-	2 [LEO MAINFHAME/Hegion(%)	>= 99.5%	}		99.92%			
OSS-	2 [LEO UNIX/Hegion(%)	>= 99.5%						
OSS-	2 [LESOG/Region(%)	>= 99.5%			100.00%			
OSS-	2 [1 AG/Region(%)	>= 99,5%			99.96%			
USS-	2 [PSIMS/Region(%)	>= 99.5%			100.00%			
% int	erface Availability - BST & CLEC							
OSS-	2 [ATLAS/COFFI/Region(%)	>= 99.5%			99.96%			
OSS-	2 BOCRIS/Region(%)	>= 99.5%			99.96%			
OSS-	2 DSAP/Region(%)	>= 99.5%			100.00%			
OSS-	2 RSAG/Region(%)	>= 99.5%			99.96%			
OSS-	2 SOCS/Region(%)	>= 99.5%			99.96%			
OSS-	2 SONGS/Region(%)	>= 99.5%			99.96%			
oss-	2 (DOE/Region(%)	>= 99.5%			100.00%			
Avera	Ige Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Se	conds)	F	4 010 504	1 1 10	0.44 0.00		
OSS-	1 [HSAG, by IN/Hegion (seconds)	HNS + HSAG, by IN + 2 Sec	2.92	1,212,504	1.66	241,323		
055-	1 IRSAG, by IN/Region (seconds)	HOS - RSAG, by IN + 2 Sec	3.27	/,414	1.66	241,323		
055-	1 IRSAG, by ADDR/Region (seconds)	HNS - HSAG, BY ADDR + 2 Sec	3.04	4,294,956	1.45	169,294		
USS-	1 [HSAG, by ADDH/Region (seconds)	HUS - KSAG, BY ADDR + 2 Sec	5./5	612,549	1.45	169,294		
OSS-	1 ATLAS/Hegion (seconds)	HNS - ATLAS + 2 Sec	5.12	349,603	1.08	64,940		
OSS-	1 ATLAS/Hegion (seconds)	HOS - ATLAS + 2 Sec	2.64	293,422	1.08	64,940		· · · ·
OSS-	1 DSAP/Hegion (seconds)	HNS - USAP + 2 Sec	2.69	558,850	0.69	604		
OSS-	1 (DSAP/Hegion (seconds)	HUS - USAP + 2 Sec	2.70	301,692	0.69	604		
OSS-	1 HAL/CHIS/Hegion (seconds)	HNS-CHSAUCIS+2Sec	3.66	2,461,808	13.09	583,242		
055-	T HAL/CHIS/Hegion (seconds)	HUS - CHSOCSH # 2 Sec	3.23	4/2,421	13.09	583,242		
055-	1 COFFI/USOC/Hegion (seconds)		4.09	1,925,182	0.94	35,669		
055-	1 [COFFI/USOC/Hegion (seconds)	HUS - UASISBIG + 2 Sec	4.36	622,170	0.94	35,669		
055-	1 PSIMS/ORB/Region (seconds)		4.09	1,925,182	0.11	69,519		
055		HUS - UASISBIG + 2 Sec	4.30	622,170	0.11	69,519		
Avera	I IBSAG, by TN/Benion, (seconds)	ends)	202	1 212 504	1 1 86	132 711		_
000	1 IBSAG, by TN/Begion (seconds)	BOS - BSAG by TN + 2 Sec	3.27	7 414	1.86	132,711		
000	1 IPSAG by ADDR/Region (seconds)	BNS - BSAG by ADDR + 2 Sec	3.04	4 204 056	1.00	136,71		
000	1 IPSAG, by ADDR/Region (seconds)		5.04	612540	1.00	436,474		
033-	1 ATLAS - MI H/Region (seconds)	NOS * NOAG, by ADDA + 2 Sec	5.75	012,040	1.03	400,474		
000	1 ATLAS - MILH/Region (seconds)	Diagnostic			· · · · · · · · · · · · · · · · · · ·			
000	1 ATLAS - MERINAGION (seconds)	Diagnostic			0.50	7		
033	1 ATLAS - DID/Region (seconds)	Diagnostic			0.59	7		<u> </u>
055-	1 ATLAS - DID/Region (seconds)		5.10	240 602	0.59	1		
055-	1 ATLAS - INVREGION (Seconds)		0.12	349,003	1.30	4,070		
055-	1 ATLAS - Invitegion (seconds)	HUS - AILAS - IN + 2 Sec	2.04	293,422	1.30	4,0/0		
055	1 DOAD(Partian (concerds)		2.09	336,850	2.19	<u>36∠,836</u>		· · ·
055	1 USAP/negion (seconds)	HUS - USAP + 2 Sec	2.70	301,692	2.19	382,836		
055-	1 JUHSEUSH/Hegion (seconds)		3.00	2,461,808	2.07	63,821		
055-	I JCHSEUSK/Hegion (seconds)	HUS - CHSUCSH + 2 Sec	3.23	4/2,421	1 2.07	63,821		
055-	I JUNSEINT/Region(seconds)		lui i i i i i i i i i i i i i i i i i i		ासल संबंध ।	tor applicable aff	ar 2-1-2001, 484 D.1.4.9.1	
055-	I JURSEINT/Region(seconds)	HUS - CHSUCSH + 2 Sec	<u> </u>	L 0 101 002	This data i		er 5-7-2007, see D 1.4.9.2	
055-	1 JUNSEUSHL/Hegion (seconds)	HINS - CHSAUUTS + 2 Sec	3.00	2,461,808	1.52	1,805		
055-	I TOHOEOOHL/Hegion (seconds)	HUS - CHOUCSH + 2 Sec	3.23	4/2,421	1.32	1,805		

Benchmark /

Analog

BST

Measure

BST

Volume

CLEC

Measure

CLEC

Volume

Standard Standard

Error

Deviation

Operations Support Systems - Maintenance and Repair

	Florida. June 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	, , , , , , , , , ,	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Jun-01 Equity
		5								
	% Interface Availability - PCT									
D211	OSS3 DAFDecim/%	>= 99.5%	99 99%							VES
D.2.1.1		<i>y=</i> 00.0 /k	33.33 /0							163
	% Interface Availability - CLEC	_								
D.2.2.1	OSS-3 CLEC TAFI/Region(%)	>= 99.5%			100.00%					YES
D.2.2.2	OSS-3 CLEC ECTA/Region(%)	>= 99.5%			100.00%					YES
	% Interface Availability - BST & CLEC									
D231	OSS-3 ICBIS/Begion(%)	>= 99.5%			99.96%					VES
D232	OSS-3 I MOS HOST/Begion(%)	>= 99.5%			99.99%					VES
0233	OSS-3 UNP/Region(%)	>= 99.5%			100.00%					VES
D234	OSS-3 MARCH/Region(%)	>= 99.5%			100.00%					YES
D235	OSS-3 OSPCM/Region(%)	>= 99.5%			100.00%					YES
D.2.3.6	OSS-3 Predictor/Region(%)	>= 99.5%			100.00%					YES
D.2.3.7	OSS-3 SOCS/Region(%)	>= 99.5%			99.96%					YES
	Average Response Interval	1	05.040	0.000.005	0 4 7004					
D.2.4.1.1	OSS-4 CRIS/Region(%) <= 4 Seconds	Parity w Retail	95.81%	2,003,035	94.76%	91,414	-	0.00068	15.5566	NO
D.2.4.1.2	OSS-4 CHIS/Hegion(%) <= 10 Seconds	Parity w Hetall	98.89%	2,003,035	99.13%	91,414		0.00035	-6.8796	YES
D.2.4.1.3	OSS-4 CHIS/Region(%) > 10 Seconds	Parky w Retail	1.11%	2,003,035	0.87%	91,414	-	0.00035	6.8/96	YES
D.2.4.2.1	OSS-4 DLE TH/Region(%) <= 4 Seconds	Parity w Petali	0.56%	45,003	7.99%	989	-	0.01016	3.2176	NO VEO
D.2.4.2.2	OSS-4 DLE TH/Region(%) > 10 Seconds	Parity w Potali	10.44%	45,003	10.02%	909	-	0.012/2	-6.6965	YES
D.2.4.2.3	OSS-4 DLL HIFTEGION (%) > 10 Seconds	Parity w Retail	7 8194	35 412	10.92%	2909		0.01272	0.0905	YES
D.2.4.3.1	OSS-4 DLH/Hegion(%) <= 4 Seconds	Parity w Retail	96 7894	35,412	19.09%	20,159		0.00214	-56.4185	YES
D.2.4.3.2	OSS-4 DER/Region %) > 10 Seconds	Parity w Betail	13 22%	35 412	97.01% 0.10%	20,159		0.00270	-40.7915	VEC
D.2.4.0.0	OSS-4 DEMINEGRATION / > 10 Occurred	Parity w Retail	99.84%	2 002 949	00.86%	91 415		0.00270	40.7915	VEC
0.2.4.4.1	OSS-4 I MOS/Begion(%) <= 10 Seconds	Parity w Retail	99.96%	2,002,949	99.98%	91 415	-	0.00013	-1.3033	VES
D2443	OSS-4 I MOS/Begion(%) > 10 Seconds	Parity w Betail	0.04%	2,002,949	0.02%	91 415		0.00007	2.8228	VES
D2451	OSS-4 I MOSund/Begion(%) <= 4 Seconds	Parity w Retail	97.01%	1,520,638	96 15%	57,750		0.00072	11 9446	NO
D2452	OSS-4 I MOSund/Berjon(%) <= 10 Seconds	Parity w Retail	99.56%	1,520,638	99.33%	57,750		0.00072	8 2120	NO
D2453	OSS-4 (MOSupd/Begion(%) > 10 Seconds	Parity w Retail	0.44%	1,520,638	0.67%	57,750		0.00028	-8 2120	NO 1
D.2.4.6.1	OSS-4 LNP/Region(%) <= 4 Seconds	Parity w Retail	99.35%	141,409	98.78%	5.427		0.00111	5.1220	NO NO
D.2.4.6.2	OSS-4 LNP/Region(%) <= 10 Seconds	Parity w Retail	99.67%	141,409	99.67%	5,427		0.00079	0.0719	YES
D.2.4.6.3	OSS-4 LNP/Region(%) > 10 Seconds	Parity w Retail	0.33%	141,409	0.33%	5,427		0.00079	-0.0719	YES
D.2.4.7.1	OSS-4 MARCH/Region(%) <= 4 Seconds	Parity w Retail	30.53%	9,777	32.22%	360		0.02472	-0.6843	YES
D.2.4.7.2	OSS-4 MARCH/Region(%) <= 10 Seconds	Parity w Retail	30.53%	9,777	32.22%	360	1	0.02472	-0.6843	YES
D.2.4.7.3	OSS-4 MARCH/Region(%) > 10 Seconds	Parity w Retail	69.47%	9,777	67.78%	360		0.02472	0.6843	YES
D.2.4.8.1	OSS-4 OSPCM/Region(%) <= 4 Seconds	Parity w Retail	42.22%	7,897	35.53%	76		0.05693	1.1756	YES
D.2.4.8.2	OSS-4 OSPCM/Region(%) <= 10 Seconds	Parity w Retail	96.32%	7,897	94.74%	76		0.02171	0.7268	YES
D.2.4.8.3	OSS-4 OSPCM/Region(%) > 10 Seconds	Parity w Retail	3.68%	7,897	5.26%	76		0.02171	-0.7268	YES
D.2.4.9.1	OSS-4 Predictor/Region(%) <= 4 Seconds	Parity w Retail	16.63%	76,539	24.91%	3,757		0.00622	-13.3141	YES
D.2.4.9.2	OSS-4 Predictor/Region(%) <= 10 Seconds	Parity w Retail	16.63%	76,539	24.91%	3,757		0.00622	-13.3141	YES
D.2.4.9.3	OSS-4 Predictor/Region(%) > 10 Seconds	Parity w Retail	83.37%	76,539	75.09%	3,757		0.00622	13.3141	YES
D.2.4.10.1	OSS-4 SOCS/Region(%) <= 4 Seconds	Parity w Retail	99.80%	253,250	99.88%	13,680		0.00040	-2.2163	YES
D.2.4.10.2	OSS-4 SOCS/Region(%) <= 10 Seconds	Parity w Retail	99.96%	253,250	99.99%	13,680		0.00017	-1.3468	YES
D.2.4.10.3	OSS-4 SOCS/Region(%) > 10 Seconds	Parity w Retail	0.04%	253,250	0.01%	13,680		0.00017	1.3468	YES
D.2.4.11.1	OSS-4 NIW/Region(%) <= 4 Seconds	Parity w Retail	82.70%	79,013	82.44%	3,616		0.00643	0.4080	YES
D.2.4.11.2	OSS-4 NIW/Region(%) <= 10 Seconds	Parity w Retail	99.55%	79,013	99.64%	3,616		0.00114	-0.8094	YES
D.2.4.11.3	OSS-4 [NIW/Region(%) > 10 Seconds	Parity w Retail	0.45%	79,013	0.36%	3,616		0.00114	0.8094	YES

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Florida, June 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
COLLOCATION - Collocation									
Average Response Time C-1 Virtual/FL (calendar davs)	<= 15 davs			4	3				YES

E.1.1.1	C-1	Virtual/FL (calendar days)	<= 15 days	4	3		YES
E.1.1.2	C-1	Physical-Caged/FL (calendar days)	<= 15 days	6	10		YES
E.1.1.3	C-1	Physical-Cageless/FL (calendar days)	<= 15 days	6	40		YES
	Avera	ige Arrangement Time				•	
E.1.2.1	C-2	Virtual/FL (calendar days)	<= 60 days				
E.1.2.2	C-2	Virtual-Augments/FL (calendar days)	<= 60 days	54	2		YES
E.1.2.3	C-2	Virtual-Augments - Additional Space Required/FL (calendar days)	<= 60 days				
E.1.2.4	C-2	Physical Caged-Ordinary/FL (calendar days)	<= 90 days				
E.1.2.5	C-2	Physical Caged-Augments/FL (calendar days)	<= 45 days	26	6		YES
E.1.2.6	C-2	Physical Caged-Augments - Additional Space Required/FL (calendar days)	<= 90 days				
E.1.2.7	C-2	Physical Cageless/FL (calendar days)	<= 90 days	67	3		YES
E.1.2.8	C-2	Physical Cageless-Augments/FL (calendar days)	<= 45 days	10	24		YES
E.1.2.9	C-2	Physical Cageless-Augments - Additional Space Required/FL (calendar days)	<= 90 days	91	7		YES
	% Du	e Dates Missed					
E.1.3.1	C-3	Virtual/FL (%)	< 5% missed	0.00%	2		YES
E.1.3.2	C-3	Physical/FL (%)	< 5% missed	0.00%	40		YES

Florida, June 2001

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Ger	neral - Flow Through				······
% F	low Through Service Requests				
0-3	Summary/Region(%)	Diagnostic	88.16%	230,255	
0-3	Aggregate/Region(%)	Diagnostic	88.16%	230,255	
0-3	Residence/Region(%)	>= 95%	92.21%	172.960	
0-3	Business/Region(%)	>= 90%	57.26%	6.507	-
0-3	UNE/Region(%)	>= 85%	78.33%	50,788	
% F	low Through Service Requests - Achieved	_			
0-3	Summary/Region(%)	Diagnostic	72.88%	278,519	0
O-3	Aggregate/Region(%)	Diagnostic	72.88%	278,519	C
O-3	Residence/Region(%)	Diagnostic	79.67%	200,170	
0-3	Business/Region(%)	Diagnostic	41.13%	9.059	
0-3	UNE/Region(%)	Diagnostic	57.41%	69,290	
<u>%</u> F	low Through Service Requests - LNP	-			
0-3	Summary/Region(%)	>= 85%	91.83%	8,854	
0-3	Aggregate/Region(%)	>= 85%	91.83%	8,854	
0-3	Residence/Region(%)	Diagnostic			~~
0-3	Business/Region(%)	Diagnostic			
Ger	neral - Pre-Ordering				
Loo	p Makeup Inquiry (Manual)				
PO-	1 Loops/FL(%)	>= 95% w in 3 bus days	95.00%	136	
Loc	p Makeup Inquiry (Electronic)	-			
PO	2 [Loops/FL(%)	>= 95% w in 5 min	100.00%	1,842	
Ger	neral - Ordering				
Ser	vice Inquiry with Firm Order				
0-1	0 IxDSL (ADSL HDSL and UCLVEL(%)	>= 95% w in 5 bus days	93.00%	234	
0-1	0 Local Interoffice Transport/FL(%)	>= 95% w in 5 bus days	100.00%	0 1	
Ger	neral - Ordering				
Ave	rane Speed of Answer			<u></u>	· · · · · · · · · · · · · · · · · · ·
0-1	2 [Region(seconds)	Parity w Retail	134.12 6,948,605 65.30	33,796	
Ger	neral - Maintenance Center	······································			
Ave	erage Answer Time	_			
M&	R-6 Region(seconds)	Parity w Retail	143.87 1,829,998 28.66	107,969	
Ger	neral - Operator Services (Toll)				
Ave	erage Speed to Answer				
OS	1 FL(seconds)	PBD	3.81		
% A	Answered in 10 seconds	•••		_	
os		PBD	98.00%		
Ger	neral - Directory Assistance	······································			·
Ave	erage Speed to Answer				
DA-	1 (FL(seconds)	PBD	4.94		
_					

Benchmark /

Analog

BST

Measure

BST

Volume

CLEC

Measure

CLEC

Volume

Standard Standard

Error

Deviation

ZScore Jun-01 Equity

	Florida, June 2001	Benchmark / Ánalog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
F.7.2	% Answered in 10 seconds DA-2	PBD			95.90%					PBD
	General - E911									
F.8.1	Mean Interval E-3 JFL(hours)	PBD			1.18	1,118				PBD
F.8.2	% Accuracy E-2 [FL(%)	PBD			94.93%	583,595		,		PBD
F.8.3	% Timeliness E-1 [FL(%)	PBD			100.00%	1,118				PBD
	General - Billing	-		<u>.</u>						
F.9.1	Usage Data Delivery Accuracy B-3 [Region(%)	Parity w Retail	99.65%	6,064	100.00%	14,967		0.00089	-3.8726	YES
F.9.2	Usage Data Detivery Timeliness B-5 [Region(%)	Parity w Retail	97.39%	36,844	98.21%	193,986,433		0.00083	-9.8353	YES
F.9.3	Usage Data Delivery Completeness B-4 [Region(%)	Parity w Retail	99.78%	36,844	99.94%	193,986,433		0.00024	-6.6806	YES
F.9.4										
F.9.5.1 F.9.5.2 F.9.5.3	B-7 [Resate/FL(%)] B-7 UNE/FL(%) B-7 Interconnection/FL(%)	Parity w Retail >≈ 90% >= 90%	80.29%	\$19,572,491	97.02% 79.19% 92.85%	\$735,791 \$498,059 \$15,378		0.00106	-157.2729	YES NO YES
F.9.6.1 F.9.6.2 F.9.6.3	Non-Recurring Charge Completeness B-8 Resale/FL(%) B-8 UNE/FL(%) B-8 Interconnection/FL(%)	Parity w Retail >= 90% >= 90%	90.78%	\$21,294,222	96.72% 98.54% 78.34%	\$683,576 \$1,789,802 \$1,013,881		0.00117	-50.6671	YES YES NO
	General - Change Management			<u> </u>						
F.10.1	% Software Release Notices Sent On Time CM-1 _FL(%)	>= 98% w in 30 days	100.00%	- 1						YES
F.10.2	Average Software Release Notice Delay Days CM-2 FL(average)	>= 25 bus days prior to release	C							
F.10.3	% Change Management Documentation Sent On Time	>= 98% w in 30 days	<u>-</u>							
F.10.5	Average Documentation Release Delay Days CM-4 [FL(average)	>= 25 bus days prior to release	<u></u>							
F.10.6	% CLEC Interface Outages Sent within 15 Minutes CM-5 [FL(%)	>= 97% w in 15 min			100.00%	29				YES
	General - New Business Requests									
F.11.1	% New Business Requests Processed within 30 Business Days BFH-1 [Region(%)	>= 90% w in 30 bus days			100.00%	4				YES
F.11.2.1 F.11.2.2	% Quotes Provided within X Business Days BFR-2A Region(%) BFR-2B Region(%)	>= 90% w m 10 bus days >= 90% w m 30 bus days			0.00%	2				NO
	Florida, June 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Jun-01 Equity
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F.11.2.3	BFR-2C Region(%)	>= 90% w in 60 bus days			100.00%	2				YËS
	General - Ordering									
	Acknowledgement Message Timeliness									
F.12.1.1 F.12.1.2	0-1 EDI/Region(%) D-1 TAG/Region(%)	>= 90% w in 30 min >= 95% w in 30 min			96.90% 99.96%	58,137 127,390				YES
	Acknowledgement Message Completeness							_		
F.12.2.1 F.12.2.2	O-2 [EDI/Region(%) O-2 TAG/Region(%)	100% 100%		_	97.14% 99.96%	58,137 127,390				NO NO
	General - Database Updates									<u> </u>
	Average Database Update Interval							_		
F.13.1.1	D-1 JLIDB/FL(hours)	PBD	1.19	21	1.19	21				PBD
F.13.1.2	D-1 Directory Listings/FL(hours)	PBD	0.12	26	0.12	26				PBD
F.13.1.3	U-1 [Directory Assistance/-L(nours)	PBD	3.28	25	3.28	25				PBD
	% Update Accuracy									
F.13.2.1	D-2 LIDB/FL(%)	>= 95%			100.00%	62				Yes
F.13.2.2 F 13.2.3	D-2 Directory Listings/FL(%)	>= 35%			100.00%	135	-			VES
1.10.2.0					10010070			_		
F.13.3	% NXX67 Letite Loaded by LEHG Energive Date D-3 [FL(%)]	100%			100.00%	46				YES
	General - Network Outage Notification									
	Mean Time to Notify CLEC of Major Network Outages									
F.14.1	M&R-7 JFL (minutes)	Parity w Retail	102	3	4,013	3				NO

BellSouth Monthly Performance Summary

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	ACHIEVED	ADJUSTED FLOW-
	FLOW-THROUGH %	THROUGH %
CLEC AGGREGATE		
REGION ALL SERVICES	72.88%	88.16%
	FLOW-THROUGH %	
BST AGGREGATE	· · ·	
REGION		
- RETAIL RESIDENCE	94.40%	
- RETAIL BUSINESS**	TBD	
**NOTE: BellSouth is reinstituting the reporting of busi Public Service Commission. BellSouth currently has no Operating System (ROS) interface used by business re- service requests submitted from all sources, including of an accurate report and will reflect this measure as se	ness retail flow through as di o way to measure flow throug tail. BellSouth retail reports c manually. BellSouth has initi oon as its development is con	rected by the Georgia h for the Regional apture all business ated the development nplete

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PF	ROCESSING								FLOWT	IROUGH
						L	ESOG									
		м	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#1		0	115	0	115	32	28	0	55	22	6	16	33	46.48%	60.00%	84.62%
#2		52	0	0	52	3	8	0	41	11	8	3	30	73.17%	73.17%	78.95%
#3		0	3454	0	3454	570	666	0	2218	1967	151	1816	251	25.82%	11.32%	62.44%
#4		976	0	0	976	71	86	16	803	219	107	112	584	76.64%	72.73%	84.52%
#5		12	0	0	12	0	2	0	10	0	0	0	10	100.00%	100.00%	100.00%
#6		0	19	0	19	5	2	0	12	6	6	0	6	35.29%	50.00%	50.00%
#7		0	21	0	21	5	2	0	14	6	3	3	8	50.00%	57.14%	72.73%
#8		30	0	0	30	2	3	7	18	11	10	1	7	36.84%	38.89%	41.18%
#0		915	0	0	915	107	112	0	696	38	28	10	658	82.98%	94.54%	95.92%
#10		164	0	0	164	28	8	2	126	60	51	9	66	45.52%	52.38%	56.41%
#10		1797	0	- <u></u>	1797	228	182	3	1384	190	161	29	1194	75.43%	86.27%	88.12%
#12	·	2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#13		0	0	13	13	2	1	0	10	4	3	1	6	54.55%	60.00%	66.67%
#10	+	2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#15		0	0	17	17	13	3	0	1	1	1	0	0	0.00%	0.00%	0.00%
#15		2250	0	0	2259	290	285	36	1648	374	255	119	1274	70.04%	77.31%	83.32%
#10		350	0	0	359	19	28	1	311	25	23	2	286	87.20%	91.96%	92 56%
#10		286	0	0	286	36	32	3	215	72	66	6	143	58.37%	66.51%	68 42%
#10		1382		0	1382	136	49	5	1192	76	62	14	1116	84 93%	93.62%	94.74%
#13		0	0	6	6	0	2	0	4	3	3	0	1	25.00%	25.00%	25.00%
#20		38	0	0	38	15	7	2	14	9	8	1	5	17 86%	35 71%	38.46%
#22		0	0	2	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#23		0	0	1477	1477	209	253	8	1007	427	349	78	580	50.97%	57 60%	62.43%
#23		107	0	0	197	37	15	2	143	9	8	1	134	74 86%	93 71%	94 37%
#25		17	0	- <u> </u>	17	2	3	1	11	2	2	0	9	69.23%	81.82%	81.82%
#26		76	0	0	76	- 5	15	2	54	31	28	3	23	41.07%	42.59%	45.10%
#20			0	900	900	130	133	1	636	256	219	37	380	52.13%	59.75%	63.44%
#28		0	0	18	18	1	1	0	16	16	7	9	0	0.00%	0.00%	0.00%
#20			- <u> </u>	23	23	4	4	2	13	6	2	4	7	53.85%	53 85%	77.78%
#29		100		0	100	10	10	2	78	31	24	7	47	58.02%	60.26%	66 20%
#30		54		+	54	13	4	0	37	2	2	 	35	70.00%	94 59%	94 59%
#31		- 34		1007	1097	291	246	11	7/0	345	242	103	404	43 58%	53 04%	62 54%
#32				1207	010	160	240	0	143 96	2010	10	1	A 4	3 10%	23 08%	24 00%
#33		0	212		212	400	114	2	20	110	90	30	190	24 619/	63 21%	70.26%
#34	l	914		0	914	499	114	2	299	110			109	£0.00%	50,00%	50.00%
#35		2	0	0	2	0	0	0	2	1	170	40	1	50.00%		60.40%
#36		0	0	780	780	120	155	9	496	222	1/9	43	2/4	47.82%	50.41%	DU.49%
#37		0	0	554	554	112	104	11	327	136	115	21	191	45.69%	58.41%	62.42%

AGGREGATE ORDER TYPES		<u> </u>														
Company Info						LSR PF	ROCESSING								FLOWT	HROUGH
	-					L	ESOG									[
		M	echanized	Interface	Used	Manual	Rejects		Validated		Errors					
			T	r	1	Total		Pending		Total	I I	CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#38		273	0	0	273	38	14	0	221	20	15	5	201	79.13%	90.95%	93.06%
#39		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%
#40		524	0	0	524	130	25	0	369	15	12	3	354	' 71.37%	95.93%	96.72%
#41		479	0	0	479	51	48	9	371	108	79	29	263	66.92%	70.89%	76.90%
#42		0	198	0	198	25	16	8	149	69	53	16	80	50.63%	53.69%	60.15%
#43		4	0	0	4	3	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#44		17	0	0	17	1	1	0	15	1	1	0	14	87.50%	93.33%	93.33%
#45		0	0	32	32	10	2	0	20	4	4	0	16	53.33%	80.00%	80.00%
#46		65	0	0	65	8	13	0	44	5	3	2	39	78.00%	88.64%	92.86%
#47		333	0	0	333	24	29	1	279	30	20	10	249	84.98%	89.25%	92.57%
#48		704	0	0	704	175	57	6	466	144	121	23	322	52.10%	69.10%	72.69%
#49		2090	0	0	2090	440	164	7	1479	403	324	79	1076	58.48%	72.75%	76.86%
#50		52	0	0	52	12	4	1	35	9	6	3	26	59.09%	74.29%	81.25%
#51		65	0	0	65	15	12	0	38	9	9	0	29	54.72%	76.32%	76.32%
#52		571	0	0	571	55	6	0	510	4	4	0	506	89.56%	99.22%	99.22%
#53		48	0	0	48	0	0	1	47	47	46	1	0	0.00%	0.00%	0.00%
#54		514	0	0	514	28	25	1	460	28	26	2	432	88.89%	93.91%	94.32%
#55		36	0	0	36	3	1	1	31	4	2	2	27	84.38%	87.10%	93.10%
#56		0	0	29	29	11	5	1	12	9	1	8	3	20.00%	25.00%	75.00%
#57		711	0	0	711	93	115	1	496	84	63	21	412	/2.54%	83.06%	86.74%
#58		0	0	11	11	0	2	2	7	7	3	4	0	0.00%	0.00%	0.00%
#59		112	0	0	112	8	10	9	85	/9	54	25	6	8.82%	7.06%	10.00%
#60		26	0	0	26	3	2	5	16	13	4	9	3	30.00%	18.75%	42.80%
#61		1083	0	0	1083	162	88	5	828	/9	64	15	/49	76.82%	90.46%	92.13%
#62		29	0	0	29	4	4		21	2	2		19	/6.00%	90.48%	50 50%
#63		0	0	581	581	93	87		400	1/5	159	10	225	47.17%	50.25%	94.07%
#64		520	0	0	520	48	43	4	425	82	60	1/	343	75.22%	00.71%	04.07 /0
#65		2076	0	0	2076	276	131	9	1660	142	121	21	1518	19.21%	91.45%	92.02 /0
#66		1548	0	0	1548	67	249	6	1226	253	156	9/	973	81.35%	79.36%	00.10%
#67		184	0	0	184	13	15	0	156	8			148	88.10%	94.87%	95.46%
#68		0	0	2	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#69		105	0	0	105	30	18	0	57	16	10	6	41	50.62%	<u>/1.93%</u>	60.39%
#70		0	5	0	5	3	0	0	2	1		0		20.00%	50.00%	0.00%
#71		3	0	0	3	0	3	0	0	0	0	0	0	0.00%	0.00%	50.00%
#72		2	0	0	2	0	0	0	2			0	$+$ $\frac{1}{2}$	50.00%	50.00%	100.00%
#73		0	4	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	66 670/
#74	l l	0	0	33	33	7	12	2	12	4	4	0	8	42.11%	66.67%	00.01%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
						L	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps	I SP'a	System	BST Caused	Caused	leeuod SO's	Achieved	Base	Excluded Calculation
Name	RESH / OCN	LENS	EDI	TAG	LSR'S	Fallout	Clarification	(Z Status)	LORS	Failout		ranout	155060 50 5	1 iowanougii	Culculation	Culculation
#75		44	0	0	44	4	11	2	27	9	8	1	18	60.00%	66.67%	69.23%
#76		83	0	0	83	9	10	0	64	23	13	10	41	65.08%	64.06%	/5.93%
#77		74	0	0	74	12	10	4	48	18	16	2	30	51.72%	62.50%	65.22%
#78		72	0	0	72	14	5	0	53	15	14	1	38	57.58%	/1./0%	13.08%
#79		0	0	13	13	2	6	0	5	3	3	0	2	28.57%	40.00%	40.00%
#80	ļ	11	0	0	11	3	4	0	4	1	1	0	3	42.86%	75.00%	70.00%
#81		86	0	0	86	18	13	5	50	16	13	3	34	52.31%	08.00%	12.34%
#82		793	0	0	793	130	50	6	607	55	39	16	552	76.56%	90.94%	93.40%
#83		0	0	105	105	11	6	0	88	34	28	6	54	58.06%	61.36%	00.17%
#84	1	129	0	0	129	18	6	1	104	20	17	3	84	70.59%	80.77%	83.17%
#85		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#86		0	0	2	2	1	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#87		31	0	0	31	4	1	1	25	12	1	11	13	12.22%	52.00%	92.80%
#88	1	0	0	1	1	0	0	0			1	0	0	0.00%	0.00%	0.00%
#89		27	0	0	27	1	5	1	20	3	3	0	<u> </u>	80.95%	85.00%	100.00%
#90		11	0	0	11	1	1	0	9	0	0	0	9	90.00%	100.00%	00.51%
#91		294	0	0	294	32	27	0	235	19	15	4	216	82.13%	91.91%	93.31%
#92		14	0	0	14	3	2	2		5	3	2	2	25.00%	28.57%	40.00%
#93		0	0	2	2	0	0	0	2	2	2	0	0	0.00%	0.00%	0.00%
#94		38	0	0	38	6	2	0	30	8	4	4	22	68.75%	73.33%	70 179/
#95		195	0	0	195	42	8	5	140	50	33	17	90	54.55%	64.29%	FE 02%
#96		0	0	241	241	35	26	1	179	94	6/	21	85	45.45%	47.49%	00.01%
#97		235	0	0	235	17	26	7	185	35	24	11	150	/8.53%	81.08%	66.67%
#98		90	0	0	90	35	12		42	16	13	3	26	35.14%	61.90%	00.07 /6
#99		93	0	0	93	10	0	0	83	6	6	0		82.80%	92.11%	92.11%
#100		9	0	0	9	0	0	0	9	3	2		6	75.00%	66.67%	75.00%
#101		0	6	0	6	2	0	0	4	4	1	3	0	0.00%	0.00%	0.00%
#102		3	0	0	3	2	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#103		228	0	0	228	30	15	0	183	10	6	4	173	82.78%	94.54%	90.03%
#104		26	0	0	26	3	3	0	20	5	3	2	15	71.43%	75.00%	83.33%
#105		14	0	0	14	0	0	0	14	2	2	0	12	85.71%	85.71%	85.71%
#106		611	0	0	611	83	30	2	496	48	39	9	448 -	78.60%	90.32%	91.99%
#107		0	24	0	24	11	0	2	11	11	10	1	0	0.00%	0.00%	07.47%
#108		332	0	0	332	35	18	0	279	9	7	2	270	86.54%	96.77%	91.41%
#109		0	0	604	604	27	74	0	503	19	14	5	484	92.19%	96.22%	97.19%
#110		468	0	0	468	49	66	2	351	26	21	5	325	82.28%	92.59%	93.93%
#111		172	0	0	172	25	74	1	72	9	5	4	63	67.74%	87.50%	92.65%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	ROCESSING								FLOWT	IROUGH
						L	ESOG									
		М	echanized	Interface I	Jsed	Manual	Rejects		Validated		Errors					
	· · · · · · · · · · · · · · · · · · ·		I			Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps	1.000	System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSH's	Fallout	Clarification	(Z Status)	LSH'8	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#112		97	0	0	97	9	0	0	88	2	2	0	86	88.66%	97.73%	97.73%
#113		4	0	0	4	0	1	0	3	3	3	0	0	0.00%	0.00%	0.00%
#114		1000	0	0	1000	103	88	2	807	43	32	11	764	' 84.98%	94.67%	95.98%
#115		293	0	0	293	41	8	0	244	23	20	3	221	78.37%	90.57%	91.70%
#116		163	0	0	163	37	8	2	116	18	16	2	98	64.90%	84.48%	85.96%
#117		0	0	28	28	7	2	0	19	3	2	1	16	64.00%	84.21%	88.89%
#118		36	0	0	36	1	3	0	32	3	2	1	29	90.63%	90.63%	93.55%
#119		0	0	777	777	11	63	0	703	25	16	9	678	96.17%	96.44%	97.69%
#120		449	0	0	449	124	85	2	238	36	27	9	202	57.22%	84.87%	88.21%
#121		212	0	0	212	28	13	1	170	15	15	0	155	78.28%	91.18%	91.18%
#122		440	0	0	440	47	24	0	369	10	5	5	359	87.35%	97.29%	98.63%
#123	1	686	0	0	686	17	115	0	554	65	58	. 7	489	86.70%	88.27%	89.40%
#124		0	0	39	39	12	0	2	25	9	9	0	16	43.24%	64.00%	64.00%
#125		0	114	0	114	31	9	0	74	27	20	7	47	47.96%	63.51%	70.15%
#126		49	0	0	49	9	3	0	37	12	4	8	25	65.79%	67.57%	86.21%
#127		72	0	0	72	10	2	2	58	17	17	0	41	60.29%	70.69%	70.69%
#128		6	0	0	6	0	2	0	4	1	1	0	3	75.00%	75.00%	75.00%
#129		250	0	0	250	48	29	0	173	36	32	4	137	63.13%	79.19%	81.07%
#130		31	0	0	31	2	2	0	27	4	3	1	23	82.14%	85.19%	88.46%
#131		33	0	0	33	3	2	0	28	5	2	3	23	82.14%	82.14%	92.00%
#132	<u> </u>	2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#133		0	58	0	58	14	7	0	37	10	3	1	27	61.36%	72.97%	90.00%
#134		26	0	0	26	- /	4	0	15	6	0	6	9	56.25%	60.00%	100.00%
#135		135	0	0	135	11	1	0	123	2	2	0	121	90.30%	98.37%	98.37%
#136		237	0	0	237	56	9	0	172	8	8	0.	164	71.93%	95.35%	95.35%
#137		1561	0	0	1561	132	47	0	1382	72	58	14	1310	87.33%	94.79%	95.76%
#138		0	0	882	882	143	13	/8	648	489	433	56	159	21.63%	24.54%	26.86%
#139		880	0	0	880	82	28	1	769	71	66	5	698	82.51%	90.77%	91.36%
#140		1646	0	0	1646	133	95	5	1413	79	53	26	1334	87.76%	94.41%	96.18%
#141		3805	0	0	3805	536	193	37	3039	201	162	39	2838	80.26%	93.39%	94.60%
#142		86	0	0	86	6	4	1	75	12	9	3	63	80.77%	84.00%	87.50%
#143		41	0	0	41	4	9	0	28	2	2	0	26	81.25%	92.86%	92.86%
#144	ļ	0	25	0	25	0	6	3	16	16	0	16	0	0.00%	0.00%	0.00%
#145		4	0	0	4	0	2	0	2	2	1	1	0	0.00%	0.00%	0.00%
#146		34	0	0	34	5	4	2	23	11	5	6	12	54.55%	52.17%	/0.59%
#147		1.	0	0	1	0	1	0	0	0	0	0	<u> </u>	0.00%	0.00%	0.00%
#148		33	0	0	33	16	6	0	11	7	6	1	4	15.38%	36.36%	40.00%

AGGREGATE ORDER TYPES																
Company Info	1					LSR PF	OCESSING								FLOWTI	HROUGH
						L	ESOG									
		M	echanized	Interface I	Used	Manual	Rejects		Validated		Errors					
						Total		Pending		Total		CLEC	1			CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SU's	Flowthrough	Calculation	Calculation
#149		213	0	0	213	16	9	0	188	8	8	0	180	88.24%	95.74%	95.74%
#150		196	0	0	196	19	20	0	157	9	7	2	148	85.06%	94.27%	95.48%
#151		2	0	0	2	2	0	0	0	0	0	0	0	' 0.00%	0.00%	0.00%
#152		506	0	0	506	66	41	3	396	26	15	11	370	82.04%	93.43%	96.10%
#153		83	0	0	83	1	1	0	81	6	3	3	75	94.94%	92.59%	96.15%
#154		106	0	0	106	21	11	0	74	20	13	7	54	61.36%	72.97%	80.60%
#155		0	0	397	397	70	30	4	293	70	61	9	223	62.99%	76.11%	78.52%
#156		966	0	0	966	138	74	10	744	131	105	26	613	/1.61%	82.39%	85.38%
#157		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#158		0	0	13	13	0	0	0	13	8	3	5	5	62.50%	38.46%	62.50%
#159		230	0	0	230	28	21	0	181	6	3	3	1/5	84.95%	96.69%	98.31%
#160		424	0	0	424	203	30	8	183	89	68	21	94	25.75%	51.37%	58.02%
#161		55	0	0	55	1	1	0	53	5	5	0	48	88.89%	90.57%	90.37%
#162		0	0	5	5	0	1	0	4	1	1	0	3	75.00%	75.00%	75.00%
#163		38	0	0	38	2	1	1	34	4	4	0	30	83.33%	88.24%	50.24% 50.00%
#164		23	0	0	23	3	3	0	17	9	8	1	8	42.11%	47.06%	06.01%
#165		1441	0	0	1441	215	109	1	1116	57	44	13	1059	80.35%	94.89%	30.01%
#166		214	0	0	214	42	23	5	144	49	40	9	95	53.67%	05.97%	100.00%
#167		14	0	0	14	2	1	0	11	2	0	2	9	81.82%	61.62%	EE 249/
#168		111	0	0	111	23	6	4	78	38	31		40	42.55%	01.28%	15 299/
#169		0	0	15	15	0	0	0	15	13	11	2	2	10.38%	04 34%	01.26%
#170		0	0	381	381	20	162	1	198	31	10	15	107	00.00%	07.70%	08.06%
#171		952	0	0	952	65	61	0	826	19	16	3	807	90.00%	97.70%	90.00%
#172		780	0	0	780	84	129	1	566	68	60	8	498	11.51%	67.44%	60.05%
#173		75	0	0	75	20	10	2	43	14	13	1	29	46.77%	07.44%	09.05%
#174		128	0	0	128	18	15	0	95	11	9	2	84	73.06%	00.42%	90.32 /6
#175		7236	0	0	7236	486	572	10	6168	401	311	90	5/6/	87.86%	93.50%	94.08 /6
#176		342	0	0	342	30	27	1	284	37	36	- 1	247	78.91%	00.97%	07.20%
#177		1272	0	0	1272	112	76	3	1081	82	80	2	999	83.88%	92.41%	100.00%
#178		0	0	117	117	6	15	0	96	0	0	0	96	94.12%	100.00%	05.60%
#179		213	0	0	213	95	26	0	92	5	4		8/	40.//%	94.0/%	50.00%
#180		45	0	0	45	13	3	0	29	16	13	3	13	33.33%	44.03%	02 219/
#181		32	0	0	32	2	4	0	26	2	2		24	85.71%	92.31%	92.31%
#182		203	0	0	203	32	2	1	168	8	6	2	160	80.81%	95.24%	90.39%
#183		414	0	0	414	103	61	0	250	26	13	13	224	<u>~~888.cd</u>	09.00%	100.00%
#184		1	0	0	1	0	0	0	1	0	0	0	1	100.00%		04.60%
#185		147	0	0	147	25	9	0	113	6	6	0	107	77.54%	94.69%	94.69%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	ROCESSING								FLOWTI	IROUGH
						L	ESOG									
		м	echanized	Interface (Jsed	Manual	Rejects		Validated		Errors					
	-					Total		Pending		Total		CLEC				CLEC Error
				740	Total Mech	Manual	Auto	Supps	L C Dia	System	BST Caused	Caused	looved 60's	Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LORS	Failout	Ciarmeauon		Lans	Fallout	Fallout	Fallout	Issued SU's	Flowthough	Calculation	Calculation
#186		0	0	9999	9999	5052	1320	96	3531	1548	1105	443	1983	24.36%	56.16%	64.22%
#187		8434	0	0	8434	447	820	38	7129	735	565	170	6394	86.34%	89.69%	91.88%
#188		2069	0	0	2069	281	105	6	1677	163	107	56	1514	* 79.60%	90.28%	93.40%
#189		77	0	0	77	2	20	2	53	39	22	17	14	36.84%	26.42%	38.89%
#190		4	0	0	4	0	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
#191		43	0	0	43	2	7	0	34	14	7	7	20	68.97%	58.82%	74.07%
#192		2403	0	0	2403	145	250	2	2006	137	77	60	1869	89.38%	93.17%	96.04%
#193		1215	0	0	1215	86	75	1	1053	59	44	15	994	88.43%	94.40%	95.76%
#194		96	0	0	96	5	16	0	75	4	4	0	71	88.75%	94.67%	94.67%
#195		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#196		120	0	0	120	52	9	1	58	18	16	2	40	37.04%	68.97%	71.43%
#197		10	0	0	10	3	1	0	6	3	3	0	3	33.33%	50.00%	50.00%
#198		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#199		0	0	3	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#200		65	0	0	65	19	15	0	31	12	12	0	19	38.00%	61.29%	61.29%
#201		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#202		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#203		4	0	0	4	0	0	0	4	2	2	0	2	50.00%	50.00%	50.00%
#204		0	0	23	23	7	8	0	8	4	2	2	4	30.77%	50.00%	66.67%
#205		195	0	0	195	30	34	1	130	26	23	3	104	66.24%	80.00%	81.89%
#206		74	0	0	74	7	5	0	62	5	4	1	57	83.82%	91.94%	93.44%
#207		10	0	0	10	0	2	3	5	3	3	0	2	40.00%	40.00%	40.00%
#208		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#209		30	0	0	30	0	2	0	28	23	12	11	5	29.41%	17.86%	29.41%
#210		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#211		4	0	0	4	0	0	0	4	1	0	1	3	100.00%	75.00%	100.00%
#212		333	0	0	333	92	34	1	206	81	70	11	125	43.55%	60.68%	64.10%
#213		22	0	0	22	4	9	1	8	7	4	3	11	11.11%	12.50%	20.00%
#214		0	0	2	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#215		5	0	0	5	0	0	0	5	3	2	1	2	50.00%	40.00%	50.00%
#216		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#217		135	0	0	135	35	16	2	82	44	35	9	38	35.19%	46.34%	52.05%
#218		47	0	0	47	9	4	0	34	30	25	5	4	10.53%	11.76%	13.79%
#219		96	0	0	96	24	5	0	67	5	4	1	62	68.89%	92.54%	93.94%
#220		7	0	0	7	0	1	0	6	0	0	0	6	100.00%	100.00%	100.00%
#221		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%
#222		0	0	982	982	126	58	15	783	82	42	40	701	80.67%	89.53%	94.35%

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AGGREGATE ORDER TYPES						-										
Company Info						LSR PF	ROCESSING								FLOWT	HROUGH
						L	ESOG		-							
		м	echanized	Interface l	Jsed	Manual	Rejects		Validated		Errors					
					T	Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps	1.00%	System	BST Caused	Caused	looved COlo	Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSH'8	Fallout	rallout	Fallout	Issued 50 s	FIOWINOUGH	Calculation	Calculation
#223		422	0	0	422	19	30	0	373	8	7	1	365	93.35%	97.86%	98.12%
#224		65	0	0	65	2	9	3	51	16	12	4	35	71.43%	68.63%	74.47%
#225		13	0	0	13	1	2	0	10	4	2	2	6	' 66.67%	60.00%	75.00%
#226		692	0	0	692	23	38	3	628	21	18	3	607	93.67%	96.66%	97.12%
#227		0	0	4028	4028	116	68	15	3829	277	221	56	3552	91.33%	92.77%	94.14%
#228		8803	0	0	8803	658	388	5	7752	278	243	35	7474	89.24%	96.41%	96.85%
#229		9	0	0	9	2	3	1	3	2	2	0	1	20.00%	33.33%	33.33%
#230		0	0	3341	3341	56	454	29	2802	1182	822	360	1620	64.85%	57.82%	66.34%
#231		0	61	0	61	26	11	0	24	4	4	0	20	40.00%	83.33%	83.33%
#232		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#233		30	0	0	30	14	9	0	7	3	2	1	4	20.00%	57.14%	66.67%
#234		24	0	0	24	13	4	1	6	5	5	0	1	5.26%	16.67%	16.67%
#235		779	0	0	779	87	31	7	654	43	34	9	611	83.47%	93.43%	94.73%
#236		45	0	0	45	6	7	0	32	8	8	0	24	63.16%	75.00%	75.00%
#237		735	0	0	735	65	16	1	653	17	15	2	636	88.83%	97.40%	97.70%
#238		389	0	0	389	49	23	1	316	16	10	6	300	83.57%	94.94%	96.77%
#239		831	0	0	831	58	62	0	711	46	36	10	665	87.62%	93.53%	94.86%
#240		242	0	0	242	22	39	9	172	75	52	23	97	56.73%	56.40%	65.10%
#241		238	0	0	238	8	10	1	219	39	28	11	180	83.33%	82.19%	86.54%
#242		914	0	0	914	95	79	8	732	93	82	11	639	78.31%	87.30%	88.63%
#243		177	0	0	177	18	46	2	111	69	53	16	42	37.17%	37.84%	44.21%
#244		49	0	0	49	0	2	2	45	44	28	16	1	3.45%	2.22%	3.45%
#245		0	0	929	929	20	95	1	813	11	8	3	802	96.63%	98.65%	99.01%
#246		82	0	0	82	1	8	1	72	8	6	2	64	90.14%	88.89%	91.43%
#247		338	0	0	338	47	13	1	277	27	15	12	250	80.13%	90.25%	94.34%
#248		65	0	0	65	34	9	0	22	15	11	4	7	13.46%	31.82%	38.89%
#249		24305	0	0	24305	7393	3409	108	13395	4104	3046	1058	9291	47.09%	69.36%	75.31%
#250		373	0	0	373	31	24	0	318	22	19	3	296	85.55%	93.08%	93.97%
#251		0	0	251	251	46	59	1	145	33	29	4	112	59.89%	77.24%	79.43%
#252		392	0	0	392	55	82	1	254	81	70	11	173	58.05%	68.11%	71.19%
#253		0	0	23	23	2	2	0	19	3	2	1	16	80.00%	84.21%	88.89%
#254		69	0	0	69	13	13	0	43	4	4	0	39	69.64%	90.70%	90.70%
#255		0	0	7	7	1	1	0	5	1	1	0	4	66.67%	80.00%	80.00%
#256		26	0	0	26	8	3	0	15	7	6	1	8	36.36%	53.33%	57.14%
#257		38	0	0	38	3	6	0	29	1	1	0	28	87.50%	96.55%	96.55%
#258		24	0	0	24	1	7	2	14	7	6	1	7	50.00%	50.00%	53.85%
#259		283	0	0	283	21	3	0	259	7	6	1	252	90.32%	97.30%	97.67%

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AGGREGATE ORDER TYPES																
Company Info						LSR PF	ROCESSING								FLOWT	IROUGH
						L	ESOG									
		M	echanized	Interface l	Jsed	Manual	Rejects		Validated		Errors					
· · · · · · · · · · · · · · · · · · ·						Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps	1.00	System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSH'S	Fallout	Fallout	Fallout	Issued SU's	Flowthrough	Calculation	Calculation
#260		30	0	0	30	2	0	0	28	0	0	0	28	93.33%	100.00%	100.00%
#261		124	0	0	124	26	6	3	89	27	22	5	62	56.36%	69.66%	73.81%
#262		3070	0	0	3070	226	358	2	2484	103	90	13	2381	88.28%	95.85%	96.36%
#263		0	520	0	520	384	56	0	80	48	30	18	32	7.17%	40.00%	51.61%
#264		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#265		12	0	0	12	0	6	0	6	5	3	2	1	25.00%	16.67%	25.00%
#266		0	0	14	14	7	3	0	4	1	1	0	3	27.27%	75.00%	75.00%
#267		36	0	0	36	8	2	1	25	6	4	2	19	61.29%	76.00%	82.61%
#268		0	1458	0	1458	74	103	0	1281	172	144	28	1109	83.57%	86.57%	88.51%
#269		2	0	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#270		0	0	61	61	30	7	0	24	14	11	3	10	19.61%	41.67%	47.62%
#271		49	0	0	49	6	4	0	39	14	13	1	25	56.82%	64.10%	65.79%
#272	ļ	0	18760	0	18760	3338	2248	7	13167	1960	1518	442	11207	69.77%	85.11%	88.07%
#273		10	0	0	10	2	4	0	4	3	0	3	1	33.33%	25.00%	100.00%
#274		6	0	0	6	0	3	0	3	1	1	0	2	66.67%	66.67%	66.67%
#275		2	0	0	2	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#276		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#277		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%
#278		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#279		4	0	0	4	0	0	0	4	0	0	0	4	100.00%	100.00%	100.00%
#280		0	399	0	399	249	121	2	27	27	7	20	0	0.00%	0.00%	0.00%
#281		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#282		17	0	0	17	4	0	1	12	5	3	2	7	50.00%	58.33%	70.00%
#283		0	607	0	607	398	91	4	114	44	31	13	70	14.03%	61.40%	69.31%
#284		26	0	0	26	2	3	0	21	14	7	7	7	43.75%	33.33%	50.00%
#285	l	65	0	0	65	0	5	3	57	21	12	9	36	75.00%	63.16%	/5.00%
#286		0	0	4	4	0	0	1	3	0	0	0	3	100.00%	100.00%	100.00%
#287		0	14	0	14	11	0	0	3	3	1	2	0	0.00%	0.00%	0.00%
#288		108	0	0	108	23	8	3	74	17	10	/	57	63.33%	77.03%	85.07%
#289	-	0	6225	0	6225	746	1222	3	4254	159	66	93	4095	83.45%	96.26%	98.41%
#290		204	0	0	204	14	10	0	180	11	9	2	169	88.02%	93.89%	94.94%
#291		52	0	0	52	30	5	1	16	6	3	3	10 .	23.26%	62.50%	/6.92%
#292		0	9087	0	9087	1496	1807	5	5779	301	134	167	5478	77.07%	94.79%	97.61%
#293		346	0	0	346	23	27	0	296	16	14	2	280	88.33%	94.59%	95.24%
#294		0	0	13	13	0	4	0	9	2	0	2	7	100.00%	77.78%	100.00%
#295	ļ	0	0	267	267	101	53	7	106	45	34	11	61	31.12%	57.55%	64.21%
#296	1	792	0	0	792	167	105	15	505	204	149	55	301	48.78%	59.60%	66.89%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES			1													
Company Info						LSR PF	ROCESSING								FLOWT	HROUGH
						L	ESOG									
		м	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
						Total		Pending	1	Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSH'8	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#297		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#298		8	0	0	8	5	1	0	2	2	2	0	0	0.00%	0.00%	0.00%
#299		2	0	0	2	2	0	0	0	0	0	0	0	· 0.00%	0.00%	0.00%
#300		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#301		482	0	0	482	31	32	4	415	62	50	12	353	81.34%	85.06%	87.59%
#302		184	0	0	184	28	13	1	142	24	20	4	118	71.08%	83.10%	85.51%
#303	<u> </u>	848	0	0	848	69	84	10	685	377	181	196	308	55.20%	44.96%	62.99%
#304		7	0	0	7	1	0	0	6	4	4	0	2	28.57%	33.33%	33.33%
#305		0	9257	0	9257	164	1781	3	7309	1844	1227	617	5465	79.71%	74.77%	81.66%
#306		25	0	0	25	0	5	4	16	14	12	2	2	14.29%	12.50%	14.29%
#307		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#308		279	0	0	279	13	38	0	228	24	22	2	204	85.36%	89.47%	90.27%
#309		35	0	0	35	5	7	1	22	12	7	5	10	45.45%	45.45%	58.82%
#310		0	0	25	25	2	4	0	19	5	1	4	14	82.35%	73.68%	93.33%
#311		35	0	0	35	14	0	0	21	15	11	4	6	19.35%	28.57%	35.29%
#312		0	0	36	36	0	5	1	30	11	8	3	19	70.37%	63.33%	70.37%
#313		26	0	0	26	10	2	2	12	7	6	1	5	23.81%	41.67%	45.45%
#314		491	0	0	491	18	27	0	446	18	14	4	428	93.04%	95.96%	96.83%
#315		36	0	0	36	4	4	0	28	2	2	0	26	81.25%	92.86%	92.86%
#316		5	0	0	5	2	1	0	2	0	0	0	2	50.00%	100.00%	100.00%
#317		0	0	8	8	0	1	0	7	4	4	0	3	42.86%	42.86%	42.86%
#318		18	0	0	18	5	3	1	9	4	1	3	5	45.45%	55.56%	83.33%
#319		10	0	0	10	0	0	0	10	2	2	0	8	80.00%	80.00%	80.00%
#320		916	0	0	916	216	174	6	520	243	190	53	277	40.56%	53.27%	59.31%
#321		2	0	0	2	0	1	0	1	1	1	0 ·	0	0.00%	0.00%	0.00%
#322		11	0	0	11	0	1	0	10	0	0	0	10	100.00%	100.00%	100.00%
#323		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#324		3386	0	0	3386	427	224	8	2727	224	193	31	2503	80.15%	91.79%	92.84%
#325		0	0	3412	3412	520	321	55	2516	339	220	119	2177	74.63%	86.53%	90.82%
#326		15101	0	0	15101	385	979	10	13727	253	198	55	13474	95.85%	98.16%	98.55%
#327		0	56	0	56	2	4	0	50	11	6	5	39	82.98%	78.00%	86.67%
#328		170	0	0	170	2	6	0	162	5	4	1	157	96.32%	96.91%	97.52%
#329		494	0	0	494	19	73	2	400	5	4	1	395	94.50%	98 .75%	99.00%
#330		0	0	3	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100.00%
#331		190	0	0	190	35	23	1	131	27	23	4	104	64.20%	79.39%	81.89%
#332		0	2192	0	2192	1076	260	11	845	235	129	106	610	33.61%	72.19%	82.54%
#333		221	0	0	221	75	20	1	125	53	44	9	72	37.70%	57.60%	62.07%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING								FLOWTI	IROUGH
						L	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
	· · · · · · · · · · · · · · · · · · ·					Total	·	Pending		Total	1	CLEC	1			CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SU's	Flowthrough	Calculation	Calculation
#334		0	0	33	33	10	10	0	13	2	2	0	11	47.83%	84.62%	84.62%
#335		59	0	0	59	17	12	1	29	13	8	5	16	39.02%	55.17%	66.67%
#336		0	0	9555	9555	277	701	16	8561	245	191	54	8316	94.67%	97.14%	97.75%
#337		3146	0	0	3146	408	211	19	2508	261	202	59	2247	78.65%	89.59%	91.75%
#338		706	0	0	706	75	63	13	555	146	124	22	409	67.27%	73.69%	76.74%
#339		135	0	0	135	19	7	2	107	42	31	11	65	56.52%	60.75%	67.71%
#340		0	417	0	417	210	74	6	127	80	58	22	47	14.92%	37.01%	44.76%
#341		615	0	0	615	99	38	5	473	120	100	20	353	63.95%	/4.63%	17.92%
#342		1061	0	0	1061	222	165	14	660	305	241	64	355	43.40%	53.79%	59.56%
#343		45	0	0	45	27	3	0	15	12	10	2	3	7.50%	20.00%	23.08%
#344		1038	0	0	1038	147	39	1	851	46	30	16	805	81.98%	94.59%	96.41%
#345		345	0	0	345	7	28	0	310	15	10	5	295	94.55%	95.16%	90.72%
#346		0	0	1370	1370	38	122	1	1209	23	19		1186	95.41%	98.10%	96.42%
#347		105	0	0	105	5	2	0	98	5	5	0	93	90.29%	94.90%	70 449/
#348		374	0	0	374	52	44	5	273	85	68	1/	188	61.04%	08.80%	73.44% 63.50%
#349		41	0	0	41	4	5	0	32	12	12	0	20	55.56%	02.30%	40 96%
#350		15	0	0	15	6	1	0	8	5	4	1	3	23.08%	37.50%	42.00%
#351		129	0	0	129	7	14	1	107	9	8	<u> </u>	98	80.73%	91.39%	92.40%
#352		1007	0	0	1007	88	57	1	861	82	/4	8	//9	82.78%	90.48%	91.32 %
#353		13	0	0	13	0	1	0	12	1	1	0	11	91.67%	91.07%	91.07%
#354		0	0	1527	1527	167	99	5	1256	148	118	30	1108	79.54%	04.50%	90.38%
#355		83	0	0	83	4	5	0	/4	4	4		1105	B9.74%	94.39%	97.07%
#356		0	2447	0	2447	854	195	1	1397	212	162	50	1185	03.84%	04.02%	70.99%
#357		3176	0	0	3176	440	1/3	1/	2546	581	495	00	1905	07.70%	20.00%	29.57%
#358		24	0	0	24	2	9	3	10	8	5	3	2	22.22%	20.00%	20.37 /8
#359		0	0	1787	1787	35	330	4	1418	14	12		1404	90.70%	99.01%	99.13 /6
#360		49	0	0	49	1	5	0	43	<u>b</u>	6	0	3/	84.09%	00.03%	25.00%
#361		4	0	0	4	0	0	0	4	3	3		1	25.00%	25.00%	25.00%
#362		579	0	0	579	39	38	1	501	23	16		4/8	89.08%	95.41%	50.70%
#363		100	0	0	100	22	11	0	67	29	25	4	38	44./1%	56.72%	51 25%
#364		0	169	0	169	115	11		40	21	18	3	19	12.50%	47.50%	0.00%
#365		1	0	0	1	1	0		0	0		0	0	0.00%	0.00%	95 71%
#366		0	0	13	13	6	0	0	$\frac{7}{2}$	1		U 0	6	40.15%	50.00%	66 67%
#367		12	0	0	12	0	2	2	8	4	2	2	4	06.01%	00.00%	00.07 /0
#368		0	0	485	485	10	76	0	399	4	3		395	96.81%	99.00%	77 70%
#369		21	0	0	21	2	11	0	18	4	4	0	14	/0.00%	70.000/	01 000/
#370		67	0	0	67	8	10	3	46	10	8	2	36	69.23%	/8.26%	01.02%

AGGREGATE ORDER TYPES																
Company Info					-	LSR PF	ROCESSING								FLOWTI	IROUGH
						Ľ	ESOG									
		M	echanized	Interface L	Jsed	Manual	Rejects		Validated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
			501	TAC	Total Mech	Manual Fallout	Auto	Supps (7 Status)		System	BST Caused	Caused	lequed SO's	Achieved	Base	Excluded
Name	HESH / OCN	LENS	EDI	TAG	Lons	Failout	Clarineadon	(Z Status)	Lons	Failout	Failout	Failout	155000 50 5	riowaliougii	Calculation	Calculation
#371		2418	0	0	2418	487	266	9	1656	688	566	122	968	47.90%	58.45%	63.10%
#372		463	0	0	463	82	24	1	356	34	31	3	322	74.02%	90.45%	91.22%
#373		53	0	0	53	12	2	0	39	0	0	0	39	' 76.47%	100.00%	100.00%
#374		136	0	0	136	2	24	4	106	39	12	27	67	82.72%	63.21%	84.81%
#375		323	0	0	323	22	10	1	290	17	13	4	273	88.64%	94.14%	95.45%
#376		46	0	0	46	7	0	0	39	8	7	1	31	68.89%	79.49%	81.58%
#377		0	0	58	58	2	8	0	48	3	3	0	45	90.00%	93.75%	93.75%
#378	-	101	0	0	101	16	3	0	82	3	3	0	79	80.61%	96.34%	96.34%
#379		0	4162	0	4162	547	804	2	2809	115	57	58	2694	81.69%	95.91%	97.93%
#380		170	0	0	170	8	5	0	157	28	27	1	129	78.66%	82.17%	82.69%
#381		0	0	186	186	62	49	6	69	49	41	8	20	16.26%	28.99%	32.79%
#382		9347	0	0	9347	1178	641	106	7422	1863	1646	217	5559	66.31%	74.90%	77.15%
#383		0	272	0	272	165	54	0	53	19	15	4	34	15.89%	64.15%	69.39%
#384		26	0	0	26	4	7	0	15	5	4	1	10	55.56%	66.67%	71.43%
#385		786	0	0	786	146	83	9	548	194	164	30	354	53.31%	64.60%	68.34%
#386		95	0	0	95	4	1	0	84	1	1	0	83	94.32%	98.81%	98.81%
#387		71	0	0	71	6	12	2	51	22	18	4	29	54.72%	56.86%	61.70%
#388		0	94	0	94	56	3	1	34	13	12	1	21	23.60%	61.76%	63.64%
#389		10	0	0	10	1	2	0	/	1	0	1	6	85.71%	85.71%	100.00%
#390		43	0	0	43	6	5	1	31	9	/	2	22	62.86%	70.97%	/5.86%
#391		448	0	0	448	37	15	1	395	31	25	6	364	85.45%	92.15%	93.57%
#392		3028	0	0	3028	518	230	1	2273	297	224	73	1976	/2.70%	86.93%	89.82%
#393		1	0	0		0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#394		40	0	0	40	5	11	0	24	9	8	1	15	53.57%	62.50%	65.22%
#395		94	0	0	94	2	8	0	84	28	23	5	56	69.14%	66.67%	70.89%
#396		27110	0	0	27110	3811	24/9	63	20/5/	1618	1364	254	19139	/8./2%	92.21%	93.35%
#397		13	0	0	13	2	0	0	11	4	3	1	/	58.33%	63.64%	70.00%
#398		82	0	0	82	51	10	0	21	6	3	3	15	21.74%	/1.43%	83.33%
#399		549	0	0	549	55	62	4	428	44	34	10	384	81.18%	89.72%	91.87%
#400		387	0	0	387	44	22	2	319	61	37	24	258	76.11%	80.88%	87.46%
#401		160	0	0	160	15	14	0	131	2	2	0	129	88.36%	98.47%	98.47%
#402		168	0	0	168	39	10	2	117	47	36	11	70	48.28%	59.83%	66.04%
#403		60	0	0	60	0	3	0	57	<u> </u>	1	0	56	98.25%	98.25%	98.25%
#404		272	0	0	272	9	26	0	237	12	9	3	225	92.59%	94.94%	96.15%
#405		122	0	0	122	3	7	0	112	6	5	1	106	92.98%	94.64%	95.50%
#406		167	0	0	167	17	11	3	136	19	18	1	117	76.97%	86.03%	86.67%
#407		89	0	0	89	5	11	0	73	4	3	1	69	89.61%	94.52%	95.83%

AGGREGATE ORDER TYPES				1			T			1						
						LSR PF	ROCESSING								FLOWT	HROUGH
company mic	1						ESOG								l	
			echanized	Interface i	lead	Manual	Rejects		Validated	·	Frors		.		· · · ·	
			Contamized			Total		Pending	Vandatou	Total		CLEC				CLEC Error
]		Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Failout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#408		0	2	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#409		523	0	0	523	77	59	20	367	156	125	31	211	51.09%	57.49%	62.80%
#410		0	560	0	560	109	61	2	388	128	101	27	260	' 55.32%	67.01%	72.02%
#411		664	0	0	664	108	110	8	438	146	125	21	292	55.62%	66.67%	70.02%
#412		0	2	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#413		2229	0	0	2229	116	177	6	1930	153	136	17	1777	87.58%	92.07%	92.89%
#414		400	0	0	400	50	17	1	332	29	21	8	303	81.02%	91.27%	93.52%
#415		18	0	0	18	1	8	1	8	3	3	0	5	55.56%	62.50%	62.50%
#416		701	0	0	701	43	42	1	615	27	18	9	588	90.60%	95.61%	97.03%
#417		1036	0	0	1036	131	134	8	763	183	167	16	580	66.06%	76.02%	77.64%
#418		0	137	0	137	33	41	2	61	17	11	6	44	50.00%	72.13%	80.00%
#419		0	0	21	21	9	8	0	4	0	0	0	4	30.77%	100.00%	100.00%
#420		42	0	0	42	4	9	0	29	5	5	0	24	72.73%	82.76%	82.76%
#421		123	0	0	123	22	12	2	87	27	27	0	60	55.05%	68.97%	68.97%
#422		0	152	0	152	52	20	3	77	43	38	5	34	27.42%	44.16%	47.22%
#423		1385	0	0	1385	222	166	35	962	528	448	80	434	39.31%	45.11%	49.21%
#424		0	8	0	8	0	1	0	7	2	2	0	5	71.43%	71.43%	71.43%
#425		2075	0	0	2075	221	218	25	1611	847	696	151	764	45.45%	47.42%	52.33%
#426		367	0	0	367	27	26	0	314	19	17	2	295	87.02%	93.95%	94.55%
#427		85	0	0	85	13	12	2	58	26	24	2	32	46.38%	55.17%	57.14%
#428		3281	0	0	3281	325	254	9	2693	164	112	52	2529	85.27%	93.91%	95.76%
#429		226	0	0	226	147	22	7	50	29	21	8	21	11.11%	42.00%	50.00%
#430		207	0	0	207	3	1	4	199	39	36	3	160	80.40%	80.40%	81.63%
#431		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#432		7	0	0	7	1	0	0	6	1	0	1	5	83.33%	83.33%	100.00%
#433		1397	0	0	1397	125	91	8	1173	111	81	30	1062	83.75%	90.54%	92.91%
#434		48	0	0	48	2 ·	6	0	40	12	9	3	28	71.79%	70.00%	75.68%
#435		13	0	0	13	7	0	0	6	6	4	2	0	0.00%	0.00%	0.00%
#436		7	0	0	7	1	0	0	6	1	1	0	5	71.43%	83.33%	83.33%
#437		1221	0	0	1221	173	48	7	993	173	154	19	820	71.49%	82.58%	84.19%
#438		608	0	0	608	134	114	15	345	197	150	47	148	34.26%	42.90%	49.66%
LENS Subtota	1	215175	0	0	215175	29256	19129	1058	165732	23344	18359	4985	142388	74.94%	85.91%	88.58%
EDI Subtota	1	0	61313	0	61313	10966	9731	68	40548	7619	4045	3574	32929	68.69%	81.21%	89.06%
TAG Subtota	u l	0	0	47859	47859	8042	5284	387	34146	6472	4860	1612	27674	68.20%	81.05%	85.06%
TOTAL INTERFACES	s	215175	61313	47859	324347	48264	34144	1513	240426	37435	27264	10171	202991	72.88%	84.43%	88.16%

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING							F	LOWTHROUG	iH .
						L	ESOG									-
		M	echanized	Interface I	Used	Manual	Rejects	Valid	ated		Errors					-
						Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused	leaved CO's	Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSH'8	Failout	Clarification	(Z Status)	LSHS	Fanout	Fallout	Fanout	Issued SU s	Flowanough	Calculation	Calculation
#1		0	8	0	8	1	0	0	7	0	0	0	7	87.50%	100.00%	100.00%
#2		0	34	0	34	1	4	0	29	12	0	12	17	94.44%	58.62%	100.00%
#3		8	0	0	8	2	4	0	2		1	0	1	25.00%	50.00%	50.00%
#4		0	4	0	4	1	0	0	3	2	1	1	1	33.33%	33.33%	50.00%
#5		914	0	0	914	107	112	0	695	38	28	10	657	82.95%	94.53%	95.91%
#6		164	0	0	164	28	8	2	126	60	51	9	66	45.52%	52.38%	56.41%
#7		458	0	0	458	56	66	2	334	53	45	8	281	73.56%	84.13%	86.20%
#8		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#9		29	0	0	29	2	7	0	20	14	11	3	6	31.58%	30.00%	35.29%
#10		358	0	0	358	19	28	1	310	25	23	2	285	87.16%	91.94%	92.53%
#11		267	0	0	267	36	26	3	202	61	56	5	141	60.52%	69.80%	71.57%
#12		1382	0	0	1382	136	49	5	1192	76	62	14	1116	84.93%	93.62%	94.74%
#13		0	0	4	4	0	2	0	2	2	2	0	0	0.00%	0.00%	0.00%
#14		196	0	0	196	37	15	2	142	9	8	1	133	74.72%	93.66%	94.33%
#15		17	0	0	17	2	3	1	11	2	2	0	9	69.23%	81.82%	81.82%
#16		54	0	0	54	13	4	0	37	2	2	0	35	70.00%	94.59%	94.59%
#17		0	8	0	8	0	5	0	3	0	0	0	3	100.00%	100.00%	100.00%
#18		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#19		273	0	0	273	38	14	0	221	20	15	5	201	79.13%	90.95%	93.06%
#20		524	0	0	524	130	25	0	369	15	12	3	354	71.37%	95.93%	96.72%
#21		17	0	0	17	1	1	0	15	1	1	0	14	87.50%	93.33%	93.33%
#22		333	0	0	333	24	29	1	279	30	20	10	249	84.98%	89.25%	92.57%
#23		4	0	0	4	1	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#24		17	0	0	17	0	11	1	5	3	2	1	2	50.00%	40.00%	50.00%
#25		51	0	0	51	12	4	1	34	9	6	3	25	58.14%	73.53%	80.65%
#26		65	0	0	65	15	12	0	38	9	9	0	29	54.72%	76.32%	76.32%
#27		571	0	0	571	55	6	0	510	4	4	0	506	89.56%	99.22%	99.22%
#28		513	0	0	513	28	25	1	459	28	26	2	431	88.87%	93.90%	94.31%
#29		36	0	0	36	3	1	1	31	4	2	2	27	84.38%	87.10%	93.10%
#30		0	0	14	14	0	3	1	10	8	1	7	2	66.67%	20.00%	66.67%
#31		673	0	0	673	90	102	5	476	77	58	19	399	72.94%	83.82%	87.31%
#32		17	0	0	17	3	4	0	10	9	6	3	1	10.00%	10.00%	14.29%
#33		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#34		1079	0	0	1079	162	85	5	827	79	64	15	748	76.80%	90.45%	92.12%
#35		29	0	0	29	4	4	0	21	2	2	0	19	76.00%	90.48%	90.48%
#36	1	6	0	0	6	0	4	0	2	2	2	0	0	0.00%	0.00%	0.00%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		M	echanized	Interface I	Used	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
			EDI	TAC	Total Mech	Manual	Auto	Supps (7 Status)	I SD'e	System	BST Caused	Caused	leeued SO'e	Achieved	Base Celculation	Excluded
Name	HESH / UCN	LENS		TAG	Lon 8	railout	Clarification		Lona	Tanout	ranout	raisouc		Tiowanough	Culculation	Calculation
#37		2066	0	0	2066	273	130	9	1654	140	119	21	1514	79.43%	91.54%	92.71%
#38		1548	0	0	1548	67	249	6	1226	253	156	97	973	81.35%	79.36%	86.18%
#39		182	0	0	182	13	15	0	154	8	7	1	146	87.95%	94.81%	95.42%
#40		3	0	0	3	3	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#41		3	0	0	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#42		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#43		66	0	0	66	6	6	0	54	18	10	8	36	69.23%	66.67%	78.26%
#44		2	0	0	2	0	0	1	1	1	1	0	0	0.00%	0.00%	0.00%
#45		49	0	0	49	13	5	1	30	6	5	1	24	57.14%	80.00%	82.76%
#46		793	0	0	793	130	50	6	607	55	39	16	552	76.56%	90.94%	93.40%
#47		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#48		11	0	0	11	1	1	0	9	0	0	0	9	90.00%	100.00%	100.00%
#49		281	0	0	281	31	21	0	229	18	14	4	211	82.42%	92.14%	93.78%
#50		13	0	0	13	3	2	2	6	. 4	2	2	2	28.57%	33.33%	50.00%
#51		3	0	0	3	2	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#52		4	0	0	4	1	0	0	3	1	0	1	2	66.67%	66.67%	100.00%
#53		83	0	0	83	35	11	1	36	14	12	2	22	31.88%	61.11%	64.71%
#54		93	0	0	93	10	0	0	83	6	6	0	77	82.80%	92.77%	92.77%
#55		228	0	0	228	30	15	0	183	10	6	4	173	82.78%	94.54%	96.65%
#56		26	0	0	26	3	3	0	20	5	3	2	15	71.43%	75.00%	83.33%
#57		611	0	0	611	83	30	2	496	48	39	9	448	78.60%	90.32%	91.99%
#58		332	0	0	332	35	18	0	279	9	7	2	270	86.54%	96.77%	97.47%
#59		0	0	604	604	27	74	0	503	19	14	5	484	92.19%	96.22%	97.19%
#60		468	0	0	468	49	66	2	351	26	21	5	325	82.28%	92.59%	93.93%
#61		172	0	0	172	25	74	1	72	9	5	4	63	67.74%	87.50%	92.65%
#62		97	0	0	97	9	0	0	88	2	2	0	86	88.66%	97.73%	97.73%
#63		998	0	0	998	103	88	2	805	43	32	11	762	84.95%	94.66%	95.97%
#64		293	0	0	293	41	8	0	244	23	20	3	221	78.37%	90.57%	91.70%
#65		163	0	0	163	37	8	2	116	18	16	2	98	64.90%	84.48%	85.96%
#66		0	0	777	777	11	63	0	703	25	16	9	678	96.17%	96.44%	97.69%
#67		449	0	0	449	124	85	2	238	36	27	9	202	57.22%	84.87%	88.21%
#68		212	0	0	212	28	13	1	170	15	15	0	155	78.28%	91.18%	91.18%
#69		440	0	0	440	47	24	0	369	10	5	5	359	87.35%	97.29%	98.63%
#70		686	0	0	686	17	115	0	554	65	58	7	489	86.70%	88.27%	89.40%
#71	1	3	0	0	3	2	0	0	1	0	0	0	1	33.33%	100.00%	100.00%
#72		72	0	0	72	10	2	2	58	17	17	0	41	60.29%	70.69%	70.69%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		M	echanized	Interface I	Used	Manual	Rejects	Valic	lated		Errors					
· · · · · · · · · · · · · · · · · · ·						Total		Pending		Total		CLEC			_	CLEC Error
		LENG	ED 1	TAG	Total Mech	Manuai Sellout	Auto	Supps (7 Status)	I CD'e	System Fellout	BST Caused	Caused	leaved SO's	Achieved	Base	Excluded
Name	RESH / OCN	LENS		TAG	Lons	Fallout	Clarmcation	(Z Status)	Lons	ranout	Failout	Failout	issued SU s	riowuirougn	Calculation	Calculation
#73		214	0	0	214	35	20	0	159	22	18	4	137	72.11%	86.16%	88.39%
#74		31	0	0	31	2	2	0	27	4	3	1	23	82.14%	85.19%	88.46%
#75		30	0	0	30	2	2	0	26	4	2	2	22	84.62%	84.62%	91.67%
#76		0	7	0	7	2	0	0	5	4	0	4	1	33.33%	20.00%	100.00%
#77		131	0	0	131	10	1	0	120	2	2	0	118	90.77%	98.33%	98.33%
#78		237	0	0	237	56	9	0	172	8	8	0	164	71.93%	95.35%	95.35%
#79		1561	0	0	1561	132	47	0	1382	72	58	14	1310	87.33%	94.79%	95.76%
#80		874	0	0	874	80	28	1	765	71	66	5	694	82.62%	90.72%	91.32%
#81	-	1639	0	0	1639	132	95	4	1408	77	53	24	1331	87.80%	94.53%	96.17%
#82		3805	0	0	3805	536	193	37	3039	201	162	39	2838	80.26%	93.39%	94.60%
#83		86	0	0	86	6	4	1	75	12	9	3	63	80.77%	84.00%	87.50%
#84		41	0	0	41	4	9	0	28	2	2	0	26	81.25%	92.86%	92.86%
#85		6	0	0	6	0	4	0	2	0	0	0	2	100.00%	100.00%	100.00%
#86		213	0	0	213	16	9	0	188	8	8	0	180	88.24%	95.74%	95.74%
#87		178	0	0	178	18	17	0	143	6	4	2	137	86.16%	95.80%	97.16%
#88		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#89		506	0	0	506	66	41	3	396	26	15	11	370	82.04%	93.43%	96.10%
#90		83	0	0	83	1	1	0	81	6	3	3	75	94.94%	92.59%	96.15%
#91		3	0	0	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100.00%
#92		224	0	0	224	28	21	0	175	4	3	1	171	84.65%	97.71%	98.28%
#93		6	0	0	6	2	2	0	2	2	2	0	0	0.00%	0.00%	0.00%
#94		43	0	0	43	1	1	0	41	1	1	0	40	95.24%	97.56%	97.56%
#95		38	0	0	38	2	1	1	34	4	4	0	30	83.33%	88.24%	88.24%
#96		1441	0	0	1441	215	109	1	1116	57	44	13	1059	80.35%	94.89%	96.01%
#97		41	0	0	41	5	5	0	31	13	9	4	18	56.25%	58.06%	66.67%
#98		0	0	381	381	20	162	1	198	31	16	15	167	82.27%	84.34%	91.26%
#99		948	0	0	948	64	60	0	824	17	14	3	807	91.19%	97.94%	98.29%
#100		777	0	0	777	83	129	1	564	68	60	8	496	77.62%	87.94%	89.21%
#101		31	0	0	31	7	2	0	22	2	1	1	20	71.43%	90.91%	95.24%
#102		128	0	0	128	18	15	0	95	11	9	2	84	75.68%	88.42%	90.32%
#103		7236	0	0	7236	486	572	10	6168	401	311	90	5767	87.86%	93.50%	94.88%
#104		321	0	0	321	25	21	0	275	33	32	1	242	80.94%	88.00%	88.32%
#105		1272	0	0	1272	112	76	3	1081	82	80	2	999	83.88%	92.41%	92.59%
#106		0	0	117	117	6	15	0	96	0	0	0	96	94.12%	100.00%	100.00%
#107		213	0	0	213	95	26	0	92	5	4	1	87	46.77%	94.57%	95.60%
#108		32	0	0	32	2	4	0	26	2	2	0	24	85.71%	92.31%	92.31%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	н
· · · · · · · · · · · · · · · · · · ·						LI	ESOG									
		M	echanized	Interface	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
				THE	Total Mech	Manual Selleut	Auto	Supps (7 Status)	I CD'e	System	BST Caused	Caused	leeuad SO's	Achieved	Base Celculation	Excluded Calculation
Name	HESH / OCN	LENS	EDI	TAG	Lans	Fallout	Clarification	(Z Status)	Lona		Failout	Tailout	136000 50 8	Tiowiniough	Calculation	Calculation
#109		203	0	0	203	32	2	1	168	8	6	2	160	80.81%	95.24%	96.39%
#110		406	0	0	406	99	61	0	246	25	12	13	221	66.57%	89.84%	94.85%
#111		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#112		147	0	0	147	25	9	0	113	6	6	0	107	77.54%	94.69%	94.69%
#113		0	0	17	17	3	6	2	6	6	2	4	0	0.00%	0.00%	0.00%
#114		78	0	0	78	4	68	0	6	1	1	0	5	50.00%	83.33%	83.33%
#115		2049	0	0	2049	281	101	6	1661	157	102	55	1504	79.70%	90.55%	93.65%
#116	l	2403	0	0	2403	145	250	2	2006	137	77	60	1869	89.38%	93.17%	96.04%
#117		1215	0	0	1215	86	75	1	1053	59	44	15	994	88.43%	94.40%	95.76%
#118		94	0	0	94	5	15	0	74	4	4	0	70	88.61%	94.59%	94.59%
#119		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#120		10	0	0	10	3	1	0	6	3	3	0	3	33.33%	50.00%	50.00%
#121		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#122		40	0	0	40	8	12	0	20	8	8	0	12	42.86%	60.00%	60.00%
#123		74	0	0	74	7	5	0	62	5	4	1	57	83.82%	91.94%	93.44%
#124		8	0	0	8	0	2	3	3	3	3	0	0	0.00%	0.00%	0.00%
#125		297	0	0	297	84	33	1	179	69	60	9	110	43.31%	61.45%	64.71%
#126		22	0	0	22	4	9	1	8	7	4	3	1	11.11%	12.50%	20.00%
#127		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#128		10	0	0	10	0	3	0	7	6	5	1	1	16.67%	14.29%	16.67%
#129		96	0	0	96	24	5	0	67	5	4	1	62	68.89%	92.54%	93.94%
#130		4	0	0	4	1	0	0	3	0	0	0	3	/5.00%	100.00%	100.00%
#131		0	0	5	5	3	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#132		21	0	0	21	4	16	0	1	1	1	0	0	0.00%	0.00%	0.00%
#133		692	0	0	692	23	38	3	628	21	18	3	607	93.67%	96.66%	97.12%
#134		0	0	4027	4027	116	68	15	3828	2//	221	56	3551	91.33%	92.76%	94.14%
#135		8802	0	0	8802	658	388	5	7751	278	243	35	/4/3	89.24%	96.41%	90.85%
#136		0	0	3319	3319	54	448	29	2788	1172	813	359	1616	65.08%	57.96%	66.53%
#137		765	0	0	765	85	29	7	644	41	33	8	603	83.63%	93.63%	94.81%
#138		2	0	0	2	1	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#139	-	735	0	0	735	65	16	1	653	17	15	2	636	88.83%	97.40%	97.70%
#140		389	0	0	389	49	23	1	316	16	10	6	300	83.57%	94.94%	96.77%
#141		831	0	0	831	58	62	0	711	46	36	10	665	87.62%	93.53%	94.86%
#142		27	0	0	27	1	7	2	17	4	0	4	13	92.86%	76.47%	100.00%
#143		238	0	0	238	8	10	1	219	39	28	11	180	83.33%	82.19%	86.54%
#144		876	0	0	876	91	69	8	708	79	71	8	629	79.52%	88.84%	89.86%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	lated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
			E01	TAC	Total Mech	Manual	Auto	Supps (7 Status)	1 SP'e	System Fallout	BST Caused	Caused	lequed SO's	Achieved	Base	Excluded Celculation
Name	RESH / OCN	LENS	EDI	TAG	LONS	Fallout	Clarinication	(Z Status)	Lons	Fanout	Failout	Fallout	138UEU 30 8	Fiowiniough	Calculation	Calculation
#145		11	0	0	11	0	4	0	7	4	3	1	3	50.00%	42.86%	50.00%
#146		0	0	929	929	20	95	1	813	11	8	3	802	96.63%	98.65%	99.01%
#147		82	0	0	82	1	8	1	72	8	6	2	64	90.14%	88.89%	91.43%
#148		338	0	0	338	47	13	1	277	27	15	12	250	80.13%	90.25%	94.34%
#149		22766	0	0	22766	7149	3128	91	12398	3568	2642	926	8830	47.42%	71.22%	76.97%
#150		373	0	0	373	31	24	0	318	22	19	3	296	85.55%	93.08%	93.97%
#151		0	0	219	219	43	42	1	133	24	22	2	109	62.64%	81.95%	83.21%
#152		230	0	0	230	18	44	1	167	33	30	3	134	73.63%	80.24%	81.71%
#153		69	0	0	69	13	13	0	43	4	4	0	39	69.64%	90.70%	90.70%
#154		0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#155		18	0	0	18	4	3	0	11	5	4	1	6	42.86%	54.55%	60.00%
#156		38	0	0	38	3	6	0	29	1	1	0	28	87.50%	96.55%	96.55%
#157		3	0	0	3	0	1	0	2	0	0	0	2	100.00%	100.00%	100.00%
#158		283	0	0	283	21	3	0	259	7	6	1	252	90.32%	97.30%	97.67%
#159		30	0	0	30	2	0	0	28	0	0	0	28	93.33%	100.00%	100.00%
#160		63	0	0	63	6	1	1	55	13	10	3	42	72.41%	76.36%	80.77%
#161		3064	0	0	3064	225	358	2	2479	102	89	13	2377	88.33%	95.89%	96.39%
#162		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#163		0	1458	0	1458	74	103	0	1281	172	144	28	1109	83.57%	86.57%	88.51%
#164		0	0	10	10	5	1	0	4	1	1	0	3	33.33%	75.00%	75.00%
#165		8	0	0	8	2	2	0	4	3	2	1	1	20.00%	25.00%	33.33%
#166		0	95	0	95	7	3	0	85	1	1	0	84	91.30%	98.82%	98.82%
#167		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#168		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%
#169		41	0	0	41	0	4	0	37	1	0	1	36	100.00%	97.30%	100.00%
#170		0	6225	0	6225	746	1222	3	4254	159	66	93	4095	83.45%	96.26%	98.41%
#171		204	0	0	204	14	10	0	180	11	9	2	169	88.02%	93.89%	94.94%
#172		0	9087	0	9087	1496	1807	5	5779	301	134	167	5478	77.07%	94.79%	97.61%
#173		346	0	0	346	23	27	0	296	16	14	2	280	88.33%	94.59%	95.24%
#174		0	0	127	127	24	26	4	73	22	15		51	56.67%	69.86%	77.27%
#175		304	0	0	304	19	41	.5	239	67	39	28	172	74.78%	71.97%	81.52%
#176		2	0	0	2	1	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#177		433	0	0	433	27	24	3	379	43	36	7	336	84.21%	88.65%	90.32%
#178		118	0	0	118	13	10	0	95	6	4	2	89	83.96%	93.68%	95.70%
#179		271	0	0	271	6	20	2	243	100	54	46	143	70.44%	58.85%	72.59%
#180		0	9257	0	9257	164	1781	3	7309	1844	1227	617	5465	79.71%	74.77%	81.66%

ACCORCATE OPDED TYPES	1		r	l	1		1	Γ	1	Γ			Τ	Γ	1	1
AGGREGATE ORDER TIPES	1		1											· · · · · · · · · · · · · · · · · · ·		1
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			L				Deieste	Matta	and a		<u> </u>				· · · · · · · · · · · · · · · · · · ·	
		M	echanizéd	Interface	Used	Manual	Hejects	Pending	ated	Total	Errors		_			CIEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#181		268	0	0	268	13	30	0	225	21	20	1	204	86.08%	90.67%	91.07%
#182		34	0	0	34	4	7	1	22	12	7	5	10	47.62%	45.45%	58.82%
#183		18	0	0	18	9	0	0	9	9	9	0	0	0.00%	0.00%	0.00%
#184		6	0	0	6	1	2	0	3	0	0	0	3	75.00%	100.00%	100.00%
#185		491	0	0	491	18	27	0	446	18	14	4	428	93.04%	95.96%	96.83%
#186		36	0	0	36	4	4	0	28	2	2	0	26	81.25%	92.86%	92.86%
#187		8	0	0	8	0	3	0	5	2	1	1	3	75.00%	60.00%	75.00%
#188		10	0	0	10	0	0	0	10	2	2	0	8	80.00%	80.00%	80.00%
#189		159	0	0	159	51	22	0	86	26	13	13	60	48.39%	69.77%	82.19%
#190		11	0	0	11	0	1	0	10	0	0	0	10	100.00%	100.00%	100.00%
#191		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#192		3384	0	0	3384	427	224	8	2725	224	193	31	2501	80.13%	91.78%	92.84%
#193		0	0	3355	3355	505	317	54	2479	313	209	104	2166	75.21%	87.37%	91.20%
#194		15096	0	0	15096	383	977	10	13726	253	198	55	13473	95.87%	98.16%	98.55%
#195		0	56	0	56	2	4	0	50	11	6	5	39	82.98%	78.00%	86.67%
#196		170	0	0	170	2	6	0	162	5	4	1	157	96.32%	96.91%	97.52%
#197		494	0	0	494	19	73	2	400	5	4	1	395	94.50%	98.75%	99.00%
#198		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#199		129	0	0	129	15	18	1	95	15	13	2	80	74.07%	84.21%	86.02%
#200		0	152	0	152	22	33	1	96	36	15	21	60	61.86%	62.50%	80.00%
#201		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#202		0	0	9555	9555	277	701	16	8561	245	191	54	8316	94.67%	97.14%	97.75%
#203		3146	0	0	3146	408	211	19	2508	261	202	59	2247	78.65%	89.59%	91.75%
#204		335	0	0	335	46	27	3	259	44	35	9	215	72.64%	83.01%	86.00%
#205		29	0	0	29	4	2	0	23	7	7	0	16	59.26%	69.57%	69.57%
#206		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#207		125	0	0	125	16	13	2	94	31	22	9	63	62.38%	67.02%	74.12%
#208		32	0	0	32	20	2	0	10	7	5	2	3	10.71%	30.00%	37.50%
#209		1036	0	0	1036	147	39	1	849	46	30	16	803	81.94%	94.58%	96.40%
#210		345	0	0	345	7	28	0	310	15	10	5	295	94.55%	95.16%	96.72%
#211		0	0	1370	1370	38	122	1	1209	23	19	4	1186	95.41%	98.10%	98.42%
#212		105	0	0	105	5	2	0	98	5	5	0	93	90.29%	94.90%	94.90%
#213		230	0	0	230	18	17	2	193	30	28	2	163	77.99%	84.46%	85.34%
#214		23	0	0	23	2	3	0	18	3	3	0	15	75.00%	83.33%	83.33%
#215		129	0	0	129	7	14	1	107	9	8	1	98	86.73%	91.59%	92.45%
#216		1007	0	0	1007	88	57	1	861	82	74	8	779	82.78%	90.48%	91.32%

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Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		Me	echanized	Interface I	Used	Manual	Rejects	Valid	ated		Errors					
					Total Mech	Total Manuai	Auto	Pending Supps		Total System	BST Caused	CLEC Caused		Achieved	Base	CLEC Error Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Failout	Fallout	issued SO's	Flowthrough	Calculation	Calculation
#217		13	0	0	13	0	1	0	12	1	1	0	11	91.67%	91.67%	91.67%
#218		0	0	1527	1527	167	99	5	1256	148	118	30	1108	79.54%	88.22%	90.38%
#219		83	0	0	83	4	5	0	74	4	4	0	70	89.74%	94.59%	94.59%
#220		0	2447	0	2447	854	195	1	1397	212	162	50	1185	53.84%	84.82%	87.97%
#221		3176	0	0	3176	440	173	17	2546	581	495	86	1965	67.76%	77.18%	79.88%
#222		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#223		0	0	1787	1787	35	330	4	1418	14	12	2	1404	96.76%	99.01%	99.15%
#224		49	0	0	49	1	5	0	43	6	6	0	37	84.09%	86.05%	86.05%
#225		4	0	0	4	0	0	0	4	3	3	0	1	25.00%	25.00%	25.00%
#226		574	0	0	574	36	38	1	499	23	16	7	476	90.15%	95.39%	96.75%
#227		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#228		0	0	6	6	2	0	0	4	0	0	0	4	66.67%	100.00%	100.00%
#229		0	0	485	485	10	76	0	399	4	3	1	395	96.81%	99.00%	99.25%
#230	1	21	0	0	21	2	1	0	18	. 4	4	0	14	70.00%	77.78%	77.78%
#231		59	0	0	59	8	8	1	42	7	6	1	35	71.43%	83.33%	85.37%
#232		463	0	0	463	82	24	1	356	34	31	3	322	74.02%	90.45%	91.22%
#233		53	0	0	53	12	2	0	39	0	0	0	39	76.47%	100.00%	100.00%
#234		323	0	0	323	22	10	1	290	17	13	4	273	88.64%	94.14%	95.45%
#235		8	0	0	8	0	0	0	8	2	1	1	6	85.71%	75.00%	85.71%
#236		0	0	58	58	2	8	0	48	3	3	0	45	90.00%	93.75%	93.75%
#237		101	0	0	101	16	3	0	82	3	3	0	79	80.61%	96.34%	96.34%
#238		0	4162	0	4162	547	804	2	2809	115	57	58	2694	81.69%	95.91%	97.93%
#239		170	0	0	170	8	5	0	157	28	27	1	129	78.66%	82.17%	82.69%
#240		287	0	0	287	46	38	4	199	36	31	5	163	67.92%	81.91%	84.02%
#241		26	0	0	26	4	7	0	15	5	4	1	10	55.56%	66.67%	71.43%
#242		95	0	0	95	4	7	0	84	1	1	0	83	94.32%	98.81%	98.81%
#243		10	0	0	10	1	2	0	7	1	0	1	6	85.71%	85.71%	100.00%
#244		448	0	0	448	37	15	1	395	31	25	6	364	85.45%	92.15%	93.57%
#245		3028	0	0	3028	518	230	7	2273	297	224	73	1976	72.70%	86.93%	89.82%
#246		27110	0	0	27110	3811	2479	63	20757	1618	1364	254	19139	78.72%	92.21%	93.35%
#247		50	0	0	50	32	7	0	11	1	0	1	10	23.81%	90.91%	100.00%
#248		548	0	0	548	54	62	4	428	44	34	10	384	81.36%	89.72%	91.87%
#249	-	387	0	0	387	44	22	2	319	61	37	24	258	76.11%	80.88%	87.46%
#250		160	0	0	160	15	14	0	131	2	2	0	129	88.36%	98.47%	98.47%
#251		60	0	0	60	0	3	0	57	1	1	0	56	98.25%	98.25%	98.25%
#252		272	0	0	272	9	26	0	237	12	9	3	225	92.59%	94.94%	96.15%
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Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	H
						L	ESOG									
		Me	chanized	Interface (Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#253		122	0	0	122	3	7	0	112	6	5	1	106	92.98%	94.64%	95.50%
#254		167	0	0	167	17	11	3	136	19	18	1	117	76.97%	86.03%	86.67%
#255		89	0	0	89	5	11	0	73	4	3	1	69	' 89.61%	94.52%	95.83%
#256		30	0	0	30	3	1	1	25	3	2	1	22	81.48%	88.00%	91.67%
#257	· · · · · · · · · · · · · · · · · · ·	0	2	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#258		2194	0	0	2194	113	169	6	1906	148	135	13	1758	87.64%	92.24%	92.87%
#259		400	0	0	400	50	17	1	332	29	21	8	303	81.02%	91.27%	93.52%
#260		18	0	0	18	1	8	1	8	3	3	0	5	55.56%	62.50%	62.50%
#261		693	0	0	693	38	42	1	612	25	17	8	587	91.43%	95.92%	97.19%
#262		1031	0	0	1031	131	134	8	758	179	166	13	579	66.10%	76.39%	77.72%
#263		56	0	0	56	5	7	0	44	8	8	0	36	73.47%	81.82%	81.82%
#264		0	12	0	12	8	1	0	3	1	1	0	2	18.18%	66.67%	66.67%
#265		145	0	0	145	12	9	0	124	21	19	2	103	76.87%	83.06%	84.43%
#266		5	0	0	5	0	3	0	2	2	2	0	0	0.00%	0.00%	0.00%
#267		367	0	0	367	27	26	0	314	19	17	2	295	87.02%	93.95%	94.55%
#268		49	0	0	49	11	7	2	29	11	10	1	18	46.15%	62.07%	64.29%
#269		3261	0	0	3261	322	254	9	2676	159	110	49	2517	85.35%	94.06%	95.81%
#270		42	0	0	42	2	12	6	22	10	8	2	12	54.55%	54.55%	60.00%
#271		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#272		1395	0	0	1395	125	91	8	1171	109	80	29	1062	83.82%	90.69%	92.99%
#273		36	0	0	36	1	5	0	30	8	5	3	22	78.57%	73.33%	81.48%
#274		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#275		1221	0	0	1221	173	48	7	993	173	154	19	820	71.49%	82.58%	84.19%
LENS Subtotal	1	166308	0	0	166308	21917	14141	489	129761	12701	9975	2726	117060	78.59%	90.21%	92.15%
EDI Subtotal		0	33014	0	33014	3925	5964	15	23110	2870	1814	1056	20240	77.91%	87.58%	91.77%
TAG Subtotal	1	0	0	28697	28697	1368	2661	134	24534	2350	1687	663	22184	87.90%	90.42%	92.93%
TOTAL INTERFACES	5	166308	33014	28697	228019	27210	22766	638	177405	17921	13476	4445	159484	79.67%	89.90%	92.21%

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AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	iH
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused		Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSR's	Fallout	Clarification	(Z Status)	LSR's	Fallout	Fallout	Fallout	Issued SO's	Flowthrough	Calculation	Calculation
#1		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#2		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#3		1	0	0	1	0	0	0	1	0	0	0	1	' 100.00%	100.00%	100.00%
#4		19	0	0	19	0	6	0	13	11	10	1	2	16.67%	15.38%	16.67%
#5		0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#6		5	0	0	5	0	1	1	3	2	2	0	1	33.33%	33.33%	33.33%
#7		0	0	10	10	2	0	0	8	3	1	2	5	62.50%	62.50%	83.33%
#8		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#9		76	0	0	76	5	15	2	54	31	28	3	23	41.07%	42.59%	45.10%
#10		0	0	4	4	0	3	0	1	0	0	0	1	100.00%	100.00%	100.00%
#11		0	0	10	10	0	1	0	9	6	4	2	3	42.86%	33.33%	42.86%
#12		0	11	0	11	0	5	0	6	3	3	0	3	50.00%	50.00%	50.00%
#13		0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#14		0	0	3	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%
#15		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%
#16		5	0	0	5	2	0	0	3	2	2	0	1	20.00%	33.33%	33.33%
#17		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#18		12	0	0	12	1	6	0	5	1	1	0	4	66.67%	80.00%	80.00%
#19		57	0	0	57	8	14	0	35	14	8	6	21	56.76%	60.00%	72.41%
#20		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#21		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#22		0	0	15	15	11	2	0	2	1	0	1	1	8.33%	50.00%	100.00%
#23		34	0	0	34	3	12	1	18	5	3	2	13	68.42%	72.22%	81.25%
#24		2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#25		24	0	0	24	3	0	5	16	13	4	9	3	30.00%	18.75%	42.86%
#26		4	0	0	4	0	3	0	1	0	0	0	1	100.00%	100.00%	100.00%
#27		0	0	2	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
#28		28	0	0	28	4	5	0	19	7	4	3	12	60.00%	63.16%	75.00%
#29		10	0	0	10	3	1	0	6	2	2	0	4	44.44%	66.67%	66.67%
#30		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#31		4	0	0	4	1	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#32		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#33		2	0	0	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
#34		17	0	0	17	3	4	0	10	5	3	2	5	45.45%	50.00%	62.50%
#35		6	0	0	6	0	2	1	3	1	1	0	2	66.67%	66.67%	66.67%
#36		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	iH
						L	ESOG									
		M	echanized	Interface	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
					Total Mech	Manual	Auto	Supps	I CDie	System	BST Caused	Caused		Achieved	Base Celculation	Excluded Celculation
Name	RESH / OCN	LENS	EDI	IAG	LSH'8	railout	Clanncation		LON 8	Failout	Failout	Fanout	Issued 50 s	Tiowanough	Calculation	Calculation
#37		37	0	0	37	5	8	4	20	10	8	2	10	43.48%	50.00%	55.56%
#38		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#39		13	0	0	13	1	6	0	6	1	1	0	5	71.43%	83.33%	83.33%
#40		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#41		5	0	0	5	0	0	0	5	1	1	0	4	80.00%	80.00%	80.00%
#42		0	0	1	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#43		9	0	0	9	0	2	0	7	3	2	1	4	66.67%	57.14%	66.67%
#44		7	0	0	7	0	1	0	6	2	1	1	4	80.00%	66.67%	80.00%
#45		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#46		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#47		3	0	0	3	1	0	0	2	1	0	1	1	50.00%	50.00%	100.00%
#48		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%
#49		6	0	0	6	2	0	0	4	0	0	0	4	66.67%	100.00%	100.00%
#50		7	0	0	7	1	0	1	5	2	0	2	3	75.00%	60.00%	100.00%
#51		27	0	0	27	16	2	0	9	7	6	1	2	8.33%	22.22%	25.00%
#52		18	0	0	18	1	3	0	14	3	3	0	11	73.33%	78.57%	78.57%
#53		10	0	0	10	3	1	0	6	3	3	0	3	33.33%	50.00%	50.00%
#54		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#55		35	0	0	35	0	4	1	30	12	7	5	18	72.00%	60.00%	72.00%
#56		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#57		6	0	0	6	0	0	0	6	2	0	2	4	100.00%	66.67%	100.00%
#58		12	0	0	12	0	0	0	12	4	4	0	8	66.67%	66.67%	66.67%
#59		23	0	0	23	3	3	0	17	9	8	1	8	42.11%	47.06%	50.00%
#60		9	0	0	9	1	1	0	7	3	3	0	4	50.00%	57.14%	57.14%
#61		14	0	0	14	2	1	0	11	2	0	2	9	81.82%	81.82%	100.00%
#62		70	0	0	70	18	1	4	47	25	22	3	22	35.48%	46.81%	50.00%
#63		4	0	0	4	1	1	0	2	2	2	0	0	0.00%	0.00%	0.00%
#64		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%
#65		44	0	0	44	13	8	2	21	12	12	0	9	26.47%	42.86%	42.86%
#66		21	0	0	21	5	6	1	9	4	4	0	5	35.71%	55.56%	55.56%
#67	1	45	0	0	45	13	3	0	29	16	13	3	13	33.33%	44.83%	50.00%
#68		8	0	0	8	4	0	0	4	1	1	0	3	37.50%	75.00%	75.00%
#69		0	0	11	11	10	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#70		62	0	0	62	6	8	0	48	12	11	1	36	67.92%	75.00%	76.60%
#71		20	0	0	20	0	4	0	16	6	5	1	10	66.67%	62.50%	66.67%
#72		32	0	0	32	2	4	1	25	11	11	0	14	51.85%	56.00%	56.00%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES							[
Company Info						LSR PF	OCESSING							F	LOWTHROUG	ЭН
	Τ					L	ESOG									
		M	echanized	Interface	Jsed	Manual	Rejects	Valid	lated		Errors					
		-				Total		Pending		Total		CLEC				CLEC Error
		LENC	EDI	TAC	Total Mech	Manual	Auto	Supps (7 Status)	I SP'e	System Fellout	BST Caused	Caused	leaved SO's	Achieved	Base Celoulation	Excluded
Name	RESH / OCN	LENS	EDI	TAG	Lon 8	Fallout	Clarification	(2 518108)	Lon 8	Failout	Failout	Fallout	1880ed 50 8	Flowinough	Calculation	Calculation
#73		28	0	0	28	2	0	0	26	6	5	1	20	74.07%	76.92%	80.00%
#74		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#75		4	0	0	4	0	2	1	1	1	1	0	0	0.00%	0.00%	0.00%
#76		25	0	0	25	11	3	0	11	4	4	0	7	31.82%	63.64%	63.64%
#77		36	0	0	36	8	1	0	27	12	10	2	15	45.45%	55.56%	60.00%
#78		33	0	0	33	7	1	0	25	22	19	3	3	10.34%	12.00%	13.64%
#79		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#80		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#81		9	0	0	9	2	3	1	3	2	2	0	1	20.00%	33.33%	33.33%
#82		0	0	22	22	2	6	0	14	10	9	1	4	26.67%	28.57%	30.77%
#83		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#84		18	0	0	18	7	5	0	6	2	2	0	4	30.77%	66.67%	66.67%
#85		24	0	0	24	13	4	1	6	5	5	0	1	5.26%	16.67%	16.67%
#86		14	0	0	14	2	2	0	10	2	1	1	8	72.73%	80.00%	88.89%
#87		43	0	0	43	5	7	0	31	7	7	0	24	66.67%	77.42%	77.42%
#88		215	0	0	215	21	32	7	155	71	52	19	84	53.50%	54.19%	61.76%
#89		38	0	0	38	4	10	0	24	14	11	3	10	40.00%	41.67%	47.62%
#90		155	0	0	155	18	40	2	95	57	48	9	38	36.54%	40.00%	44.19%
#91		4	0	0	4	0	2	0	2	1	1	0	1	50.00%	50.00%	50.00%
#92		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#93		1499	0	0	1499	226	273	17	983	522	393	129	461	42.69%	46.90%	53.98%
#94		0	0	32	32	3	17	0	12	9	7	2	3	23.08%	25.00%	30.00%
#95		117	0	0	117	22	19	0	76	37	33	4	39	41.4 9 %	51.32%	54.17%
#96		0	0	5	5	1	1	0	3	0	0	0	3	75.00%	100.00%	100.00%
#97		8	0	0	8	4	0	0	4	2	2	0	2	25.00%	50.00%	50.00%
#98		11	0	0	11	0	2	2	7	2	1	1	5	83.33%	71.43%	83.33%
#99		61	0	0	61	20	5	2	34	14	12	2	20	38.46%	58.82%	62.50%
#100		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%
#101		0	0	7	7	0	3	0	4	1	1	0	3	75.00%	75.00%	75.00%
#102		34	0	0	34	8	2	0	24	6	4	2	18	60.00%	75.00%	81.82%
#103		0	0	51	51	25	6	0	20	13	10	3	7	16.67%	35.00%	41.18%
#104		41	0	0	41	4	2	0	35	11	11	0	24	61.54%	68.57%	68.57%
#105		6	0	0	6	0	3	0	3	1	1	0	2	66.67%	66.67%	66.67%
#106		2	0	0	2	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#107		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%
#108		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	àН
						L	ESOG									
		M	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
			501	TAG	Total Mech	Manuai	Auto	Supps (7 Status)	I SD'e	System	BST Caused	Caused	leeund SO'e	Achieved	Base	Excluded
Name	HESH / UCN	LENS	EDI		Lon 8	Failout	Clarmcauon		Lans	FailUut	Failout	Fallout	Issued SU's	Filowaniougn	Calculation	Calculation
#109		4	0	0	4	0	0	0	4	0	0	0	4	100.00%	100.00%	100.00%
#110		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#111		13	0	0	13	3	0	1	9	5	3	2	4	40.00%	44.44%	57.14%
#112		26	0	0	26	. 2	3	0	21	14	7	7	7	43.75%	33.33%	50.00%
#113		0	14	0	14	11	0	0	3	3	1	2	0	0.00%	0.00%	0.00%
#114		52	0	0	52	30	5	1	16	6	3	3	10	23.26%	62.50%	76.92%
#115		0	0	136	136	75	25	3	33	23	19	4	10	9.62%	30.30%	34.48%
#116		384	0	0	384	93	23	10	258	129	102	27	129	39.81%	50.00%	55.84%
#117		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#118		6	0	0	6	4	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
#119		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#120		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#121		49	0	0	49	4	8	1	36	19	14	5	17	48.57%	47.22%	54.84%
#122		58	0	0	58	12	3	0	43	. 14	12	2	29	54.72%	67.44%	70.73%
#123		577	0	0	577	63	64	8	442	277	127	150	165	46.48%	37.33%	56.51%
#124		7	0	0	7	1	0	0	6	4	4	0	2	28.57%	33.33%	33.33%
#125		4	0	0	4	0	1	0	3	1	1	0	2	66.67%	66.67%	66.67%
#126		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#127		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#128		0	0	11	11	2	4	0	5	1	0	1	4	66.67%	80.00%	100.00%
#129		17	0	0	17	5	0	0	12	6	2	4	6	46.15%	50.00%	75.00%
#130		15	0	0	15	8	0	0	7	6	5	1	1	7.14%	14.29%	16.67%
#131		3	0	0	3	0	1	0	2	0	0	0	2	100.00%	100.00%	100.00%
#132		10	0	0	10	5	0	1	4	2	0	2	2	28.57%	50.00%	100.00%
#133		571	0	0	571	117	45	5	404	187	151	36	217	44.74%	53.71%	58.97%
#134		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#135		0	0	3	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%
#136		61	0	0	61	20	5	0	36	12	10	2	24	44.44%	66.67%	70.59%
#137		0	504	0	504	238	88	6	172	59	41	18	113	28.83%	65.70%	73.38%
#138		78	0	0	78	19	8	0	51	16	13	3	35	52.24%	68.63%	72.92%
#139		0	0	33	33	10	10	0	13	2	2	0	11	47.83%	84.62%	84.62%
#140		57	0	0	57	16	11	1	29	13	8	5	16	40.00%	55.17%	66.67%
#141		367	0	0	367	29	36	8	294	102	89	13	192	61.94%	65.31%	68.33%
#142	1	106	0	0	106	15	5	2	84	35	24	11	49	55.68%	58.33%	67.12%
#143		22	0	0	22	5	9	1	7	6	5	1	1	9.09%	14.29%	16.67%
#144		861	0	0	861	202	103	10	546	254	201	53	292	42.01%	53.48%	59.23%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING							F	LOWTHROUG	Н
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
		****				Total		Pending		Totai		CLEC			_	CLEC Error
					Total Mech	Manual	Auto	Supps		System	BST Caused	Caused	logued SO's	Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSH'8	Fallout	Clarincation		LORS	ranout	Fallout	ranout	issued SO's	Flowingugi		Calculation
#145		13	0	0	13	7	1	0	5	5	5	0	0	0.00%	0.00%	0.00%
#146		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#147		104	0	0	104	30	10	2	62	37	29	8	25	29.76%	40.32%	46.30%
#148		18	0	0	18	2	2	0	14	9	9	0	5	31.25%	35.71%	35.71%
#149		15	0	0	15	6	1	0	8	5	4	1	3	23.08%	37.50%	42.86%
#150		22	0	0	22	2	8	3	9	8	5	3	1	12.50%	11.11%	16.67%
#151		5	0	0	5	3	0	0	2	0	0	0	2	40.00%	100.00%	100.00%
#152		99	0	0	99	22	11	0	66	28	24	4	38	45.24%	57.58%	61.29%
#153		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#154		0	0	7	7	4	0	0	3	1	1	0	2	28.57%	66.67%	66.67%
#155		8	0	0	8	0	2	2	4	3	2	1	1	33.33%	25.00%	33.33%
#156		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#157		3	0	0	3	1	1	0	1	0	0	0	1	50.00%	100.00%	100.00%
#158		38	0	0	38	7	0	0	31	6	6	0	25	65.79%	80.65%	80.65%
#159		0	0	92	92	15	30	5	42	40	35	5	2	3.85%	4.76%	5.41%
#160		871	0	0	871	262	113	9	487	239	213	26	248	34.30%	50.92%	53.80%
#161		53	0	0	53	6	3	1	43	16	13	3	27	58.70%	62.79%	67.50%
#162		43	0	0	43	6	5	1	31	9	7	2	22	62.86%	70.97%	75.86%
#163		40	0	0	40	5	11	0	24	9	8	1	15	53.57%	62.50%	65.22%
#164		94	0	0	94	2	8	0	84	28	23	5	56	69.14%	66.67%	70.89%
#165		13	0	0	13	2	0	0	11	4	3	1	7	58.33%	63.64%	70.00%
#166		29	0	0	29	18	2	0	9	4	2	2	5	20.00%	55.56%	71.43%
#167		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#168		167	0	0	167	39	10	2	116	46	35	11	70	48.61%	60.34%	66.67%
#169		0	2	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#170		474	0	0	474	69	55	13	337	148	120	28	189	50.00%	56.08%	61.17%
#171		0	1	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#172		58	0	0	58	23	11	1	23	18	14	4	5	11.90%	21.74%	26.32%
#173		35	0	0	35	3	8	0	24	5	1	4	19	82.61%	79.17%	95.00%
#174		8	0	0	8	5	0	0	3	2	1	1	1	14.29%	33.33%	50.00%
#175		5	0	0	5	0	0	0	5	4	1	3	1	50.00%	20.00%	50.00%
#176		0	0	6	6	0	6	0	0	0	0	0	0	0.00%	0.00%	0.00%
#177		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#178	1	67	0	0	67	17	5	2	43	19	19	0	24	40.00%	55.81%	55.81%
#179		0	140	0	140	44	19	3	74	42	37	5	32	28.32%	43.24%	46.38%
#180		1139	0	0	1139	204	125	21	789	459	387	72	330	35.83%	41.83%	46.03%

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AGGREGATE ORDER TYPES	i iiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiii															
Company Info						LSR PF	ROCESSING							F	LOWTHROUG	iΗ
						L	ESOG									
		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#181		44	0	0	44	4	18	2	20	18	18	0	2	8.33%	10.00%	10.00%
#182		35	0	0	35	2	5	0	28	14	13	1	14	48.28%	50.00%	51.85%
#183		20	0	0	20	3	0	0	17	5	2	3	12	' 70.59%	70.59%	85.71%
#184		184	0	0	184	145	10	1	28	19	13	6	9	5.39%	32.14%	40.91%
#185		6	0	0	6	3	0	0	3	1	1	0	2	33.33%	66.67%	66.67%
#186		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#187		6	0	0	6	1	0	0	5	1	0	1	4	80.00%	80.00%	100.00%
#188		2	0	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#189		12	0	0	12	1	1	0	10	4	4	0	6	54.55%	60.00%	60.00%
#190		13	0	0	13	7	0	0	6	6	4	2	0	0.00%	0.00%	0.00%
#191		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%
#192		11	0	0	11	8	0	0	3	3	3	0	0	0.00%	0.00%	0.00%
LENS Subtotal	T	10449	0	0	10449	2097	1323	166	6863	3354	2604	750	3509	42.74%	51.13%	57.40%
EDI Subtotal	1	0	672	0	672	293	112	9	258	110	83	27	148	28.24%	57.36%	64.07%
TAG Subtotal		0	0	469	469	162	115	8	184	115	94	21	69	21.23%	37.50%	42.33%
TOTAL INTERFACES		10449	672	469	11590	2552	1550	183	7305	3579	2781	798	3726	41.13%	51.01%	57.26%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PF	OCESSING								FLOWT	HROUGH
						L	ESOG									· · · ·
		M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
		1 540		TAC	Total Mech	Manual	Auto	Supps (7 Status)		System	BST Caused	Caused	looved COle	Achieved	Base Coloulation	Excluded
Name	HESH / OCN	LENƏ		TAG	LON 8	Fanoul	Claimication	(Z Status)	Lons	Failout	Failout	Fallout	Issued SU's	Flowunough	Calculation	Calculation
#1	· · · · · · · · · · · · · · · · · · ·	0	107	0	107	31	28	0	48	22	6	16	26	41.27%	54.17%	81.25%
#2		52	0	0	52	3	8	0	41	11	8	3	30	73.17%	73.17%	78.95%
#3	· · · · · · · · · · · · · · · · · · ·	0	3420	0	3420	569	662	0	2189	1955	151	1804	234	24.53%	10.69%	60.78%
#4		967	0	0	967	69	82	16	800	218	106	112	582	76.88%	72.75%	84.59%
#5		12	0	0	12	0	2	0	10	0	0	0	10	100.00%	100.00%	100.00%
#6		0	19	0	19	5	2	0	12	6	6	0	6	35.29%	50.00%	50.00%
#7		0	17	0	17	4	2	0	11	4	2	2	7	53.85%	63.64%	77.78%
#8		30	0	0	30	2	3	7	18	11	10	1	7	36.84%	38.89%	41.18%
#9		1339	0	0	1339	172	116	1	1050	137	116	21	913	76.02%	86.95%	88.73%
#10		0	0	13	13	2	1	0	10	4	3	1	6	54.55%	60.00%	66.67%
#11		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#12		0	0	17	17	13	3	0	1	1	1	0	0	0.00%	0.00%	0.00%
#13		2230	0	0	2230	288	278	36	1628	360	244	116	1268	70.44%	77.89%	83.86%
#14		33	0	0	33	15	6	1	11	7	6	1	4	16.00%	36.36%	40.00%
#15		0	0	2	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#16		0	0	1467	1467	207	253	8	999	424	348	76	575	50.88%	57.56%	62.30%
#17		0	0	896	896	130	130	1	635	256	219	37	379	52.06%	59.69%	63.38%
#18		0	0	18	18	1	1	0	16	16	7	9	0	0.00%	0.00%	0.00%
#19		0	0	23	23	4	4	2	13	6	2	4	7	53.85%	53.85%	77.78%
#20		100	0	0	100	10	10	2	78	31	24	7	47	58.02%	60.26%	66.20%
#21		0	0	1277	1277	281	245	11	740	339	238	101	401	43.59%	54.19%	62.75%
#22		0	193	0	193	163	13	0	17	17	16	11	0	0.00%	0.00%	0.00%
#23		914	0	0	914	499	114	2	299	110	80	30	189	24.61%	63.21%	70.26%
#24		0	0	778	778	120	155	9	494	221	178	43	273	47.81%	55.26%	60.53%
#25		0	0	551	551	112	104	11	324	135	114	21	189	45.54%	58.33%	62.38%
#26		474	0	0	474	49	48	9	368	106	77	29	262	67.53%	71.20%	77.29%
#27		0	198	0	198	25	16	8	149	69	53	16	80	50.63%	53.69%	60.15%
#28		4	0	0	4	3	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#29		0	0	32	32	10	2	0	20	4	4	0	16	53.33%	80.00%	80.00%
#30		63	0	0	63	8	12	0	43	5	3	2	38	77.55%	88.37%	92.68%
#31		688	0	0	688	173	48	6	461	143	120	23	318	52.05%	68.98%	72.60%
#32		2016	0	0	2016	432	139	6	1439	386	314	72	1053	58.53%	73.18%	77.03%
#33		48	0	0	48	0	0	1	47	47	46	1	0	0.00%	0.00%	0.00%
#34		4	0	0	4	0	1	1	2	2	2	0	0	0.00%	0.00%	0.00%
#35		0	0	11	11	0	2	2	7	7	3	4	0	0.00%	0.00%	0.00%
#36		93	0	0	93	5	5	9	74	69	48	21	5	8.62%	6.76%	9.43%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWT	IROUGH
						L	ESOG									
		M	echanized	Interface i	Used	Manuai	Rejects	Valic	lated		Errors					
						Total		Pending		Total		CLEC			_	CLEC Error
				740	Total Mech	Manual	Auto	Supps (7 Status)	I CDIa	System	BST Caused	Caused	leaund SO's	Achieved	Base	Excluded
Name	RESH / OCN	LENS	EDI	TAG	LSHS	Fallout	Cianneation	(Z. Status)	Lon 8	Fanoul	Fanout	railout	issueu 30 s	riowunougu	Calculation	Calculation
#37		0	0	579	579	92	87	1	399	175	159	16	224	47.16%	56.14%	58.49%
#38		486	0	0	486	44	34	4	404	73	59	14	331	76.27%	81.93%	84.87%
#39		0	0	2	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#40		98	0	0	98	26	15	0	57	16	10	6	41	53.25%	71.93%	80.39%
#41		0	5	0	5	3	0	0	2	1	1	0	1	20.00%	50.00%	50.00%
#42		0	4	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%
#43		0	0	33	33		12	2	12	4	4		8	42.11%	00.00%	70.00%
#44		42	0	0	42	4	10	2	26	8	1		18	62.07%	69.23%	72.00%
#45		66	0	0	66	12	6	2	<u>44</u> 51	10	14	- 2	20	57.91%	03.04%	74.00%
#46		/0	0	10	10	14	5	0	5	14	13	0	31	28 57%	12.55%	10.00%
#47		0	0	13	13	2		0	J J	1		0	3	42.86%	75.00%	75.00%
#48			0	105	105	11		0	88	34	28	6	54	58.06%	61.36%	65 85%
#49		107	0	105	103	18	6	1	102	19	16	3	83	70.94%	81.37%	83.84%
#50		0	0	2	2	1	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#51		31	0	0	31	4	1	1	25	12	1	11	13	72.22%	52.00%	92.86%
#52		0	0	1	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#54		27	0	0	27	1	5	1	20	3	3	0	17	80.95%	85.00%	85.00%
#55		0	0	2	2	0	0	0	2	2	2	0	0	0.00%	0.00%	0.00%
#56		35	0	0	35	4	2	0	29	7	4	3	22	73.33%	75.86%	84.62%
#57		186	0	0	186	41	8	5	132	48	32	16	84	53.50%	63.64%	72.41%
#58		0	0	240	240	35	26	1	178	93	66	27	85	45.70%	47.75%	56.29%
#59		226	0	0	226	17	24	7	178	32	22	10	146	78.92%	82.02%	86.90%
#60	1	9	0	0	9	0	0	0	9	3	2	1	6	75.00%	66.67%	75.00%
#61		0	6	0	6	2	0	0	4	4	1	3	0	0.00%	0.00%	0.00%
#62		3	0	0	3	2	0	0	1	1	0	1	0	0.00%	0.00%	0.00%
#63		14	0	0	14	0	0	0	14	2	2	0	12	85.71%	85.71%	85.71%
#64		0	24	0	24	11	0	2	11	11	10	1	0	0.00%	0.00%	0.00%
#65		4	0	0	4	0	1	0	3	3	3	0	0	0.00%	0.00%	0.00%
#66		0	0	28	28	7	2	0	19	3	2	1	16	64.00%	84.21%	88.89%
#67		35	0	0	35	1	2	0	32	3	2	1	29	90.63%	90.63%	93.55%
#68		0	114	0	114	31	9	0	74	27	20	7	47	47.96%	63.51%	70.15%
#69		0	0	39	39	12	0	2	25	9	9	0	16	43.24%	64.00%	64.00%
#70		46	0	0	46	7	3	0	36	12	4	8	24	68.57%	66.67%	85.71%
#71		6	0	0	6	0	2	0	4	1	1	0	3	75.00%	75.00%	75.00%
#72		36	0	0	36	13	9	0	14	14	14	0	0	0.00%	0.00%	0.00%

AGGREGATE ORDER TYPES																
Company Info						LSR PA	OCESSING								FLOWTI	IROUGH
						LI	ESOG									
		M	echanized	Interface l	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total	-	Pending		Total	ŀ	CLEC				CLEC Error
			501	TAC	Total Mech	Manual	Auto	Supps		System	BST Caused	Caused	leeved SO's	Achieved	Base Celculation	Excluded
Name	RESH / OCN	LENS	EDI	TAG	Lons	Fallout	Clarification	(2 Status)	Lons	Failout	railout	Failout	Issued SC s	Tiowanough	Oliculation	Calculation
#73		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#74		0	51	0	51	12	7	0	32	6	3	3	26	63.41%	81.25%	89.66%
#75		26	0	0	26	7	4	0	15	6	0	6	9	56.25%	60.00%	100.00%
#76		0	0	882	882	143	13	78	648	489	433	56	159	21.63%	24.54%	26.86%
#77		0	25	0	25	0	6	3	16	16	0	16	0	0.00%	0.00%	0.00%
#78		4	0	0	4	0	2	0	2	2	1	1	0	0.00%	0.00%	0.00%
#79		34	0	0	34	5	4	2	23	11	5	6	12	54.55%	52.17%	70.59%
#80		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#81		96	0	0	96	18	10	0	68	17	10	7	51	64.56%	75.00%	83.61%
#82		0	0	396	396	70		4	292	70	61	9	222	62.89%	76.03%	78.45%
#83		928	0	0	928	138	68	9	713	119	98	21	594	71.57%	83.31%	85.84%
#84		0	0	13	13	0	0	0	13	8	3	5	5	62.50%	38.46%	62.50%
#85		418	0	0	418	201	28	8	181	87	66	21	94	26.04%	51.93%	58.75%
#86		0	0	5	5	0	1	0	4	. 1	1	0	3	75.00%	75.00%	75.00%
#87		205	0	0	205	41	22	5	137	46	37	9	91	53.85%	66.42%	/1.09%
#88		0	0	15	15	0	0	0	15	13	11	2	2	15.38%	13.33%	15.38%
#89		0	0	9971	9971	5039	1314	94	3524	1541	1102	439	1983	24.41%	56.27%	64.28%
#90		8294	0	0	8294	437	744	38	7075	722	553	169	6353	86.52%	89.80%	91.99%
#91		45	0	0	45	0	16	1	28	28	11	17	0	0.00%	0.00%	0.00%
#92		4	0	0	4	0	0	0	4	4	0	4	0	0.00%	0.00%	0.00%
#93		15	0	0	15	0	7	0	8	8	2	6	0	0.00%	0.00%	0.00%
#94	-	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#95		115	0	0	115	51	7	0	57	17	15	2	40	37.74%	70.18%	12.73%
#96		0	0	3	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#97		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#98		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#99		4	0	0	4	0	0	0	4	2	2	0	2	50.00%	50.00%	50.00%
#100	1	0	0	23	23	7	8	0	8	4	2	2	4	30.77%	50.00%	66.67%
#101		195	0	0	195	30	34	1	130	26	23	3	104	66.24%	80.00%	81.89%
#102		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#103		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#104		30	0	0	30	0	2	0	28	23	12	11	5	29.41%	17.86%	29.41%
#105		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#106		4	0	0	4	0	0	0	4	1	0	1	3	100.00%	75.00%	100.00%
#107		0	0	2	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#108		5	0	0	5	0	0	0	5	3	2	1	2	50.00%	40.00%	50.00%

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Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWTH	HROUGH
						L	ESOG									
		M	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC				CLEC Error
	prov (oou		501	TAC	Total Mech	Manual	Auto	Supps (7 Statue)	I SP'e	System Fellout	BST Caused	Caused	leeued SO'e	Achieved	Base Calculation	Excluded Calculation
Name	RESH / OCN	LENS	EDI	TAG	Lons	Failout		(z. Status)	LJN 8		ranout	T dilout		Tiowanough	Galcalation	Outculdaon
#109		135	0	0	135	35	16	2	82	44	35	9	38	35.19%	46.34%	52.05%
#110		4	0	0	4	2	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#111		7	0	0	7	0	1	0	6	0	0	0	6	100.00%	100.00%	100.00%
#112		0	0	977	977	123	57	15	782	81	42	39	701	80.95%	89.64%	94.35%
#113		401	0	0	401	15	14	0	372	7	6	1	365	94.56%	98.12%	98.38%
#114		65	0	0	65	2	9	3	51	16	12		35	71.43%	68.63%	74.47%
#115		13	0	0	13	1	2	0	10	4	2	2	6	66.67%	60.00%	75.00%
#116		0	61	0	61	26	11	0	24	4	4	0	20	40.00%	83.33%	83.33%
#117		12	0	0	12	7	4	0	1	1	0	1	0	0.00%	0.00%	0.00%
#118		11	0	0	11	0	2	0	9	8	2	6	1	33.33%	11.11%	33.33%
#119		45	0	0	45	0	0	2	43	43	27	16	0	0.00%	0.00%	0.00%
#120		64	0	0	64	34	8	0	22	15	11	4	7	13.46%	31.82%	38.89%
#121		40	0	0	40	18	8	0	14	14	11	3	0	0.00%	0.00%	0.00%
#122		45	0	0	45	15	19	0	11	11	7	4	0	0.00%	0.00%	0.00%
#123		0	0	23	23	2	2	0	19	3	2	1	16	80.00%	84.21%	88.89%
#124		10	0	0	10	1	4	0	5	5	5	0	0	0.00%	0.00%	0.00%
#125		0	520	0	520	384	56	0	80	48	30	18	32	7.17%	40.00%	51.61%
#126		12	0	0	12	0	6	0	6	5	3	2	1	25.00%	16.67%	25.00%
#127		0	0	7	7	7	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#128		2	0	0	2	0	0	1	1	0	0	0	11	100.00%	100.00%	100.00%
#129		2	0	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#130		0	18665	0	18665	3331	2245	7	13082	1959	1517	442	11123	69.64%	85.03%	88.00%
#131		10	0	0	10	2	4	0	4	3	0	3	1	33.33%	25.00%	100.00%
#132		0	399	0	399	249	121	2	27	27	7	20	0	0.00%	0.00%	0.00%
#133		0	607	0	607	398	91	4	114	44	31	13	70	14.03%	61.40%	69.31%
#134		65	0	0	65	0	5	3	57	21	12	9	36	75.00%	63.16%	75.00%
#135		0	0	4	4	0	0	1	3	0	0	0	3	100.00%	100.00%	100.00%
#136		67	0	0	67	23	4	3	37	16	10	6	21	38.89%	56.76%	67.74%
#137	1	0	0	13	13	0	4	0	9	2	0	2	7	100.00%	77.78%	100.00%
#138		0	0	4	4	2	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#139		104	0	0	104	55	41	0	8	8	8	0	0	0.00%	0.00%	0.00%
#140		8	0	0	8	3	0	1	4	4	4	0	0	0.00%	0.00%	0.00%
#141		21	0	0	21	0	4	4	13	13	11	2	0	0.00%	0.00%	0.00%
#142		11	0	0	11	0	8	0	3	3	2	1	0	0.00%	0.00%	0.00%
#143		0	0	14	14	0	0	0	14	4	1	3	10	90.91%	71.43%	90.91%
#144		0	0	36	36	0	5	1	30	11	8	3	19	70.37%	63.33%	70.37%

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWTI	HROUGH
						L	esog									
		M	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors					
						Total		Pending		Total		CLEC			_	CLEC Error
			EDL	TAC	Total Mech	Manual Fallout	Auto	Supps (7 Status)	I CD'e	System Fellout	BST Caused	Caused	leeued SO'e	Achieved	Base Celculation	Excluded
Name	HESH / UCN	LENS	EUI	TAG	Lons	Fanoul	Clarification	(Z Status)	Lons	Fanout	railout	FailOut	Issueu 30 s	Flowthough	Calculation	Calculation
#145		5	0	0	5	1	0	2	2	1	1	0	1	33.33%	50.00%	50.00%
#146		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#147		0	0	8	8	0	1	0	7	4	4	0	3	42.86%	42.86%	42.86%
#148		186	0	0	186	48	107	1	30	30	26	4	0	0.00%	0.00%	0.00%
#149		2	0	0	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
#150		0	0	54	54	15	4	1	34	26	11	15	8	23.53%	23.53%	42.11%
#151		5	0	0	5	2	2	0	1	0	0	0	1	33.33%	100.00%	100.00%
#152		0	0	2	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#153		0	1536	0	1536	816	139	4	577	140	73	67	437	32.96%	75.74%	85.69%
#154		141	0	0	141	56	12	1	72	37	31	6	35	28.69%	48.61%	53.03%
#155		2	0	0	2	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#156		4	0	0	4	0	0	2	2	0	0	0	2	100.00%	100.00%	100.00%
#157		0	417	0	417	210	74	6	127	80	58	22	47	14.92%	37.01%	44.76%
#158		592	0	0	592	94	28	4	466	114	95	19	352	65.06%	75.54%	78.75%
#159		75	0	0	75	4	49	2	20	20	18	2	0	0.00%	0.00%	0.00%
#160		40	0	0	40	4	17	1	18	18	11	7	0	0.00%	0.00%	0.00%
#161		0	169	0	169	115	11	3	40	21	18	3	19	12.50%	47.50%	51.35%
#162		12	0	0	12	0	2	2	8	4	2	2	4	66.67%	50.00%	66.67%
#163		2417	0	0	2417	487	266	9	1655	688	566	122	967	47.87%	58.43%	63.08%
#164		133	0	0	133	1	23	4	105	39	12	27	66	83.54%	62.86%	84.62%
#165		0	0	94	94	47	19	1	27	9	6	3	18	25.35%	66.67%	75.00%
#166		8189	0	0	8189	870	490	93	6736	1588	1402	186	5148	69.38%	76.43%	78.60%
#167		0	272	0	272	165	54	0	53	19	15	4	34	15.89%	64.15%	69.39%
#168		786	0	0	786	146	83	9	548	194	164	30	354	53.31%	64.60%	68.34%
#169		18	0	0	18	0	9	1	8	6	5	1	2	28.57%	25.00%	28.57%
#170		0	94	0	94	56	3	1	34	13	12	1	21	23.60%	61.76%	63.64%
#171	1	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#172		3	0	0	3	1	1	0	1	1	1	0	0	0.00%	0.00%	0.00%
#173		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#174		19	0	0	19	5	3	6	5	5	3	2	0	0.00%	0.00%	0.00%
#175		0	559	0	559	109	61	2	387	127	101	26	260	55.32%	67.18%	72.02%
#176		606	0	0	606	85	99	7	415	128	111	17	287	59.42%	69.16%	72.11%
#177		0	137	0	137	33	41	2	61	17	11	6	44	50.00%	72.13%	80.00%
#178		0	0	15	15	9	2	0	4	0	0	0	4	30.77%	100.00%	100.00%
#179		41	0	0	41	4	8	0	29	5	5	0	24	72.73%	82.76%	82.76%
#180		101	0	0	101	6	32	14	49	48	42	6	1	2.04%	2.04%	2.33%

Exhibit June PM Data Attachment 2A

AGGREGATE ORDER TYPES																
Company Info						LSR PR	OCESSING								FLOWT	IROUGH
						L	ESOG									
		Me	chanized	Interface U	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#181		0	8	0	8	0	1	0	7	2	2	0	5	71.43%	71.43%	71.43%
#182		2026	0	0	2026	217	197	23	1589	827	676	151	762	46.04%	47.95%	52.99%
#183		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#184		201	0	0	201	0	1	4	196	38	35	3	158	81.87%	80.61%	81.87%
#185		597	0	0	597	126	114	15	342	194	147	47	148	35.15%	43.27%	50.17%
LENS Subtotal		38418	0	0	38418	5242	3665	403	29108	728 9	5780	1509	21819	66.44%	74.96%	79.06%
EDI Subtotal		0	27627	0	27627	6748	3655	44	17180	4639	2148	2491	12541	58.50%	73.00%	85.38%
TAG Subtotal		0	0	18693	18693	6512	2508	245	9428	4007	3079	928	5421	36.11%	57.50%	63.78%
TOTAL INTERFACES		38418	27627	18693	84738	18502	9828	692	55716	15935	11007	4928	39781	57.41%	71.40%	78.33%

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REPORT: PERCENT LNP FLOW THROUGH SERVICE REQUESTS (SUMMARY) REPORT PERIOD: 06/01/2001 - 06/30/2001

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	Achieved Flow- Through %	ADJUSTED FLOW- THROUGH %
CLEC AGGREGATE REGION ALL SERVICES	54.30%	91.83%

AGGREGATE ORDER TYPES																										
Company Info							LSR	PROCESS	SING			FL	OWTHROU	GH												
		Mechan	ized Interfa	ce Used	Manual	Rejects	Validated		Errors																	
				Total	Total			Total		CLEC			_	CLEC Error												
				Mech	Manual	Auto		System	BST Caused	Caused	Issued	Achieved	Base	Excluded												
Name	RESH / OCN	EDI	TAG	LSR'8	Fallout	Clarification	LSH'S	Fallout	Fallout	Failout	508	Flowthrough	Calculation	Calculation												
#1		322	0	322	167	11	144	83	65	18	61	20.82%	42.36%	48.41%												
#2		621	0	621	274	57	290	55	31	24	235	43.52%	81.03%	88.35%												
#3		0	31	31	17	3	11	10	1	9	1	5.26%	9.09%	50.00%												
#4		4	0	4	1	0	3	0	0	0	3	75.00%	100.00%	100.00%												
#5		1	0	1	0	0	1	0	0	0	1	100.00%	100.00%	100.00%												
#6		0	1588	1588	633	124	831	302	149	153	529	40.35%	63.66%	78.02%												
#7		677	0	677	548	55	74	32	20	12	42	6.89%	56.76%	67.74%												
#8		0	41	41	22	5	14	8	4	4	6	18.75%	42.86%	60.00%												
#9		0	1496	1496	1418	78	0	0	0	0	0	0.00%	0.00%	0.00%												
#10		174	0	174	97	7	70	28	16	12	42	27.10%	60.00%	72.41%												
#11		9	0	9	3	2	4	3	2	1	1	16.67%	25.00%	33.33%												
#12		0	85	85	27	8	50	11	9	2	39	52.00%	78.00%	81.25%												
#13		2046	0	2046	735	156	1155	241	129	112	914	51.41%	79.13%	87.63%												
#10		0	217	217	112	23	82	- 17	11	6	65	34.57%	79.27%	85.53%												
#15		2746	0	2746	308	29	2409	103	44	59	2306	86.76%	95.72%	98.13%												
#15		94	0	94	68	4	22	7	0	7	15	18.07%	68.18%	100.00%												
#17		0	2	2	0	2	0	0	0	0	0	0.00%	0.00%	0.00%												
#10		152	-	152	145	0	7	5	0	5	2	1.36%	28.57%	100.00%												
#10		4230	0	4239	519	83	3637	230	66	164	3407	85.35%	93.68%	98.10%												
#15			83	83	40	5	38	13	3	10	25	36,76%	65.79%	89.29%												
#20		7	0	7	3	0	4	4	3	1	0	0.00%	0.00%	0.00%												
#21		1012	0	1013	636	45	332	116	41	75	216	24,19%	65.06%	84.05%												
#22		013	18	18	7	0	11	8	6	2	3	18,75%	27.27%	33.33%												
#23		262	0	362	150	17	195	83	64	19	112	34 36%	57 44%	63.64%												
#24		12		13	1	2	10	0	0	0	10	90.91%	100.00%	100.00%												
#25		270	0	370	189	<u> </u>	172	76	59	17	96	27.91%	55.81%	61.94%												
#20	 	12950	0	12850	3844	477	8529	1066	540	526	7463	62,99%	87.50%	93.25%												
EDI Subtota		0	3561	3561	2276	248	1037	369	183	186	668	21.36%	64.42%	78.50%												
TOTAL INTERFACES		12850	3561	16411	6120	725	9566	1435	723	712	8131	54.30%	85.00%	91.83%												
										Trun	k Grou	up Perf	orman	ce - Aç	grega	te						 			<u>T</u>	
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Florida			Average t	locking pe	ercentage	by hour	6	6	7	8	9	10		12	13	14	15	16	17	18	19	20	21	22	23	24
		_	1	2	3	4	5	0		•			- 0000	0.0000	0.0040	0.0062	0.00023	0.0029	0.0071	0.0506	0.0417	0.0003	0.0017	0 0001	0 0028	0.0195
Jul-00	NF	BellSouth	0.0000	0.0000	0.0000	0.0000	0.0000	0.0001	0.0025	0.0216	0.0023	0.0018	0.0696	0.0036	0.0043	0.0002	0 1125	0 1642	0.1449	0.0568	0 0510	0 0869	0 1488	0 1304	0.0850	0 0497
		Difference	-0 0187	-0.0011	0 0000	-0.0025	-0.0697	-0.0272	0.0025	0.0079	-0 0343	-0.0635	0 0665	-0.0696	-0.0474	-0 0531	-0 1063 0.0151	-0.1604	-0 1378 0.0141	-0.0062	-0.0093	0.0015	0.0026	0.0432	0 0043	0.0002
	SF	BellSouth	0.0003	0.0000	0.0000	0 0014	0.0000	0.0042	0.0469	0.1511	0.2000	0.2013	0.3152	0.3400	0 2127	0.2187	0.1886	0.2476	0.2047	0.1738	0.1517	0.1558	0 2024	0 3215	0.1808	0 0340
		Difference	-0.0070	-0 0007	-0.0008	-0.0034	-0 0025	-0.0042	-0.0449	-0.0927	-0.1865	-0 2003	-0.3046	-0.3304	-0 1879	-0.1920	-01/34	-0.2354	-0.1907	-0.1586	-0.1473	-0 1343	-0 1350	0.2,00		
Aug-00	NF	BellSouth	0.0000	0.0001	0.0000	0.0000	0.0000	0.0004	0.0025	0.0083	0.0048	0.0071	0.0024	0.0037	0.0066	0.0022	0 0019	0.0025	0.0066	0.0044	0.0054	0.0045	0.0339	0.0247	0.0002	0.0005
		CLEC	0.0164	0.0000	0.0018	0.0000	0.0124	0.0122	0.0000	0.0003	-0.0393	-0.0385	-0.0194	-0.0096	-0.0275	-0.0037	-0 0299	-0.0630	-0.0393	-0 0187	-0 0333	-0.0196	-0.0581	-0.1768	-0 1067	-0.0816
L	SF	BellSouth	0.0013	0.0000	0.0002	0.0000	0.0002	0 0006	0.0001	0.0239	0.0145	0 0012	0.0097	0.0106	0.0135	0.0123	0 0086	0.0116	0.0185	0 0170	0.0039	0.0065	0.0347	0.0665	0.0012	0 0524
		CLEC	0.0108	0.0004	0.0003	0.0019	0.0002	-0.0038	-0.0581	-0.2158	-0.2063	-0 2444	-0 2614	-0 2501	-0.2068	-0.1993	-0.2768	-0.2387	02166	-0 2265	-0.2308	-0.2670	-0 2964	-0 3666	-0 2146	-0.0520
	I	Datatetice	-0.0033	-0.0001	0 0000	0.0012			0.0000	0.0000	0.0022	0.0152	0.0106	0.0300	0.0404	0.0022	0.0044	0.0010	0.0016	0 0210	0 0006	0 0019	0 0506	0 0374	0 0001	0.0000
Sep-00	NF	BellSouth	0.0000	0.0000	0.0021	0.0000	0.0000	0.0004	0.0008	0.0020	0.0023	0.0472	0.0471	0 0205	0 0058	0.0021	0 0603	0 0037	0.0043	0.0065	0 0088	0 1071	0.1115	0.2683	0.0656	0 0090
		Difference	-0.0104	-0.0002	-0.0203	0.0000	-0.0221	-0.0010	-0.0002	-0.0033	-0.0115	-0.0320	-0.0365	0.0105	0 0346	0.0692	-0 0559	-0 0026	-0.0027	0.1003	0.0467	0.0467	0 0910	0.1364	0 0132	0.0032
	SF	BellSouth	0.0002	0.0039	0.0010	0 0011	0.0000	0.0002	0.0346	0.2650	0.1887	0 0884	0 1032	0.1029	0 0872	0 0977	0.0918	0.1204	0.1906	0.2477	0 1692	0.1639	0 2998	0.3763	0 1803	0 2295
		Difference	-0.1398	-0.0031	-0.0113	-0 0057	0.0000	-0.0009	-0.0308	0 0313	-0 1241	-0 0789	-0 0787	-0 0788	-0 0497	-0.0385	-0.0453	-00/11	-0 1424	-0.1474	-0.1224	-01172	-02000	-02333		
Oct-00	NF	BellSouth	0.0000	0.0001	0.0000	0.0000	0 0000	0.0001	0 0005	0.0058	0.0023	0 0004	0 0015	0.0013	0.0201	0.0058	0.0017	0.0260	0 0178	0.0120	0.0005	0 0389	0.0692	0.0021	0.0422	0.0000
		CLEC	0.0000	0.0003	0.0000	0.0075	0 0116	0.0124	0.0246	0 0267	0.0227	-0.0305	-0.0359	-0.0431	-0.0225	-0.0460	-0.0445	-0.0196	-0 1072	-0.0844	-0.0251	-0 1093	-0 1304	-0 1160	-0.0422	-0.0024
	SF	BellSouth	0.0001	0.0000	0.0000	0 0000	0 0007	0 0001	0.0048	1.0465	0.0611	0.1227	0 1306	0 1554	0.0797	0 0908	0 1474	0 1861	0.1597	0.0782	0 1886	0 1202	0 1576	0 0847	0.0688	0.0267
		CLEC	0.0064	0.0015	-0.0009	0 0302	0 0042	-0.0146	0.0135	0 0018	-0.0420	-0.1197	-0 3455	-0 4689	-0 2570	-0 1907	-0.1860	-0 4912	-0 2919	-0 4404	-0 4782	-0 4360	-0 6576	0.4132	-0 0526	-0 0244
		Dinerence		-0.0013	0 0000			0.0004	0.0400	0.0076	0.0044	0.0026	0.0136	0.0043	0.0202	0.0347	0.0095	0 0026	0 0093	0.0068	0.0022	0 0487	0.0534	0 0008	0.0006	0.0003
Nov-00	NF	BellSouth	0.0000	0.0048	0.0000	0.0000	0.0004	0.0001	0.0101	0.0078	0.0153	0.0195	0 0295	0.0388	0.0275	0.0194	0.0195	0.0235	0.0572	0 0388	0 0743	0.3211	0 3956	0 3121	0 1101	0.0175
	<u> </u>	Difference	0.0002	0.0033	0.0000	-0.0020	-0 0154	-0.0015	0.0088	-0 0093	-0.0109	-0.0159	0 0159	-0.0345	-0.0072	0.0269	0.0741	0 0631	0.0478	0 0328	0.0121	0 2856	0.0830	0 0093	0 0087	0 1080
	SF	BellSouth	0.0001	0.0003	0.0000	0.0001	0.0508	0.0009	0.0024	0 2590	0.0570	0.0561	0.1436	0 0904	0.1293	0 2250	0 1318	0 1112	0 1385	0 2163	0 1705	0.3610	0.2158	0 2098	0 1247	0.1014
		Difference	-0 0042	-0 0005	-0.0141	-0.0029	-0.0508	-0.0004	0 0144	-0 1821	-0 0503	-0 0363	-0 1006	-0 0739	-0 1190	-0 1982	1 -0 05/8	-0.0991	-0.0765	-0 1855	-01555	00/34	01020	0.2000		
Dec-00	NF	BellSouth	0.0000	0.0002	0.0002	0.0000	0.0000	0.0001	0.0167	0.0186	0 0108	0.0071	0.0053	0.0143	0.0133	0.0157	0.0124	0.0094	0.0130	0.0096	0.0019	0.0605	0.0592	0 0074	0 0000	0.0000
		CLEC	0.0009	0.0002	0.0014	0.0010	0.0192	0 0060	0.0005	-0.0076	-0.0643	-0.0487	-0 0434	-0.0327	0.0045	0.0004	0.0008	-0 0047	-0 0040	-0 0006	-0.0160	-0 0393	-0.0485	-0 0796	-0 0274	-0.0085
	SF	BellSouth	0 0025	0.0026	0.0004	0 0006	0.0000	0 0010	0 0281	0.1194	0.0723	0 1712	0 1686	0.1635	0.1162	0.0818	0.0875	0 0712	0 0682	0.0945	0 1315	0.0629	0 0/92	0.1025	0 0156	0 0049
		CLEC	0.0004	0.0000	-0.0008	0.0006	-0.0052	-0.0247	-0.0927	-0 1341	-0.1164	0 0146	0 0758	0.0636	0.0065	-0.0042	0 0034	-0.0180	-0 0602	-0.1701	-0.0627	-0.0311	-0 0085	-0.0602	-0 0137	0 0009
		Childrence	0.002	0.0000	0.0000	0.0000	0.0000	0,0000	0.0027	0.0056	0.0012	0 0007	0.0039	0.0037	0.0037	0.0013	0 0012	0.0104	0 0379	0 0110	0.0061	0.1843	0 3420	0.0163	0 0000	0.0000
Jan-01	NF	BellSouth	0.0000	0.0000	0.0005	0.0000	0.0000	0.0009	0.0002	0.0006	0.0025	0 0178	0.0153	0.0084	0 0042	0.0066	0.0132	0.0315	0 0687	0.0247	0.0566	0 4227	0.6889	02345	-0.0272	0.0015
<u></u>	-	Difference	-0.002	7 -0.0001	0.0001	-0.0001	0.0000	-0.0009	0.0024	0.0050	-0.0012	0.0056	0.0114	0.0146	0.0005	0.0053	0.0241	0.0688	0 0727	0 0388	0 0102	0.0301	0 0499	0 0064	0 0039	0 0013
	SF	CLEC	0 0030	0.0010	0.0001	0.0000	0.0261	0 0954	0.0272	0 1394	0 0829	0 0397	0 1624	0 2275	0 0997	0.0409	0 0643	0 1242	0 2107	0 3766	0 1524	0.2638	0 4444	0 3759	-0 0202	-0 0246
	-	Difference	-0.025	-0 0010	-0.0045	0 0000	-0.0261	-0 0954	-0.0219	-0.1266	-0 0449	-0.0341	-01431	-02130	-0 0313	-0.0100	00102					0.0744	0.0470	0.0040	0.0000	0.0010
Feb-01	NF	BellSouth	0 0000	0.0001	0 0000	0 0000	0.0000	0.0000	0.0000	0.0040	0.0003	0.0002	0.0062	0.0130	0.0073	0.0206	0.0154	0.0095	0.0131	0.0218	0.0089	0.0/14	0.9690	0 4856	0 0288	0 0018
	-	CLEC	0.000	3 0 0002	0.0002	-0.0002	-0 0779	-0.0292	0.0000	0.0038	-0.0256	-0 0945	-0 1209	-0 0891	-0.0454	-0.0167	-0.0681	-0.0888	-0.0733	-0.0141	-0.0576	-0.4285	-0 7213	-0 4546	-0 0288	-0 0008
	SF	BellSouth	0.000	1 0.0000	0.0000	0 0000	0.0000	0.0006	0.0089	0.0102	0.0161	0.0214	0.0167	0.0230	0.0131	0.0273	0 1393	0.0378	0 3487	0.4954	0 1330	0 1577	0 3080	0 3467	0 0211	0.0017
· ·		Difference	-0 000	6 0.0062 5 -0.0062	-0.0169	-0.0032	-0.0217	-0 0001	-0.0106	-0.1055	-0.1559	-0.0606	-0 3381	-0 4184	-0.0473	-0.0224	-0 1177	-0.3186	-0 2911	-0 4359	-0 1296	-0.1235	-0 2750	-0 3217	0 0210	-0.0008
		0.00	0.000	1 0.0000	0.0004	0.0000	0.000	0.0001	0.0027	0.0582	0.0131	0.0193	0.0211	0.0294	0 0060	0.0097	0 0122	0 0227	0 0332	0 0260	0.0143	0 0461	0.0735	0 0068	0 0001	0 0047
Mar-01	FL	CLEC	0 491	4 0.0066	0.0004	0.0000	0.0008	0 0070	0.0170	0 1675	0.0418	0.0329	0 0980	0 129	0 0504	0 0292	2 0.0502	0 1276	02120	0 2847	0.1275	0 1480	0 2645	0 1083	0 0055	0 0256
		Difference	-0 491	3 -0.0066	0 0049	-0 0072	-0 0008	-0.0069	-0 0144	-0.1093	-0.0287	1 -0 0137	-0.076	-0.0995	J -0 0444	-0019	-0.0300	-01043	1	0100	0.110		0.1010		0.0000	0.0004
Apr-01	FL	BellSouth	0 000	8 0 0001	0.0000	0 0053	0 0000	0 0003	0 0011	0 0082	0 0234	0 0025	0.0320	0.035	0 0134	0.028	6 0.029	0.0487	0.0449	0 0.0114 0 3232	0.0008	0.0034	0 0104	0 0100	0.0002	0 0004
	Ţ.	CLEC	0.001	0 0 0028	3 0.0007	0.0293	0 0002	0 0011	0.0150	0.0501	0.0764	-0.0290	0.028	0.042	3 -0.016	0.020	2 -0.019	-0.0490	-0 186	-0 31 18	-0.0921	-0 0388	0 0767	0 1329	0 0379	-0 0043
		Uniterence	-0.000	0.0021	0.0007	0.02.40	5 0002		0.000	- 0 ++C2	0.007	0.000	0.015	0.079	0.0074	0 103	9 0.098	0 0564	0 056	0 0 0 1 74	0.004	0 0039	0 0060	0 0023	0 0003	0 0002
May-01	FL	BellSouth	0,000	1 0.000	0.0094	0 0000	0 0000	0.0040	0.0183	0.190	0.06/5	0 0255	0 031	0.072	3 0.0154	0.033	5 0.051	0 1592	0 202	0 3416	0 0852	0 0391	0 0845	0 1109	0.0386	0.0024
		Difference	-0 003	0 -0 0420	8 0 0066	3 -0.0109	0 0218	-0 0035	-0 0153	-0.0666	-0.0546	0 0200	0 016	0 011	6 -0 0078	8 0.070	5 0.046	j -0.1026	-0 146	/10 3241	0.080	oj ∹0-0352	-0.0785	-v 1086	-0.0003	~0.0021
lun 04	1	BollCourts	0.000	0 000		0.0000	0.000	0 0004	0 0021	0 0506	0.0686	6 0 0047	0 012	0.017	2 0.010	0 0 0 10	4 0.007	0 0033	0 005	0.0117	0 001	0 0025	0 0132	0 0034	0.0145	0 0005
Jun-01	- FL	CLEC	0 113	9 0 0374	4 0.0890	0.0669	0.077	0 0678	0.0278	0.0296	0 040	0.0946	0 084	0.084	6 0.0413	3 0.029	2 0.066	0 0916	5 0 069 3 -0 064		0.062	-0 1385	-0 3694	-0 2859	-0 1012	-0.0521
		Difference	-0 113	7 -0 0374	4 -0.0890	0 0669	-0.077	-0.0674	-0.0257	10210	0.0281	-0.0899	-00/2	-0.00/	1 0.000	1 0010	-1	1 50000	, , , , , , , , , , , , , , , , , , , ,	J						

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