

Lisa S. Foshee
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BellSouth Telecommunications, Inc.
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September 28, 2001

Mrs. Blanca S. Bayó
Director, Division of the Commission Clerk
and Administrative Services
Florida Public Service Commission
2540 Shumard Oak Boulevard
Tallahassee, FL 32399-0850

Re: 960786-B-TL (Section 271)

Dear Ms. Bayó:

Enclosed please find the original and fifteen copies of BellSouth Telecommunications, Inc.'s Notice of Filing with attached Affidavit of Alphonso J. Varner which we ask that you file in the captioned docket.

A copy of this letter is enclosed. Please mark it to indicate that the original was filed and return the copy to me. Copies have been served to the parties by Federal Express as shown on the attached Certificate of Service.

Sincerely,

Lisa S. Foshee
Lisa S. Foshee (LA)

Enclosures

cc: All Parties of Record
Marshall M. Criser III
Fred J. McCallum

DOCUMENT NUMBER DATE

12309 SEP 28 01

FPSC-COMMISSION CLERK

**CERTIFICATE OF SERVICE
DOCKET NO. 960786-B-TL**

I HEREBY CERTIFY that a true and correct copy of the foregoing was served by
Federal Express this 28th day of September, 2001 to the following:

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Lisa S. Foshee (LA)

(+) Signed Protective Agreement

BEFORE THE FLORIDA PUBLIC SERVICE COMMISSION

In Re: Consideration of BellSouth)
Telecommunications, Inc.'s entry into)
interLATA services pursuant to Section)
271 of the Federal Telecommunications)
Act of 1996.)
_____)

Docket No. 960786-B-TL

Filed: September 28, 2001

BELLSOUTH TELECOMMUNICATIONS, INC.'S NOTICE OF FILING

BellSouth Telecommunications, Inc. ("BellSouth") hereby files the Affidavit of Alphonso J. Varner that attaches BellSouth's performance data reflecting performance for the month of July 2001. The Affidavit and the accompanying attachments describe the performance data and explain the conclusions that can be drawn from it.

Respectfully submitted this 28th day of September 2001.

BELLSOUTH TELECOMMUNICATIONS, INC.

Nancy B. White

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Before the
Florida Public Service Commission
Tallahassee, Florida

AFFIDAVIT OF ALPHONSO J. VARNER
ON BEHALF OF BELLSOUTH TELECOMMUNICATIONS, INC.
FILED SEPTEMBER 28, 2001

I, Alphonso J. Varner, being of lawful age and duly sworn upon my oath, depose and state:

1. My name is Alphonso J. Varner. I am employed by BellSouth as Senior Director in Interconnection Services. My business address is 675 West Peachtree Street, Atlanta, Georgia 30375.

PROFESSIONAL AND EDUCATIONAL BACKGROUND

2. I graduated from Florida State University in 1972 with a Bachelor of Engineering Science degree in systems design engineering. I immediately joined Southern Bell in the division of revenues organization with the responsibility for preparation of all Florida investment separations studies for division of revenues and for reviewing interstate settlements.
3. Subsequently, I accepted an assignment in the rates and tariffs organization with responsibilities for administering selected rates and tariffs including preparation of tariff filings. In January 1994, I was appointed Senior Director of Pricing for the nine-state region. I was named Senior Director for Regulatory Policy and Planning in August 1994.

In April 1997, I was named Senior Director of Regulatory for the nine-state BellSouth region, and I accepted my current position in March 2001.

II. PURPOSE OF AFFIDAVIT

4. The purpose of my Affidavit is to provide data specific to BellSouth's operations in Florida. This filing reflects performance for the month of July 2001. Exhibit July PM Data and Attachments 1B through 3B that accompany this filing describe the data and explain the conclusions that can be drawn from it.

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Resale - Ordering										
% Rejected Service Requests - Mechanized										
A 111	O-7	Residence/FL(%)	Diagnostic		13 39%	43,322				Diagnostic
A 112	O-7	Business/FL(%)	Diagnostic		20 66%	2,464				Diagnostic
A 113	O-7	Design (Specials)/FL(%)	Diagnostic							Diagnostic
A 114	O-7	PBX/FL(%)	Diagnostic		0 00%	5				Diagnostic
A 115	O-7	Centrex/FL(%)	Diagnostic							Diagnostic
A 116	O-7	ISDN/FL(%)	Diagnostic							Diagnostic
% Rejected Service Requests - Partially Mechanized										
A 121	O-7	Residence/FL(%)	Diagnostic		30 13%	14,684				Diagnostic
A 122	O-7	Business/FL(%)	Diagnostic		39 01%	1,884				Diagnostic
A 123	O-7	Design (Specials)/FL(%)	Diagnostic							Diagnostic
A 124	O-7	PBX/FL(%)	Diagnostic		0 00%	4				Diagnostic
A 125	O-7	Centrex/FL(%)	Diagnostic							Diagnostic
A 126	O-7	ISDN/FL(%)	Diagnostic		0 00%	1				Diagnostic
% Rejected Service Requests - Non-Mechanized										
A 131	O-7	Residence/FL(%)	Diagnostic		47 06%	527				Diagnostic
A 132	O-7	Business/FL(%)	Diagnostic		43 11%	726				Diagnostic
A 133	O-7	Design (Specials)/FL(%)	Diagnostic		44 87%	78				Diagnostic
A 134	O-7	PBX/FL(%)	Diagnostic		27 45%	51				Diagnostic
A 135	O-7	Centrex/FL(%)	Diagnostic		66 67%	3				Diagnostic
A 136	O-7	ISDN/FL(%)	Diagnostic		47 37%	19				Diagnostic
Reject Interval - Mechanized										
A 141	O-8	Residence/FL(%)	>= 97% w in 1 hr		96 00%	5,799				NO
A 142	O-8	Business/FL(%)	>= 97% w in 1 hr		98 82%	509				YES
A 143	O-8	Design (Specials)/FL(%)	>= 97% w in 1 hr							
A 144	O-8	PBX/FL(%)	>= 97% w in 1 hr							
A 145	O-8	Centrex/FL(%)	>= 97% w in 1 hr							
A 146	O-8	ISDN/FL(%)	>= 97% w in 1 hr							
Reject Interval - Partially Mechanized - 24 hours										
A 151	O-8	Residence/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
A 152	O-8	Business/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
A 153	O-8	Design (Specials)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
A 154	O-8	PBX/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
A 155	O-8	Centrex/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
A 156	O-8	ISDN/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
Reject Interval - Partially Mechanized - 18 hours										
A 161	O-8	Residence/FL(%)	>= 85% w in 18 hrs		97 45%	4,424				YES
A 162	O-8	Business/FL(%)	>= 85% w in 18 hrs		96 46%	735				YES
A 163	O-8	Design (Specials)/FL(%)	>= 85% w in 18 hrs							
A 164	O-8	PBX/FL(%)	>= 85% w in 18 hrs							
A 165	O-8	Centrex/FL(%)	>= 85% w in 18 hrs							
A 166	O-8	ISDN/FL(%)	>= 85% w in 18 hrs							
Reject Interval - Non-Mechanized										
A 181	O-8	Residence/FL(%)	>= 85% w in 24 hrs		97 98%	248				YES
A 182	O-8	Business/FL(%)	>= 85% w in 24 hrs		99 04%	313				YES
A 183	O-8	Design (Specials)/FL(%)	>= 85% w in 24 hrs		94 29%	35				YES
A 184	O-8	PBX/FL(%)	>= 85% w in 24 hrs		92 86%	14				YES
A 185	O-8	Centrex/FL(%)	>= 85% w in 24 hrs		100 00%	2				YES
A 186	O-8	ISDN/FL(%)	>= 85% w in 24 hrs		100 00%	9				YES
FOC Timeliness - Mechanized										
A 191	O-9	Residence/FL(%)	>= 95% w in 3 hrs		98 50%	31,389				YES
A 192	O-9	Business/FL(%)	>= 95% w in 3 hrs		99 60%	1,250				YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A 1 9 3	O-9	Design (Specials)/FL(%)	>= 95% w in 3 hrs							
A 1 9 4	O-9	PBX/FL(%)	>= 95% w in 3 hrs							
A 1 9 5	O-9	Centrex/FL(%)	>= 95% w in 3 hrs							
A 1 9 6	O-9	ISDN/FL(%)	>= 95% w in 3 hrs							
FOC Timeliness - Partially Mechanized										
A 1 10 1	O-9	Residence/FL(%)	>= 85% w in 36 hrs							
A 1 10 2	O-9	Business/FL(%)	>= 85% w in 36 hrs							
A 1 10 3	O-9	Design (Specials)/FL(%)	>= 85% w in 36 hrs							
A 1 10 4	O-9	PBX/FL(%)	>= 85% w in 36 hrs							
A 1 10 5	O-9	Centrex/FL(%)	>= 85% w in 36 hrs							
A 1 10 6	O-9	ISDN/FL(%)	>= 85% w in 36 hrs							
FOC Timeliness - Partially Mechanized - 18 hours										
A 1 11 1	O-9	Residence/FL(%)	>= 85% w in 18 hrs		96 63%	11,113				YES
A 1 11 2	O-9	Business/FL(%)	>= 85% w in 18 hrs		94 84%	1,356				YES
A 1 11 3	O-9	Design (Specials)/FL(%)	>= 85% w in 18 hrs							
A 1 11 4	O-9	PBX/FL(%)	>= 85% w in 18 hrs		50 00%	4				NO
A 1 11 5	O-9	Centrex/FL(%)	>= 85% w in 18 hrs							
A 1 11 6	O-9	ISDN/FL(%)	>= 85% w in 18 hrs		100 00%	1				YES
FOC Timeliness - Non-Mechanized										
A 1 13 1	O-9	Residence/FL(%)	>= 85% w in 36 hrs		97 86%	374				YES
A 1 13 2	O-9	Business/FL(%)	>= 85% w in 36 hrs		99 03%	411				YES
A 1 13 3	O-9	Design (Specials)/FL(%)	>= 85% w in 36 hrs		96 00%	50				YES
A 1 13 4	O-9	PBX/FL(%)	>= 85% w in 36 hrs		100.00%	25				YES
A 1 13 5	O-9	Centrex/FL(%)	>= 85% w in 36 hrs							
A 1 13 6	O-9	ISDN/FL(%)	>= 85% w in 36 hrs		100 00%	21				YES
FOC & Reject Response Completeness - Mechanized										
A 1 14 1	O-11	Residence/FL(%)	>= 95%		85 86%	43,322				NO
A 1 14 2	O-11	Business/FL(%)	>= 95%		71 35%	2,464				NO
A 1 14 3	O-11	Design (Specials)/FL(%)	>= 95%							
A 1 14 4	O-11	PBX/FL(%)	>= 95%		0 00%	5				NO
A 1 14 5	O-11	Centrex/FL(%)	>= 95%							
A 1 14 6	O-11	ISDN/FL(%)	>= 95%							
FOC & Reject Response Completeness - Partially Mechanized										
A 1 15 1	O-11	Residence/FL(%)	>= 95%		100 00%	14,684				YES
A 1 15 2	O-11	Business/FL(%)	>= 95%		100 00%	1,884				YES
A 1 15 3	O-11	Design (Specials)/FL(%)	>= 95%							
A 1 15 4	O-11	PBX/FL(%)	>= 95%		100 00%	4				YES
A 1 15 5	O-11	Centrex/FL(%)	>= 95%							
A 1 15 6	O-11	ISDN/FL(%)	>= 95%		100 00%	1				YES
FOC & Reject Response Completeness - Non-Mechanized										
A 1 16 1	O-11	Residence/FL(%)	>= 95%		93 49%	753				NO
A 1 16 2	O-11	Business/FL(%)	>= 95%		95 75%	894				YES
A 1 16 3	O-11	Design (Specials)/FL(%)	>= 95%		96 91%	97				YES
A 1 16 4	O-11	PBX/FL(%)	>= 95%		87 80%	41				NO
A 1 16 5	O-11	Centrex/FL(%)	>= 95%		100 00%	2				YES
A 1 16 6	O-11	ISDN/FL(%)	>= 95%		100 00%	21				YES
FOC & Reject Response Completeness (Multiple Responses) - Mechanized										
A 1 17 1	O-11	Residence/FL(%)	>= 95%		100 00%	37,195				YES
A 1 17 2	O-11	Business/FL(%)	>= 95%		100 00%	1,758				YES
A 1 17 3	O-11	Design (Specials)/FL(%)	>= 95%							
A 1 17 4	O-11	PBX/FL(%)	>= 95%							
A 1 17 5	O-11	Centrex/FL(%)	>= 95%							
A 1 17 6	O-11	ISDN/FL(%)	>= 95%							
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized										
A 1 18 1	O-11	Residence/FL(%)	>= 95%		95 40%	14,684				YES
A 1 18 2	O-11	Business/FL(%)	>= 95%		90 50%	1,884				NO

BellSouth Monthly State Summary
Florida, July 2001

A 1 18 3
A 1 18 4
A 1 18 5
A 1 18 6

O-11	Design (Specials)/FL(%)
O-11	PBX/FL(%)
O-11	Centrex/FL(%)
O-11	ISDN/FL(%)

Benchmark /
Analog

>= 95%
>= 95%
>= 95%
>= 95%

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
		100 00%	4				YES
		100 00%	1				YES

A 1 19 1
A 1 19 2
A 1 19 3
A 1 19 4
A 1 19 5
A 1 19 6

FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized

O-11	Residence/FL(%)
O-11	Business/FL(%)
O-11	Design (Specials)/FL(%)
O-11	PBX/FL(%)
O-11	Centrex/FL(%)
O-11	ISDN/FL(%)

>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%

	88 64%	704					NO
	93 69%	856					NO
	96 81%	94					YES
	100 00%	36					YES
	100 00%	2					YES
	100 00%	21					YES

Resale - Provisioning

Order Completion Interval

A 2 1 1 1
A 2 1 1 2
A 2 1 1 2
A 2 1 1 2
A 2 1 2 1
A 2 1 2 1
A 2 1 2 1
A 2 1 2 2
A 2 1 3 1
A 2 1 3 1
A 2 1 3 1
A 2 1 3 2
A 2 1 4 1
A 2 1 4 1
A 2 1 4 1
A 2 1 4 2
A 2 1 5 1
A 2 1 5 1
A 2 1 5 2
A 2 1 5 2
A 2 1 6 1
A 2 1 6 1
A 2 1 6 2
A 2 1 6 2
A 2 1 6 2

P-4	Residence/<10 circuits/Dispatch/FL(days)
P-4	Residence/<10 circuits/Non-Dispatch/FL(days)
P-4	Residence/>=10 circuits/Dispatch/FL(days)
P-4	Residence/>=10 circuits/Non-Dispatch/FL(days)
P-4	Business/<10 circuits/Dispatch/FL(days)
P-4	Business/<10 circuits/Non-Dispatch/FL(days)
P-4	Business/>=10 circuits/Dispatch/FL(days)
P-4	Business/>=10 circuits/Non-Dispatch/FL(days)
P-4	Design (Specials)/<10 circuits/Dispatch/FL(days)
P-4	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
P-4	Design (Specials)/>=10 circuits/Dispatch/FL(days)
P-4	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
P-4	PBX/<10 circuits/Dispatch/FL(days)
P-4	PBX/<10 circuits/Non-Dispatch/FL(days)
P-4	PBX/>=10 circuits/Dispatch/FL(days)
P-4	PBX/>=10 circuits/Non-Dispatch/FL(days)
P-4	Centrex/<10 circuits/Dispatch/FL(days)
P-4	Centrex/<10 circuits/Non-Dispatch/FL(days)
P-4	Centrex/>=10 circuits/Dispatch/FL(days)
P-4	Centrex/>=10 circuits/Non-Dispatch/FL(days)
P-4	ISDN/<10 circuits/Dispatch/FL(days)
P-4	ISDN/<10 circuits/Non-Dispatch/FL(days)
P-4	ISDN/>=10 circuits/Dispatch/FL(days)
P-4	ISDN/>=10 circuits/Non-Dispatch/FL(days)

Res
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Res
Res
Bus
Bus
Bus
Bus
Design
Design
Design
Design
PBX
PBX
PBX
PBX
Centrex
Centrex
Centrex
Centrex
ISDN
ISDN
ISDN
ISDN

4 87	37,428	4 50	1,812	5 131	0 12343	3 0069	YES
0 83	570,798	0 71	30,163	1 288	0 00761	16 1076	YES
4 72	69	3 00	3	3 091	1 82309	0 9460	YES
1 17	2			1 181			
3 13	32,636	3 97	285	6 735	0 40069	-2 0751	NO
1 56	45,339	0 91	1,914	3 582	0 08359	7 8144	YES
10 58	327	4 00	2	17 201	12 19994	0 5392	YES
21 46	13	0 33	2	30 628	23 26371	0 9083	YES
22 17	3,971	4 25	4	25 286	12 64952	1 4163	YES
17 33	52	8 00	1	27 182	27 44182	0 3399	YES
15 00	1			0 000			
12 73	64	5 43	17	13 472	3 67590	1 9853	YES
3 18	307	2 29	24	5 260	1 11483	0 7979	YES
11 25	4	1 00	1	9 069	10 13967	1 0109	YES
3 37	91	1 13	13	4 592	1 36141	1 6447	YES
7 78	575	5 00	9	9 729	3 26832	0 8517	YES
1 64	1,276	2 00	12	2 374	0 68847	-0 5277	YES
16 64	38			22 162			
2 75	100	0 33	1	3 147	3 16260	0 7650	YES
26 88	812	6 00	4	28 116	14 09273	1 4819	YES
11 79	817	11 00	65	58 063	7 48277	0 1052	YES
19 00	5			16 613			
2 62	62	2 67	3	3 049	1 80231	-0 0243	YES

Held Orders

A 2 2 1 1
A 2 2 1 2
A 2 2 1 3
A 2 2 1 2
A 2 2 1 2
A 2 2 1 3
A 2 2 1 1
A 2 2 1 2
A 2 2 1 3
A 2 2 2 1
A 2 2 2 2
A 2 2 2 2
A 2 2 2 3
A 2 2 3 1
A 2 2 3 1
A 2 2 3 1
A 2 2 3 2
A 2 2 3 2
A 2 2 3 2
A 2 2 4 1

P-1	Residence/<10 circuits/Facility/FL(days)
P-1	Residence/<10 circuits/Equipment/FL(days)
P-1	Residence/<10 circuits/Other/FL(days)
P-1	Residence/>=10 circuits/Facility/FL(days)
P-1	Residence/>=10 circuits/Equipment/FL(days)
P-1	Residence/>=10 circuits/Other/FL(days)
P-1	Business/<10 circuits/Facility/FL(days)
P-1	Business/<10 circuits/Equipment/FL(days)
P-1	Business/<10 circuits/Other/FL(days)
P-1	Business/>=10 circuits/Facility/FL(days)
P-1	Business/>=10 circuits/Equipment/FL(days)
P-1	Business/>=10 circuits/Other/FL(days)
P-1	Design (Specials)/<10 circuits/Facility/FL(days)
P-1	Design (Specials)/<10 circuits/Equipment/FL(days)
P-1	Design (Specials)/<10 circuits/Other/FL(days)
P-1	Design (Specials)/>=10 circuits/Facility/FL(days)
P-1	Design (Specials)/>=10 circuits/Equipment/FL(days)
P-1	Design (Specials)/>=10 circuits/Other/FL(days)
P-1	PBX/<10 circuits/Facility/FL(days)

Res
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Bus
Bus
Bus
Bus
Bus
Bus
Design
Design
Design
Design
Design
Design
PBX

10 40	484	10 50	12	12 034	3 51678	-0 0270	YES
0 00	0	0 00	0				YES
21 77	52	42 00	2	27 125	19 54593	-1 0350	YES
0 00	0	0 00	0				YES
0 00	0	0 00	0				YES
0 00	0	0 00	0				YES
12 34	130	43 25	4	20 143	10 22532	-3 0230	NO
0 00	0	0 00	0				YES
27 82	11	0 00	0	31 397			YES
15 50	2	0 00	0	20 506			YES
0 00	0	0 00	0				YES
0 00	0	0 00	0				YES
21 00	2	0 00	0	12 728			YES
0 00	0	0 00	0				YES
37 19	31	0 00	0	80 627			YES
15 86	7			28 038			
10 50	2	0 00	0	2 121			YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
A 224 12	P-1 PBX/<10 circuits/Equipment/FL(days)	PBX	0 00	0	0 00	0			YES	
A 224 13	P-1 PBX/<10 circuits/Other/FL(days)	PBX	0 00	0	0 00	0			YES	
A 224 21	P-1 PBX/>=10 circuits/Facility/FL(days)	PBX	0 00	0	0 00	0			YES	
A 224 22	P-1 PBX/>=10 circuits/Equipment/FL(days)	PBX	0 00	0	0 00	0			YES	
A 224 23	P-1 PBX/>=10 circuits/Other/FL(days)	PBX	0 00	0	0 00	0			YES	
A 225 11	P-1 Centrex/<10 circuits/Facility/FL(days)	Centrex	12 30	10	0 00	0	6 093		YES	
A 225 12	P-1 Centrex/<10 circuits/Equipment/FL(days)	Centrex	0 00	0	0 00	0			YES	
A 225 13	P-1 Centrex/<10 circuits/Other/FL(days)	Centrex	8 00	4	0 00	0	4 243		YES	
A 225 21	P-1 Centrex/>=10 circuits/Facility/FL(days)	Centrex	1 00	1	0 00	0	0 000		YES	
A 225 22	P-1 Centrex/>=10 circuits/Equipment/FL(days)	Centrex	0 00	0	0 00	0			YES	
A 225 23	P-1 Centrex/>=10 circuits/Other/FL(days)	Centrex	0 00	0	0 00	0			YES	
A 226 11	P-1 ISDN/<10 circuits/Facility/FL(days)	ISDN	13 56	9	1 00	1	5 769	6 08074	2 0648	YES
A 226 12	P-1 ISDN/<10 circuits/Equipment/FL(days)	ISDN	0 00	0	0 00	0			YES	
A 226 13	P-1 ISDN/<10 circuits/Other/FL(days)	ISDN	0 00	0	0 00	0			YES	
A 226 21	P-1 ISDN/>=10 circuits/Facility/FL(days)	ISDN	2 00	2	0 00	0	0 000		YES	
A 226 22	P-1 ISDN/>=10 circuits/Equipment/FL(days)	ISDN	0 00	0	0 00	0			YES	
A 226 23	P-1 ISDN/>=10 circuits/Other/FL(days)	ISDN	0 00	0	0 00	0			YES	
% Jeopardies - Mechanized										
A 24 1	P-2 Residence/FL(%)	Res	0 68%	659,134	0 43%	35,314		0 00045	5 6231	YES
A 24 2	P-2 Business/FL(%)	Bus	1 77%	80,736	1 18%	2,619		0 00262	2 2514	YES
A 24 3	P-2 Design (Specials)/FL(%)	Design	32 09%	5,151	0 00%	126		0 04209	7 6237	YES
A 24 4	P-2 PBX/FL(%)	PBX	6 97%	545	0 00%	33		0 04566	1 5271	YES
A 24 5	P-2 Centrex/FL(%)	Centrex	5 54%	2,112	0 00%	12		0 06622	0 8365	YES
A 24 6	P-2 ISDN/FL(%)	ISDN	10 95%	2,063	0 00%	14		0 08376	1 3080	YES
% Jeopardies - Non-Mechanized										
A 25 1	P-2 Residence/FL(%)	Diagnostic			1 71%	351				Diagnostic
A 25 2	P-2 Business/FL(%)	Diagnostic			1 92%	313				Diagnostic
A 25 3	P-2 Design (Specials)/FL(%)	Diagnostic			9 09%	11				Diagnostic
A 25 4	P-2 PBX/FL(%)	Diagnostic			0 00%	40				Diagnostic
A 25 5	P-2 Centrex/FL(%)	Diagnostic			0 00%	25				Diagnostic
A 25 6	P-2 ISDN/FL(%)	Diagnostic			0 00%	65				Diagnostic
Average Jeopardy Notice Interval - Mechanized										
A 27 1	P-2 Residence/FL(hours)	>= 48 hrs			200 68	152				YES
A 27 2	P-2 Business/FL(hours)	>= 48 hrs			265 55	31				YES
A 27 3	P-2 Design (Specials)/FL(hours)	>= 48 hrs								
A 27 4	P-2 PBX/FL(hours)	>= 48 hrs								
A 27 5	P-2 Centrex/FL(hours)	>= 48 hrs								
A 27 6	P-2 ISDN/FL(hours)	>= 48 hrs								
Average Jeopardy Notice Interval - Non-Mechanized										
A 28 1	P-2 Residence/FL(hours)	Diagnostic			140 00	6				Diagnostic
A 28 2	P-2 Business/FL(hours)	Diagnostic			128 00	6				Diagnostic
A 28 3	P-2 Design (Specials)/FL(hours)	Diagnostic			168 00	1				Diagnostic
A 28 4	P-2 PBX/FL(hours)	Diagnostic								Diagnostic
A 28 5	P-2 Centrex/FL(hours)	Diagnostic								Diagnostic
A 28 6	P-2 ISDN/FL(hours)	Diagnostic								Diagnostic
% Jeopardy Notice >= 48 hours - Mechanized										
A 29 1	P-2 Residence/FL(%)	95% >= 48 hrs			69 23%	13				NO
A 29 2	P-2 Business/FL(%)	95% >= 48 hrs			85 71%	7				NO
A 29 3	P-2 Design (Specials)/FL(%)	95% >= 48 hrs								
A 29 4	P-2 PBX/FL(%)	95% >= 48 hrs								
A 29 5	P-2 Centrex/FL(%)	95% >= 48 hrs								
A 29 6	P-2 ISDN/FL(%)	95% >= 48 hrs								
% Jeopardy Notice >= 48 hours - Non-Mechanized										
A 210 1	P-2 Residence/FL(%)	Diagnostic			91 38%	116				Diagnostic
A 210 2	P-2 Business/FL(%)	Diagnostic			87 50%	16				Diagnostic
A 210 3	P-2 Design (Specials)/FL(%)	Diagnostic								Diagnostic
A 210 4	P-2 PBX/FL(%)	Diagnostic								Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

A 2 10 5
A 2 10 6

P-2	Centrex/FL(%)
P-2	ISDN/FL(%)

% Missed Installation Appointments

A 2 11 1 1	P-3	Residence/<10 circuits/Dispatch/FL(%)
A 2 11 1 2	P-3	Residence/<10 circuits/Non-Dispatch/FL(%)
A 2 11 2 1	P-3	Residence/>=10 circuits/Dispatch/FL(%)
A 2 11 2 2	P-3	Residence/>=10 circuits/Non-Dispatch/FL(%)
A 2 11 2 1	P-3	Business/<10 circuits/Dispatch/FL(%)
A 2 11 2 2	P-3	Business/<10 circuits/Non-Dispatch/FL(%)
A 2 11 2 1	P-3	Business/>=10 circuits/Dispatch/FL(%)
A 2 11 2 2	P-3	Business/>=10 circuits/Non-Dispatch/FL(%)
A 2 11 3 1	P-3	Design (Specials)/<10 circuits/Dispatch/FL(%)
A 2 11 3 2	P-3	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)
A 2 11 3 2 1	P-3	Design (Specials)/>=10 circuits/Dispatch/FL(%)
A 2 11 3 2 2	P-3	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)
A 2 11 4 1	P-3	PBX/<10 circuits/Dispatch/FL(%)
A 2 11 4 2	P-3	PBX/<10 circuits/Non-Dispatch/FL(%)
A 2 11 4 2 1	P-3	PBX/>=10 circuits/Dispatch/FL(%)
A 2 11 4 2 2	P-3	PBX/>=10 circuits/Non-Dispatch/FL(%)
A 2 11 5 1	P-3	Centrex/<10 circuits/Dispatch/FL(%)
A 2 11 5 2	P-3	Centrex/<10 circuits/Non-Dispatch/FL(%)
A 2 11 5 2 1	P-3	Centrex/>=10 circuits/Dispatch/FL(%)
A 2 11 5 2 2	P-3	Centrex/>=10 circuits/Non-Dispatch/FL(%)
A 2 11 6 1	P-3	ISDN/<10 circuits/Dispatch/FL(%)
A 2 11 6 2	P-3	ISDN/<10 circuits/Non-Dispatch/FL(%)
A 2 11 6 2 1	P-3	ISDN/>=10 circuits/Dispatch/FL(%)
A 2 11 6 2 2	P-3	ISDN/>=10 circuits/Non-Dispatch/FL(%)

% Provisioning Troubles within 30 Days

A 2 12 1 1	P-9	Residence/<10 circuits/Dispatch/FL(%)
A 2 12 1 2	P-9	Residence/<10 circuits/Non-Dispatch/FL(%)
A 2 12 1 2	P-9	Residence/>=10 circuits/Dispatch/FL(%)
A 2 12 1 2 1	P-9	Residence/>=10 circuits/Non-Dispatch/FL(%)
A 2 12 2 1	P-9	Business/<10 circuits/Dispatch/FL(%)
A 2 12 2 2	P-9	Business/<10 circuits/Non-Dispatch/FL(%)
A 2 12 2 2 1	P-9	Business/>=10 circuits/Dispatch/FL(%)
A 2 12 2 2 2	P-9	Business/>=10 circuits/Non-Dispatch/FL(%)
A 2 12 3 1	P-9	Design (Specials)/<10 circuits/Dispatch/FL(%)
A 2 12 3 1 2	P-9	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)
A 2 12 3 2 1	P-9	Design (Specials)/>=10 circuits/Dispatch/FL(%)
A 2 12 3 2 2	P-9	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)
A 2 12 4 1	P-9	PBX/<10 circuits/Dispatch/FL(%)
A 2 12 4 2	P-9	PBX/<10 circuits/Non-Dispatch/FL(%)
A 2 12 4 2 1	P-9	PBX/>=10 circuits/Dispatch/FL(%)
A 2 12 4 2 2	P-9	PBX/>=10 circuits/Non-Dispatch/FL(%)
A 2 12 5 1	P-9	Centrex/<10 circuits/Dispatch/FL(%)
A 2 12 5 2	P-9	Centrex/<10 circuits/Non-Dispatch/FL(%)
A 2 12 5 2 1	P-9	Centrex/>=10 circuits/Dispatch/FL(%)
A 2 12 5 2 2	P-9	Centrex/>=10 circuits/Non-Dispatch/FL(%)
A 2 12 6 1	P-9	ISDN/<10 circuits/Dispatch/FL(%)
A 2 12 6 2	P-9	ISDN/<10 circuits/Non-Dispatch/FL(%)
A 2 12 6 2 1	P-9	ISDN/>=10 circuits/Dispatch/FL(%)
A 2 12 6 2 2	P-9	ISDN/>=10 circuits/Non-Dispatch/FL(%)

Average Completion Notice Interval - Mechanized

A 2 14 1 1	P-5	Residence/<10 circuits/Dispatch/FL(hours)
A 2 14 1 2	P-5	Residence/<10 circuits/Non-Dispatch/FL(hours)
A 2 14 1 2 1	P-5	Residence/>=10 circuits/Dispatch/FL(hours)
A 2 14 1 2 2	P-5	Residence/>=10 circuits/Non-Dispatch/FL(hours)
A 2 14 2 1	P-5	Business/<10 circuits/Dispatch/FL(hours)
A 2 14 2 2	P-5	Business/<10 circuits/Non-Dispatch/FL(hours)

Benchmark /
Analog

Diagnostic
Diagnostic

Res
Res
Res
Res
Bus
Bus
Bus
Bus
Design
Design
Design
Design
PBX
PBX
PBX
PBX
Centrex
Centrex
Centrex
Centrex
ISDN
ISDN
ISDN
ISDN

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
6 24%	47,778	3 99%	2,130		0 00536	4 1982	YES
0 08%	607,521	0 14%	33,535		0 00014	-6 0077	NO
1 20%	83	0 00%	3		0 06412	0 1879	YES
0 00%	2						
2 09%	33,647	3 50%	429		0 00694	-2 0306	NO
0 04%	45,845	0 77%	2,462		0 00043	-16 8541	NO
6 80%	353	0 00%	3		0 14595	0 4658	YES
0 00%	19	0 00%	2		0 00000		YES
4 55%	4,001	7 63%	131		0 01850	-1 6673	NO
16 07%	56	0 00%	2		0 26429	0 6081	YES
0 00%	1						
12 33%	73	0 00%	21		0 08141	1 5144	YES
0 90%	334	0 00%	63		0 01296	0 6931	YES
25 00%	4	0 00%	1		0 48412	0 5164	YES
0 00%	101	0 00%	18		0 00000		YES
7 98%	639	0 00%	13		0 07592	1 0512	YES
0 15%	1,296	0 00%	21		0 00863	0 1787	YES
2 50%	40						
0 00%	106	0 00%	2		0 00000		YES
5 65%	850	0 00%	9		0 07735	0 7301	YES
1 31%	841	0 00%	72		0 01395	0 9375	YES
0 00%	5						
0 00%	62	0 00%	3		0 00000		YES
8 24%	47,584	7 88%	1,954		0 00635	0 5655	YES
3 52%	669,560	4 60%	33,424		0 00103	-10 5182	NO
6 45%	93	0 00%	4		0 12545	0 5143	YES
0 00%	3						
3 14%	32,933	4 14%	435		0 00841	-1 1902	YES
4 82%	45,097	4 44%	2,726		0 00423	0 9092	YES
6 33%	379	15 38%	13		0 06870	-1 3177	YES
1 56%	64	0 00%	19		0 03240	0 4822	YES
3 95%	4,661	1 85%	108		0 01895	1 1058	YES
0 51%	3,165	1 35%	592		0 00318	-2 6634	NO
0 00%	28						
0 00%	42	0 00%	19		0 00000		YES
2 83%	106	0 00%	10		0 05486	0 5159	YES
0 00%	351	0 00%	33		0 00000		YES
0 00%	3	0 00%	2		0 00000		YES
0 00%	88	0 00%	8		0 00000		YES
0 93%	645	20 00%	5		0 04310	-4 4247	NO
0 00%	1,722	0 00%	68		0 00000		YES
2 27%	44	0 00%	1		0 15072	0 1508	YES
0 00%	317	0 00%	6		0 00000		YES
0 00%	1,102	0 00%	11		0 00000		YES
0 00%	511	0 00%	33		0 00000		YES
6 15	33,452	1 78	1,571	41 173	1 06290	4 1135	YES
1 47	462,986	0 70	27,129	8 190	0 05116	15 1255	YES
3 78	65	0 01	3	15 527	9 16894	0 4107	YES
0 81	2			0 127			
8 31	9,273	2 61	279	31 148	1 89260	3 0120	YES
1 85	34,020	0 80	1,533	12 514	0 32674	3 2219	YES

BellSouth Monthly State Summary
Florida, July 2001

			Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A 2 14 2 2 1	P-5	Business/>=10 circuits/Dispatch/FL(hours)	Bus	8 76	204	0 00	2	24 775	17 60395	0 4978	YES
A 2 14 2 2 2	P-5	Business/>=10 circuits/Non-Dispatch/FL(hours)	Bus	1 10	13	0 63	1	1 297	1 34596	0 3492	YES
A 2 14 3 1 1	P-5	Design (Specials)/<10 circuits/Dispatch/FL(hours)	Design	149 06	2,737			457 225			
A 2 14 3 1 2	P-5	Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)	Design	85 26	30			345 456			
A 2 14 3 2 1	P-5	Design (Specials)/>=10 circuits/Dispatch/FL(hours)	Design	0 00	1			0 000			
A 2 14 3 2 2	P-5	Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)	Design								
A 2 14 4 1 1	P-5	PBX/<10 circuits/Dispatch/FL(hours)	PBX	47 35	46	2 08	6	150 359	65 26443	0 6937	YES
A 2 14 4 1 2	P-5	PBX/<10 circuits/Non-Dispatch/FL(hours)	PBX	5 30	250	6 86	2	28 090	19 94213	-0 0780	YES
A 2 14 4 2 1	P-5	PBX/>=10 circuits/Dispatch/FL(hours)	PBX	0 04	2			0 050			
A 2 14 4 2 2	P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)	PBX	0 65	84	19 48	1	0 262	0 26366	-71 4 194	NO
A 2 14 5 1 1	P-5	Centrex/<10 circuits/Dispatch/FL(hours)	Centrex	11 38	479			37 436			
A 2 14 5 1 2	P-5	Centrex/<10 circuits/Non-Dispatch/FL(hours)	Centrex	2 33	1,056	0 67	1	11 812	11 81719	0 1402	YES
A 2 14 5 2 1	P-5	Centrex/>=10 circuits/Dispatch/FL(hours)	Centrex	5 10	26			14 116			
A 2 14 5 2 2	P-5	Centrex/>=10 circuits/Non-Dispatch/FL(hours)	Centrex	3 08	88			4 194			
A 2 14 6 1 1	P-5	ISDN/<10 circuits/Dispatch/FL(hours)	ISDN	137 93	471	5 87	1	524 975	525 53160	0 2513	YES
A 2 14 6 1 2	P-5	ISDN/<10 circuits/Non-Dispatch/FL(hours)	ISDN	342 76	570	3 56	2	1644 804	1094 25581	0 3100	YES
A 2 14 6 2 1	P-5	ISDN/>=10 circuits/Dispatch/FL(hours)	ISDN	0 23	3			0 202			
A 2 14 6 2 2	P-5	ISDN/>=10 circuits/Non-Dispatch/FL(hours)	ISDN	4 06	53			4 490			
Average Completion Notice Interval - Non-Mechanized											
A 2 15 1 1 1	P-5	Residence/<10 circuits/Dispatch/FL(hours)	Diagnostic			51 42	102				Diagnostic
A 2 15 1 1 2	P-5	Residence/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			37 91	166				Diagnostic
A 2 15 1 2 1	P-5	Residence/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 1 2 2	P-5	Residence/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 2 1 1	P-5	Business/<10 circuits/Dispatch/FL(hours)	Diagnostic			43 35	41				Diagnostic
A 2 15 2 1 2	P-5	Business/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			31 19	406				Diagnostic
A 2 15 2 2 1	P-5	Business/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 2 2 2	P-5	Business/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 3 1 1	P-5	Design (Specials)/<10 circuits/Dispatch/FL(hours)	Diagnostic			81 73	6				Diagnostic
A 2 15 3 1 2	P-5	Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			85 71	2				Diagnostic
A 2 15 3 2 1	P-5	Design (Specials)/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 3 2 2	P-5	Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 4 1 1	P-5	PBX/<10 circuits/Dispatch/FL(hours)	Diagnostic			22 92	3				Diagnostic
A 2 15 4 1 2	P-5	PBX/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			28 67	17				Diagnostic
A 2 15 4 2 1	P-5	PBX/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 4 2 2	P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			32 48	7				Diagnostic
A 2 15 5 1 1	P-5	Centrex/<10 circuits/Dispatch/FL(hours)	Diagnostic			47 07	4				Diagnostic
A 2 15 5 1 2	P-5	Centrex/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			14 00	16				Diagnostic
A 2 15 5 2 1	P-5	Centrex/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 5 2 2	P-5	Centrex/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 6 1 1	P-5	ISDN/<10 circuits/Dispatch/FL(hours)	Diagnostic			83 79	4				Diagnostic
A 2 15 6 1 2	P-5	ISDN/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			29 42	6				Diagnostic
A 2 15 6 2 1	P-5	ISDN/>=10 circuits/Dispatch/FL(hours)	Diagnostic								Diagnostic
A 2 15 6 2 2	P-5	ISDN/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic			14 00	1				Diagnostic
Total Service Order Cycle Time - Mechanized											
A 2 17 1 1 1	P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			4 41	1,352				Diagnostic
A 2 17 1 1 2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			0 92	20,999				Diagnostic
A 2 17 1 2 1	P-10	Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic			3 00	3				Diagnostic
A 2 17 1 2 2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 2 1 1	P-10	Business/<10 circuits/Dispatch/FL(days)	Diagnostic			3 82	102				Diagnostic
A 2 17 2 1 2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1 33	639				Diagnostic
A 2 17 2 2 1	P-10	Business/>=10 circuits/Dispatch/FL(days)	Diagnostic			38 00	1				Diagnostic
A 2 17 2 2 2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 3 2 1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 3 2 2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 4 1 1	P-10	PBX/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 4 1 2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 4 2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2 17 4 2 2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

A 2 23 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
A 2 23 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
A 2 23 3 2 1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
A 2 23 3 2 2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
A 2 23 4 1 1	P-10	PBX/<10 circuits/Dispatch/FL(days)
A 2 23 4 1 2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
A 2 23 4 2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
A 2 23 4 2 2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
A 2 23 5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A 2 23 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A 2 23 5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
A 2 23 5 2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A 2 23 6 1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A 2 23 6 1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A 2 23 6 2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
A 2 23 6 2 2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)

Benchmark /
Analog

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
		13 00	1				Diagnostic
							Diagnostic
							Diagnostic
							Diagnostic
							Diagnostic
		6 00	3				Diagnostic
							Diagnostic
		6 50	2				Diagnostic
		3 00	1				Diagnostic
		4 86	7				Diagnostic
							Diagnostic
							Diagnostic
		14 00	1				Diagnostic
		4 00	2				Diagnostic
							Diagnostic
		10 00	2				Diagnostic

% Completions w/o Notice or < 24 hours

A 2 24 1 1	P-6	Residence/Dispatch/FL(%)
A 2 24 1 2	P-6	Residence/Non-Dispatch/FL(%)
A 2 24 2 1	P-6	Business/Dispatch/FL(%)
A 2 24 2 2	P-6	Business/Non-Dispatch/FL(%)
A 2 24 3 1	P-6	Design (Specials)/Dispatch/FL(%)
A 2 24 3 2	P-6	Design (Specials)/Non-Dispatch/FL(%)
A 2 24 4 1	P-6	PBX/Dispatch/FL(%)
A 2 24 4 2	P-6	PBX/Non-Dispatch/FL(%)
A 2 24 5 1	P-6	Centrex/Dispatch/FL(%)
A 2 24 5 2	P-6	Centrex/Non-Dispatch/FL(%)
A 2 24 6 1	P-6	ISDN/Dispatch/FL(%)
A 2 24 6 2	P-6	ISDN/Non-Dispatch/FL(%)

Diagnostic
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Diagnostic

		100 00%	1,815				Diagnostic
		100 00%	30,163				Diagnostic
		100 00%	287				Diagnostic
		100 00%	1,916				Diagnostic
		100 00%	4				Diagnostic
		100 00%	1				Diagnostic
		100 00%	18				Diagnostic
		100 00%	37				Diagnostic
		100 00%	9				Diagnostic
		100 00%	13				Diagnostic
		100 00%	4				Diagnostic
		100 00%	68				Diagnostic

Service Order Accuracy

A 2 25 1 1 1	P-11	Residence/<10 circuits/Dispatch/FL(%)
A 2 25 1 1 2	P-11	Residence/<10 circuits/Non-Dispatch/FL(%)
A 2 25 1 2 1	P-11	Residence/>=10 circuits/Dispatch/FL(%)
A 2 25 1 2 2	P-11	Residence/>=10 circuits/Non-Dispatch/FL(%)
A 2 25 2 1 1	P-11	Business/<10 circuits/Dispatch/FL(%)
A 2 25 2 1 2	P-11	Business/<10 circuits/Non-Dispatch/FL(%)
A 2 25 2 2 1	P-11	Business/>=10 circuits/Dispatch/FL(%)
A 2 25 2 2 2	P-11	Business/>=10 circuits/Non-Dispatch/FL(%)
A 2 25 3 1 1	P-11	Design (Specials)/<10 circuits/Dispatch/FL(%)
A 2 25 3 1 2	P-11	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)
A 2 25 3 2 1	P-11	Design (Specials)/>=10 circuits/Dispatch/FL(%)
A 2 25 3 2 2	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)

>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%

		100 00%	1				YES
		95 15%	165				YES
		0 00%	1				NO
		45 45%	11				NO
		85 49%	193				NO
		100 00%	1				YES
		0 00%	1				NO
		50 00%	4				NO

Resale - Maintenance and Repair

Missed Repair Appointments

A 3 11 1	M&R-1	Residence/Dispatch/FL(%)
A 3 11 2	M&R-1	Residence/Non-Dispatch/FL(%)
A 3 12 1	M&R-1	Business/Dispatch/FL(%)
A 3 12 2	M&R-1	Business/Non-Dispatch/FL(%)
A 3 13 1	M&R-1	Design (Specials)/Dispatch/FL(%)
A 3 13 2	M&R-1	Design (Specials)/Non-Dispatch/FL(%)
A 3 14 1	M&R-1	PBX/Dispatch/FL(%)
A 3 14 2	M&R-1	PBX/Non-Dispatch/FL(%)
A 3 15 1	M&R-1	Centrex/Dispatch/FL(%)
A 3 15 2	M&R-1	Centrex/Non-Dispatch/FL(%)
A 3 16 1	M&R-1	ISDN/Dispatch/FL(%)
A 3 16 2	M&R-1	ISDN/Non-Dispatch/FL(%)

Res
Res
Bus
Bus
Design
Design
PBX
PBX
Centrex
Centrex
ISDN
ISDN

12 56%	83,197	5 93%	2,494			0 00674	9 8417	YES
1 88%	47,153	2 06%	1,358			0 00374	-0 4774	YES
13 18%	16,591	8 04%	796			0 01227	4 1888	YES
3 33%	9,633	4 23%	402			0 00914	-0 9813	YES
2 33%	2,707	10 53%	19			0 03471	-2 3621	NO
0 59%	2,890	0 00%	4			0 03826	0 1537	YES
21 95%	287	8 33%	12			0 12196	1 1166	YES
4 04%	198	20 00%	5			0 08916	-1 7899	NO
23 39%	1,257	0 00%	17			0 10336	2 2629	YES
8 57%	770	0 00%	11			0 08501	1 0083	YES
5 01%	479	0 00%	9			0 07340	0 6826	YES
2 43%	452	20 00%	5			0 06929	-2 5351	NO

Customer Trouble Report Rate

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
A 3 2 1 1	M&R-2	Residence/Dispatch/FL(%)	Res	1 85%	4,509,319	1 80%	138,343	0 00037	1 1392	YES	
A 3 2 1 2	M&R-2	Residence/Non-Dispatch/FL(%)	Res	1 05%	4,509,319	0 98%	138,343	0 00028	2 2951	YES	
A 3 2 2 1	M&R-2	Business/Dispatch/FL(%)	Bus	1 30%	1,275,487	1 33%	59,978	0 00048	-0 5539	YES	
A 3 2 2 2	M&R-2	Business/Non-Dispatch/FL(%)	Bus	0 76%	1,275,487	0 67%	59,978	0 00036	2 3408	YES	
A 3 2 3 1	M&R-2	Design (Specials)/Dispatch/FL(%)	Design	0 34%	786,097	0 14%	13,657	0 00051	4 0522	YES	
A 3 2 3 2	M&R-2	Design (Specials)/Non-Dispatch/FL(%)	Design	0 37%	786,097	0 03%	13,657	0 00052	6 4654	YES	
A 3 2 4 1	M&R-2	PBX/Dispatch/FL(%)	PBX	0 20%	140,197	0 28%	4,243	0 00071	-1 1078	YES	
A 3 2 4 2	M&R-2	PBX/Non-Dispatch/FL(%)	PBX	0 14%	140,197	0 12%	4,243	0 00059	0 3994	YES	
A 3 2 5 1	M&R-2	Centrex/Dispatch/FL(%)	Centrex	0 53%	236,698	0 45%	3,774	0 00120	0 6742	YES	
A 3 2 5 2	M&R-2	Centrex/Non-Dispatch/FL(%)	Centrex	0 33%	236,698	0 29%	3,774	0 00094	0 3616	YES	
A 3 2 6 1	M&R-2	ISDN/Dispatch/FL(%)	ISDN	1 28%	37,337	0 56%	1,604	0 00289	2 4992	YES	
A 3 2 6 2	M&R-2	ISDN/Non-Dispatch/FL(%)	ISDN	1 21%	37,337	0 31%	1,604	0 00281	3 2038	YES	
Maintenance Average Duration											
A 3 3 1 1	M&R-3	Residence/Dispatch/FL(hours)	Res	25 26	83,197	20 59	2,494	25 100	0 51008	9 1587	YES
A 3 3 1 2	M&R-3	Residence/Non-Dispatch/FL(hours)	Res	9 27	47,153	5 88	1,358	13 868	0 38170	8 8897	YES
A 3 3 2 1	M&R-3	Business/Dispatch/FL(hours)	Bus	17 86	16,591	17 28	796	21 890	0 79425	0 7271	YES
A 3 3 2 2	M&R-3	Business/Non-Dispatch/FL(hours)	Bus	6 10	9,633	4 02	402	12 127	0 61733	3 3693	YES
A 3 3 3 1	M&R-3	Design (Specials)/Dispatch/FL(hours)	Design	5 87	2,707	7 86	19	39 035	8 98657	-0 2217	YES
A 3 3 3 2	M&R-3	Design (Specials)/Non-Dispatch/FL(hours)	Design	2 62	2,890	1 30	4	22 574	11,29471	0 1164	YES
A 3 3 4 1	M&R-3	PBX/Dispatch/FL(hours)	PBX	21 63	287	15 90	12	26 857	7 91349	0 7237	YES
A 3 3 4 2	M&R-3	PBX/Non-Dispatch/FL(hours)	PBX	5 90	198	8 86	5	11 369	5 14808	-0 5764	YES
A 3 3 5 1	M&R-3	Centrex/Dispatch/FL(hours)	Centrex	20 96	1,257	9 07	17	26 089	6 37018	1 8654	YES
A 3 3 5 2	M&R-3	Centrex/Non-Dispatch/FL(hours)	Centrex	4 54	770	3 20	11	9 017	2 73808	0 4915	YES
A 3 3 6 1	M&R-3	ISDN/Dispatch/FL(hours)	ISDN	8 16	479	7 99	9	9 299	3 12872	0 0524	YES
A 3 3 6 2	M&R-3	ISDN/Non-Dispatch/FL(hours)	ISDN	3 13	452	9 00	5	7 783	3 50000	-1 6775	NO
% Repeat Troubles within 30 Days											
A 3 4 1 1	M&R-4	Residence/Dispatch/FL(%)	Res	23 14%	83,197	18 48%	2,494	0 00857	5 4286	YES	
A 3 4 1 2	M&R-4	Residence/Non-Dispatch/FL(%)	Res	20 19%	47,153	20 25%	1,358	0 01105	-0 0550	YES	
A 3 4 2 1	M&R-4	Business/Dispatch/FL(%)	Bus	18 79%	16,591	17 84%	796	0 01417	0 6689	YES	
A 3 4 2 2	M&R-4	Business/Non-Dispatch/FL(%)	Bus	16 83%	9,633	17 66%	402	0 01904	-0 4380	YES	
A 3 4 3 1	M&R-4	Design (Specials)/Dispatch/FL(%)	Design	43 55%	2,707	36 84%	19	0 11415	0 5880	YES	
A 3 4 3 2	M&R-4	Design (Specials)/Non-Dispatch/FL(%)	Design	39 86%	2,890	25 00%	4	0 24498	0 6067	YES	
A 3 4 4 1	M&R-4	PBX/Dispatch/FL(%)	PBX	18 82%	287	16 67%	12	0 11516	0 1866	YES	
A 3 4 4 2	M&R-4	PBX/Non-Dispatch/FL(%)	PBX	13 13%	198	40 00%	5	0 15294	-1 7568	NO	
A 3 4 5 1	M&R-4	Centrex/Dispatch/FL(%)	Centrex	18 62%	1,257	17 65%	17	0 09504	0 1019	YES	
A 3 4 5 2	M&R-4	Centrex/Non-Dispatch/FL(%)	Centrex	15 84%	770	9 09%	11	0 11088	0 6090	YES	
A 3 4 6 1	M&R-4	ISDN/Dispatch/FL(%)	ISDN	31 73%	479	22 22%	9	0 15660	0 6073	YES	
A 3 4 6 2	M&R-4	ISDN/Non-Dispatch/FL(%)	ISDN	32 30%	452	20 00%	5	0 21028	0 5850	YES	
Out of Service > 24 hours											
A 3 5 1 1	M&R-5	Residence/Dispatch/FL(%)	Res	28 38%	55,436	23 32%	1,797	0 01081	4 6876	YES	
A 3 5 1 2	M&R-5	Residence/Non-Dispatch/FL(%)	Res	12 22%	14,680	6 61%	545	0 01429	3 9301	YES	
A 3 5 2 1	M&R-5	Business/Dispatch/FL(%)	Bus	19 31%	10,718	17 00%	553	0 01721	1 3449	YES	
A 3 5 2 2	M&R-5	Business/Non-Dispatch/FL(%)	Bus	5 65%	3,840	2 70%	222	0 01594	1 8498	YES	
A 3 5 3 1	M&R-5	Design (Specials)/Dispatch/FL(%)	Design	2 33%	2,707	10 53%	19	0 03471	-2 3621	NO	
A 3 5 3 2	M&R-5	Design (Specials)/Non-Dispatch/FL(%)	Design	0 59%	2,890	0 00%	4	0 03826	0 1537	YES	
A 3 5 4 1	M&R-5	PBX/Dispatch/FL(%)	PBX	21 43%	182	16 67%	6	0 17025	0 2797	YES	
A 3 5 4 2	M&R-5	PBX/Non-Dispatch/FL(%)	PBX	7 44%	121	0 00%	3	0 15336	0 4850	YES	
A 3 5 5 1	M&R-5	Centrex/Dispatch/FL(%)	Centrex	25 80%	876	7 14%	14	0 11787	1 5828	YES	
A 3 5 5 2	M&R-5	Centrex/Non-Dispatch/FL(%)	Centrex	8 81%	295	0 00%	8	0 10158	0 8677	YES	
A 3 5 6 1	M&R-5	ISDN/Dispatch/FL(%)	ISDN	4 81%	478	0 00%	8	0 07630	0 6307	YES	
A 3 5 6 2	M&R-5	ISDN/Non-Dispatch/FL(%)	ISDN	2 21%	452	20 00%	5	0 06614	-2 6893	NO	

Resale - Billing

Invoice Accuracy										
A 4 1	B-1	FL(%)	BST - State	98 79%	\$493,225,276	99 88%	\$10,792,601	0 00003	323 3794	YES
Mean Time to Deliver Invoices - CRIS										
A 4 2	B-2	Region(business days)	BST - Region	5 04	1	3 47	1,763			YES

**BellSouth Monthly State Summary
Florida, July 2001**

Benchmark / Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

Unbundled Network Elements - Ordering

% Rejected Service Requests - Mechanized

B 111	0-7	Switch Ports/FL(%)
B 112	0-7	Local Interoffice Transport/FL(%)
B 113	0-7	Loop + Port Combinations/FL(%)
B 114	0-7	Combo Other/FL(%)
B 115	0-7	xDSL (ADSL, HDSL and UCL)/FL(%)
B 116	0-7	ISDN Loop (UDN, UDC)/FL(%)
B 117	0-7	Line Sharing/FL(%)
B 118	0-7	2W Analog Loop Design/FL(%)
B 119	0-7	2W Analog Loop Non-Design/FL(%)
B 1110	0-7	2W Analog Loop w/INP Design/FL(%)
B 1111	0-7	2W Analog Loop w/INP Non-Design/FL(%)
B 1112	0-13	2W Analog Loop w/LNP Design/FL(%)
B 1113	0-13	2W Analog Loop w/LNP Non-Design/FL(%)
B 1114	0-7	Other Design/FL(%)
B 1115	0-7	Other Non-Design/FL(%)
B 1116	0-7	INP Standalone/FL(%)
B 1117	0-13	LNP (Standalone)/FL(%)

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									Diagnostic
									Diagnostic
			12.58%	8,408					Diagnostic
									Diagnostic
			15.18%	224					Diagnostic
			0.00%	5					Diagnostic
			33.33%	3					Diagnostic
			24.59%	305					Diagnostic
			15.50%	258					Diagnostic
									Diagnostic
									Diagnostic
			50.48%	105					Diagnostic
			72.13%	61					Diagnostic
			28.68%	136					Diagnostic
									Diagnostic
									Diagnostic
			3.94%	1,778					Diagnostic

% Rejected Service Requests - Partially Mechanized

B 121	0-7	Switch Ports/FL(%)
B 122	0-7	Local Interoffice Transport/FL(%)
B 123	0-7	Loop + Port Combinations/FL(%)
B 124	0-7	Combo Other/FL(%)
B 125	0-7	xDSL (ADSL, HDSL and UCL)/FL(%)
B 126	0-7	ISDN Loop (UDN, UDC)/FL(%)
B 127	0-7	Line Sharing/FL(%)
B 128	0-7	2W Analog Loop Design/FL(%)
B 129	0-7	2W Analog Loop Non-Design/FL(%)
B 1210	0-7	2W Analog Loop w/INP Design/FL(%)
B 1211	0-7	2W Analog Loop w/INP Non-Design/FL(%)
B 1212	0-13	2W Analog Loop w/LNP Design/FL(%)
B 1213	0-13	2W Analog Loop w/LNP Non-Design/FL(%)
B 1214	0-7	Other Design/FL(%)
B 1215	0-7	Other Non-Design/FL(%)
B 1216	0-7	INP Standalone/FL(%)
B 1217	0-13	LNP (Standalone)/FL(%)

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									Diagnostic
									Diagnostic
			31.61%	5,312					Diagnostic
									Diagnostic
			3.03%	66					Diagnostic
			75.00%	4					Diagnostic
			100.00%	1					Diagnostic
			25.38%	465					Diagnostic
			23.44%	640					Diagnostic
									Diagnostic
									Diagnostic
			40.71%	1,039					Diagnostic
			24.05%	1,031					Diagnostic
			32.35%	68					Diagnostic
									Diagnostic
									Diagnostic
			35.28%	1,216					Diagnostic

% Rejected Service Requests - Non-Mechanized

B 131	0-7	Switch Ports/FL(%)
B 132	0-7	Local Interoffice Transport/FL(%)
B 133	0-7	Loop + Port Combinations/FL(%)
B 134	0-7	Combo Other/FL(%)
B 135	0-7	xDSL (ADSL, HDSL and UCL)/FL(%)
B 136	0-7	ISDN Loop (UDN, UDC)/FL(%)
B 137	0-7	Line Sharing/FL(%)
B 138	0-7	2W Analog Loop Design/FL(%)
B 139	0-7	2W Analog Loop Non-Design/FL(%)
B 1310	0-7	2W Analog Loop w/INP Design/FL(%)
B 1311	0-7	2W Analog Loop w/INP Non-Design/FL(%)
B 1312	0-13	2W Analog Loop w/LNP Design/FL(%)
B 1313	0-13	2W Analog Loop w/LNP Non-Design/FL(%)
B 1314	0-7	Other Design/FL(%)
B 1315	0-7	Other Non-Design/FL(%)
B 1316	0-7	INP Standalone/FL(%)
B 1317	0-13	LNP Standalone/FL(%)

Diagnostic
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Diagnostic

			33.33%	3					Diagnostic
			33.33%	15					Diagnostic
			48.39%	2,174					Diagnostic
									Diagnostic
			20.00%	650					Diagnostic
			2.22%	45					Diagnostic
			20.87%	321					Diagnostic
			5.68%	88					Diagnostic
			37.64%	2,019					Diagnostic
			7.69%	13					Diagnostic
									Diagnostic
			48.51%	303					Diagnostic
			20.22%	727					Diagnostic
			11.20%	125					Diagnostic
									Diagnostic
			33.87%	1,491					Diagnostic
			33.75%	1,582					Diagnostic

Reject Interval - Mechanized

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 4 1	O-8	Switch Ports/FL(%)	>= 97% w in 1 hr							
B 1 4 2	O-8	Local Interoffice Transport/FL(%)	>= 97% w in 1 hr							
B 1 4 3	O-8	Loop + Port Combinations/FL(%)	>= 97% w in 1 hr		95 94%	1,058				NO
B 1 4 4	O-8	Combo Other/FL(%)	>= 97% w in 1 hr							
B 1 4 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 97% w in 1 hr		97 06%	34				YES
B 1 4 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 97% w in 1 hr							
B 1 4 7	O-8	Line Sharing/FL(%)	>= 97% w in hr		100 00%	1				YES
B 1 4 8	O-8	2W Analog Loop Design/FL(%)	>= 97% w in hr		97 33%	75				YES
B 1 4 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 97% w in hr		97 50%	40				YES
B 1 4 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 97% w in hr							
B 1 4 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 97% w in hr							
B 1 4 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 97% w in hr		96 23%	53				NO
B 1 4 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 97% w in hr		95 45%	44				NO
B 1 4 14	O-8	Other Design/FL(%)	>= 97% w in hr		94 87%	39				NO
B 1 4 15	O-8	Other Non-Design/FL(%)	>= 97% w in hr							
B 1 4 16	O-8	INP Standalone/FL(%)	>= 97% w in hr							
B 1 4 17	O-14	LNP (Standalone)/FL(%)	>= 97% w in hr		100 00%	70				YES
Reject Interval - Partially Mechanized - 24 hours										
B 1 5 1	O-8	Switch Ports/FL(%)	>= 85% w in 2 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 2 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 4	O-8	Combo Other/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 7	O-8	Line Sharing/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 8	O-8	2W Analog Loop Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 14	O-8	Other Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 15	O-8	Other Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 16	O-8	INP Standalone/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 17	O-14	LNP (Standalone)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
Reject Interval - Partially Mechanized - 18 hours										
B 1 6 1	O-8	Switch Ports/FL(%)	>= 85% w in 18 hrs							
B 1 6 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 18 hrs							
B 1 6 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 18 hrs		98 33%	1,679				YES
B 1 6 4	O-8	Combo Other/FL(%)	>= 85% w in 18 hrs							
B 1 6 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 18 hrs		100 00%	2				YES
B 1 6 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 18 hrs		100 00%	3				YES
B 1 6 7	O-8	Line Sharing/FL(%)	>= 85% w in 18 hrs		100 00%	1				YES
B 1 6 8	O-8	2W Analog Loop Design/FL(%)	>= 85% w in 18 hrs		93 22%	118				YES
B 1 6 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 18 hrs		82 67%	150				NO
B 1 6 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 18 hrs		96 22%	423				YES
B 1 6 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 18 hrs		95 56%	248				YES
B 1 6 14	O-8	Other Design/FL(%)	>= 85% w in 18 hrs		81 82%	22				NO
B 1 6 15	O-8	Other Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 16	O-8	INP Standalone/FL(%)	>= 85% w in 18 hrs							
B 1 6 17	O-14	LNP (Standalone)/FL(%)	>= 85% w in 18 hrs		96 27%	429				YES
B 1 8 1	O-8	Switch Ports/FL(%)	>= 85% w in 24 hrs		100 00%	1				YES
B 1 8 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs		100 00%	5				YES
B 1 8 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs		99 81%	1,052				YES
B 1 8 4	O-8	Combo Other/FL(%)	>= 85% w in 24 hrs							
B 1 8 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs		97 69%	130				YES

**Bellsouth Monthly State Summary
Florida, July 2001**

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 8 6	O-8 ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs		100 00%	1				YES
B 1 8 7	O-8 Line Sharing/FL(%)	>= 85% w in 24 hrs		95 52%	67				YES
B 1 8 8	O-8 2W Analog Loop Design/FL(%)	>= 85% w in 24 hrs		60.00%	5				NO
B 1 8 9	O-8 2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs		96 58%	760				YES
B 1 8 10	O-8 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 24 hrs		0 00%	1				NO
B 1 8 11	O-8 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 24 hrs							
B 1 8 12	O-14 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs		98 64%	147				YES
B 1 8 13	O-14 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs		95 24%	147				YES
B 1 8 14	O-8 Other Design/FL(%)	>= 85% w in 24 hrs		64 29%	14				NO
B 1 8 15	O-8 Other Non-Design/FL(%)	>= 85% w in 24 hrs							
B 1 8 16	O-8 INP Standalone/FL(%)	>= 85% w in 24 hrs		98 42%	505				YES
B 1 8 17	O-14 LNP (Standalone)/FL(%)	>= 85% w in 24 hrs		97 57%	534				YES
FOC Timeliness - Mechanized									
B 1 9 1	O-9 Switch Ports/FL(%)	>= 95% w in 3 hrs							
B 1 9 2	O-9 Local Interoffice Transport/FL(%)	>= 95% w in 3 hrs							
B 1 9 3	O-9 Loop + Port Combinations/FL(%)	>= 95% w in 3 hrs		98 49%	5,491				YES
B 1 9 4	O-9 Combo Other/FL(%)	>= 95% w in 3 hrs							
B 1 9 5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% w in 3 hrs		81 03%	485				NO
B 1 9 6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 95% w in 3 hrs		100 00%	5				YES
B 1 9 7	O-9 Line Sharing/FL(%)	>= 95% w in 3 hrs							
B 1 9 8	O-9 2W Analog Loop Design/FL(%)	>= 95% w in 3 hrs		98 26%	172				YES
B 1 9 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 95% w in 3 hrs		95 09%	163				YES
B 1 9 10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 95% w in 3 hrs		100 00%	45				YES
B 1 9 13	O-15 2W Analog Loop w/LNP Non-Design/FL(%)	>= 95% w in 3 hrs		100 00%	24				YES
B 1 9 14	O-9 Other Design/FL(%)	>= 95% w in 3 hrs		100 00%	64				YES
B 1 9 15	O-9 Other Non-Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 16	O-9 INP Standalone/FL(%)	>= 95% w in 3 hrs							
B 1 9 17	O-15 LNP Standalone/FL(%)	>= 95% w in 3 hrs		99 81%	1,564				YES
FOC Timeliness - Partially Mechanized									
B 1 10 1	O-9 Switch Ports/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 2	O-9 Local Interoffice Transport/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 3	O-9 Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 4	O-9 Combo Other/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 7	O-9 Line Sharing/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 8	O-9 2W Analog Loop Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 13	O-15 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 14	O-9 Other Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 15	O-9 Other Non-Design/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 16	O-9 INP Standalone/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
B 1 10 17	O-15 LNP Standalone/FL(%)	>= 85% w in 36 hrs		<i>This data not applicable after 6-1-2001, see below</i>					
FOC Timeliness - Partially Mechanized - 18 hours									
B 1 11 1	O-9 Switch Ports/FL(%)	>= 85% w in 18 hrs							
B 1 11 2	O-9 Local Interoffice Transport/FL(%)	>= 85% w in 18 hrs							
B 1 11 3	O-9 Loop + Port Combinations/FL(%)	>= 85% w in 18 hrs		97 31%	3,975				YES
B 1 11 4	O-9 Combo Other/FL(%)	>= 85% w in 18 hrs							
B 1 11 5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 18 hrs		90 20%	51				YES
B 1 11 6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 18 hrs		100 00%	1				YES
B 1 11 7	O-9 Line Sharing/FL(%)	>= 85% w in 18 hrs							
B 1 11 8	O-9 2W Analog Loop Design/FL(%)	>= 85% w in 18 hrs		97 84%	370				YES
B 1 11 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 85% w in 18 hrs		99 31%	576				YES
B 1 11 10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 18 hrs							

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 11 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 11 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 18 hrs		97.47%	712				YES
B 1 11 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 18 hrs		98.81%	1,088				YES
B 1 11 14	O-9	Other Design/FL(%)	>= 85% w in 18 hrs		100.00%	54				YES
B 1 11 15	O-9	Other Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 11 16	O-9	INP Standalone/FL(%)	>= 85% w in 18 hrs							
B 1 11 17	O-15	LNP Standalone/FL(%)	>= 85% w in 18 hrs		99.24%	1,054				YES
FOC Timeliness - Non-Mechanized										
B 1 13 1	O-9	Switch Ports/FL(%)	>= 85% w in 36 hrs		100.00%	1				YES
B 1 13 2	O-9	Local Interoffice Transport/FL(%)	>= 85% w in 36 hrs		87.50%	8				YES
B 1 13 3	O-9	Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs		99.82%	1,132				YES
B 1 13 4	O-9	Combo Other/FL(%)	>= 85% w in 36 hrs							
B 1 13 5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 36 hrs		99.48%	575				YES
B 1 13 6	O-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs		100.00%	41				YES
B 1 13 7	O-9	Line Sharing/FL(%)	>= 85% w in 36 hrs		100.00%	240				YES
B 1 13 8	O-9	2W Analog Loop Design/FL(%)	>= 85% w in 36 hrs		100.00%	74				YES
B 1 13 9	O-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 36 hrs		99.53%	1,477				YES
B 1 13 10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs		100.00%	22				YES
B 1 13 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 36 hrs							
B 1 13 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs		91.16%	147				YES
B 1 13 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 36 hrs		97.01%	569				YES
B 1 13 14			>= 85% w in 36 hrs		99.06%	106				YES
B 1 13 15			>= 85% w in 36 hrs							
B 1 13 16			>= 85% w in 36 hrs							
B 1 13 17			>= 85% w in 36 hrs		95.79%	903				YES
FOC & Reject Response Completeness - Mechanized										
B 1 14 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 14 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 14 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		78.00%	8,408				NO
B 1 14 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 14 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		86.97%	284				NO
B 1 14 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100.00%	5				YES
B 1 14 7	O-11	Line Sharing/FL(%)	>= 95%		33.33%	3				NO
B 1 14 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		80.98%	305				NO
B 1 14 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		79.46%	258				NO
B 1 14 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 14 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 14 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100.00%	476				YES
B 1 14 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		100.00%	256				YES
B 1 14 14	O-11	Other Design/FL(%)	>= 95%		75.74%	136				NO
B 1 14 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 14 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 14 17	O-11	LNP Standalone/FL(%)	>= 95%		100.00%	437				YES
FOC & Reject Response Completeness - Partially Mechanized										
B 1 15 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 15 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 15 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		100.00%	5,312				YES
B 1 15 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 15 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		43.40%	53				NO
B 1 15 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100.00%	4				YES
B 1 15 7	O-11	Line Sharing/FL(%)	>= 95%		100.00%	1				YES
B 1 15 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		100.00%	465				YES
B 1 15 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		100.00%	640				YES
B 1 15 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 15 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 15 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100.00%	670				YES
B 1 15 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		100.00%	830				YES
B 1 15 14	O-11	Other Design/FL(%)	>= 95%		100.00%	68				YES
B 1 15 15	O-11	Other Non-Design/FL(%)	>= 95%							

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 15 16	O-11 INP Standalone/FL(%)								
B 1 15 17	O-11 LNP Standalone/FL(%)			100 00%	2,560				YES
FOC & Reject Response Completeness - Non-Mechanized									
B 1 16 1	O-11 Switch Ports/FL(%)								
B 1 16 2	O-11 Local Interoffice Transport/FL(%)			100 00%	12				YES
B 1 16 3	O-11 Loop + Port Combinations/FL(%)			98 55%	2,269				YES
B 1 16 4	O-11 Combo Other/FL(%)								
B 1 16 5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)			62 16%	37				NO
B 1 16 6	O-11 ISDN Loop (UDN, UDC)/FL(%)			97 44%	39				YES
B 1 16 7	O-11 Line Sharing/FL(%)			96 34%	355				YES
B 1 16 8	O-11 2W Analog Loop Design/FL(%)			98 70%	77				YES
B 1 16 9	O-11 2W Analog Loop Non-Design/FL(%)			94 35%	2,726				NO
B 1 16 10	O-11 2W Analog Loop w/INP Design/FL(%)			75 00%	4				NO
B 1 16 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)								
B 1 16 12	O-11 2W Analog Loop w/LNP Design/FL(%)			98 95%	190				YES
B 1 16 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)			99 39%	658				YES
B 1 16 14	O-11 Other Design/FL(%)			100 00%	125				YES
B 1 16 15	O-11 Other Non-Design/FL(%)								
B 1 16 16	O-11 INP Standalone/FL(%)								95%
B 1 16 17	O-11 LNP Standalone/FL(%)			99 92%	1,194				YES
FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
B 1 17 1	O-11 Switch Ports/FL(%)								
B 1 17 2	O-11 Local Interoffice Transport/FL(%)								
B 1 17 3	O-11 Loop + Port Combinations/FL(%)			99 98%	6,558				YES
B 1 17 4	O-11 Combo Other/FL(%)								
B 1 17 5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)			95 14%	247				YES
B 1 17 6	O-11 ISDN Loop (UDN, UDC)/FL(%)			100 00%	5				YES
B 1 17 7	O-11 Line Sharing/FL(%)			100 00%	1				YES
B 1 17 8	O-11 2W Analog Loop Design/FL(%)			100 00%	247				YES
B 1 17 9	O-11 2W Analog Loop Non-Design/FL(%)			99 02%	205				YES
B 1 17 10	O-11 2W Analog Loop w/INP Design/FL(%)								
B 1 17 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)								
B 1 17 12	O-11 2W Analog Loop w/LNP Design/FL(%)			100 00%	476				YES
B 1 17 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)			98 83%	256				YES
B 1 17 14	O-11 Other Design/FL(%)			100 00%	103				YES
B 1 17 15	O-11 Other Non-Design/FL(%)								
B 1 17 16	O-11 INP Standalone/FL(%)								95%
B 1 17 17	O-11 LNP Standalone/FL(%)			100 00%	437				YES
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized									
B 1 18 1	O-11 Switch Ports/FL(%)								
B 1 18 2	O-11 Local Interoffice Transport/FL(%)								
B 1 18 3	O-11 Loop + Port Combinations/FL(%)			94 31%	5,312				NO
B 1 18 4	O-11 Combo Other/FL(%)								
B 1 18 5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)			86 96%	23				NO
B 1 18 6	O-11 ISDN Loop (UDN, UDC)/FL(%)			100 00%	4				YES
B 1 18 7	O-11 Line Sharing/FL(%)			100 00%	1				YES
B 1 18 8	O-11 2W Analog Loop Design/FL(%)			93 98%	465				NO
B 1 18 9	O-11 2W Analog Loop Non-Design/FL(%)			92 50%	640				NO
B 1 18 10	O-11 2W Analog Loop w/INP Design/FL(%)								
B 1 18 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)								
B 1 18 12	O-11 2W Analog Loop w/LNP Design/FL(%)			100 00%	670				YES
B 1 18 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)			100 00%	830				YES
B 1 18 14	O-11 Other Design/FL(%)			89 71%	68				NO
B 1 18 15	O-11 Other Non-Design/FL(%)								
B 1 18 16	O-11 INP Standalone/FL(%)								95%
B 1 18 17	O-11 LNP Standalone/FL(%)			99 88%	2,560				YES
FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized									
B 1 19 1	O-11 Switch Ports/FL(%)								

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 19 2	O-11 Local Interoffice Transport/FL(%)	>= 95%		83 33%	12				NO
B 1 19 3	O-11 Loop + Port Combinations/FL(%)	>= 95%		94 99%	2,236				NO
B 1 19 4	O-11 Combo Other/FL(%)	>= 95%							
B 1 19 5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		100 00%	23				YES
B 1 19 6	O-11 ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	38				YES
B 1 19 7	O-11 Line Sharing/FL(%)	>= 95%		95 32%	342				YES
B 1 19 8	O-11 2W Analog Loop Design/FL(%)	>= 95%		86 84%	76				NO
B 1 19 9	O-11 2W Analog Loop Non-Design/FL(%)	>= 95%		92 81%	2,572				NO
B 1 19 10	O-11 2W Analog Loop w/INP Design/FL(%)	>= 95%		100 00%	3				YES
B 1 19 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 19 12	O-11 2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	188				YES
B 1 19 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		99 85%	654				YES
B 1 19 14	O-11 Other Design/FL(%)	>= 95%		88 80%	125				NO
B 1 19 15	O-11 Other Non-Design/FL(%)	>= 95%							
B 1 19 16	O-11 INP Standalone/FL(%)	>= 95%							
B 1 19 17	O-11 LNP Standalone/FL(%)	>= 95%		100 00%	1,193				YES

Unbundled Network Elements - Provisioning

Order Completion Interval									
B 2 1 1 1	P-4 Switch Ports/<10 circuits/Dispatch/FL(days)	R&B (POTS)	4 06	70,064				5 995	
B 2 1 1 2	P-4 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0 89	616,137				1 587	
B 2 1 1 2	P-4 Switch Ports/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9 56	396				15 836	
B 2 1 1 2	P-4 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	18 75	15				29 243	
B 2 1 2 1	P-4 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	DS1/DS3			16 90	10			
B 2 1 2 2	P-4 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	DS1/DS3							
B 2 1 2 2	P-4 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	DS1/DS3							
B 2 1 2 2	P-4 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	DS1/DS3							
B 2 1 3 1	P-4 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	R&B	4 08	70,634	2 69	220	6 044	0 40809	3 4282
B 2 1 3 2	P-4 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	R&B	0 89	618,135	0 79	7 809	1 589	0 01833	5 3248
B 2 1 3 3	P-4 Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(days)	R&B	0 33	315,649	0 34	4,686	0 449	0 00661	-0 3548
B 2 1 3 4	P-4 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	R&B	1 73	245,506	1 52	2,923	2 188	0 04072	5 1084
B 2 1 3 2	P-4 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	R&B	10 18	436	2 14	7	16 544	6 30285	1 2758
B 2 1 3 2	P-4 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(days)	R&B	4 12	205	2 33	3	9 422	5 47945	0 3260
B 2 1 3 2	P-4 Loop + Port Combinations/>=10 circuits/Dispatch In/FL(days)	R&B	2 06	54			12 747		
B 2 1 3 2	P-4 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	R&B	4 85	151	2 33	3	7 827	4 56347	0 5524
B 2 1 4 1	P-4 Combo Other/<10 circuits/Dispatch/FL(days)	R&B&D - Disp	5 29	75,461			9 893		
B 2 1 4 1	P-4 Combo Other/<10 circuits/Non-Dispatch/FL(days)	R&B&D - Disp							
B 2 1 4 2	P-4 Combo Other/>=10 circuits/Dispatch/FL(days)	R&B&D - Disp	10 30	444			16 508		
B 2 1 4 2	P-4 Combo Other/>=10 circuits/Non-Dispatch/FL(days)	R&B&D - Disp							
B 2 1 5 3	P-4 xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/FL(days)	ADSL to Retail	5 70	1,928	4 55	207	11 826	0 86493	1 3296
B 2 1 5 3	P-4 xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	2 99	570			1 893		
B 2 1 5 4	P-4 xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/FL(days)	ADSL to Retail							
B 2 1 5 4	P-4 xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail							
B 2 1 5 5	P-4 xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/FL(days)	ADSL to Retail							
B 2 1 5 5	P-4 xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/FL(days)	ADSL to Retail							
B 2 1 6 3	P-4 UNE ISDN/<6 circuits/Dispatch/FL(days)	ISDN - BRI	18 07	439	10 58	321	17 316	1 27167	5 8899
B 2 1 6 3	P-4 UNE ISDN/<6 circuits/Non-Dispatch/FL(days)	ISDN - BRI	2 16	526			4 254		
B 2 1 6 4	P-4 UNE ISDN/6-13 circuits/Dispatch/FL(days)	ISDN - BRI							
B 2 1 6 4	P-4 UNE ISDN/6-13 circuits/Non-Dispatch/FL(days)	ISDN - BRI	2 00	1			0 000		
B 2 1 6 5	P-4 UNE ISDN/>=14 circuits/Dispatch/FL(days)	ISDN - BRI							
B 2 1 6 5	P-4 UNE ISDN/>=14 circuits/Non-Dispatch/FL(days)	ISDN - BRI							
B 2 1 7 3	P-4 Line Sharing/<6 circuits/Dispatch/FL(days)	ADSL to Retail	5 70	1,928	5 80	5	11 826	5 29542	0 0189
B 2 1 7 3	P-4 Line Sharing/<6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	2 99	570	3 67	70	1 893	0 23971	-2 8368
B 2 1 7 4	P-4 Line Sharing/6-13 circuits/Dispatch/FL(days)	ADSL to Retail							
B 2 1 7 4	P-4 Line Sharing/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail							
B 2 1 7 5	P-4 Line Sharing/>=14 circuits/Dispatch/FL(days)	ADSL to Retail							
B 2 1 7 5	P-4 Line Sharing/>=14 circuits/Non-Dispatch/FL(days)	ADSL to Retail							
B 2 1 8 1	P-4 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	4 08	70,634	4 75	105	6 044	0 59022	-1 1265
B 2 1 8 1	P-4 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	4 08	70,634			6 044		

BellSouth Monthly State Summary
Florida, July 2001

B 2 1 8 2 1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B 2 1 8 2 2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 9 1 1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B 2 1 9 1 4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(days)
B 2 1 9 2 1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B 2 1 9 2 4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(days)
B 2 1 10 1 1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B 2 1 10 1 2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 1 10 2 1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B 2 1 10 2 2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 11 1 1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B 2 1 11 1 4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(days)
B 2 1 11 2 1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(days)
B 2 1 11 2 4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(days)
B 2 1 12 1 1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 1 12 1 2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 1 12 2 1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B 2 1 12 2 2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 13 1 1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B 2 1 13 1 4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(days)
B 2 1 13 2 1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B 2 1 13 2 4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(days)
B 2 1 14 1 1	P-4	Other Design/<10 circuits/Dispatch/FL(days)
B 2 1 14 1 2	P-4	Other Design/<10 circuits/Non-Dispatch/FL(days)
B 2 1 14 2 1	P-4	Other Design/>=10 circuits/Dispatch/FL(days)
B 2 1 14 2 2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 15 1 1	P-4	Other Non-Design/<10 circuits/Dispatch/FL(days)
B 2 1 15 1 2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2 1 15 2 1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B 2 1 15 2 2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 16 1 1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2 1 16 1 2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B 2 1 16 2 1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B 2 1 16 2 2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 17 1 1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2 1 17 1 2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B 2 1 17 2 1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B 2 1 17 2 2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 18 1 1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B 2 1 18 1 2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B 2 1 18 2 1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B 2 1 18 2 2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 19 1 1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B 2 1 19 1 2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B 2 1 19 2 1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
B 2 1 19 2 2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)

Order Completion Interval within X days		
B 2 2 1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL(days)
B 2 2 2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL(days)

Held Orders		
B 2 3 1 1 1	P-1	Switch Ports/<10 circuits/Facility/FL(days)
B 2 3 1 1 2	P-1	Switch Ports/<10 circuits/Equipment/FL(days)
B 2 3 1 1 3	P-1	Switch Ports/<10 circuits/Other/FL(days)
B 2 3 1 2 1	P-1	Switch Ports/>=10 circuits/Facility/FL(days)
B 2 3 1 2 2	P-1	Switch Ports/>=10 circuits/Equipment/FL(days)
B 2 3 1 2 3	P-1	Switch Ports/>=10 circuits/Other/FL(days)
B 2 3 2 1 1	P-1	Local Interoffice Transport/<10 circuits/Facility/FL(days)
B 2 3 2 1 2	P-1	Local Interoffice Transport/<10 circuits/Equipment/FL(days)
B 2 3 2 1 3	P-1	Local Interoffice Transport/<10 circuits/Other/FL(days)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
R&B - Disp	10 18	436	7 00	1	16 544	16 56246	0 1923	YES
R&B - Disp	10 18	436			16 544			
R&B (POTS) excl SB Or	4 06	70,039	4 09	201	5 995	0 42348	-0 0680	YES
R&B (POTS) excl SB Or	1 73	244,414	3 86	7	2 186	0 82636	-2 5790	NO
R&B (POTS) excl SB Or	9 56	396	6 00	2	15 836	11 22584	0 3170	YES
R&B (POTS) excl SB Or	18 60	10			23 458			
R&B - Disp	4 08	70,634	5 50	4	6 044	3 02184	-0 4685	YES
R&B - Disp	4 08	70,634			6 044			
R&B - Disp	10 18	436			16 544			
R&B - Disp	10 18	436			16 544			
R&B (POTS) excl SB Or	4 06	70,039	4 20	5	5 995	2 68128	-0 0520	YES
R&B (POTS) excl SB Or	1 73	244,414	5 00	2	2 186	1 54595	-2 1178	NO
R&B (POTS) excl SB Or	9 58	396			15 836			
R&B (POTS) excl SB Or	18 60	10			23 458			
R&B - Disp	4 08	70,634	5.87	223	6 044	0 40534	-4 4055	NO
R&B - Disp	4 08	70,634			6 044			
R&B - Disp	10 18	436	8 00	16	16 544	4 21108	0 5187	YES
R&B - Disp	10 18	436			16 544			
R&B (POTS) excl SB Or	4 06	70,039	4 60	146	5 995	0 49669	-1 0858	YES
R&B (POTS) excl SB Or	1 73	244,414			2 186			
R&B (POTS) excl SB Or	9 56	396	7 50	4	15 836	7 95779	0 2586	YES
R&B (POTS) excl SB Or	18 60	10			23 458			
Design	22 91	4,852	7 88	43	25 680	3 93352	3 8212	YES
Design	23 16	454			65 006			
Design	16 50	8	6 00	1	13 939	14 78412	0 7102	YES
Design	2 87	63			3 591			
R&B	4 08	70,634	5 11	27	6 044	1 16329	-0 8824	YES
R&B	0 89	618,135	2 67	5	1 589	0 71076	-2 5000	NO
R&B	10 18	436	1 00	1	16 544	16 56246	0 5545	YES
R&B	4 12	205			9 422			
R&B (POTS)	4 06	70,064			5 995			
R&B (POTS)	0 89	616,137			1 587			
R&B (POTS)	9 56	396			15 836			
R&B (POTS)	18 75	15			29 243			
R&B (POTS)	4 06	70,064	1 66	5	5 995	2 68128	0 8953	YES
R&B (POTS)	0 89	616,137	0 72	2,515	1 587	0 03171	5 2668	YES
R&B (POTS)	9 56	396	19 00	1	15 836	15 85578	-0 5955	YES
R&B (POTS)	18 75	15	2 76	10	29 243	11 93853	1 3398	YES
Digital Loop < DS1	17 80	526	8 22	525	20 253	1 24943	7 6655	YES
Digital Loop < DS1	5 83	622			7 006			
Digital Loop < DS1								
Digital Loop < DS1								
Digital Loop >= DS1	17 41	82	7 74	78	22 153	3 50374	2 7576	YES
Digital Loop >= DS1	13 54	18			18 577			
Digital Loop >= DS1								
Digital Loop >= DS1								

14 days	11 00	1						YES
7 days	4 51	206						YES

R&B (POTS)	10 81	614			14 146			
R&B (POTS)	0 00	0						
R&B (POTS)	22 83	63			27 742			
R&B (POTS)	15 50	2			20 506			
R&B (POTS)	0 00	0						
R&B (POTS)	0 00	0						
DS1/ DS3 - Interoffice			0 00	0				
DS1/ DS3 - Interoffice			49 00	1				
DS1/ DS3 - Interoffice			0 00	0				

BellSouth Monthly State Summary
Florida, July 2001

B 2 3 2 2 1	P-1	Local Interoffice Transport/>=10 circuits/Facility/FL(days)
B 2 3 2 2 2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/FL(days)
B 2 3 2 2 3	P-1	Local Interoffice Transport/>=10 circuits/Other/FL(days)
B 2 3 3 1 1	P-1	Loop + Port Combinations/<10 circuits/Facility/FL(days)
B 2 3 3 1 2	P-1	Loop + Port Combinations/<10 circuits/Equipment/FL(days)
B 2 3 3 1 3	P-1	Loop + Port Combinations/<10 circuits/Other/FL(days)
B 2 3 3 2 1	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL(days)
B 2 3 3 2 2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL(days)
B 2 3 3 2 3	P-1	Loop + Port Combinations/>=10 circuits/Other/FL(days)
B 2 3 4 1 1	P-1	Combo Other/<10 circuits/Facility/FL(days)
B 2 3 4 1 2	P-1	Combo Other/<10 circuits/Equipment/FL(days)
B 2 3 4 1 3	P-1	Combo Other/<10 circuits/Other/FL(days)
B 2 3 4 2 1	P-1	Combo Other/>=10 circuits/Facility/FL(days)
B 2 3 4 2 2	P-1	Combo Other/>=10 circuits/Equipment/FL(days)
B 2 3 4 2 3	P-1	Combo Other/>=10 circuits/Other/FL(days)
B 2 3 5 1 1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL(days)
B 2 3 5 1 2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL(days)
B 2 3 5 1 3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL(days)
B 2 3 5 2 1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL(days)
B 2 3 5 2 2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL(days)
B 2 3 5 2 3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL(days)
B 2 3 6 1 1	P-1	UNE ISDN/<10 circuits/Facility/FL(days)
B 2 3 6 1 2	P-1	UNE ISDN/<10 circuits/Equipment/FL(days)
B 2 3 6 1 3	P-1	UNE ISDN/<10 circuits/Other/FL(days)
B 2 3 6 2 1	P-1	UNE ISDN/>=10 circuits/Facility/FL(days)
B 2 3 6 2 2	P-1	UNE ISDN/>=10 circuits/Equipment/FL(days)
B 2 3 6 2 3	P-1	UNE ISDN/>=10 circuits/Other/FL(days)
B 2 3 7 1 1	P-1	Line Sharing/<10 circuits/Facility/FL(days)
B 2 3 7 1 2	P-1	Line Sharing/<10 circuits/Equipment/FL(days)
B 2 3 7 1 3	P-1	Line Sharing/<10 circuits/Other/FL(days)
B 2 3 7 2 1	P-1	Line Sharing/>=10 circuits/Facility/FL(days)
B 2 3 7 2 2	P-1	Line Sharing/>=10 circuits/Equipment/FL(days)
B 2 3 7 2 3	P-1	Line Sharing/>=10 circuits/Other/FL(days)
B 2 3 8 1 1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL(days)
B 2 3 8 1 2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL(days)
B 2 3 8 1 3	P-1	2W Analog Loop Design/<10 circuits/Other/FL(days)
B 2 3 8 2 1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL(days)
B 2 3 8 2 2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL(days)
B 2 3 8 2 3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL(days)
B 2 3 9 1 1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL(days)
B 2 3 9 1 2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 9 1 3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL(days)
B 2 3 9 2 1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL(days)
B 2 3 9 2 2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL(days)
B 2 3 9 2 3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL(days)
B 2 3 10 1 1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL(days)
B 2 3 10 1 2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL(days)
B 2 3 10 1 3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL(days)
B 2 3 10 2 1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL(days)
B 2 3 10 2 2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL(days)
B 2 3 10 2 3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL(days)
B 2 3 11 1 1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL(days)
B 2 3 11 1 2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 11 1 3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL(days)
B 2 3 11 2 1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL(days)
B 2 3 11 2 2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL(days)
B 2 3 12 1 3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL(days)
B 2 3 12 2 1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL(days)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
R&B	10 84	624	0 00	0	14 053			YES
R&B	0 00	0	0 00	0				YES
R&B	21 94	67	1 50	2	27 135	19 47147	1 0498	YES
R&B	10 67	3	0 00	0	16 743			YES
R&B	0 00	0	0 00	0				YES
R&B	0 00	0	0 00	0				YES
R&B&D - Disp	10 91	636			13 959			
R&B&D - Disp	0 00	0						
R&B&D - Disp	27 70	94			51 480			
R&B&D - Disp	7 20	5			12 755			
R&B&D - Disp	0 00	0						
R&B&D - Disp	15 86	7			3 615			
ADSL to Retail	38 90	772	1 50	2	38 287	27 10790	1 3797	YES
ADSL to Retail	0 00	0	0 00	0				YES
ADSL to Retail	15 21	29	0 00	0	31 236			YES
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
ISDN - BRI	14 00	7	32 00	2	6 583	5 27798	-3 4104	NO
ISDN - BRI	0 00	0	0 00	0				YES
ISDN - BRI	0 00	0	0 00	0				YES
ISDN - BRI								
ADSL to Retail	38 90	772	8 00	1	38 287	38 31159	0 8066	YES
ADSL to Retail	0 00	0	0 00	0				YES
ADSL to Retail	15 21	29	0 00	0	31 236			YES
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
R&B - Disp	10 84	624	0 00	0	14 064			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3	0 00	0	16 743			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	10 81	614	0 00	0	14 146			YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	22 83	63	0 00	0	27 742			YES
R&B (POTS) excl SB Or	15 50	2	7 00	1	20 506	25 11474	0 3384	YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B - Disp	10 84	624	0 00	0	14 064			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3			16 743			
R&B - Disp	0 00	0						
R&B (POTS) excl SB Or	10 81	614	0 00	0	14 146			YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	22 83	63	0 00	0	27 742			YES
R&B (POTS) excl SB Or	15 50	2			20 506			
R&B (POTS) excl SB Or	0 00	0						
R&B (POTS) excl SB Or	0 00	0						
R&B - Disp	10 84	624	8 67	3	14 064	8 13923	0 2668	YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3	13 00	3	16 743	13 67077	-0 1707	YES

BellSouth Monthly State Summary
Florida, July 2001

B 2 5 16	P-2	INP (Standalone)/FL(%)
B 2 5 17	P-2	LNP (Standalone)/FL(%)
B 2 5 18	P-2	Digital Loop < DS1/FL(%)
B 2 5 19	P-2	Digital Loop >= DS1/FL(%)

% Jeopardies - Non-Mechanized

B 2 6 1	P-2	Switch Ports/FL(%)
B 2 6 2	P-2	Local Interoffice Transport/FL(%)
B 2 6 3	P-2	Loop + Port Combinations/FL(%)
B 2 6 4	P-2	Combo Other/FL(%)
B 2 6 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)
B 2 6 6	P-2	UNE ISDN/FL(%)
B 2 6 7	P-2	Line Sharing/FL(%)
B 2 6 8	P-2	2W Analog Loop Design/FL(%)
B 2 6 9	P-2	2W Analog Loop Non-Design/FL(%)
B 2 6 10	P-2	2W Analog Loop w/INP Design/FL(%)
B 2 6 11	P-2	2W Analog Loop w/INP Non-Design/FL(%)
B 2 6 12	P-2	2W Analog Loop w/LNP Design/FL(%)
B 2 6 13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)
B 2 6 14	P-2	Other Design/FL(%)
B 2 6 15	P-2	Other Non-Design/FL(%)
B 2 6 16	P-2	INP (Standalone)/FL(%)
B 2 6 17	P-2	LNP (Standalone)/FL(%)
B 2 6 18	P-2	Digital Loop < DS1/FL(%)
B 2 6 19	P-2	Digital Loop >= DS1/FL(%)

Average Jeopardy Notice Interval - Mechanized

B 2 8 1	P-2	Switch Ports/FL(hours)
B 2 8 2	P-2	Local Interoffice Transport/FL(hours)
B 2 8 3	P-2	Loop + Port Combinations/FL(hours)
B 2 8 4	P-2	Combo Other/FL(hours)
B 2 8 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)
B 2 8 6	P-2	UNE ISDN/FL(hours)
B 2 8 7	P-2	Line Sharing/FL(hours)
B 2 8 8	P-2	2W Analog Loop Design/FL(hours)
B 2 8 9	P-2	2W Analog Loop Non-Design/FL(hours)
B 2 8 10	P-2	2W Analog Loop w/INP Design/FL(hours)
B 2 8 11	P-2	2W Analog Loop w/INP Non-Design/FL(hours)
B 2 8 12	P-2	2W Analog Loop w/LNP Design/FL(hours)
B 2 8 13	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)
B 2 8 14	P-2	Other Design/FL(hours)
B 2 8 15	P-2	Other Non-Design/FL(hours)
B 2 8 16	P-2	INP (Standalone)/FL(hours)
B 2 8 17	P-2	LNP (Standalone)/FL(hours)
B 2 8 18	P-2	Digital Loop < DS1/FL(hours)
B 2 8 19	P-2	Digital Loop >= DS1/FL(hours)

Average Jeopardy Notice Interval - Non-Mechanized

B 2 9 1	P-2	Switch Ports/FL(hours)
B 2 9 2	P-2	Local Interoffice Transport/FL(hours)
B 2 9 3	P-2	Loop + Port Combinations/FL(hours)
B 2 9 4	P-2	Combo Other/FL(hours)
B 2 9 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)
B 2 9 6	P-2	UNE ISDN/FL(hours)
B 2 9 7	P-2	Line Sharing/FL(hours)
B 2 9 8	P-2	2W Analog Loop Design/FL(hours)
B 2 9 9	P-2	2W Analog Loop Non-Design/FL(hours)
B 2 9 10	P-2	2W Analog Loop w/INP Design/FL(hours)
B 2 9 11	P-2	2W Analog Loop w/INP Non-Design/FL(hours)
B 2 9 12	P-2	2W Analog Loop w/LNP Design/FL(hours)
B 2 9 13	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)
B 2 9 14	P-2	Other Design/FL(hours)

Benchmark / Analog

R&B (POTS)
R&B (POTS)
Digital Loop < DS1
Digital Loop >= DS1

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
0.80%	739,870						
0.80%	739,870	0.00%	4,000		0.00141	5.6730	YES
15.02%	1,272	8.44%	320		0.02234	2.9445	YES
28.24%	131	64.77%	88		0.06205	-5.8870	NO

Diagnostic							Diagnostic
Diagnostic		0.00%	12				Diagnostic
Diagnostic		0.44%	1,129				Diagnostic
Diagnostic							Diagnostic
Diagnostic							Diagnostic
Diagnostic		14.45%	263				Diagnostic
Diagnostic		38.32%	381				Diagnostic
Diagnostic		0.00%	7				Diagnostic
Diagnostic		25.86%	116				Diagnostic
Diagnostic		8.50%	153				Diagnostic
Diagnostic		0.00%	5				Diagnostic
Diagnostic		0.00%	6				Diagnostic
Diagnostic		4.00%	494				Diagnostic
Diagnostic		2.00%	1,603				Diagnostic
Diagnostic		10.96%	146				Diagnostic
Diagnostic		25.00%	32				Diagnostic
Diagnostic							Diagnostic
Diagnostic		0.00%	1,460				Diagnostic
Diagnostic		28.64%	639				Diagnostic
Diagnostic		58.17%	153				Diagnostic

>= 48 hrs							
>= 48 hrs							
>= 48 hrs		172.50	16				YES
>= 48 hrs							
>= 48 hrs		174.67	18				YES
>= 48 hrs		412.80	15				YES
>= 48 hrs							
>= 48 hrs		271.11	27				YES
>= 48 hrs		241.09	44				YES
>= 48 hrs							
>= 48 hrs							
>= 48 hrs		238.75	1				YES
>= 48 hrs		124.10	1				YES
>= 48 hrs		404.57	7				YES
>= 48 hrs		318.00	4				YES
>= 48 hrs							
>= 48 hrs							
>= 48 hrs							
>= 48 hrs							
>= 48 hrs		305.78	27				YES
>= 48 hrs		338.11	57				YES

Diagnostic							Diagnostic
Diagnostic							Diagnostic
Diagnostic		240.00	5				Diagnostic
Diagnostic							Diagnostic
Diagnostic		169.89	38				Diagnostic
Diagnostic		239.51	146				Diagnostic
Diagnostic							Diagnostic
Diagnostic		138.40	30				Diagnostic
Diagnostic		247.38	13				Diagnostic
Diagnostic							Diagnostic
Diagnostic							Diagnostic
Diagnostic		105.40	18				Diagnostic
Diagnostic		109.21	30				Diagnostic
Diagnostic		253.50	16				Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 9 15	P-2 Other Non-Design/FL(hours)			144 00	8				Diagnostic
B 2 9 16	P-2 INP (Standalone)/FL(hours)								Diagnostic
B 2 9 17	P-2 LNP (Standalone)/FL(hours)								Diagnostic
B 2 9 18	P-2 Digital Loop < DS1/FL(hours)			226 23	183				Diagnostic
B 2 9 19	P-2 Digital Loop >= DS1/FL(hours)			337 89	89				Diagnostic
% Jeopardy Notice >= 48 hours - Mechanized									
B 2 10 1	P-2 Switch Ports/FL(%)								95% >= 48 hrs
B 2 10 2	P-2 Local Interoffice Transport/FL(%)								95% >= 48 hrs
B 2 10 3	P-2 Loop + Port Combinations/FL(%)			66 67%	6				NO
B 2 10 4	P-2 Combo Other/FL(%)								95% >= 48 hrs
B 2 10 5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)								95% >= 48 hrs
B 2 10 6	P-2 UNE ISDN/FL(%)								95% >= 48 hrs
B 2 10 7	P-2 Line Sharing/FL(%)								95% >= 48 hrs
B 2 10 8	P-2 2W Analog Loop Design/FL(%)								95% >= 48 hrs
B 2 10 9	P-2 2W Analog Loop Non-Design/FL(%)			93 75%	32				NO
B 2 10 10	P-2 2W Analog Loop w/INP Design/FL(%)								95% >= 48 hrs
B 2 10 11	P-2 2W Analog Loop w/INP Non-Design/FL(%)								95% >= 48 hrs
B 2 10 12	P-2 2W Analog Loop w/LNP Design/FL(%)			0 00%	1				NO
B 2 10 13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)			0 00%	1				NO
B 2 10 14	P-2 Other Design/FL(%)								95% >= 48 hrs
B 2 10 15	P-2 Other Non-Design/FL(%)								95% >= 48 hrs
B 2 10 16	P-2 INP (Standalone)/FL(%)								95% >= 48 hrs
B 2 10 17	P-2 LNP (Standalone)/FL(%)								95% >= 48 hrs
B 2 10 18	P-2 Digital Loop < DS1/FL(%)								95% >= 48 hrs
B 2 10 19	P-2 Digital Loop >= DS1/FL(%)			100 00%	21				YES
% Jeopardy Notice >= 48 hours - Non-Mechanized									
B 2 11 1	P-2 Switch Ports/FL(%)								Diagnostic
B 2 11 2	P-2 Local Interoffice Transport/FL(%)								Diagnostic
B 2 11 3	P-2 Loop + Port Combinations/FL(%)			85 71%	7				Diagnostic
B 2 11 4	P-2 Combo Other/FL(%)								Diagnostic
B 2 11 5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)			92 31%	13				Diagnostic
B 2 11 6	P-2 UNE ISDN/FL(%)								Diagnostic
B 2 11 7	P-2 Line Sharing/FL(%)								Diagnostic
B 2 11 8	P-2 2W Analog Loop Design/FL(%)								Diagnostic
B 2 11 9	P-2 2W Analog Loop Non-Design/FL(%)			88 89%	9				Diagnostic
B 2 11 10	P-2 2W Analog Loop w/INP Design/FL(%)								Diagnostic
B 2 11 11	P-2 2W Analog Loop w/INP Non-Design/FL(%)								Diagnostic
B 2 11 12	P-2 2W Analog Loop w/LNP Design/FL(%)			33 00%	18				Diagnostic
B 2 11 13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)			37 00%	30				Diagnostic
B 2 11 14	P-2 Other Design/FL(%)			100 00%	5				Diagnostic
B 2 11 15	P-2 Other Non-Design/FL(%)								Diagnostic
B 2 11 16	P-2 INP (Standalone)/FL(%)								Diagnostic
B 2 11 17	P-2 LNP (Standalone)/FL(%)								Diagnostic
B 2 11 18	P-2 Digital Loop < DS1/FL(%)			100 00%	7				Diagnostic
B 2 11 19	P-2 Digital Loop >= DS1/FL(%)			97 37%	38				Diagnostic
Coordinated Customers Conversions									
B 2 12 1	P-7 Loops with INP/FL(%)								>= 95% w in 15 min
B 2 12 2	P-7 Loops with LNP/FL(%)			99 00%	4,595				>= 95% w in 15 min YES
% Hot Cuts > 15 minutes Early									
B 2 13 1	P-7A Time-Specific SL1/FL(%)			0 62%	324				<= 5% YES
B 2 13 2	P-7A Time-Specific SL2/FL(%)			0 24%	417				<= 5% YES
B 2 13 3	P-7A Non-Time Specific SL1/FL(%)			0 00%	11				<= 5% YES
B 2 13 4	P-7A Non-Time Specific SL2/FL(%)			0 35%	288				<= 5% YES
Hot Cut Timeliness									
B 2 14 1	P-7A Time-Specific SL1/FL(%)			98 46%	324				>= 95% w in 15 min YES
B 2 14 2	P-7A Time-Specific SL2/FL(%)			99 52%	417				>= 95% w in 15 min YES
B 2 14 3	P-7A Non-Time Specific SL1/FL(%)			100 00%	11				>= 95% w in 15 min YES
B 2 14 4	P-7A Non-Time Specific SL2/FL(%)			99 31%	288				>= 95% w in 15 min YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
% Hot Cuts > 15 minutes Late										
B 2 15 1	P-7A	Time-Specific SL1/FL(%)	<= 5%		0 93%	324				YES
B 2 15 2	P-7A	Time-Specific SL2/FL(%)	<= 5%		0 24%	417				YES
B 2 15 3	P-7A	Non-Time Specific SL1/FL(%)	<= 5%		0 00%	11				YES
B 2 15 4	P-7A	Non-Time Specific SL2/FL(%)	<= 5%		0 35%	288				YES
Average Recovery Time - CCC										
B 2 16 1	P-7B	Loops with INP/FL(time units)	Diagnostic							Diagnostic
B 2 16 2	P-7B	Loops with LNP/FL(time units)	Diagnostic		428.04	11				Diagnostic
% Provisioning Troubles within 7 Days - Hot Cuts										
B 2 17 1 1	P-7C	UNE Loop Design/Dispatch/FL(%)	<= 5%		2 19%	3,330				YES
B 2 17 1 2	P-7C	UNE Loop Design/Non-Dispatch/FL(%)	<= 5%							
B 2 17 2 1	P-7C	UNE Loop Non-Design/Dispatch/FL(%)	<= 5%		1 01%	2,283				YES
B 2 17 2 2	P-7C	UNE Loop Non-Design/Non-Dispatch/FL(%)	<= 5%		0 85%	1,173				YES
% Missed Installation Appointments										
B 2 18 1 1 1	P-3	Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	4 52%	81,425					
B 2 18 1 1 2	P-3	Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0 06%	653,366					
B 2 18 1 2 1	P-3	Switch Ports/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	5 73%	436					
B 2 18 1 2 2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0 00%	21					
B 2 18 2 1 1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3			7 69%	13			
B 2 18 2 1 2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 18 2 2 1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3							
B 2 18 2 2 2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 18 3 1 1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	4 54%	82,059	4 06%	394	0 01052	0 4585	YES
B 2 18 3 1 2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0 06%	655,410	0 22%	10,347	0 00024	-6 8582	NO
B 2 18 3 2 1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	0 00%	315,963	0 00%	4,791	0 00000		YES
B 2 18 3 2 2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0 14%	282,459	0 41%	5,556	0 00050	-5 5697	NO
B 2 18 3 3 1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	5 44%	478	11 11%	9	0 07631	-0 7433	YES
B 2 18 3 3 2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	0 00%	227	0 00%	5	0 00000		YES
B 2 18 3 3 3	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	0 00%	58	0 00%	1	0 00000		YES
B 2 18 3 3 4	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	0 00%	169	0 00%	4	0 00000		YES
B 2 18 4 1 1	P-3	Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	4 57%	86,953					
B 2 18 4 1 2	P-3	Combo Other/<10 circuits/Non-Dispatch/FL(%)	R&B&D - Disp							
B 2 18 4 2 1	P-3	Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	5 56%	486					
B 2 18 4 2 2	P-3	Combo Other/>=10 circuits/Non-Dispatch/FL(%)	R&B&D - Disp							
B 2 18 5 1 1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7 25%	16,850	1 57%	508	0 01167	4 8580	YES
B 2 18 5 1 2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 15%	683					
B 2 18 5 2 1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0 00%	7					
B 2 18 5 2 2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail							
B 2 18 6 1 1	P-3	UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	10 13%	454	9 24%	422	0 02040	0 4364	YES
B 2 18 6 1 2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	2 08%	530					
B 2 18 6 2 1	P-3	UNE ISDN/>=10 circuits/Dispatch/FL(%)	ISDN - BRI							
B 2 18 6 2 2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI							
B 2 18 7 1 1	P-3	Line Sharing/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7 25%	16,850	28 57%	7	0 09801	-2 1758	NO
B 2 18 7 1 2	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 15%	683	0 00%	101	0 00408	0 3592	YES
B 2 18 7 2 1	P-3	Line Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0 00%	7					
B 2 18 7 2 2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail							
B 2 18 8 1 1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4 54%	82,059	1 34%	224	0 01393	2 2994	YES
B 2 18 8 1 2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4 54%	82,059					
B 2 18 8 2 1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5 44%	478	0 00%	4	0 11387	0 4777	YES
B 2 18 8 2 2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 44%	478					
B 2 18 9 1 1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4 52%	81,390	3 39%	560	0 00881	1 2822	YES
B 2 18 9 1 2	P-3	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS) excl SB Or	0 13%	281,331	0 00%	10	0 01158	0 1160	YES
B 2 18 9 2 1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5 73%	436	25 00%	8	0 08295	-2 3227	NO
B 2 18 9 2 2	P-3	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS) excl SB Or	0 00%	14					
B 2 18 10 1 1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4 54%	82,059	0 00%	6	0 08502	0 5344	YES
B 2 18 10 1 2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4 54%	82,059					
B 2 18 10 2 1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5 44%	478					
B 2 18 10 2 2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 44%	478					

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 18 11 1 1	P-3 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4 52%	81,390	0 00%	6	0 08484	0 5331	YES
B 2 18 11 1 4	P-3 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 13%	281,331	0 00%	4	0 01832	0 0734	YES
B 2 18 11 2 1	P-3 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5 73%	436					
B 2 18 11 2 4	P-3 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 00%	14					
B 2 18 12 1 1	P-12 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4 54%	82,059	1 59%	691	0 00796	3 7120	YES
B 2 18 12 1 2	P-12 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4 54%	82,059					
B 2 18 12 2 1	P-12 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5 44%	478	0 00%	27	0 04486	1 2125	YES
B 2 18 12 2 2	P-12 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 44%	478					
B 2 18 12 2 1 1	P-12 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS) excl SB Or	4 52%	81,390	3 04%	626	0 00834	1 7784	YES
B 2 18 12 2 2 1	P-12 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS) excl SB Or	0 13%	281,331					
B 2 18 13 1 4	P-12 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	5 73%	436	0 00%	19	0 05449	1 0524	YES
B 2 18 13 2 1	P-12 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	0 00%	14					
B 2 18 13 2 4	P-12 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	4 97%	4,929	3 65%	192	0 01599	0 8286	YES
B 2 18 14 1 1	P-3 Other Design/<10 circuits/Dispatch/FL(%)	Design	3 93%	483					
B 2 18 14 1 2	P-3 Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	12 50%	8	0 00%	2	0 26146	0 4781	YES
B 2 18 14 2 1	P-3 Other Design/>=10 circuits/Dispatch/FL(%)	Design	0 00%	63					
B 2 18 14 2 2	P-3 Other Design/>=10 circuits/Non-Dispatch/FL(%)	R&B	4 54%	82,059	2 44%	41	0 03253	0 6468	YES
B 2 18 15 1 1	P-3 Other Non-Design/<10 circuits/Dispatch/FL(%)	R&B	0 06%	655,410	0 00%	8	0 00853	0 0683	YES
B 2 18 15 1 2	P-3 Other Non-Design/<10 circuits/Non-Dispatch/FL(%)	R&B	5 44%	478	0 00%	3	0 13135	0 4141	YES
B 2 18 15 2 1	P-3 Other Non-Design/>=10 circuits/Dispatch/FL(%)	R&B	0 00%	227					
B 2 18 15 2 2	P-3 Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	4 52%	81,425					
B 2 18 16 1 1	P-3 INP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	0 06%	653,366					
B 2 18 16 1 2	P-3 INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	5 73%	436					
B 2 18 16 2 1	P-3 INP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	0 00%	21					
B 2 18 16 2 2	P-3 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	4 52%	81,425	0 00%	6	0 08484	0 5331	YES
B 2 18 17 1 1	P-12 LNP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	0 06%	653,366	0 23%	2,569	0 00047	-3 6375	NO
B 2 18 17 1 2	P-12 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	5 73%	436	0 00%	1	0 23276	0 2463	YES
B 2 18 17 2 1	P-12 LNP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	0 00%	21	0 00%	25	0 00000		YES
B 2 18 17 2 2	P-12 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	9 80%	551	5 11%	901	0 01608	2 9198	YES
B 2 18 18 1 1	P-3 Digital Loop < DS1/<10 circuits/Dispatch/FL(%)	Digital Loop < DS1	0 00%	676					
B 2 18 18 1 2	P-3 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1							
B 2 18 18 2 1	P-3 Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop < DS1							
B 2 18 18 2 2	P-3 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1							
B 2 18 19 1 1	P-3 Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	9 41%	85	8 05%	236	0 03694	0 3684	YES
B 2 18 19 1 2	P-3 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	5 56%	18					
B 2 18 19 2 1	P-3 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop >= DS1							
B 2 18 19 2 2	P-3 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1							
% Provisioning Troubles within 30 Days									
B 2 19 1 1 1	P-9 Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	6 15%	80,517					
B 2 19 1 1 2	P-9 Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3 60%	714,657					
B 2 19 1 2 1	P-9 Switch Ports/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	6 36%	472					
B 2 19 1 2 2	P-9 Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	1 49%	67					
B 2 19 2 1 1	P-9 Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3			5 26%	19			
B 2 19 2 1 2	P-9 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 19 2 2 1	P-9 Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3							
B 2 19 2 2 2	P-9 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 19 3 1 1	P-9 Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	6 11%	81,168	3 72%	698	0 00911	2 6215	YES
B 2 19 3 1 2	P-9 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	3 59%	717,054	3 04%	10,241	0 00185	2 9689	YES
B 2 19 3 1 3	P-9 Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B							
B 2 19 3 1 4	P-9 Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	R&B							
B 2 19 3 2 1	P-9 Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	5 97%	519	20 00%	15	0 06207	-2 2600	NO
B 2 19 3 2 2	P-9 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	0 21%	471	0 00%	5	0 02069	0 1026	YES
B 2 19 3 2 3	P-9 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)	R&B							
B 2 19 3 2 4	P-9 Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	R&B							
B 2 19 4 1 1	P-9 Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	5 91%	87,031					
B 2 19 4 1 2	P-9 Combo Other/<10 circuits/Dispatch In/FL(%)	R&B&D - Disp							
B 2 19 4 2 1	P-9 Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	5 67%	547					
B 2 19 4 2 2	P-9 Combo Other/>=10 circuits/Dispatch In/FL(%)	R&B&D - Disp							
B 2 19 5 1 1	P-9 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail	6 95%	16,771	4 83%	476	0 01182	1 7894	YES
B 2 19 5 1 2	P-9 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	6 66%	871					
B 2 19 5 2 1	P-9 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	25 00%	4					

BellSouth Monthly State Summary
Florida, July 2001

B 2 19 5 2 2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 6 1 1	P-9	UNE ISDN/<10 circuits/Dispatch/FL(%)
B 2 19 6 1 2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)
B 2 19 6 2 1	P-9	UNE ISDN/>=10 circuits/Dispatch/FL(%)
B 2 19 6 2 2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 7 1 1	P-9	Line Sharing/<10 circuits/Dispatch/FL(%)
B 2 19 7 1 2	P-9	Line Sharing/<10 circuits/Non-Dispatch/FL(%)
B 2 19 7 2 1	P-9	Line Sharing/>=10 circuits/Dispatch/FL(%)
B 2 19 7 2 2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 8 1 1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)
B 2 19 8 1 2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 8 2 1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)
B 2 19 8 2 2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 9 1 1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 9 1 4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)
B 2 19 9 2 1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 9 2 4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)
B 2 19 10 1 1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)
B 2 19 10 1 2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 10 2 1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)
B 2 19 10 2 2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 11 1 1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 11 1 4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)
B 2 19 11 2 1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 11 2 4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)
B 2 19 12 1 1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)
B 2 19 12 1 2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 12 2 1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)
B 2 19 12 2 2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 13 1 1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 13 1 4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)
B 2 19 13 2 1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 13 2 4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)
B 2 19 14 1 1	P-9	Other Design/<10 circuits/Dispatch/FL(%)
B 2 19 14 1 2	P-9	Other Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 14 2 1	P-9	Other Design/>=10 circuits/Dispatch/FL(%)
B 2 19 14 2 2	P-9	Other Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 15 1 1	P-9	Other Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 15 1 2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 15 2 1	P-9	Other Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 15 2 2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 16 1 1	P-9	INP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 19 16 1 2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 19 16 2 1	P-9	INP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 19 16 2 2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 17 1 1	P-9	LNP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 19 17 1 2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 19 17 2 1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 19 17 2 2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 18 1 1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)
B 2 19 18 1 2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 19 18 2 1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)
B 2 19 18 2 2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 19 1 1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)
B 2 19 19 1 2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 19 19 2 1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)
B 2 19 19 2 2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)

Average Completion Notice Interval - Mechanized

B 2 21 1 11	P-5	Switch Ports/<10 circuits/Dispatch/FL(hours)
B 2 21 1 12	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)

Benchmark / Analog

ADSL to Retail
ISDN - BRI
ISDN - BRI
ISDN - BRI
ISDN - BRI
ADSL to Retail
ADSL to Retail
ADSL to Retail
R&B - Disp
R&B - Disp
R&B - Disp
R&B - Disp
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B - Disp
R&B - Disp
R&B - Disp
R&B - Disp
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B - Disp
R&B - Disp
R&B - Disp
R&B - Disp
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
R&B (POTS) excl SB Or
Design
Design
Design
Design
R&B
R&B
R&B
R&B
R&B (POTS)
R&B (POTS)
R&B (POTS)
R&B (POTS)
R&B (POTS)
R&B (POTS)
R&B (POTS)
Digital Loop < DS1
Digital Loop < DS1
Digital Loop < DS1
Digital Loop < DS1
Digital Loop >= DS1
Digital Loop >= DS1
Digital Loop >= DS1

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
0 00%	491	9 98%	401		0 00000		NO
0 00%	446						
6 95%	16,771	0 00%	2		0 17979	0 3864	YES
6 66%	871	13 04%	69		0 03118	-2.0465	NO
25 00%	4						
6 11%	81,168	0 25%	401		0 01199	4 8888	YES
6 11%	81,168						
5 97%	519	0 00%	5		0 10649	0 5609	YES
5 97%	519						
6 15%	80,517	0 00%	432		0 01159	5 3077	YES
		0 00%	2				
6 36%	472	0 00%	4		0 12250	0 5189	YES
6 11%	81,168	0 00%	5		0 10713	0 5705	YES
6 11%	81,168						
5 97%	519						
5 97%	519						
6 15%	80,517	0 00%	13		0 06665	0 9231	YES
		0 00%	1				
6 36%	472						
6 11%	81,168	3 53%	1,133		0 00717	3 6030	YES
6 11%	81,168						
5 97%	519	0 00%	22		0 05159	1 1579	YES
5 97%	519						
6 15%	80,517	0 00%	700		0 00912	6 7452	YES
		0 00%	1,096				
6 36%	472	0 00%	56		0 03448	1 8433	YES
		0 00%	11				
3 17%	5,863	2 35%	85		0 01915	0 4280	YES
0 48%	3,352						
0 00%	28						
0 00%	43						
6 11%	81,168	0 00%	13		0 06644	0 9199	YES
3 59%	717,054	0 00%	12		0 05368	0 6681	YES
5 97%	519	0 00%	6		0 09731	0 6138	YES
0 21%	471						
6 15%	80,517						
3 60%	714,657						
6 36%	472						
1 49%	67						
6 15%	80,517						
3 60%	714,657						
6 36%	472						
1 49%	67						
0 00%	598	7 26%	813		0 00000		NO
0 00%	9						
0 00%	76	11 71%	222		0 00000		NO
0 00%	16						
6 62	42,725			39 237			
1 50	497,006			8 558			

BellSouth Monthly State Summary
Florida, July 2001

B 2 2 1 1 2 1	P-5	Switch Ports/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 1 2 2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 2 1 1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/FL(hours)
B 2 2 1 2 1 2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 2 2 1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 2 2 2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 3 1 1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)
B 2 2 1 3 1 2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 3 1 3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)
B 2 2 1 3 1 4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/FL(hours)
B 2 2 1 3 2 1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 3 2 2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 3 2 3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)
B 2 2 1 3 2 4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)
B 2 2 1 4 1 1	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)
B 2 2 1 4 1 4	P-5	Combo Other/<10 circuits/Dispatch In/FL(hours)
B 2 2 1 4 2 1	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 4 2 4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)
B 2 2 1 5 1 1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)
B 2 2 1 5 1 2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 5 2 1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 5 2 2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 6 1 1	P-5	UNE ISDN/<10 circuits/Dispatch/FL(hours)
B 2 2 1 6 1 2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 6 2 1	P-5	UNE ISDN/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 6 2 2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 7 1 1	P-5	Line Sharing/<10 circuits/Dispatch/FL(hours)
B 2 2 1 7 1 2	P-5	Line Sharing/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 7 2 1	P-5	Line Sharing/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 7 2 2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 8 1 1	P-5	2W Analog Loop Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 8 1 2	P-5	2W Analog Loop Designr/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 8 2 1	P-5	2W Analog Loop Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 8 2 2	P-5	2W Analog Loop Designr/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 9 1 1	P-5	2W Analog Loop Non-Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 9 1 4	P-5	2W Analog Loop Non-Designr/<10 circuits/Dispatch In/FL(hours)
B 2 2 1 9 2 1	P-5	2W Analog Loop Non-Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 9 2 4	P-5	2W Analog Loop Non-Designr/>=10 circuits/Dispatch In/FL(hours)
B 2 2 1 10 1 1	P-5	2W Analog Loop w/INP Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 10 1 2	P-5	2W Analog Loop w/INP Designr/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 10 2 1	P-5	2W Analog Loop w/INP Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 10 2 2	P-5	2W Analog Loop w/INP Designr/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 11 1 1	P-5	2W Analog Loop w/INP Non-Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 11 1 4	P-5	2W Analog Loop w/INP Non-Designr/<10 circuits/Dispatch In/FL(hours)
B 2 2 1 11 2 1	P-5	2W Analog Loop w/INP Non-Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 11 2 4	P-5	2W Analog Loop w/INP Non-Designr/>=10 circuits/Dispatch In/FL(hours)
B 2 2 1 12 1 1	P-5	2W Analog Loop w/LNP Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 12 1 2	P-5	2W Analog Loop w/LNP Designr/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 12 2 1	P-5	2W Analog Loop w/LNP Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 12 2 2	P-5	2W Analog Loop w/LNP Designr/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 13 1 1	P-5	2W Analog Loop w/LNP Non-Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 13 1 4	P-5	2W Analog Loop w/LNP Non-Designr/<10 circuits/Dispatch In/FL(hours)
B 2 2 1 13 2 1	P-5	2W Analog Loop w/LNP Non-Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 13 2 4	P-5	2W Analog Loop w/LNP Non-Designr/>=10 circuits/Dispatch In/FL(hours)
B 2 2 1 14 1 1	P-5	Other Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 14 1 2	P-5	Other Designr/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 14 2 1	P-5	Other Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 14 2 2	P-5	Other Designr/>=10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 15 1 1	P-5	Other Non-Designr/<10 circuits/Dispatch/FL(hours)
B 2 2 1 15 1 2	P-5	Other Non-Designr/<10 circuits/Non-Dispatch/FL(hours)
B 2 2 1 15 2 1	P-5	Other Non-Designr/>=10 circuits/Dispatch/FL(hours)
B 2 2 1 15 2 2	P-5	Other Non-Designr/>=10 circuits/Non-Dispatch/FL(hours)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
R&B (POTS)	7.56	269			23.038			
R&B (POTS)	1.06	15			1.206			
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
R&B	6.65	43,194	1.27	227	39,186	2,60767	2,0631	YES
R&B	1.50	498,627	0.89	6,883	8,574	0.10405	5,8723	YES
R&B	1.97	262,221	0.84	2,676	10,345	0.20099	5,6613	YES
R&B	0.97	236,324	0.92	4,207	5,962	0.09273	0,5250	YES
R&B	7.34	295	0.99	7	22,380	8,55842	0,7417	YES
R&B	1.84	186	0.75	4	3,143	1,58811	0,6870	YES
R&B	2.76	49	0.42	1	4,124	4,16547	0,5620	YES
R&B	1.61	137	0.86	3	2,656	1,55020	0,4206	YES
R&B	16.44	46,429			139,220			
R&B&D - Disp								
R&B&D - Disp								
R&B&D - Disp	7.20	301			22,194			
R&B&D - Disp								
ADSL to Retail	14.80	13,392			40,665			
ADSL to Retail	2.56	555			21,642			
ADSL to Retail	13.67	7			35,164			
ISDN - BRI	35.47	316	0.02	1	73,684	73,80020	0,4803	YES
ISDN - BRI	5.74	393			24,125			
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	14.80	13,392			40,665			
ADSL to Retail	2.56	555			21,642			
ADSL to Retail	13.67	7			35,164			
ADSL to Retail								
R&B - Disp	6.65	43,194	20.02	125	39,186	3,50994	-3,8112	NO
R&B - Disp	6.65	43,194			39,186			
R&B - Disp	7.34	295	53.29	3	22,380	12,98634	-3,5384	NO
R&B - Disp	7.34	295			22,380			
R&B (POTS) excl SB Or	6.62	42,696	1.05	389	39,237	1,99844	2,7888	YES
R&B (POTS) excl SB Or	0.96	235,382	0.01	2	5,927	4,19097	0,2274	YES
R&B (POTS) excl SB Or	7.56	269	2.38	6	23,038	9,50937	0,5446	YES
R&B (POTS) excl SB Or	1.13	11			1,417			
R&B - Disp	6.65	43,194			39,186			
R&B - Disp	6.65	43,194			39,186			
R&B - Disp	7.34	295			22,380			
R&B - Disp	7.34	295			22,380			
R&B (POTS) excl SB Or	6.62	42,696			39,237			
R&B (POTS) excl SB Or	0.96	235,382			5,927			
R&B (POTS) excl SB Or	7.56	269			23,038			
R&B (POTS) excl SB Or	1.13	11			1,417			
R&B - Disp	6.65	43,194	20.63	277	39,186	2,36198	-5,9204	NO
R&B - Disp	6.65	43,194			39,186			
R&B - Disp	7.34	295	11.31	11	22,380	6,87233	-0,5774	YES
R&B - Disp	7.34	295			22,380			
R&B (POTS) excl SB Or	6.62	42,696	13.53	276	39,237	2,36942	-2,9149	NO
R&B (POTS) excl SB Or	0.96	235,382			5,927			
R&B (POTS) excl SB Or	7.56	269	43.94	5	23,038	10,39804	-3,4989	NO
R&B (POTS) excl SB Or	1.13	11			1,417			
Design	145.96	3,264			465,613			
Design	694.98	285			2082,695			
Design	0.13	6			0,174			
Design	3.99	54			4,475			
R&B	6.65	43,194			39,186			
R&B	1.50	498,627			8,574			
R&B	7.34	295			22,380			

Bellsouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 22 10 2 1	P-5 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 10 2 2	P-5 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 11 1 1	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)			19 51	6				Diagnostic
B 2 22 11 1 4	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)			19 90	1				Diagnostic
B 2 22 11 2 1	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 11 2 4	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)								Diagnostic
B 2 22 12 1 1	P-5 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)			42 51	180				Diagnostic
B 2 22 12 1 2	P-5 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 12 2 1	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)			91 21	8				Diagnostic
B 2 22 12 2 2	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 13 1 1	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)			30 00	237				Diagnostic
B 2 22 13 1 4	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)								Diagnostic
B 2 22 13 2 1	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)			34 41	10				Diagnostic
B 2 22 13 2 4	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)								Diagnostic
B 2 22 14 1 1	P-5 Other Design/<10 circuits/Dispatch/FL(hours)			38 18	27				Diagnostic
B 2 22 14 1 2	P-5 Other Design/<10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 14 2 1	P-5 Other Design/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 14 2 2	P-5 Other Design/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 15 1 1	P-5 Other Non-Design/<10 circuits/Dispatch/FL(hours)			40 07	16				Diagnostic
B 2 22 15 1 2	P-5 Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)			42 28	2				Diagnostic
B 2 22 15 2 1	P-5 Other Non-Design/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 15 2 2	P-5 Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 16 1 1	P-5 INP (Standalone)/<10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 16 1 2	P-5 INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 16 2 1	P-5 INP (Standalone)/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 16 2 2	P-5 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 17 1 1	P-5 LNP (Standalone)/<10 circuits/Dispatch/FL(hours)			28 03	61				Diagnostic
B 2 22 17 1 2	P-5 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)			27 91	788				Diagnostic
B 2 22 17 2 1	P-5 LNP (Standalone)/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 17 2 2	P-5 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)			38 01	6				Diagnostic
B 2 22 18 1 1	P-5 Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)			63 86	523				Diagnostic
B 2 22 18 1 2	P-5 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 18 2 1	P-5 Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 18 2 2	P-5 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 19 1 1	P-5 Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)			56 54	126				Diagnostic
B 2 22 19 1 2	P-5 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)								Diagnostic
B 2 22 19 2 1	P-5 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)								Diagnostic
B 2 22 19 2 2	P-5 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)								Diagnostic
Total Service Order Cycle Time - Mechanized									
B 2 24 1 1 1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 1 1 2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 1 2 1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 1 2 2	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 2 1 1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 2 1 2	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 2 2 1	P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 2 2 2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 3 1 1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)			3 33	61				Diagnostic
B 2 24 3 1 2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)			1 10	3,761				Diagnostic
B 2 24 3 2 1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)			3 00	2				Diagnostic
B 2 24 3 2 2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)			3 00	2				Diagnostic
B 2 24 4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 4 1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 4 2 1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 4 2 2	P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 5 1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 5 1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 5 2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 24 5 2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 24 6 1 1	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)								Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 29 4 2 1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 4 2 2	P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 5 1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 5 1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 5 2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 5 2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 6 1 1	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 6 1 2	P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 6 2 1	P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 6 2 2	P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 7 1 1	P-10 Line Sharing/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 7 1 2	P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 7 2 1	P-10 Line Sharing/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 7 2 2	P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 8 1 1	P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)			7 91	34				Diagnostic
B 2 29 8 1 2	P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 8 2 1	P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 8 2 2	P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 9 1 1	P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)			5 03	100				Diagnostic
B 2 29 9 1 2	P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 9 2 1	P-10 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 9 2 2	P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 10 1 1	P-10 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 10 1 2	P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 10 2 1	P-10 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 10 2 2	P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 11 1 1	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 11 1 2	P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 11 2 1	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 11 2 2	P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 12 1 1	P-14 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)			7 89	120				Diagnostic
B 2 29 12 1 2	P-14 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 12 2 1	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)			9 18	11				Diagnostic
B 2 29 12 2 2	P-14 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 13 1 1	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)			6 14	58				Diagnostic
B 2 29 13 1 2	P-14 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)			5 14	49				Diagnostic
B 2 29 13 2 1	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)			10 00	1				Diagnostic
B 2 29 13 2 2	P-14 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)			7.50	2				Diagnostic
B 2 29 14 1 1	P-10 Other Design/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 14 1 2	P-10 Other Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 14 2 1	P-10 Other Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 14 2 2	P-10 Other Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 15 1 1	P-10 Other Non-Design/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 15 1 2	P-10 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 15 2 1	P-10 Other Non-Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 15 2 2	P-10 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 16 1 1	P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 16 1 2	P-10 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 16 2 1	P-10 INP (Standalone)/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 16 2 2	P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 17 1 1	P-14 LNP (Standalone)/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 17 1 2	P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)			6 48	451				Diagnostic
B 2 29 17 2 1	P-14 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 17 2 2	P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 18 1 1	P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 18 1 2	P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 18 2 1	P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 18 2 2	P-10 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 19 1 1	P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)			8 08	13				Diagnostic
B 2 29 19 1 2	P-10 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 29 19 2 1	P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 29 19 2 2	P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic

BellSouth Monthly State Summary Florida, July 2001

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
P-10 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
Total Service Order Cycle Time (offered) - Non-Mechanized								
P-10 Switch Ports/<10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)			25	14		7		Diagnostic
P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)			4	88		32		Diagnostic
P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)			2	87		610		Diagnostic
P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Combo Other/<10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Combo Other/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)			8	07		101		Diagnostic
P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)			13	42		231		Diagnostic
P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Line Sharnng/<10 circuits/Dispatch/FL(days)			6	50		4		Diagnostic
P-10 Line Sharnng/<10 circuits/Non-Dispatch/FL(days)			6	05		56		Diagnostic
P-10 Line Sharnng/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Line Sharnng/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop Designr/<10 circuits/Dispatch/FL(days)			12	13		16		Diagnostic
P-10 2W Analog Loop Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop Designr/>=10 circuits/Dispatch/FL(days)			19	00		1		Diagnostic
P-10 2W Analog Loop Designr/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop Non-Designr/<10 circuits/Dispatch/FL(days)			7	81		47		Diagnostic
P-10 2W Analog Loop Non-Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop Non-Designr/>=10 circuits/Dispatch/FL(days)			9	50		2		Diagnostic
P-10 2W Analog Loop Non-Designr/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Designr/<10 circuits/Dispatch/FL(days)			6	00		1		Diagnostic
P-10 2W Analog Loop w/LNP Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Designr/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Designr/>=10 circuits/Non-Dispatch/FL(days)			8	40		5		Diagnostic
P-10 2W Analog Loop w/LNP Non-Designr/<10 circuits/Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Non-Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Non-Designr/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 2W Analog Loop w/LNP Non-Designr/>=10 circuits/Non-Dispatch/FL(days)			9	03		30		Diagnostic
P-14 2W Analog Loop w/LNP Designr/<10 circuits/Dispatch/FL(days)								Diagnostic
P-14 2W Analog Loop w/LNP Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-14 2W Analog Loop w/LNP Designr/>=10 circuits/Dispatch/FL(days)			8	00		1		Diagnostic
P-14 2W Analog Loop w/LNP Designr/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-14 2W Analog Loop w/LNP Non-Designr/<10 circuits/Dispatch/FL(days)			12	33		70		Diagnostic
P-14 2W Analog Loop w/LNP Non-Designr/<10 circuits/Non-Dispatch/FL(days)			7	18		115		Diagnostic
P-14 2W Analog Loop w/LNP Non-Designr/>=10 circuits/Dispatch/FL(days)			9	00		1		Diagnostic
P-14 2W Analog Loop w/LNP Non-Designr/>=10 circuits/Non-Dispatch/FL(days)			15	00		1		Diagnostic
P-10 Other Designr/<10 circuits/Dispatch/FL(days)			11	71		17		Diagnostic
P-10 Other Designr/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Other Designr/>=10 circuits/Dispatch/FL(days)								Diagnostic
P-10 Other Designr/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
P-10 Other Non-Designr/<10 circuits/Dispatch/FL(days)			9	07		14		Diagnostic
P-10 Other Non-Designr/<10 circuits/Non-Dispatch/FL(days)			7	00		1		Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
% Cooperative Test Attempts for xDSL									
B 2 33 1	P-8 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% of requests		97 31%	409				YES
B 2 33 2	P-8 xDSL Other/FL(%)	>= 95% of requests							
Service Order Accuracy									
B 2 34 1 1 1	P-11 Design (Specials)/<10 circuits/Dispatch/FL(%)	>= 95%		62 30%	61				NO
B 2 34 1 1 2	P-11 Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95%		60 20%	98				NO
B 2 34 1 2 1	P-11 Design (Specials)/>=10 circuits/Dispatch/FL(%)	>= 95%							
B 2 34 1 2 2	P-11 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	>= 95%		50 00%	2				NO
B 2 34 2 1 1	P-11 Loops Non-Design/<10 circuits/Dispatch/FL(%)	>= 95%		0 00%	4				NO
B 2 34 2 1 2	P-11 Loops Non-Design/<10 circuits/Non-Dispatch/FL(%)	>= 95%		60 78%	51				NO
B 2 34 2 2 1	P-11 Loops Non-Design/>=10 circuits/Dispatch/FL(%)	>= 95%		100 00%	3				YES
B 2 34 2 2 2	P-11 Loops Non-Design/>=10 circuits/Non-Dispatch/FL(%)	>= 95%		0 00%	2				NO
Unbundled Network Elements - Maintenance and Repair									
Missed Repair Appointments									
B 3 11 1	M&R-1 Switch Ports/Dispatch/FL(%)	R&B (POTS)	12 67%	99,788					
B 3 11 2	M&R-1 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	2 13%	56,786					
B 3 12 1	M&R-1 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1 19%	1,093	0 00%	2	0 07673	0 1550	YES
B 3 12 2	M&R-1 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0 00%	702	0 00%	8	0 00000		YES
B 3 13 1	M&R-1 Loop + Port Combinations/Dispatch/FL(%)	R&B	12 83%	101,333	7 40%	1,271	0 00944	5 7537	YES
B 3 13 2	M&R-1 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	2 22%	57,756	1 41%	567	0 00622	1 3031	YES
B 3 14 1	M&R-1 Combo Other/Dispatch/FL(%)	R&B&D - Disp	12 47%	104,233	0 00%	11	0 00961	1 2517	YES
B 3 14 2	M&R-1 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	12 47%	104,233	0 00%	6	0 13487	0 9244	YES
B 3 15 1	M&R-1 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	19 51%	1,148	7 46%	67	0 04981	2 4192	YES
B 3 15 2	M&R-1 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	2 10%	381	0 00%	14	0 03902	0 5382	YES
B 3 16 1	M&R-1 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	6 09%	361	11 86%	118	0 02537	-2 2747	NO
B 3 16 2	M&R-1 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	3 09%	291	2 63%	76	0 02230	0 2068	YES
B 3 17 1	M&R-1 Line Sharing/Dispatch/FL(%)	ADSL to Retail	19 51%	1,148	40 00%	10	0 12586	-1 6278	YES
B 3 17 2	M&R-1 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	2 10%	381	16 22%	37	0 02469	-5 7178	NO
B 3 18 1	M&R-1 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	12 83%	101,333	5 84%	1,216	0 00965	7 2434	YES
B 3 18 2	M&R-1 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	12 83%	101,333	3 01%	299	0 01937	5 0686	YES
B 3 19 1	M&R-1 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	12 61%	99,503	10 82%	628	0 01319	1 3647	YES
B 3 19 2	M&R-1 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	1 81%	47,644	3 57%	28	0 02521	-0 6982	YES
B 3 10 1	M&R-1 Other Design/Dispatch/FL(%)	Design	2 70%	3,185	0 00%	15	0 04195	0 6437	YES
B 3 10 2	M&R-1 Other Design/Non-Dispatch/FL(%)	Design	0 84%	3,340	0 00%	7	0 03450	0 2430	YES
B 3 11 1	M&R-1 Other Non-Design/Dispatch/FL(%)	R&B	12 83%	101,333	15 22%	46	0 04931	-0 4849	YES
B 3 11 2	M&R-1 Other Non-Design/Non-Dispatch/FL(%)	R&B	2 22%	57,756	4 88%	41	0 02302	-1 1538	YES
B 3 12 1	M&R-1 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	12 67%	99,788					
B 3 12 2	M&R-1 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	2 13%	56,786					
Customer Trouble Report Rate									
B 3 21 1	M&R-2 Switch Ports/Dispatch/FL(%)	R&B (POTS)	1 73%	5,784,806	0 00%	2	0 09287	0 1857	YES
B 3 21 2	M&R-2 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	0 98%	5,784,806	0 00%	2	0 07006	0 1401	YES
B 3 22 1	M&R-2 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	2 19%	49,965	0 17%	1,144	0 00442	4 5509	YES
B 3 22 2	M&R-2 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	1 40%	49,965	0 70%	1,144	0 00354	1 9910	YES
B 3 23 1	M&R-2 Loop + Port Combinations/Dispatch/FL(%)	R&B	1 64%	6,161,701	1 19%	107,094	0 00040	11 5811	YES
B 3 23 2	M&R-2 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	0 94%	6,161,701	0 53%	107,094	0 00030	13 6692	YES
B 3 24 1	M&R-2 Combo Other/Dispatch/FL(%)	R&B&D - Disp	1 49%	6,985,135	0 88%	1,249	0 00346	1 7690	YES
B 3 24 2	M&R-2 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	1 49%	6,985,135	0 48%	1,249	0 00346	2 9271	YES
B 3 25 1	M&R-2 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	0 56%	204,581	1 14%	5,902	0 00099	-5 8042	NO
B 3 25 2	M&R-2 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	0 19%	204,581	0 24%	5,902	0 00057	-0 8946	YES
B 3 26 1	M&R-2 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	1 42%	25,389	1 98%	5,954	0 00172	-3 2614	NO
B 3 26 2	M&R-2 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1 15%	25,389	1 28%	5,954	0 00154	-0 8452	YES
B 3 27 1	M&R-2 Line Sharing/Dispatch/FL(%)	ADSL to Retail	0 56%	204,581	1 13%	884	0 00252	-2 2578	NO
B 3 27 2	M&R-2 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	0 19%	204,581	4 19%	884	0 00145	-27 4943	NO
B 3 28 1	M&R-2 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	1 64%	6,161,701	1 64%	74,301	0 00047	0 1685	YES
B 3 28 2	M&R-2 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	1 64%	6,161,701	0 40%	74,301	0 00047	26 2447	YES
B 3 29 1	M&R-2 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	1 72%	5,784,806	1 73%	36,891	0 00069	-0 1364	YES
B 3 29 2	M&R-2 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	0 82%	5,784,806	0 08%	36,891	0 00047	15 7743	YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
B 3 2 10 1	M&R-2	Other Design/Dispatch/FL(%)	Design	0 38%	823,434	0 76%	1,972	0 00140	-2 6662	NO	
B 3 2 10 2	M&R-2	Other Design/Non-Dispatch/FL(%)	Design	0 41%	823,434	0 35%	1,972	0 00144	0 3527	YES	
B 3 2 11 1	M&R-2	Other Non-Design/Dispatch/FL(%)	R&B	1 64%	6,161,701	6 50%	708	0 00482	-10 0680	NO	
B 3 2 11 2	M&R-2	Other Non-Design/Non-Dispatch/FL(%)	R&B	0 94%	6,161,701	5 79%	708	0 00364	-13 3386	NO	
B 3 2 12 1	M&R-2	LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	1 73%	5,784,806						
B 3 2 12 2	M&R-2	LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	0 98%	5,784,806						
Maintenance Average Duration											
B 3 3 1 1	M&R-3	Switch Ports/Dispatch/FL(hours)	R&B (POTS)	24 03	99,788			24 742			
B 3 3 1 2	M&R-3	Switch Ports/Non-Dispatch/FL(hours)	R&B (POTS)	8 73	56,786			13 644			
B 3 3 2 1	M&R-3	Local Interoffice Transport/Dispatch/FL(hours)	DS1/DS3	4 00	1,093	4 88	2	4 117	2 91375	-0 3006	YES
B 3 3 2 2	M&R-3	Local Interoffice Transport/Non-Dispatch/FL(hours)	DS1/DS3	1 96	702	5 39	8	2 552	0 90739	-3 7860	NO
B 3 3 3 1	M&R-3	Loop + Port Combinations/Dispatch/FL(hours)	R&B	23 99	101,333	16 76	1,271	24 769	0 69910	10 3419	YES
B 3 3 3 2	M&R-3	Loop + Port Combinations/Non-Dispatch/FL(hours)	R&B	8 67	57,756	3 62	567	13 593	0 57365	8 7904	YES
B 3 3 4 1	M&R-3	Combo Other/Dispatch/FL(hours)	R&B&D - Disp	23 43	104,233	6 55	11	25 425	7 66645	2 2017	YES
B 3 3 4 2	M&R-3	Combo Other/Non-Dispatch/FL(hours)	R&B&D - Disp	23 43	104,233	4 39	6	25 425	10 38017	1 8347	YES
B 3 3 5 1	M&R-3	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours)	ADSL to Retail	71 47	1,148	9 16	67	48 912	6 14747	10 1358	YES
B 3 3 5 2	M&R-3	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(hours)	ADSL to Retail	30 53	381	2 97	14	43 729	11 89985	2 3156	YES
B 3 3 6 1	M&R-3	UNE ISDN/Dispatch/FL(hours)	ISDN - BRI	8 03	361	11 22	118	9 691	1 02759	-3 1060	NO
B 3 3 6 2	M&R-3	UNE ISDN/Non-Dispatch/FL(hours)	ISDN - BRI	3 48	291	5 59	76	9 230	1 18904	-1 7772	NO
B 3 3 7 1	M&R-3	Line Sharing/Dispatch/FL(hours)	ADSL to Retail	71 47	1,148	55 36	10	48 912	15 53458	1 0371	YES
B 3 3 7 2	M&R-3	Line Sharing/Non-Dispatch/FL(hours)	ADSL to Retail	30 53	381	22 60	37	43 729	7 52999	1 0521	YES
B 3 3 8 1	M&R-3	2W Analog Loop Design/Dispatch/FL(hours)	R&B - Disp	23 99	101,333	8 60	1,216	24 769	0 71454	21 5405	YES
B 3 3 8 2	M&R-3	2W Analog Loop Design/Non-Dispatch/FL(hours)	R&B - Disp	23 99	101,333	5 15	299	24 769	1 43453	13 1303	YES
B 3 3 9 1	M&R-3	2W Analog Loop Non-Design/Dispatch/FL(hours)	R&B (POTS) excl SB FT	24 02	99,503	17 34	638	24 736	0 98245	6 7956	YES
B 3 3 9 2	M&R-3	2W Analog Loop Non-Design/Non-Dispatch/FL(hours)	R&B (POTS) excl SB FT	8 91	47,644	7 91	28	13 962	2 63931	0 3792	YES
B 3 3 10 1	M&R-3	Other Design/Dispatch/FL(hours)	Design	6 20	3,185	5 35	15	37 164	9 61817	0 0882	YES
B 3 3 10 2	M&R-3	Other Design/Non-Dispatch/FL(hours)	Design	2 69	3,340	5 89	7	21 881	8 27886	-0 3872	YES
B 3 3 11 1	M&R-3	Other Non-Design/Dispatch/FL(hours)	R&B	23 99	101,333	29 51	46	24 769	3 65279	-1 5116	YES
B 3 3 11 2	M&R-3	Other Non-Design/Non-Dispatch/FL(hours)	R&B	8 67	57,756	7 53	41	13 593	2 12362	0 5365	YES
B 3 3 12 1	M&R-3	LNP (Standalone)/Dispatch/FL(hours)	R&B (POTS)	24 03	99,788			24 742			
B 3 3 12 2	M&R-3	LNP (Standalone)/Non-Dispatch/FL(hours)	R&B (POTS)	8 73	56,786			13 644			
% Repeat Troubles within 30 Days											
B 3 4 1 1	M&R-4	Switch Ports/Dispatch/FL(%)	R&B (POTS)	22 41%	99,788						
B 3 4 1 2	M&R-4	Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	19 62%	56,786						
B 3 4 2 1	M&R-4	Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	37 60%	1,093	50 00%	2		0 34283	-0 3616	YES
B 3 4 2 2	M&R-4	Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	34 62%	702	37 50%	8		0 16916	-0 1705	YES
B 3 4 3 1	M&R-4	Loop + Port Combinations/Dispatch/FL(%)	R&B	22 36%	101,333	19 98%	1,271		0 01176	2 0169	YES
B 3 4 3 2	M&R-4	Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	19 55%	57,756	21 69%	567		0 01674	-1 2830	YES
B 3 4 4 1	M&R-4	Combo Other/Dispatch/FL(%)	R&B&D - Disp	22 92%	104,233	45 45%	11		0 12674	-1 7778	NO
B 3 4 4 2	M&R-4	Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	22 92%	104,233	0 00%	6		0 17161	1 3358	YES
B 3 4 5 1	M&R-4	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	24 91%	1,148	28 36%	67		0 05436	-0 6338	YES
B 3 4 5 2	M&R-4	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	26 77%	381	7 14%	14		0 12049	1 6291	YES
B 3 4 6 1	M&R-4	UNE ISDN/Dispatch/FL(%)	ISDN - BRI	32 41%	361	26 27%	118		0 04963	1 2369	YES
B 3 4 6 2	M&R-4	UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	27 49%	291	38 16%	76		0 05751	-1 8546	NO
B 3 4 7 1	M&R-4	Line Sharing/Dispatch/FL(%)	ADSL to Retail	24 91%	1,148	50 00%	10		0 13737	-1 8263	NO
B 3 4 7 2	M&R-4	Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	26 77%	381	40 54%	37		0 07624	-1 8059	NO
B 3 4 8 1	M&R-4	2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	22 36%	101,333	19 82%	1,216		0 01202	2 1107	YES
B 3 4 8 2	M&R-4	2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	22 36%	101,333	26 42%	299		0 02413	-1 6848	NO
B 3 4 9 1	M&R-4	2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	22 39%	99,503	19 44%	638		0 01656	1 7816	YES
B 3 4 9 2	M&R-4	2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	19 87%	47,644	21 43%	28		0 07543	-0 2063	YES
B 3 4 10 1	M&R-4	Other Design/Dispatch/FL(%)	Design	41 79%	3,185	20 00%	15		0 12765	1 7070	YES
B 3 4 10 2	M&R-4	Other Design/Non-Dispatch/FL(%)	Design	38 86%	3,340	42 86%	7		0 18443	-0 2166	YES
B 3 4 11 1	M&R-4	Other Non-Design/Dispatch/FL(%)	R&B	22 36%	101,333	28 26%	46		0 06144	-0 9610	YES
B 3 4 11 2	M&R-4	Other Non-Design/Non-Dispatch/FL(%)	R&B	19 55%	57,756	21 95%	41		0 06195	-0 3882	YES
B 3 4 12 1	M&R-4	LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	22 41%	99,788						
B 3 4 12 2	M&R-4	LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	19 62%	56,786						
Out of Service > 24 hours											
B 3 5 1 1	M&R-5	Switch Ports/Dispatch/FL(%)	R&B (POTS)	26 91%	66,154						
B 3 5 1 2	M&R-5	Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	10 86%	18,520						

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 3 5 2 1	M&R-5	Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1 19%	1,093	0 00%	2	0 07673	0 1550	YES
B 3 5 2 2	M&R-5	Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0 00%	702	0 00%	8	0 00000		YES
B 3 5 3 1	M&R-5	Loop + Port Combinations/Dispatch/FL(%)	R&B	26 88%	67,212	13 84%	831	0 01547	8 4299	YES
B 3 5 3 2	M&R-5	Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	10 80%	18,938	2 37%	253	0 01965	4 2920	YES
B 3 5 4 1	M&R-5	Combo Other/Dispatch/FL(%)	R&B&D - Disp	25 77%	70,367	0 00%	11	0 13189	1 9542	YES
B 3 5 4 2	M&R-5	Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	25 77%	70,367	0 00%	6	0 17857	1 4433	YES
B 3 5 5 1	M&R-5	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	100 00%	2	7 46%	67	0 00000		YES
B 3 5 5 2	M&R-5	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail			0 00%	14			
B 3 5 6 1	M&R-5	UNE ISDN/Dispatch/FL(%)	ISDN - BRI	5 83%	360	11 86%	118	0 02486	-2 4259	NO
B 3 5 6 2	M&R-5	UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	3 09%	291	2 63%	76	0 02230	0 2068	YES
B 3 5 7 1	M&R-5	Line Sharing/Dispatch/FL(%)	ADSL to Retail	100 00%	2	100 00%	1	0 00000		YES
B 3 5 7 2	M&R-5	Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail							
B 3 5 8 1	M&R-5	2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	26 88%	67,212	5 84%	1,216	0 01283	16 4046	YES
B 3 5 8 2	M&R-5	2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	26 88%	67,212	3 01%	299	0 02570	9 2905	YES
B 3 5 9 1	M&R-5	2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	26 90%	66,124	16 87%	498	0 01994	5 0283	YES
B 3 5 9 2	M&R-5	2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	10 77%	18,415	10 53%	19	0 07115	0 0340	YES
B 3 5 10 1	M&R-5	Other Design/Dispatch/FL(%)	Design	2 70%	3,185	0 00%	15	0 04195	0 6437	YES
B 3 5 10 2	M&R-5	Other Design/Non-Dispatch/FL(%)	Design	0 81%	3,340	0 00%	7	0 03388	0 2386	YES
B 3 5 11 1	M&R-5	Other Non-Design/Dispatch/FL(%)	R&B	26 88%	67,212	44 00%	25	0 08869	-1 9300	NO
B 3 5 11 2	M&R-5	Other Non-Design/Non-Dispatch/FL(%)	R&B	10 80%	18,938	11 11%	18	0 07320	-0 0420	YES
B 3 5 12 1	M&R-5	LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	26 91%	66,154					
B 3 5 12 2	M&R-5	LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	10 86%	18,520					

Unbundled Network Elements - Billing

			BST - State							
B 4 1	Invoice Accuracy									
	B-1	FL(%)	98 79%	\$493,225,276	99 74%	\$4,856,831		0 00005	-190 9581	YES
B 4 2	Mean Time to Deliver Invoices - CRIS									
	B-2	Region/business days	5 04	1	3 46	1,191				YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
Local Interconnection Trunks - Ordering										
% Rejected Service Requests										
C 1 1	O-7 Local Interconnection Trunks/FL(%)	Diagnostic		59.90%	202				Diagnostic	
Reject Interval										
C 1 2	O-8 Local Interconnection Trunks/FL(%)	>= 85% w in 4 days		93.39%	113				YES	
FOC Timeliness										
C 1 3	O-9 Local Interconnection Trunks/FL(%)	>= 95% w in 10 days		95.30%	154				YES	
FOC & Reject Response Completeness										
C 1 4	O-11 Local Interconnection Trunks/FL(%)	>= 95%		95.35%	172				YES	
FOC & Reject Response Completeness (Multiple Responses)										
C 1 5	O-11 Local Interconnection Trunks/FL(%)	>= 95%								
Local Interconnection Trunks - Provisioning										
Order Completion Interval										
C 2 1	P-4 Local Interconnection Trunks/FL(days)	Parity w Retail	20.82	82	24.92	37	25.068	4.96451	-0.8262	YES
Held Orders										
C 2 2	P-1 Local Interconnection Trunks/FL(days)	Parity w Retail	Not Applicable for Trunks							
% Jeopardies										
C 2 3	P-2 Local Interconnection Trunks/FL(%)	Parity w Retail	Not Applicable for Trunks							
Average Jeopardy Notice Interval										
C 2 4	P-2 Local Interconnection Trunks/FL(hours)	95% >= 48 hrs	Not Applicable for Trunks							
% Missed Installation Appointments										
C 2 5	P-3 Local Interconnection Trunks/FL(%)	Parity w Retail	1.20%	83	2.13%	47		0.01992	-0.4634	YES
% Provisioning Troubles within 30 Days										
C 2 6	P-9 Local Interconnection Trunks/FL(%)	Parity w Retail	2.76%	10,432	0.00%	3,040		0.00338	8.1752	YES
Average Completion Notice Interval										
C 2 7	P-5 Local Interconnection Trunks/FL(hours)	Parity w Retail	150.59	57	114.13	30	364.436	82.20202	0.4436	YES
Total Service Order Cycle Time										
C 2 8	P-10 Local Interconnection Trunks/FL(days)	Diagnostic	Under development							
Total Service Order Cycle Time (offered)										
C 2 9	P-10 Local Interconnection Trunks/FL(days)	Diagnostic	Under development							
% Completions w/o Notice or < 24 hours										
C 2 10 1	P-6 Local Interconnection Trunks/Dispatch/FL(%)	Diagnostic			100.00%	37				Diagnostic
C 2 10 2	P-6 Local Interconnection Trunks/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
Service Order Accuracy										
C 2 11 1 1	P-11 Local Interconnection Trunks/<10 circuits/Dispatch/FL(%)	>= 95%			100.00%	6				YES
C 2 11 1 2	P-11 Local Interconnection Trunks/<10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	3				YES
C 2 11 2 1	P-11 Local Interconnection Trunks/>=10 circuits/Dispatch/FL(%)	>= 95%			94.12%	17				NO
C 2 11 2 2	P-11 Local Interconnection Trunks/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			100.00%	20				YES
Local Interconnection Trunks - Maintenance and Repair										
Missed Repair Appointments										
C 3 1 1	M&R-1 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	33.33%	3	0.00%	1		0.54433	0.6124	YES
C 3 1 2	M&R-1 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.37%	270	0.00%	79		0.00777	0.4767	YES
Customer Trouble Report Rate										
C 3 2 1	M&R-2 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	363,217	0.00%	133,484		0.00001	0.0835	YES
C 3 2 2	M&R-2 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.07%	363,217	0.06%	133,484		0.00009	1.7364	YES

**BellSouth Monthly State Summary
Florida, July 2001**

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Maintenance Average Duration									
C 3 3 1	M&R-3 Local Interconnection Trunks/Dispatch/FL(hours)	20 18	3	5 23	1	28 359	32 74638	0 4565	YES
C 3 3 2	M&R-3 Local Interconnection Trunks/Non-Dispatch/FL(hours)	0 97	270	0 26	79	1 933	0 24719	2 8926	YES
% Repeat Troubles within 30 Days									
C 3 4 1	M&R-4 Local Interconnection Trunks/Dispatch/FL(%)	33 33%	3	100 00%	1		0 54433	-1 2247	YES
C 3 4 2	M&R-4 Local Interconnection Trunks/Non-Dispatch/FL(%)	2 59%	270	1 27%	79		0 02033	0 6527	YES
Out of Service > 24 hours									
C 3 5 1	M&R-5 Local Interconnection Trunks/Dispatch/FL(%)	33 33%	3	0 00%	1		0 54433	0 6124	YES
C 3 5 2	M&R-5 Local Interconnection Trunks/Non-Dispatch/FL(%)	0 37%	270	0 00%	79		0 00777	0 4767	YES
Local Interconnection Trunks - Billing									
Invoice Accuracy									
C 4 1	B-1 FL(%)	98 79%	\$493,225,276	99 74%	\$7,500,946		0 00004	-237 2543	YES
Mean Time to Deliver Invoices - CABS									
C 4 2	B-2 Region(calendar days)	5 08	1	4 78	3,199				YES
LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING									
Trunk Group Performance - Aggregate									
C 5 1	TGP-1 FL	>0 5% dif 2 consec Hrs		0					YES

BellSouth Monthly State Summary
Florida, July 2001

Benchmark / Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

Operations Support Systems - Pre-Ordering

% Interface Availability - CLEC

D 1 1 1	OSS-2 EDV/Region(%)	>= 99.5%		100.00%				YES
D 1 1 2	OSS-2 HAL/Region(%)	>= 99.5%		99.78%				YES
D 1 1 3	OSS-2 LENS/Region(%)	>= 99.5%		99.96%				YES
D 1 1 4	OSS-2 LEO MAINFRAME/Region(%)	>= 99.5%		100.00%				YES
D 1 1 5	OSS-2 LEO UNIX/Region(%)	>= 99.5%						
D 1 1 6	OSS-2 LESOG/Region(%)	>= 99.5%		100.00%				YES
D 1 1 7	OSS-2 TAG/Region(%)	>= 99.5%		99.95%				YES
D 1 1 8	OSS-2 PSIMS/Region(%)	>= 99.5%		99.80%				YES

% Interface Availability - BST & CLEC

D 1 2 1	OSS-2 ATLAS/COFFI/Region(%)	>= 99.5%		99.92%				YES
D 1 2 2	OSS-2 BOCRIS/Region(%)	>= 99.5%		99.93%				YES
D 1 2 3	OSS-2 DSAP/Region(%)	>= 99.5%		99.88%				YES
D 1 2 4	OSS-2 RSAG/Region(%)	>= 99.5%		99.91%				YES
D 1 2 5	OSS-2 SOCS/Region(%)	>= 99.5%		99.91%				YES
D 1 2 6	OSS-2 SONGS/Region(%)	>= 99.5%		99.93%				YES
D 1 2 7	OSS-2 DOE/Region(%)	>= 99.5%		100.00%				YES
D 1 2 8	OSS-2 LNP Gateway/Region(%)	>= 99.5%		100.00%				YES
D 1 2 9	OSS-2 COG/Region(%)	>= 99.5%		100.00%				YES
D 1 2 10	OSS-2 DOM/Region(%)	>= 99.5%		100.00%				YES
D 1 2 11	OSS-2 SOG/Region(%)	>= 99.5%		100.00%				YES

Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)

D 1 3 1 1	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	2.88	3,237,125	1.80	246,086		YES
D 1 3 1 2	OSS-1 RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.28	7,266	1.80	246,086		YES
D 1 3 2 1	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	3.00	9,608,635	1.62	156,550		YES
D 1 3 2 2	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	5.57	610,414	1.62	156,550		YES
D 1 3 3 1	OSS-1 ATLAS/Region (seconds)	RNS - ATLAS + 2 Sec			1.07	59,062		
D 1 3 3 2	OSS-1 ATLAS/Region (seconds)	ROS - ATLAS + 2 Sec	2.65	293,697	1.07	59,062		YES
D 1 3 4 1	OSS-1 DSAP/Region (seconds)	RNS - DSAP + 2 Sec			0.53	1,593		
D 1 3 4 2	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.67	294,671	0.53	1,593		YES
D 1 3 5 1	OSS-1 HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.50	5,503,374	13.31	464,511		NO
D 1 3 5 2	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.19	461,218	13.31	464,511		NO
D 1 3 6 1	OSS-1 COFFI/USOC/Region (seconds)	RNS - OASISBIG + 2 Sec	4.25	11,605,575	0.78	34,324		YES
D 1 3 6 2	OSS-1 COFFI/USOC/Region (seconds)	ROS - OASISBIG + 2 Sec	4.41	605,022	0.78	34,324		YES
D 1 3 7 1	OSS-1 PSIMS/ORB/Region (seconds)	RNS - OASISBIG + 2 Sec	4.25	11,605,575	0.09	71,569		YES
D 1 3 7 2	OSS-1 PSIMS/ORB/Region (seconds)	ROS - OASISBIG + 2 Sec	4.41	605,022	0.09	71,569		YES

Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)

D 1 4 1 1	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	2.88	3,237,125	2.32	187,104		YES	
D 1 4 1 2	OSS-1 RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.28	7,266	2.32	187,104		YES	
D 1 4 2 1	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	3.00	9,608,635	2.50	72,690		YES	
D 1 4 2 2	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	5.57	610,414	2.50	72,690		YES	
D 1 4 3 1	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic						Diagnostic	
D 1 4 3 2	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic						Diagnostic	
D 1 4 4 1	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			1.61	4		Diagnostic	
D 1 4 4 2	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			1.61	4		Diagnostic	
D 1 4 5 1	OSS-1 ATLAS - TN/Region (seconds)	RNS - ATLAS - TN + 2 Sec			1.73	10,074			
D 1 4 5 2	OSS-1 ATLAS - TN/Region (seconds)	ROS - ATLAS - TN + 2 Sec	2.65	293,697	1.73	10,074		YES	
D 1 4 6 1	OSS-1 DSAP/Region (seconds)	RNS - DSAP + 2 Sec			2.28	66,065			
D 1 4 6 2	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.67	294,671	2.28	66,065		YES	
D 1 4 7 1	OSS-1 HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.50	5,503,374	3.60	133,650		NO	
D 1 4 7 2	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.19	461,218	3.60	133,650		NO	
D 1 4 8 1	OSS-1 RNS/CRSEINT/Region(seconds)	RNS - CRSACCTS + 2 sec	<i>This data not applicable after 5-1-2001, see D.1.4.7.1</i>						
D 1 4 8 2	OSS-1 ROS/CRSEINT/Region(seconds)	ROS - CRSOCSR + 2 sec	<i>This data not applicable after 5-1-2001, see D.1.4.7.2</i>						
D 1 4 9 1	OSS-1 RNS/CRSECSRL/Region(seconds)	RNS - CRSACCTS + 2 sec	<i>This data not applicable after 7-1-2001, see D.1.4.7.1</i>						
D 1 4 9 2	OSS-1 ROS/CRSECSRL/Region(seconds)	ROS - CRSOCSR + 2 sec	<i>This data not applicable after 7-1-2001, see D.1.4.7.2</i>						

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
Operations Support Systems - Maintenance and Repair											
% Interface Availability - BST											
D 2 1 1	OSS-3	TAFI/Region(%)	>= 99.5%	99.99%						YES	
% Interface Availability - CLEC											
D 2 2 1	OSS-3	CLEC TAFI/Region(%)	>= 99.5%		100.00%					YES	
D 2 2 2	OSS-3	CLEC ECTA/Region(%)	>= 99.5%		100.00%					YES	
% Interface Availability - BST & CLEC											
D 2 3 1	OSS-3	CRIS/Region(%)	>= 99.5%		99.93%					YES	
D 2 3 2	OSS-3	LMOS HOST/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 3	OSS-3	LNP/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 4	OSS-3	MARCH/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 5	OSS-3	OSPCM/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 6	OSS-3	Predictor/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 7	OSS-3	SOCS/Region(%)	>= 99.5%		99.91%					YES	
Average Response Interval											
D 2 4 1 1	OSS-4	CRIS/Region(%) <= 4 Seconds	Parity w Retail	95.82%	1,946,965	95.04%	92,271		0.00067	11,5962	NO
D 2 4 1 2	OSS-4	CRIS/Region(%) <= 10 Seconds	Parity w Retail	98.81%	1,946,965	99.19%	92,271		0.00037	-10,4334	YES
D 2 4 1 3	OSS-4	CRIS/Region(%) > 10 Seconds	Parity w Retail	1.19%	1,946,965	0.81%	92,271		0.00037	10,4334	YES
D 2 4 2 1	OSS-4	DLETH/Region(%) <= 4 Seconds	Parity w Retail	10.69%	47,554	11.48%	932		0.01022	-0,7748	YES
D 2 4 2 2	OSS-4	DLETH/Region(%) <= 10 Seconds	Parity w Retail	81.73%	47,554	89.38%	932		0.01278	-5,9848	YES
D 2 4 2 3	OSS-4	DLETH/Region(%) > 10 Seconds	Parity w Retail	18.27%	47,554	10.62%	932		0.01278	5,9848	YES
D 2 4 3 1	OSS-4	DLR/Region(%) <= 4 Seconds	Parity w Retail	6.56%	35,497	22.13%	29,083		0.00196	-79,5474	YES
D 2 4 3 2	OSS-4	DLR/Region(%) <= 10 Seconds	Parity w Retail	88.73%	35,497	98.14%	29,083		0.00250	-37,6471	YES
D 2 4 3 3	OSS-4	DLR/Region(%) > 10 Seconds	Parity w Retail	11.27%	35,497	1.86%	29,083		0.00250	37,6471	YES
D 2 4 4 1	OSS-4	LMOS/Region(%) <= 4 Seconds	Parity w Retail	99.88%	1,946,930	99.85%	92,311		0.00012	2,3442	NO
D 2 4 4 2	OSS-4	LMOS/Region(%) <= 10 Seconds	Parity w Retail	99.94%	1,946,930	99.93%	92,311		0.00008	2,1646	NO
D 2 4 4 3	OSS-4	LMOS/Region(%) > 10 Seconds	Parity w Retail	0.06%	1,946,930	0.07%	92,311		0.00008	-2,1646	NO
D 2 4 5 1	OSS-4	LMOSupd/Region(%) <= 4 Seconds	Parity w Retail	97.88%	1,463,874	97.31%	56,552		0.00062	9,1600	NO
D 2 4 5 2	OSS-4	LMOSupd/Region(%) <= 10 Seconds	Parity w Retail	99.83%	1,463,874	99.67%	56,552		0.00018	9,3826	NO
D 2 4 5 3	OSS-4	LMOSupd/Region(%) > 10 Seconds	Parity w Retail	0.17%	1,463,874	0.33%	56,552		0.00018	-9,3826	NO
D 2 4 6 1	OSS-4	LNP/Region(%) <= 4 Seconds	Parity w Retail	99.62%	134,687	99.43%	5,486		0.00085	2,2146	NO
D 2 4 6 2	OSS-4	LNP/Region(%) <= 10 Seconds	Parity w Retail	99.80%	134,687	99.84%	5,486		0.00061	-0,5469	YES
D 2 4 6 3	OSS-4	LNP/Region(%) > 10 Seconds	Parity w Retail	0.20%	134,687	0.16%	5,486		0.00061	0,5469	YES
D 2 4 7 1	OSS-4	MARCH/Region(%) <= 4 Seconds	Parity w Retail	29.87%	9,546	31.76%	403		0.02327	-0,8146	YES
D 2 4 7 2	OSS-4	MARCH/Region(%) <= 10 Seconds	Parity w Retail	29.87%	9,546	31.76%	403		0.02327	-0,8146	YES
D 2 4 7 3	OSS-4	MARCH/Region(%) > 10 Seconds	Parity w Retail	70.13%	9,546	68.24%	403		0.02327	0,8146	YES
D 2 4 8 1	OSS-4	OSPCM/Region(%) <= 4 Seconds	Parity w Retail	45.00%	9,365	34.75%	118		0.04809	2,2245	NO
D 2 4 8 2	OSS-4	OSPCM/Region(%) <= 10 Seconds	Parity w Retail	97.54%	9,365	96.61%	118		0.01434	0,6513	YES
D 2 4 8 3	OSS-4	OSPCM/Region(%) > 10 Seconds	Parity w Retail	2.46%	9,365	3.39%	118		0.01434	-0,6513	YES
D 2 4 9 1	OSS-4	Predictor/Region(%) <= 4 Seconds	Parity w Retail	16.89%	77,635	27.50%	3,898		0.00615	-17,2457	YES
D 2 4 9 2	OSS-4	Predictor/Region(%) <= 10 Seconds	Parity w Retail	16.89%	77,635	27.50%	3,898		0.00615	-17,2457	YES
D 2 4 9 3	OSS-4	Predictor/Region(%) > 10 Seconds	Parity w Retail	83.11%	77,635	72.50%	3,898		0.00615	17,2457	YES
D 2 4 10 1	OSS-4	SOCS/Region(%) <= 4 Seconds	Parity w Retail	99.83%	250,355	99.81%	14,222		0.00036	0,5068	YES
D 2 4 10 2	OSS-4	SOCS/Region(%) <= 10 Seconds	Parity w Retail	99.98%	250,355	99.96%	14,222		0.00013	0,8387	YES
D 2 4 10 3	OSS-4	SOCS/Region(%) > 10 Seconds	Parity w Retail	0.02%	250,355	0.04%	14,222		0.00013	-0,8387	YES
D 2 4 11 1	OSS-4	NIW/Region(%) <= 4 Seconds	Parity w Retail	82.85%	77,529	82.24%	3,818		0.00625	0,9734	YES
D 2 4 11 2	OSS-4	NIW/Region(%) <= 10 Seconds	Parity w Retail	99.58%	77,529	99.63%	3,818		0.00108	-0,5352	YES
D 2 4 11 3	OSS-4	NIW/Region(%) > 10 Seconds	Parity w Retail	0.42%	77,529	0.37%	3,818		0.00108	0,5352	YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
COLLOCATION - Collocation									
E 1 1 1	C-1 Virtual/FL (calendar days)								
E 1 1 2	C-1 Physical-Caged/FL (calendar days)	<= 15 days		8	11				YES
E 1 1 3	C-1 Physical-Cageless/FL (calendar days)	<= 15 days		8	16				YES
Average Arrangement Time									
E 1 2 1	C-2 Virtual/FL (calendar days)	<= 60 days							
E 1 2 2	C-2 Virtual-Augments/FL (calendar days)	<= 60 days		39	2				YES
E 1 2 3	C-2 Virtual-Augments - Additional Space Required/FL (calendar days)	<= 60 days							
E 1 2 4	C-2 Physical Caged-Ordinary/FL (calendar days)	<= 90 days							
E 1 2 5	C-2 Physical Caged-Augments/FL (calendar days)	<= 45 days		45	4				YES
E 1 2 6	C-2 Physical Caged-Augments - Additional Space Required/FL (calendar days)	<= 90 days							
E 1 2 7	C-2 Physical Cageless/FL (calendar days)	<= 90 days		62	12				YES
E 1 2 8	C-2 Physical Cageless-Augments/FL (calendar days)	<= 45 days		27	5				YES
E 1 2 9	C-2 Physical Cageless-Augments - Additional Space Required/FL (calendar days)	<= 90 days							
% Due Dates Missed									
E 1 3 1	C-3 Virtual/FL (%)	< 5% missed		0.00%	2				YFS
E 1 3 2	C-3 Physical/FL (%)	< 5% missed		0.00%	21				YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
General - Flow Through									
% Flow Through Set Point									
F 111	O-3 Summary/Region(%)	Diagnostic		87.38%	252,927				Diagnostic
F 112	O-3 Aggregate/Region(%)	Diagnostic		87.38%	252,927				Diagnostic
F 113	O-3 Residence/Region(%)	>= 95%		87.09%	183,387				NO
F 114	O-3 Business/Region(%)	>= 90%		69.92%	6,480				NO
F 115	O-3 UNE/Region(%)	>= 85%		90.00%	63,060				YES
% Target Rate Achieved									
F 21	O-3 Summary/Region(%)	Diagnostic		71.33%	309,818				Diagnostic
F 22	O-3 Aggregate/Region(%)	Diagnostic		71.33%	309,818				Diagnostic
F 23	O-3 Residence/Region(%)	Diagnostic		75.18%	212,428				Diagnostic
F 24	O-3 Business/Region(%)	Diagnostic		49.41%	9,170				Diagnostic
F 25	O-3 UNE/Region(%)	Diagnostic		64.34%	68,220				Diagnostic
Set Point									
F 31	O-3 Summary/Region(%)	>= 85%		86.36%	4,860				YES
F 32	O-3 Aggregate/Region(%)	>= 85%		86.36%	4,860				YES
F 133	O-3 Residence/Region(%)	Diagnostic							Diagnostic
F 134	O-3 Business/Region(%)	Diagnostic							Diagnostic
General - Pre-Ordering									
Loop Makeup Inquiry (Manual)									
F 211	PO-1 Loops/FL(%)	>= 95% w in 3 bus days		100.00%	172				YES
Loop Makeup Inquiry (Electronic)									
F 221	PO-2 Loops/FL(%)	>= 95% w in 5 min		100.00%	2,094				YES
General - Ordering									
Service Inquiry with Firm Order									
F 311	O-10 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% w in 5 bus days		91.00%	298				NO
F 312	O-10 Local Interoffice Transport/FL(%)	>= 95% w in 5 bus days		100.00%	2				YES
General - Ordering									
Average Speed of Answer									
F 41	O-12 Region(seconds)	Party w Retail	199.33	6,834,494	59.15	44,292			YES
General - Maintenance Center									
Average Answer Time									
F 51	M&H-6 Region(seconds)	Party w Retail	144.73	1,777,813	27.37	96,407			YES
General - Operator Services (Toll)									
Average Speed to Answer									
F 61	OS-1 FL(seconds)	PBD			4.69				PBD
% Answered in 10 seconds									
F 62	OS-2 FL(%)	PBD			97.00%				PBD
General - Directory Assistance									
Average Speed to Answer									
F 71	DA-1 FL(seconds)	PBD			4.33				PBD
% Answered in 10 seconds									

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
F 7 2	DA-2 FL(%)	PBD			97.10%					PBD
General - E911										
Mean Interval										
F 8 1	E-3 FL(hours)	PBD		1.26	1.210					PBD
% Accuracy										
F 8 2	E-2 FL(%)	PBD		96.86%	618,739					PBD
% Timeliness										
F 8 3	E-1 FL(%)	PBD		100.00%	1,210					PBD
General - Billing										
Usage Data Delivery Accuracy										
F 9 1	B-3 Region(%)	Partly w Retail	100.00%	5,950	100.00%	15,556		0.00000		YES
Usage Data Delivery Timeliness										
F 9 2	B-5 Region(%)	Partly w Retail	98.95%	40,304	96.62%	220,452,428		0.00051	45.8681	NO
Usage Data Delivery Completeness										
F 9 3	B-4 Region(%)	Partly w Retail	99.91%	40,304	99.93%	220,452,428		0.00015	-1.7954	YES
Mean Time to Deliver Usage										
F 9 4	B-6 Region(days)	Partly w Retail	3.37	40,304	3.89	220,452,428				NO
F 9 5 1	B-7 Resale/FL(%)	Partly w Retail	83.29%	\$20,734,379	97.11%	\$764,425		0.00106	-130.0705	YES
F 9 5 2	B-7 UNE/FL(%)	>= 90%			56.41%	\$864,775				NO
F 9 5 3	B-7 Interconnection/FL(%)	>= 90%			82.27%	\$12,673				NO
F 9 6 1	B-8 Resale/FL(%)	Partly w Retail	92.07%	\$24,421,913	98.34%	\$737,551		0.00113	-55.3521	YES
F 9 6 2	B-8 UNE/FL(%)	>= 90%			98.07%	\$1,586,157				YES
F 9 6 3	B-8 Interconnection/FL(%)	>= 90%			86.94%	\$710,888				NO
General - Change Management										
% Software Release Notices Sent On Time										
F 10 1	CM-1 FL(%)	>= 98% w in 30 days								
Average Software Release Notice Delay Days										
F 10 2	CM-2 FL(average)	>= 25 bus days prior to release								
% Change Management Documentation Sent On Time										
F 10 3	CM-3 FL(%)	>= 98% w in 30 days			50.00%	4				NO
Average Documentation Release Delay Days										
F 10 5	CM-4 FL(average)	>= 25 bus days prior to release			6	2				NO
% CLEC Interface Outages Sent within 15 Minutes										
F 10 6	CM-5 FL(%)	>= 97% w in 15 min			100.00%	32				YES
General - New Business Requests										
% New Business Requests Processed within 30 Business Days										
F 11 1	BFR-1 Region(%)	>= 90% w in 30 bus days			100.00%	5				YES
% Quotes Provided within X Business Days										
F 11 2 1	BFR-2A Region(%)	>= 90% w in 10 bus days			66.67%	3				NO
F 11 2 2	BFR-2B Region(%)	>= 90% w in 30 bus days								
F 11 2 3	BFR-2C Region(%)	>= 90% w in 60 bus days			100.00%	2				YES
General - Ordering										

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Acknowledgement Message Timeliness										
F 12 1 1	D-1	EDI/Region(%)			96 50%	78,663				YES
F 12 1 2	O-1	TAG/Region(%)			99.75%	194,073				YES
Acknowledgement Message Completeness										
F 12 2 1	O-2	EDI/Region(%)	100%		99 95%	78,663				NO
F 12 2 2	O-2	TAG/Region(%)	100%		99 75%	194,073				NO
General - Database Updates										
Average Database Update Interval										
F 13 1 1	D-1	LIDB/FL(hours)	PBD	1 43	22	1 43	22			PBD
F 13 1 2	D-1	Directory Listings/FL(hours)	PBD	0 11	25	0 11	25			PBD
F 13 1 3	D-1	Directory Assistance/FL(hours)	PBD	3 57	26	3 56	26			PBD
% Update Accuracy										
F 13 2 1	D-2	LIDB/FL(%)	>= 95%			84 75%	59			NO
F 13 2 2	D-2	Directory Listings/FL(%)	>= 95%			100 00%	85			YES
F 13 2 3	D-2	Directory Assistance/FL(%)	>= 95%			100 00%	85			YES
% NXXs / LRNs Loaded by LERG Effective Date										
F 13 3	D-3	Region(%)	100%			99 35%	153			NO
General - Network Outage Notification										
Mean Time to Notify CLEC of Major Network Outages										
F 14 1	M&R-7	Region (minutes)	Party w Retail	4824 00	4	4582 00	4			YES

BellSouth Monthly State Summary
Florida, July 2001

Benchmark /
Analog

BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

Unbundled Network Elements - Ordering

% Rejected Service Requests - Mechanized

Item	Code	Description	Benchmark	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 1 1	O-7	Switch Ports/FL(%)	Diagnostic								Diagnostic
B 1 1 2	O-7	Local Interoffice Transport/FL(%)	Diagnostic								Diagnostic
B 1 1 3	O-7	Loop + Port Combinations/FL(%)	Diagnostic			12.58%	8,408				Diagnostic
B 1 1 4	O-7	Combo Other/FL(%)	Diagnostic								Diagnostic
B 1 1 5	O-7	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			15.18%	224				Diagnostic
B 1 1 6	O-7	ISDN Loop (UDN, UDC)/FL(%)	Diagnostic			0.00%	5				Diagnostic
B 1 1 7	O-7	Line Sharing/FL(%)	Diagnostic			33.33%	3				Diagnostic
B 1 1 8	O-7	2W Analog Loop Design/FL(%)	Diagnostic			24.59%	305				Diagnostic
B 1 1 9	O-7	2W Analog Loop Non-Design/FL(%)	Diagnostic			15.50%	258				Diagnostic
B 1 1 10	O-7	2W Analog Loop w/INP Design/FL(%)	Diagnostic								Diagnostic
B 1 1 11	O-7	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 1 12	O-13	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			50.48%	105				Diagnostic
B 1 1 13	O-13	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			72.13%	61				Diagnostic
B 1 1 14	O-7	Other Design/FL(%)	Diagnostic			28.68%	136				Diagnostic
B 1 1 15	O-7	Other Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 1 16	O-7	INP Standalone/FL(%)	Diagnostic								Diagnostic
B 1 1 17	O-13	LNP (Standalone)/FL(%)	Diagnostic			3.94%	1,778				Diagnostic

% Rejected Service Requests - Partially Mechanized

Item	Code	Description	Benchmark	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 2 1	O-7	Switch Ports/FL(%)	Diagnostic								Diagnostic
B 1 2 2	O-7	Local Interoffice Transport/FL(%)	Diagnostic								Diagnostic
B 1 2 3	O-7	Loop + Port Combinations/FL(%)	Diagnostic			31.61%	5,312				Diagnostic
B 1 2 4	O-7	Combo Other/FL(%)	Diagnostic								Diagnostic
B 1 2 5	O-7	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			3.03%	66				Diagnostic
B 1 2 6	O-7	ISDN Loop (UDN, UDC)/FL(%)	Diagnostic			75.00%	4				Diagnostic
B 1 2 7	O-7	Line Sharing/FL(%)	Diagnostic			100.00%	1				Diagnostic
B 1 2 8	O-7	2W Analog Loop Design/FL(%)	Diagnostic			25.38%	465				Diagnostic
B 1 2 9	O-7	2W Analog Loop Non-Design/FL(%)	Diagnostic			23.44%	640				Diagnostic
B 1 2 10	O-7	2W Analog Loop w/INP Design/FL(%)	Diagnostic								Diagnostic
B 1 2 11	O-7	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 2 12	O-13	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			40.71%	1,039				Diagnostic
B 1 2 13	O-13	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			24.05%	1,031				Diagnostic
B 1 2 14	O-7	Other Design/FL(%)	Diagnostic			32.35%	68				Diagnostic
B 1 2 15	O-7	Other Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 2 16	O-7	INP Standalone/FL(%)	Diagnostic								Diagnostic
B 1 2 17	O-13	LNP (Standalone)/FL(%)	Diagnostic			35.28%	1,216				Diagnostic

% Rejected Service Requests - Non-Mechanized

Item	Code	Description	Benchmark	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 3 1	O-7	Switch Ports/FL(%)	Diagnostic			33.33%	3				Diagnostic
B 1 3 2	O-7	Local Interoffice Transport/FL(%)	Diagnostic			33.33%	15				Diagnostic
B 1 3 3	O-7	Loop + Port Combinations/FL(%)	Diagnostic			48.39%	2,174				Diagnostic
B 1 3 4	O-7	Combo Other/FL(%)	Diagnostic								Diagnostic
B 1 3 5	O-7	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			20.00%	650				Diagnostic
B 1 3 6	O-7	ISDN Loop (UDN, UDC)/FL(%)	Diagnostic			2.22%	45				Diagnostic
B 1 3 7	O-7	Line Sharing/FL(%)	Diagnostic			20.87%	321				Diagnostic
B 1 3 8	O-7	2W Analog Loop Design/FL(%)	Diagnostic			5.68%	88				Diagnostic
B 1 3 9	O-7	2W Analog Loop Non-Design/FL(%)	Diagnostic			37.64%	2,019				Diagnostic
B 1 3 10	O-7	2W Analog Loop w/INP Design/FL(%)	Diagnostic			7.69%	13				Diagnostic
B 1 3 11	O-7	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 3 12	O-13	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			48.51%	303				Diagnostic
B 1 3 13	O-13	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			20.22%	727				Diagnostic
B 1 3 14	O-7	Other Design/FL(%)	Diagnostic			11.20%	125				Diagnostic
B 1 3 15	O-7	Other Non-Design/FL(%)	Diagnostic								Diagnostic
B 1 3 16	O-7	INP Standalone/FL(%)	Diagnostic			33.87%	1,491				Diagnostic
B 1 3 17	O-13	LNP Standalone/FL(%)	Diagnostic			33.75%	1,582				Diagnostic

Reject Interval - Mechanized

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 4 1	O-8	Switch Ports/FL(%)	>= 97% w in 1 hr							
B.1 4 2	O-8	Local Interoffice Transport/FL(%)	>= 97% w in 1 hr							
B 1 4 3	O-8	Loop + Port Combinations/FL(%)	>= 97% w in 1 hr		95 94%	1,058				NO
B 1 4 4	O-8	Combo Other/FL(%)	>= 97% w in 1 hr							
B 1 4 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 97% w in 1 hr		97 06%	34				YES
B 1 4 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 97% w in 1 hr							
B 1 4 7	O-8	Line Sharing/FL(%)	>= 97% w in 1 hr		100 00%	1				YES
B.1 4 8	O-8	2W Analog Loop Design/FL(%)	>= 97% w in 1 hr		97 33%	75				YES
B 1 4 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 97% w in 1 hr		97 50%	40				YES
B 1 4 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 97% w in 1 hr							
B 1 4 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 97% w in 1 hr							
B 1 4 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 97% w in 1 hr		96 23%	53				NO
B 1 4 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 97% w in 1 hr		95 45%	44				NO
B 1 4 14	O-8	Other Design/FL(%)	>= 97% w in 1 hr		94 87%	39				NO
B 1 4 15	O-8	Other Non-Design/FL(%)	>= 97% w in 1 hr							
B 1 4 16	O-8	INP Standalone/FL(%)	>= 97% w in 1 hr							
B 1 4 17	O-14	LNP (Standalone)/FL(%)	>= 97% w in 1 hr		100 00%	70				YES
Reject Interval - Partially Mechanized - 24 hours										
B 1 5 1	O-8	Switch Ports/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 4	O-8	Combo Other/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 7	O-8	Line Sharing/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 8	O-8	2W Analog Loop Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 14	O-8	Other Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 15	O-8	Other Non-Design/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 16	O-8	INP Standalone/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
B 1 5 17	O-14	LNP (Standalone)/FL(%)	>= 85% w in 24 hrs		<i>This data not applicable after 5-1-2001, see below</i>					
Reject Interval - Partially Mechanized - 18 hours										
B 1 6 1	O-8	Switch Ports/FL(%)	>= 85% w in 18 hrs							
B 1 6 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 18 hrs							
B 1 6 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 18 hrs		98 33%	1,679				YES
B 1 6 4	O-8	Combo Other/FL(%)	>= 85% w in 18 hrs							
B 1 6 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 18 hrs		100 00%	2				YES
B 1 6 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 18 hrs		100 00%	3				YES
B.1 6 7	O-8	Line Sharing/FL(%)	>= 85% w in 18 hrs		100 00%	1				YES
B 1 6 8	O-8	2W Analog Loop Design/FL(%)	>= 85% w in 18 hrs		93 22%	118				YES
B 1 6 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 18 hrs		82 67%	150				NO
B 1 6 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 18 hrs		96 22%	423				YES
B 1 6 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 18 hrs		95 56%	248				YES
B 1 6 14	O-8	Other Design/FL(%)	>= 85% w in 18 hrs		81.82%	22				NO
B 1 6 15	O-8	Other Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 6 16	O-8	INP Standalone/FL(%)	>= 85% w in 18 hrs							
B 1 6 17	O-14	LNP Standalone/FL(%)	>= 85% w in 18 hrs		96 27%	429				YES
Rej Interval Non-M c										
B 1 8 1	O-8	Switch Ports/FL(%)	>= 85% w in 24 hrs		100 00%	1				YES
B 1 8 2	O-8	Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs		100 00%	5				YES
B 1 8 3	O-8	Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs		99 81%	1,052				YES
B 1 8 4	O-8	Combo Other/FL(%)	>= 85% w in 24 hrs							
B 1 8 5	O-8	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs		97 69%	130				YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 8 6	O-8	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs		100 00%	1				YES
B 1 8 7	O-8	Line Sharing/FL(%)	>= 85% w in 24 hrs		95 52%	67				YES
B 1 8 8	O-8	2W Analog Loop Design/FL(%)	>= 85% w in 24 hrs		60 00%	5				NO
B 1 8 9	O-8	2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs		96 58%	760				YES
B 1 8 10	O-8	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 24 hrs		0 00%	1				NO
B 1 8 11	O-8	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 24 hrs							
B 1 8 12	O-14	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs		98 64%	147				YES
B 1 8 13	O-14	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs		95 24%	147				YES
B 1 8 14	O-8	Other Design/FL(%)	>= 85% w in 24 hrs		64.29%	14				NO
B 1 8 15	O-8	Other Non-Design/FL(%)	>= 85% w in 24 hrs							
B 1 8 16	O-8	INP Standalone/FL(%)	>= 85% w in 24 hrs		98.42%	505				YES
B 1 8 17	O-14	LNP (Standalone)/FL(%)	>= 85% w in 24 hrs		97.57%	534				YES
FOC Timeliness - Mechanized										
B 1 9 1	O-9	Switch Ports/FL(%)	>= 95% w in 3 hrs							
B 1 9 2	O-9	Local Interface Transport/FL(%)	>= 95% w in 3 hrs							
B 1 9 3	O-9	Loop + Port Combinations/FL(%)	>= 95% w in 3 hrs		98.49%	5,491				YES
B 1 9 4	O-9	Combo Other/FL(%)	>= 95% w in 3 hrs							
B 1 9 5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% w in 3 hrs		81 03%	485				NO
B 1 9 6	O-9	ISDN Loop (UDN, UDC)/FL(%)	>= 95% w in 3 hrs		100 00%	5				YES
B 1 9 7	O-9	Line Sharing/FL(%)	>= 95% w in 3 hrs							
B 1 9 8	O-9	2W Analog Loop Design/FL(%)	>= 95% w in 3 hrs		98 26%	172				YES
B 1 9 9	O-9	2W Analog Loop Non-Design/FL(%)	>= 95% w in 3 hrs		95 09%	163				YES
B 1 9 10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 95% w in 3 hrs		100 00%	45				YES
B 1 9 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95% w in 3 hrs		100 00%	24				YES
B 1 9 14	O-9	Other Design/FL(%)	>= 95% w in 3 hrs		100 00%	64				YES
B 1 9 15	O-9	Other Non-Design/FL(%)	>= 95% w in 3 hrs							
B 1 9 16	O-9	INP Standalone/FL(%)	>= 95% w in 3 hrs							
B 1 9 17	O-15	LNP Standalone/FL(%)	>= 95% w in 3 hrs		99 81%	1,564				YES
FOC Timeliness - Partially Mechanized										
B 1 10 1	O-9	Switch Ports/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 2	O-9	Local Interface Transport/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 3	O-9	Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 4	O-9	Combo Other/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 6	O-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 7	O-9	Line Sharing/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 8	O-9	2W Analog Loop Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 9	O-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 14	O-9	Other Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 15	O-9	Other Non-Design/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 16	O-9	INP Standalone/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
B 1 10 17	O-15	LNP Standalone/FL(%)	>= 85% w in 36 hrs							<i>This data not applicable after 6-1-2001, see below</i>
FOC Timeliness - Partially Mechanized - 18 hours										
B 1 11 1	O-9	Switch Ports/FL(%)	>= 85% w in 18 hrs							
B 1 11 2	O-9	Local Interface Transport/FL(%)	>= 85% w in 18 hrs							
B 1 11 3	O-9	Loop + Port Combinations/FL(%)	>= 85% w in 18 hrs		97 31%	3,975				YES
B 1 11 4	O-9	Combo Other/FL(%)	>= 85% w in 18 hrs							
B 1 11 5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 18 hrs		90 20%	51				YES
B 1 11 6	O-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 18 hrs		100 00%	1				YES
B 1 11 7	O-9	Line Sharing/FL(%)	>= 85% w in 18 hrs							
B 1 11 8	O-9	2W Analog Loop Design/FL(%)	>= 85% w in 18 hrs		97 84%	370				YES
B 1 11 9	O-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 18 hrs		99 31%	576				YES
B 1 11 10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 18 hrs							

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 11 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 18 hrs							YES
B 1 11 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 18 hrs		97 47%	712				YES
B 1 11 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 18 hrs		98 81%	1,088				YES
B 1 11 14	O-9	Other Design/FL(%)	>= 85% w in 18 hrs		100 00%	54				YES
B 1 11 15	O-9	Other Non-Design/FL(%)	>= 85% w in 18 hrs							
B 1 11 16	O-9	INP Standalone/FL(%)	>= 85% w in 18 hrs							
B 1 11 17	O-15	LNP Standalone/FL(%)	>= 85% w in 18 hrs		99 24%	1,054				YES
FOC Timeliness - Non-Mechanized										
B 1 13 1	O-9	Switch Ports/FL(%)	>= 85% w in 36 hrs		100 00%	1				YES
B 1 13 2	O-9	Local Interoffice Transport/FL(%)	>= 85% w in 36 hrs		87 50%	8				YES
B 1 13 3	O-9	Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs		99 82%	1,132				YES
B 1 13 4	O-9	Combo Other/FL(%)	>= 85% w in 36 hrs							
B 1 13 5	O-9	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 36 hrs		99 48%	575				YES
B 1 13 6	O-9	ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs		100 00%	41				YES
B 1 13 7	O-9	Line Sharing/FL(%)	>= 85% w in 36 hrs		100 00%	240				YES
B 1 13 8	O-9	2W Analog Loop Design/FL(%)	>= 85% w in 36 hrs		100 00%	74				YES
B 1 13 9	O-9	2W Analog Loop Non-Design/FL(%)	>= 85% w in 36 hrs		99 53%	1,477				YES
B 1 13 10	O-9	2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs		100 00%	22				YES
B 1 13 11	O-9	2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 36 hrs							
B 1 13 12	O-15	2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs		91 16%	147				YES
B 1 13 13	O-15	2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 36 hrs		97 01%	569				YES
B 1 13 14	O-9	Other Design/FL(%)	>= 85% w in 36 hrs		99 06%	106				YES
B 1 13 15	O-9	Other Non-Design/FL(%)	>= 85% w in 36 hrs							
B 1 13 16	O-9	INP Standalone/FL(%)	>= 85% w in 36 hrs							
B 1 13 17	O-15	LNP Standalone/FL(%)	>= 85% w in 36 hrs		95 79%	903				YES
FOC & Reject Response Completeness - Mechanized										
B 1 14 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 14 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 14 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		78.00%	8,408				NO
B 1 14 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 14 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		86 97%	284				NO
B 1 14 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	5				YES
B 1 14 7	O-11	Line Sharing/FL(%)	>= 95%		33 33%	3				NO
B 1 14 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		80 98%	305				NO
B 1 14 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		79 46%	258				NO
B 1 14 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 14 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 14 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	476				YES
B 1 14 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		100 00%	256				YES
B 1 14 14	O-11	Other Design/FL(%)	>= 95%		75 74%	136				NO
B 1 14 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 14 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 14 17	O-11	LNP Standalone/FL(%)	>= 95%		100 00%	437				YES
FOC & Reject Response Completeness - Partially Mechanized										
B 1 15 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 15 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 15 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		100 00%	5,312				YES
B 1 15 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 15 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		43 40%	53				NO
B 1 15 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	4				YES
B 1 15 7	O-11	Line Sharing/FL(%)	>= 95%		100 00%	1				YES
B 1 15 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		100 00%	465				YES
B 1 15 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		100 00%	640				YES
B 1 15 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 15 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 15 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	670				YES
B 1 15 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		100 00%	830				YES
B 1 15 14	O-11	Other Design/FL(%)	>= 95%		100 00%	68				YES
B 1 15 15	O-11	Other Non-Design/FL(%)	>= 95%							

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 15 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 15 17	O-11	LNP Standalone/FL(%)	>= 95%		100 00%	2,560				YES
FOC & Reject Response Completeness - Non-Mechanized										
B 1 16 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 16 2	O-11	Local Interoffice Transport/FL(%)	>= 95%		100 00%	12				YES
B 1 16 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		98 55%	2,269				YES
B 1 16 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 16 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		62 16%	37				NO
B 1 16 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		97 44%	39				YES
B 1 16 7	O-11	Line Sharing/FL(%)	>= 95%		96 34%	355				YES
B 1 16 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		98 70%	77				YES
B 1 16 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		94 35%	2,726				NO
B 1 16 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%		75 00%	4				NO
B 1 16 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 16 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		98 95%	190				YES
B 1 16 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		99 39%	658				YES
B 1 16 14	O-11	Other Design/FL(%)	>= 95%		100 00%	125				YES
B 1 16 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 16 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 16 17	O-11	LNP Standalone/FL(%)	>= 95%		99 92%	1,194				YES
FOC & Reject Response Completeness (Multiple Responses) - Mechanized										
B 1 17 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 17 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 17 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		99 98%	6,558				YES
B 1 17 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 17 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		95 14%	247				YES
B 1 17 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	5				YES
B 1 17 7	O-11	Line Sharing/FL(%)	>= 95%		100 00%	1				YES
B 1 17 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		100 00%	247				YES
B 1 17 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		99 02%	205				YES
B 1 17 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 17 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 17 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	476				YES
B 1 17 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		98 83%	256				YES
B 1 17 14	O-11	Other Design/FL(%)	>= 95%		100 00%	103				YES
B 1 17 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 17 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 17 17	O-11	LNP Standalone/FL(%)	>= 95%		100 00%	437				YES
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized										
B 1 18 1	O-11	Switch Ports/FL(%)	>= 95%							
B 1 18 2	O-11	Local Interoffice Transport/FL(%)	>= 95%							
B 1 18 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		94 31%	5,312				NO
B 1 18 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 18 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		86 96%	23				NO
B 1 18 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	4				YES
B 1 18 7	O-11	Line Sharing/FL(%)	>= 95%		100 00%	1				YES
B 1 18 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		93 98%	465				NO
B 1 18 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		92 50%	640				NO
B 1 18 10	O-11	2W Analog Loop w/INP Design/FL(%)	>= 95%							
B 1 18 11	O-11	2W Analog Loop w/INP Non-Design/FL(%)	>= 95%							
B 1 18 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	670				YES
B 1 18 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		100 00%	830				YES
B 1 18 14	O-11	Other Design/FL(%)	>= 95%		89 71%	68				NO
B 1 18 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 18 16	O-11	INP Standalone/FL(%)	>= 95%							
B 1 18 17	O-11	LNP Standalone/FL(%)	>= 95%		99 88%	2,560				YES
FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized										
B 1 19 1	O-11	Switch Ports/FL(%)	>= 95%							

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 1 19 2	O-11	Local Interoffice Transport/FL(%)	>= 95%		83 33%	12				NO
B 1 19 3	O-11	Loop + Port Combinations/FL(%)	>= 95%		94 99%	2,236				NO
B 1 19 4	O-11	Combo Other/FL(%)	>= 95%							
B 1 19 5	O-11	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%		100.00%	23				YES
B 1 19 6	O-11	ISDN Loop (UDN, UDC)/FL(%)	>= 95%		100 00%	38				YES
B 1 19 7	O-11	Line Sharing/FL(%)	>= 95%		95.32%	342				YES
B 1 19 8	O-11	2W Analog Loop Design/FL(%)	>= 95%		86 84%	76				NO
B 1 19 9	O-11	2W Analog Loop Non-Design/FL(%)	>= 95%		92 81%	2,572				NO
B 1 19 10	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	3				YES
B 1 19 11	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%							
B 1 19 12	O-11	2W Analog Loop w/LNP Design/FL(%)	>= 95%		100 00%	188				YES
B 1 19 13	O-11	2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%		99 85%	654				YES
B 1 19 14	O-11	Other Design/FL(%)	>= 95%		88 80%	125				NO
B 1 19 15	O-11	Other Non-Design/FL(%)	>= 95%							
B 1 19 16	O-11	LNP Standalone/FL(%)	>= 95%							
B 1 19 17	O-11	LNP Standalone/FL(%)	>= 95%		100 00%	1,193				YES

Unbundled Network Elements - Provisioning

Order Completion Interval											
B 2 1 1 1	P-4	Switch Ports/<10 circuits/Dispatch/FL(days)	R&B (POTS)	4 06	70,064			5 995			
B 2 1 1 2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0.89	616,137			1 587			
B 2 1 2 1	P-4	Switch Ports/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9 56	396			15.836			
B 2 1 2 2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	18.75	15			29.243			
B 2 1 2 1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	DS1/DS3			16 90	10				
B 2 1 2 2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	DS1/DS3								
B 2 1 2 2	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	DS1/DS3								
B 2 1 2 2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	DS1/DS3								
B 2 1 3 1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	R&B	4 06	70,634	2 69	220	6 044	0 40809	3 4282	YES
B 2 1 3 2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	R&B	0 89	618,135	0 79	7 609	1 589	0 01833	5 3248	YES
B 2 1 3 3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(days)	R&B	0 33	315,649	0 34	4 686	0 449	0 00661	-0 3548	YES
B 2 1 3 4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL(days)	R&B	1 73	245,506	1 52	2,923	2 188	0 04072	5 1084	YES
B 2 1 3 2	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	R&B	10 18	436	2 14	7	16 544	6 30285	1 2758	YES
B 2 1 3 2 2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	R&B	4 12	205	2 33	3	9 422	5 47945	0 3260	YES
B 2 1 3 2 3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(days)	R&B	2 06	54			12 747			
B 2 1 3 2 4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(days)	R&B	4 85	151	2 33	3	7 827	4 56347	0 5524	YES
B 2 4 1 1	P-4	Combo Other/<10 circuits/Dispatch/FL(days)	R&B&D - Disp	5 29	75,461			9 893			
B 2 4 1 4	P-4	Combo Other/<10 circuits/Dispatch In/FL(days)	R&B&D - Disp								
B 2 4 2 1	P-4	Combo Other/>=10 circuits/Dispatch/FL(days)	R&B&D - Disp	10 30	444			16 508			
B 2 4 2 4	P-4	Combo Other/>=10 circuits/Dispatch In/FL(days)	R&B&D - Disp								
B 2 5 3 1	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/FL(days)	ADSL to Retail	5 70	1,928	4 55	207	11 826	0 86493	1 3296	YES
B 2 5 3 2	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	2 99	570			1 893			
B 2 5 4 1	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/FL(days)	ADSL to Retail								
B 2 5 4 2	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B 2 1 5 5 1	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/FL(days)	ADSL to Retail								
B 2 1 5 5 2	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B 2 1 6 3 1	P-4	UNE ISDN/<6 circuits/Dispatch/FL(days)	ISDN - BRI	18 07	439	10 58	321	17 316	1 27167	5 8899	YES
B 2 1 6 3 2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL(days)	ISDN - BRI	2 16	525			4 254			
B 2 1 6 4 1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL(days)	ISDN - BRI								
B 2 1 6 4 2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL(days)	ISDN - BRI	2 00	1			0 000			
B 2 1 6 5 1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL(days)	ISDN - BRI								
B 2 1 6 5 2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL(days)	ISDN - BRI								
B 2 1 7 3 1	P-4	Line Sharing/<6 circuits/Dispatch/FL(days)	ADSL to Retail	5 70	1,928	5 80	5	11 826	5 29542	-0 0189	YES
B 2 1 7 3 2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL(days)	ADSL to Retail	2 99	570	3 67	70	1 893	0 23971	-2 8368	NO
B 2 1 7 4 1	P-4	Line Sharing/6-13 circuits/Dispatch/FL(days)	ADSL to Retail								
B 2 1 7 4 2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B 2 1 7 5 1	P-4	Line Sharing/>=14 circuits/Dispatch/FL(days)	ADSL to Retail								
B 2 1 7 5 2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL(days)	ADSL to Retail								
B 2 1 8 1 1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	4 08	70,634	4 75	105	6 044	0 59022	-1 1265	YES
B 2 1 8 1 2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	4 08	70,634			6 044			

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
B 2 1 8 2 1	P-4 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	10 18	436	7 00	1	16 544	16 56246	0 1923	YES
B 2 1 8 2 2	P-4 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	10 18	436			16 544			
B 2 1 9 1 1	P-4 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	4 06	70,039	4 09	201	5 995	0 42348	-0 0680	YES
B 2 1 9 1 4	P-4 2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	1 73	244,414	3 86	7	2 186	0 82636	-2 5790	NO
B 2 1 9 2 1	P-4 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	9 56	396	6 00	2	15 836	11 22584	0 3170	YES
B 2 1 9 2 4	P-4 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	18 60	10			23 458			
B 2 1 10 1 1	P-4 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	4 08	70,634	5 50	4	6 044	3 02184	-0 4685	YES
B 2 1 10 1 2	P-4 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	4 08	70,634			6 044			
B 2 1 10 2 1	P-4 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	10 18	436			16 544			
B 2 1 10 2 2	P-4 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	10 18	436			16 544			
B 2 1 11 1 1	P-4 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	4 06	70,039	4 20	5	5 995	2 68128	-0 0520	YES
B 2 1 11 1 4	P-4 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	1 73	244,414	5 00	2	2 186	1 54595	-2 1178	NO
B 2 1 11 2 1	P-4 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	9 56	396			15 836			
B 2 1 11 2 4	P-4 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	18 60	10			23 458			
B 2 1 12 1 1	P-4 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	R&B - Disp	4 08	70,634	5 87	223	6 044	0 40534	-4 4055	NO
B 2 1 12 1 2	P-4 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	R&B - Disp	4 08	70,634			6 044			
B 2 1 12 2 1	P-4 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	R&B - Disp	10 18	436	8 00	16	16 544	4 21108	0 5187	YES
B 2 1 12 2 2	P-4 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	R&B - Disp	10 18	436			16 544			
B 2 1 13 1 1	P-4 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	4 06	70,039	4 60	146	5 995	0 49669	-1 0858	YES
B 2 1 13 1 4	P-4 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	1 73	244,414			2 186			
B 2 1 13 2 1	P-4 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	R&B (POTS) excl SB Or	9 56	396	7 50	4	15 836	7 95779	0 2586	YES
B 2 1 13 2 4	P-4 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(days)	R&B (POTS) excl SB Or	18 60	10			23 458			
B 2 1 14 1 1	P-4 Other Design/<10 circuits/Dispatch/FL(days)	Design	22 91	4,852	7 88	43	25 680	3 93352	3 8212	YES
B 2 1 14 1 2	P-4 Other Design/<10 circuits/Non-Dispatch/FL(days)	Design	23 16	454			65 006			
B 2 1 14 2 1	P-4 Other Design/>=10 circuits/Dispatch/FL(days)	Design	16 50	8	6 00	1	13 939	14 78412	0 7102	YES
B 2 1 14 2 2	P-4 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Design	2 87	63			3 591			
B 2 1 15 1 1	P-4 Other Non-Design/<10 circuits/Dispatch/FL(days)	R&B	4 08	70,634	5 11	27	6 044	1 16329	-0 8824	YES
B 2 1 15 1 2	P-4 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	R&B	0 89	618,135	2 67	5	1 589	0 71078	-2 5000	NO
B 2 1 15 2 1	P-4 Other Non-Design/>=10 circuits/Dispatch/FL(days)	R&B	10 18	436	1 00	1	16 544	16 56246	0 5545	YES
B 2 1 15 2 2	P-4 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	R&B	4 12	206			9 422			
B 2 1 16 1 1	P-4 INP (Standalone)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	4 06	70,064			5 995			
B 2 1 16 1 2	P-4 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0 89	616,137			1 587			
B 2 1 16 2 1	P-4 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9 56	396			15 836			
B 2 1 16 2 2	P-4 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	18 75	15			29 243			
B 2 1 17 1 1	P-4 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	4 06	70,064	1 66	5	5 995	2 68128	0 8953	YES
B 2 1 17 1 2	P-4 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0 89	616,137	0 72	2,515	1 587	0 03171	5 2668	YES
B 2 1 17 2 1	P-4 LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9 56	396	19 00	1	15 836	15 85578	-0 5955	YES
B 2 1 17 2 2	P-4 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	18 75	15	2 76	10	29 243	11 93853	1 3398	YES
B 2 1 18 1 1	P-4 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Digital Loop < DS1	17 80	526	8 22	525	20 253	1 24943	7 6655	YES
B 2 1 18 1 2	P-4 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1	5 83	622			7 006			
B 2 1 18 2 1	P-4 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Digital Loop < DS1								
B 2 1 18 2 2	P-4 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1								
B 2 1 19 1 1	P-4 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Digital Loop >= DS1	17 41	82	7 74	78	22 153	3 50374	2 7578	YES
B 2 1 19 1 2	P-4 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop >= DS1	13 54	18			18 577			
B 2 1 19 2 1	P-4 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Digital Loop >= DS1								
B 2 1 19 2 2	P-4 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop >= DS1								
B 2 2 1	P-4 xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL(days)	14 days			11 00	1				YES
B 2 2 2	P-4 xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL(days)	7 days			4 51	206				YES
B 2 3 1 1 1	P-1 Switch Ports/<10 circuits/Facility/FL(days)	R&B (POTS)	10 81	614			14 146			
B 2 3 1 1 2	P-1 Switch Ports/<10 circuits/Equipment/FL(days)	R&B (POTS)	0 00	0						
B 2 3 1 1 3	P-1 Switch Ports/<10 circuits/Other/FL(days)	R&B (POTS)	22 83	63			27 742			
B 2 3 1 2 1	P-1 Switch Ports/>=10 circuits/Facility/FL(days)	R&B (POTS)	15 50	2			20 506			
B 2 3 1 2 2	P-1 Switch Ports/>=10 circuits/Equipment/FL(days)	R&B (POTS)	0 00	0						
B 2 3 1 2 3	P-1 Switch Ports/>=10 circuits/Other/FL(days)	R&B (POTS)	0 00	0						
B 2 3 2 1 1	P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days)	DS1/ DS3 - Interoffice			0 00	0				
B 2 3 2 1 2	P-1 Local Interoffice Transport/<10 circuits/Equipment/FL(days)	DS1/ DS3 - Interoffice			49 00	1				
B 2 3 2 1 3	P-1 Local Interoffice Transport/<10 circuits/Other/FL(days)	DS1/ DS3 - Interoffice			0 00	0				

BellSouth Monthly State Summary
Florida, July 2001

B 2 3 2 2 1	P-1	Local Interoffice Transport/>=10 circuits/Facility/FL(days)
B 2 3 2 2 2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/FL(days)
B 2 3 2 2 3	P-1	Local interoffice Transport/>=10 circuits/Other/FL(days)
B 2 3 3 1 1	P-1	Loop + Port Combinations/<10 circuits/Facility/FL(days)
B 2 3 3 1 2	P-1	Loop + Port Combinations/<10 circuits/Equipment/FL(days)
B 2 3 3 1 3	P-1	Loop + Port Combinations/<10 circuits/Other/FL(days)
B 2 3 3 2 1	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL(days)
B 2 3 3 2 2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL(days)
B 2 3 3 2 3	P-1	Loop + Port Combinations/>=10 circuits/Other/FL(days)
B 2 3 4 1 1	P-1	Combo Other/<10 circuits/Facility/FL(days)
B 2 3 4 1 2	P-1	Combo Other/<10 circuits/Equipment/FL(days)
B 2 3 4 1 3	P-1	Combo Other/<10 circuits/Other/FL(days)
B 2 3 4 2 1	P-1	Combo Other/>=10 circuits/Facility/FL(days)
B 2 3 4 2 2	P-1	Combo Other/>=10 circuits/Equipment/FL(days)
B 2 3 4 2 3	P-1	Combo Other/>=10 circuits/Other/FL(days)
B 2 3 5 1 1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL(days)
B 2 3 5 1 2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL(days)
B 2 3 5 1 3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL(days)
B 2 3 5 2 1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL(days)
B 2 3 5 2 2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL(days)
B 2 3 5 2 3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL(days)
B 2 3 6 1 1	P-1	UNE ISDN/<10 circuits/Facility/FL(days)
B 2 3 6 1 2	P-1	UNE ISDN/<10 circuits/Equipment/FL(days)
B 2 3 6 1 3	P-1	UNE ISDN/<10 circuits/Other/FL(days)
B 2 3 6 2 1	P-1	UNE ISDN/>=10 circuits/Facility/FL(days)
B 2 3 6 2 2	P-1	UNE ISDN/>=10 circuits/Equipment/FL(days)
B 2 3 6 2 3	P-1	UNE ISDN/>=10 circuits/Other/FL(days)
B 2 3 7 1 1	P-1	Line Sharing/<10 circuits/Facility/FL(days)
B 2 3 7 1 2	P-1	Line Sharing/<10 circuits/Equipment/FL(days)
B 2 3 7 1 3	P-1	Line Sharing/<10 circuits/Other/FL(days)
B 2 3 7 2 1	P-1	Line Sharing/>=10 circuits/Facility/FL(days)
B 2 3 7 2 2	P-1	Line Sharing/>=10 circuits/Equipment/FL(days)
B 2 3 7 2 3	P-1	Line Sharing/>=10 circuits/Other/FL(days)
B 2 3 8 1 1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL(days)
B 2 3 8 1 2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL(days)
B 2 3 8 1 3	P-1	2W Analog Loop Design/<10 circuits/Other/FL(days)
B 2 3 8 2 1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL(days)
B 2 3 8 2 2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL(days)
B 2 3 8 2 3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL(days)
B 2 3 9 1 1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL(days)
B 2 3 9 1 2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 9 1 3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL(days)
B 2 3 9 2 1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL(days)
B 2 3 9 2 2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL(days)
B 2 3 9 2 3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL(days)
B 2 3 10 1 1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL(days)
B 2 3 10 1 2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL(days)
B 2 3 10 1 3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL(days)
B 2 3 10 2 1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL(days)
B 2 3 10 2 2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL(days)
B 2 3 10 2 3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL(days)
B 2 3 11 1 1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL(days)
B 2 3 11 1 2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 11 1 3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL(days)
B 2 3 11 2 1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL(days)
B 2 3 11 2 2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL(days)
B 2 3 11 2 3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL(days)
B 2 3 12 1 1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/FL(days)
B 2 3 12 1 2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL(days)
B 2 3 12 1 3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL(days)
B 2 3 12 2 1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL(days)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
DS1/ DS3 - Interoffice								
R&B	10 84	624	0 00	0	14 053			YES
R&B	0 00	0	0 00	0				YES
R&B	21 94	67	1 50	2	27 135	19 47147	1 0498	YES
R&B	10 67	3	0 00	0	16 743			YES
R&B	0 00	0	0 00	0				YES
R&B	0 00	0	0 00	0				YES
R&B&D - Disp	10 91	636			13 959			
R&B&D - Disp	0 00	0						
R&B&D - Disp	27 70	94			51 480			
R&B&D - Disp	7 20	5			12 755			
R&B&D - Disp	0 00	0						
R&B&D - Disp	15 86	7			3 615			
ADSL to Retail	38 90	772	1 50	2	38 287	27 10790	1.3797	YES
ADSL to Retail	0 00	0	0 00	0				YES
ADSL to Retail	15 21	29	0 00	0	31 236			YES
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
ISDN - BRI	14 00	7	32 00	2	6 583	5 27798	-3 4104	NO
ISDN - BRI	0 00	0	0 00	0				YES
ISDN - BRI	0 00	0	0 00	0				YES
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	38 90	772	8 00	1	38 287	38 31159	0 8066	YES
ADSL to Retail	0 00	0	0 00	0				YES
ADSL to Retail	15 21	29	0 00	0	31 236			YES
ADSL to Retail	0 00	0						
ADSL to Retail	0 00	0						
R&B - Disp	10 84	624	0 00	0	14 064			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3	0 00	0	16 743			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	10 81	614	0 00	0	14 146			YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	22 83	63	0 00	0	27 742			YES
R&B (POTS) excl SB Or	15 50	2	7 00	1	20 506	25 11474	0 3384	YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B - Disp	10 84	624	0 00	0	14 064			YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3			16 743			
R&B - Disp	0 00	0						
R&B - Disp	0 00	0						
R&B (POTS) excl SB Or	10 81	614	0 00	0	14 146			YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	22 83	63	0 00	0	27 742			YES
R&B (POTS) excl SB Or	15 50	2			20 506			
R&B (POTS) excl SB Or	0 00	0						
R&B (POTS) excl SB Or	0 00	0						
R&B - Disp	10 84	624	8 67	3	14 064	8 13923	0 2668	YES
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	21 94	67	0 00	0	27 450			YES
R&B - Disp	10 67	3	13 00	3	16 743	13 67077	-0 1707	YES

BellSouth Monthly State Summary
Florida, July 2001

B 2 3 12 2 2	P-1	2W Analog Loop w/LNP Design/≥10 circuits/Equipment/FL(days)
B 2 3 12 2 3	P-1	2W Analog Loop w/LNP Design/≥10 circuits/Other/FL(days)
B 2 3 13 1 1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL(days)
B 2 3 13 1 2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 13 1 3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL(days)
B 2 3 13 2 1	P-1	2W Analog Loop w/LNP Non-Design/≥10 circuits/Facility/FL(days)
B 2 3 13 2 2	P-1	2W Analog Loop w/LNP Non-Design/≥10 circuits/Equipment/FL(days)
B 2 3 13 2 3	P-1	2W Analog Loop w/LNP Non-Design/≥10 circuits/Other/FL(days)
B 2 3 14 1 1	P-1	Other Design/<10 circuits/Facility/FL(days)
B 2 3 14 1 2	P-1	Other Design/<10 circuits/Equipment/FL(days)
B 2 3 14 1 3	P-1	Other Design/<10 circuits/Other/FL(days)
B 2 3 14 2 1	P-1	Other Design/≥10 circuits/Facility/FL(days)
B 2 3 14 2 2	P-1	Other Design/≥10 circuits/Equipment/FL(days)
B 2 3 14 2 3	P-1	Other Design/≥10 circuits/Other/FL(days)
B 2 3 15 1 1	P-1	Other Non-Design/<10 circuits/Facility/FL(days)
B 2 3 15 1 2	P-1	Other Non-Design/<10 circuits/Equipment/FL(days)
B 2 3 15 1 3	P-1	Other Non-Design/<10 circuits/Other/FL(days)
B 2 3 15 2 1	P-1	Other Non-Design/≥10 circuits/Facility/FL(days)
B 2 3 15 2 2	P-1	Other Non-Design/≥10 circuits/Equipment/FL(days)
B 2 3 15 2 3	P-1	Other Non-Design/≥10 circuits/Other/FL(days)
B 2 3 16 1 1	P-1	INP (Standalone)/<10 circuits/Facility/FL(days)
B 2 3 16 1 2	P-1	INP (Standalone)/<10 circuits/Equipment/FL(days)
B 2 3 16 1 3	P-1	INP (Standalone)/<10 circuits/Other/FL(days)
B 2 3 16 2 1	P-1	INP (Standalone)/≥10 circuits/Facility/FL(days)
B 2 3 16 2 2	P-1	INP (Standalone)/≥10 circuits/Equipment/FL(days)
B 2 3 16 2 3	P-1	INP (Standalone)/≥10 circuits/Other/FL(days)
B 2 3 17 1 1	P-1	LNP (Standalone)/<10 circuits/Facility/FL(days)
B 2 3 17 1 2	P-1	LNP (Standalone)/<10 circuits/Equipment/FL(days)
B 2 3 17 1 3	P-1	LNP (Standalone)/<10 circuits/Other/FL(days)
B 2 3 17 2 1	P-1	LNP (Standalone)/≥10 circuits/Facility/FL(days)
B 2 3 17 2 2	P-1	LNP (Standalone)/≥10 circuits/Equipment/FL(days)
B 2 3 17 2 3	P-1	LNP (Standalone)/≥10 circuits/Other/FL(days)
B 2 3 18 1 1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL(days)
B 2 3 18 1 2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL(days)
B 2 3 18 1 3	P-1	Digital Loop < DS1/<10 circuits/Other/FL(days)
B 2 3 18 2 1	P-1	Digital Loop < DS1/≥10 circuits/Facility/FL(days)
B 2 3 18 2 2	P-1	Digital Loop < DS1/≥10 circuits/Equipment/FL(days)
B 2 3 18 2 3	P-1	Digital Loop < DS1/≥10 circuits/Other/FL(days)
B 2 3 19 1 1	P-1	Digital Loop ≥ DS1/<10 circuits/Facility/FL(days)
B 2 3 19 1 2	P-1	Digital Loop ≥ DS1/<10 circuits/Equipment/FL(days)
B 2 3 19 1 3	P-1	Digital Loop ≥ DS1/<10 circuits/Other/FL(days)
B 2 3 19 2 1	P-1	Digital Loop ≥ DS1/≥10 circuits/Facility/FL(days)
B 2 3 19 2 2	P-1	Digital Loop ≥ DS1/≥10 circuits/Equipment/FL(days)
B 2 3 19 2 3	P-1	Digital Loop ≥ DS1/≥10 circuits/Other/FL(days)

% Jeopardies - Mechanized

B 2 5 1	P-2	Switch Ports/FL(%)
B 2 5 2	P-2	Local Interoffice Transport/FL(%)
B 2 5 3	P-2	Loop + Port Combinations/FL(%)
B 2 5 4	P-2	Combo Other/FL(%)
B 2 5 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)
B 2 5 6	P-2	UNE ISDN/FL(%)
B 2 5 7	P-2	Line Sharing/FL(%)
B 2 5 8	P-2	2W Analog Loop Design/FL(%)
B 2 5 9	P-2	2W Analog Loop Non-Design/FL(%)
B 2 5 10	P-2	2W Analog Loop w/LNP Design/FL(%)
B 2 5 11	P-2	2W Analog Loop w/LNP Non-Design/FL(%)
B 2 5 12	P-2	2W Analog Loop w/LNP Design/FL(%)
B 2 5 13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)
B 2 5 14	P-2	Other Design/FL(%)
B 2 5 15	P-2	Other Non-Design/FL(%)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
R&B - Disp	0 00	0	0 00	0				YES
R&B - Disp	0 00	0	7 00	2				
R&B (POTS) excl SB Or	10 81	614	4 00	2	14 146	10 01922	0 6801	YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	22 83	63	0 00	0	27 742			YES
R&B (POTS) excl SB Or	15 50	2	0 00	0	20 506			YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
R&B (POTS) excl SB Or	0 00	0	0 00	0				YES
Design	14 23	13	61 00	1	6 809	7 06572	-6 6192	NO
Design	0 00	0	0 00	0				YES
Design	37 19	31	0 00	0	80 627			YES
Design	2 00	2	0 00	0	0 000			YES
Design	0 00	0	0 00	0				YES
Design	15 86	7	0 00	0	28 038			YES
R&B	10 84	624	0 00	0	14 053			YES
R&B	0 00	0	0 00	0				YES
R&B	21 94	67	0 00	0	27 135			YES
R&B	10 67	3	0 00	0	16 743			YES
R&B	0 00	0	0 00	0				YES
R&B	0 00	0	0 00	0				YES
R&B (POTS)	10 81	614			14 146			
R&B (POTS)	0 00	0						
R&B (POTS)	22 83	63			27 742			
R&B (POTS)	15 50	2			20 506			
R&B (POTS)	0 00	0						
R&B (POTS)	0 00	0						
R&B (POTS)	10 81	614	0 00	0	14 146			YES
R&B (POTS)	0 00	0	0 00	0				YES
R&B (POTS)	22 83	63	0 00	0	27 742			YES
R&B (POTS)	15 50	2	0 00	0	20 506			YES
R&B (POTS)	0 00	0	0 00	0				YES
R&B (POTS)	0 00	0	0 00	0				YES
Digital Loop < DS1	24 60	5	16 75	4	32 715	21 94616	0 9577	YES
Digital Loop < DS1	0 00	0	0 00	0				YES
Digital Loop < DS1	0 00	0	0 00	0				YES
Digital Loop < DS1								
Digital Loop < DS1								
Digital Loop ≥ DS1	5 00	2	12 33	3	6 657	5 16402	-1 4201	YES
Digital Loop ≥ DS1	0 00	0	0 00	0				YES
Digital Loop ≥ DS1	30 00	1	0 00	0	0 000			YES
Digital Loop ≥ DS1								
Digital Loop ≥ DS1								

R&B (POTS)	0.80%	739,870	0 00%	2				
DS1/DS3 - Interoffice								
R&B	0 81%	742,821	0 16%	9,755		0 00091	7 0887	YES
R&B&D - Disp	5 27%	151,087						
ADSL to Retail	21 21%	18,221	6 59%	273		0 02492	5 8629	YES
ISDN - BRI	13 48%	1,024	21 43%	70		0 04219	-1 8850	NO
ADSL to Retail	21 21%	18,221	0 00%	102		0 04059	5 2249	YES
R&B - Disp	0 81%	742,821	21 09%	128		0 00794	-25 5564	NO
R&B (POTS) excl SB Or	1 40%	424,523	10 21%	431		0 00566	-15 5698	NO
R&B - Disp	0 81%	742,821	0 00%	1		0 08978	0 0905	YES
R&B (POTS) excl SB Or	1 40%	424,523						
R&B - Disp	0 81%	742,821	0 00%	2,449		0 00182	4 4718	YES
R&B (POTS) excl SB Or	1 40%	424,523	0 00%	2,173		0 00253	5 5370	YES
Design	27 96%	6,920	14 29%	49		0 06434	2 1256	YES
R&B	0 81%	742,821	15 38%	26		0 01761	-8 2763	NO

BellSouth Monthly State Summary
Florida, July 2001

B 2 5 16	P-2	INP (Standalone)/FL(%)
B 2 5 17	P-2	LNP (Standalone)/FL(%)
B 2 5 18	P-2	Digital Loop < DS1/FL(%)
B 2 5 19	P-2	Digital Loop >= DS1/FL(%)

% Jeopardies - Non-Mechanized

B 2 6 1	P-2	Switch Ports/FL(%)
B 2 6 2	P-2	Local Interoffice Transport/FL(%)
B 2 6 3	P-2	Loop + Port Combinations/FL(%)
B 2 6 4	P-2	Combo Other/FL(%)
B 2 6 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(%)
B 2 6 6	P-2	UNE ISDN/FL(%)
B 2 6 7	P-2	Line Sharing/FL(%)
B 2 6 8	P-2	2W Analog Loop Design/FL(%)
B 2 6 9	P-2	2W Analog Loop Non-Design/FL(%)
B 2 6 10	P-2	2W Analog Loop w/INP Design/FL(%)
B 2 6 11	P-2	2W Analog Loop w/INP Non-Design/FL(%)
B 2 6 12	P-2	2W Analog Loop w/LNP Design/FL(%)
B 2 6 13	P-2	2W Analog Loop w/LNP Non-Design/FL(%)
B 2 6 14	P-2	Other Design/FL(%)
B 2 6 15	P-2	Other Non-Design/FL(%)
B 2 6 16	P-2	INP (Standalone)/FL(%)
B 2 6 17	P-2	LNP (Standalone)/FL(%)
B 2 6 18	P-2	Digital Loop < DS1/FL(%)
B 2 6 19	P-2	Digital Loop >= DS1/FL(%)

B 2 8 1	P-2	Switch Ports/FL(hours)
B 2 8 2	P-2	Local Interoffice Transport/FL(hours)
B 2 8 3	P-2	Loop + Port Combinations/FL(hours)
B 2 8 4	P-2	Combo Other/FL(hours)
B 2 8 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)
B 2 8 6	P-2	UNE ISDN/FL(hours)
B 2 8 7	P-2	Line Sharing/FL(hours)
B 2 8 8	P-2	2W Analog Loop Design/FL(hours)
B 2 8 9	P-2	2W Analog Loop Non-Design/FL(hours)
B 2 8 10	P-2	2W Analog Loop w/INP Design/FL(hours)
B 2 8 11	P-2	2W Analog Loop w/INP Non-Design/FL(hours)
B 2 8 12	P-2	2W Analog Loop w/LNP Design/FL(hours)
B 2 8 13	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)
B 2 8 14	P-2	Other Design/FL(hours)
B 2 8 15	P-2	Other Non-Design/FL(hours)
B 2 8 16	P-2	INP (Standalone)/FL(hours)
B 2 8 17	P-2	LNP (Standalone)/FL(hours)
B 2 8 18	P-2	Digital Loop < DS1/FL(hours)
B 2 8 19	P-2	Digital Loop >= DS1/FL(hours)

Average Jeopardy Notice Interval - Non-Mechanized

B 2 9 1	P-2	Switch Ports/FL(hours)
B 2 9 2	P-2	Local Interoffice Transport/FL(hours)
B 2 9 3	P-2	Loop + Port Combinations/FL(hours)
B 2 9 4	P-2	Combo Other/FL(hours)
B 2 9 5	P-2	xDSL (ADSL, HDSL and UCL)/FL(hours)
B 2 9 6	P-2	UNE ISDN/FL(hours)
B 2 9 7	P-2	Line Sharing/FL(hours)
B 2 9 8	P-2	2W Analog Loop Design/FL(hours)
B 2 9 9	P-2	2W Analog Loop Non-Design/FL(hours)
B 2 9 10	P-2	2W Analog Loop w/INP Design/FL(hours)
B 2 9 11	P-2	2W Analog Loop w/INP Non-Design/FL(hours)
B 2 9 12	P-2	2W Analog Loop w/LNP Design/FL(hours)
B 2 9 13	P-2	2W Analog Loop w/LNP Non-Design/FL(hours)
B 2 9 14	P-2	Other Design/FL(hours)

Benchmark / Analog

R&B (POTS)
R&B (POTS)
Digital Loop < DS1
Digital Loop >= DS1

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
0.80%	739,870						
0.80%	739,870	0.00%	4,000		0.00141	5.6730	YES
15.02%	1,272	8.44%	320		0.02234	2.9445	YES
28.24%	131	64.77%	88		0.06205	-5.8870	NO

Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
Diagnostic
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Diagnostic

	0.00%	12	Diagnostic
	0.44%	1,129	Diagnostic
	14.45%	263	Diagnostic
	38.32%	381	Diagnostic
	0.00%	7	Diagnostic
	25.86%	116	Diagnostic
	8.50%	153	Diagnostic
	0.00%	5	Diagnostic
	0.00%	6	Diagnostic
	4.00%	494	Diagnostic
	2.00%	1,603	Diagnostic
	10.96%	146	Diagnostic
	25.00%	32	Diagnostic
			Diagnostic
	0.00%	1,460	Diagnostic
	28.64%	639	Diagnostic
	58.17%	153	Diagnostic

>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs
>= 48 hrs

			YES
	172.50	16	
	174.67	18	YES
	412.80	15	YES
	271.11	27	YES
	241.09	44	YES
	238.75	1	YES
	124.10	1	YES
	404.57	7	YES
	318.00	4	YES
	305.78	27	YES
	338.11	57	YES

Diagnostic
Diagnostic
Diagnostic
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Diagnostic

			Diagnostic
			Diagnostic
	240.00	5	Diagnostic
			Diagnostic
	169.89	38	Diagnostic
	239.51	146	Diagnostic
			Diagnostic
			Diagnostic
	138.40	30	Diagnostic
	247.38	13	Diagnostic
			Diagnostic
			Diagnostic
			Diagnostic
	105.40	18	Diagnostic
	109.21	30	Diagnostic
	253.50	16	Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 9 15	P-2 Other Non-Design/FL(hours)			144 00	8				Diagnostic
B 2 9 16	P-2 INP (Standalone)/FL(hours)								Diagnostic
B 2 9 17	P-2 LNP (Standalone)/FL(hours)								Diagnostic
B 2 9 18	P-2 Digital Loop < DS1/FL(hours)			226 23	183				Diagnostic
B 2 9 19	P-2 Digital Loop >= DS1/FL(hours)			337 89	89				Diagnostic
% Jeopardy Notice >= 48 hours - Mechanized									
B 2 10 1	P-2 Switch Ports/FL(%)	95% >= 48 hrs							
B 2 10 2	P-2 Local Interoffice Transport/FL(%)	95% >= 48 hrs							
B 2 10 3	P-2 Loop + Port Combinations/FL(%)	95% >= 48 hrs	66 67%		6				NO
B 2 10 4	P-2 Combo Other/FL(%)	95% >= 48 hrs							
B 2 10 5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)	95% >= 48 hrs							
B 2 10 6	P-2 UNE ISDN/FL(%)	95% >= 48 hrs							
B 2 10 7	P-2 Line Sharing/FL(%)	95% >= 48 hrs							
B 2 10 8	P-2 2W Analog Loop Design/FL(%)	95% >= 48 hrs							
B 2 10 9	P-2 2W Analog Loop Non-Design/FL(%)	95% >= 48 hrs	93 75%		32				NO
B 2 10 10	P-2 2W Analog Loop w/INP Design/FL(%)	95% >= 48 hrs							
B 2 10 11	P-2 2W Analog Loop w/INP Non-Design/FL(%)	95% >= 48 hrs							
B 2 10 12	P-2 2W Analog Loop w/LNP Design/FL(%)	95% >= 48 hrs							
B 2 10 13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)	95% >= 48 hrs	0 00%		1				NO
B 2 10 14	P-2 Other Design/FL(%)	95% >= 48 hrs	0 00%		1				NO
B 2 10 15	P-2 Other Non-Design/FL(%)	95% >= 48 hrs							
B 2 10 16	P-2 INP (Standalone)/FL(%)	95% >= 48 hrs							
B 2 10 17	P-2 LNP (Standalone)/FL(%)	95% >= 48 hrs							
B 2 10 18	P-2 Digital Loop < DS1/FL(%)	95% >= 48 hrs							
B 2 10 19	P-2 Digital Loop >= DS1/FL(%)	95% >= 48 hrs	100 00%		21				YES
% Jeopardy Notice >= 48 hours - Non-Mechanized									
B 2 11 1	P-2 Switch Ports/FL(%)	Diagnostic							Diagnostic
B 2 11 2	P-2 Local Interoffice Transport/FL(%)	Diagnostic							Diagnostic
B 2 11 3	P-2 Loop + Port Combinations/FL(%)	Diagnostic	85 71%		7				Diagnostic
B 2 11 4	P-2 Combo Other/FL(%)	Diagnostic							Diagnostic
B 2 11 5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic	92 31%		13				Diagnostic
B 2 11 6	P-2 UNE ISDN/FL(%)	Diagnostic							Diagnostic
B 2 11 7	P-2 Line Sharing/FL(%)	Diagnostic							Diagnostic
B 2 11 8	P-2 2W Analog Loop Design/FL(%)	Diagnostic							Diagnostic
B 2 11 9	P-2 2W Analog Loop Non-Design/FL(%)	Diagnostic	88 89%		9				Diagnostic
B 2 11 10	P-2 2W Analog Loop w/INP Design/FL(%)	Diagnostic							Diagnostic
B 2 11 11	P-2 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic							Diagnostic
B 2 11 12	P-2 2W Analog Loop w/LNP Design/FL(%)	Diagnostic	33 00%		18				Diagnostic
B 2 11 13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic	37 00%		30				Diagnostic
B 2 11 14	P-2 Other Design/FL(%)	Diagnostic	100 00%		5				Diagnostic
B 2 11 15	P-2 Other Non-Design/FL(%)	Diagnostic							Diagnostic
B 2 11 16	P-2 INP (Standalone)/FL(%)	Diagnostic							Diagnostic
B 2 11 17	P-2 LNP (Standalone)/FL(%)	Diagnostic							Diagnostic
B 2 11 18	P-2 Digital Loop < DS1/FL(%)	Diagnostic	100 00%		7				Diagnostic
B 2 11 19	P-2 Digital Loop >= DS1/FL(%)	Diagnostic	97 37%		38				Diagnostic
Coordinated Customers Conversions									
B 2 12 1	P-7 Loops with INP/FL(%)	>= 95% w in 15 min							
B 2 12 2	P-7 Loops with LNP/FL(%)	>= 95% w in 15 min	99 00%		4,595				YES
% Hot Cuts > 15 minutes Early									
B 2 13 1	P-7A Time-Specific SL1/FL(%)	<= 5%	0 62%		324				YES
B 2 13 2	P-7A Time-Specific SL2/FL(%)	<= 5%	0 24%		417				YES
B 2 13 3	P-7A Non-Time Specific SL1/FL(%)	<= 5%	0 00%		11				YES
B 2 13 4	P-7A Non-Time Specific SL2/FL(%)	<= 5%	0 35%		288				YES
Hot Cut Timeliness									
B 2 14 1	P-7A Time-Specific SL1/FL(%)	>= 95% w in 15 min	98 46%		324				YES
B 2 14 2	P-7A Time-Specific SL2/FL(%)	>= 95% w in 15 min	99 52%		417				YES
B 2 14 3	P-7A Non-Time Specific SL1/FL(%)	>= 95% w in 15 min	100 00%		11				YES
B 2 14 4	P-7A Non-Time Specific SL2/FL(%)	>= 95% w in 15 min	99 31%		288				YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 15 1	P-7A Time-Specific SL1/FL(%)	<= 5%		0 93%	324				YES
B 2 15 2	P-7A Time-Specific SL2/FL(%)	<= 5%		0 24%	417				YES
B 2 15 3	P-7A Non-Time Specific SL1/FL(%)	<= 5%		0 00%	11				YES
B 2 15 4	P-7A Non-Time Specific SL2/FL(%)	<= 5%		0 35%	288				YES
Average Recovery Time - CCC									
B 2 16 1	P-7B Loops with INP/FL(time units)	Diagnostic							Diagnostic
B 2 16 2	P-7B Loops with LNP/FL(time units)	Diagnostic	428 04	11					Diagnostic
in Days									
B 2 17 1 1	P-7C UNE Loop Design/Dispatch/FL(%)	<= 5%		2 19%	3,330				YES
B 2 17 1 2	P-7C UNE Loop Design/Non-Dispatch/FL(%)	<= 5%							
B 2 17 2 1	P-7C UNE Loop Non-Design/Dispatch/FL(%)	<= 5%		1 01%	2,283				YES
B 2 17 2 2	P-7C UNE Loop Non-Design/Non-Dispatch/FL(%)	<= 5%		0 85%	1,173				YES
% Missed Installation Appointments									
B 2 18 1 1 1	P-3 Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	4 52%	81,425					
B 2 18 1 1 2	P-3 Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0 06%	653,366					
B 2 18 1 2 1	P-3 Switch Ports/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	5 73%	436					
B 2 18 1 2 2	P-3 Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0 00%	21					
B 2 18 2 1 1	P-3 Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3			7 69%	13			
B 2 18 2 1 2	P-3 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 18 2 2 1	P-3 Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3							
B 2 18 2 2 2	P-3 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3							
B 2 18 3 1 1	P-3 Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	4 54%	82,059	4 06%	394	0 01052	0 4585	YES
B 2 18 3 1 2	P-3 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0 06%	655,410	0 22%	10,347	0 00024	-6 8582	NO
B 2 18 3 1 3	P-3 Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B	0 00%	315,963	0 00%	4,791	0 00000	11,963	YES
B 2 18 3 1 4	P-3 Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	R&B	0 14%	282,459	0 41%	5,556	0 00050	-5 5697	NO
B 2 18 3 2 1	P-3 Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	5 44%	478	11 11%	9	0 07631	-0 7433	YES
B 2 18 3 2 2	P-3 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	0 00%	227	0 00%	5	0 00000		YES
B 2 18 3 2 3	P-3 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)	R&B	0 00%	58	0 00%	1	0 00000		YES
B 2 18 3 2 4	P-3 Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	R&B	0 00%	169	0 00%	4	0 00000		YES
B 2 18 4 1 1	P-3 Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	4 57%	86,953					
B 2 18 4 1 4	P-3 Combo Other/<10 circuits/Dispatch In/FL(%)	R&B&D - Disp							
B 2 18 4 2 1	P-3 Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	5 56%	486					
B 2 18 4 2 4	P-3 Combo Other/>=10 circuits/Dispatch In/FL(%)	R&B&D - Disp							
B 2 18 5 1 1	P-3 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7 25%	16,850	1 57%	508	0 01167	4 8580	YES
B 2 18 5 1 2	P-3 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 15%	683					
B 2 18 5 2 1	P-3 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0 00%	7					
B 2 18 5 2 2	P-3 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail							
B 2 18 6 1 1	P-3 UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	10 13%	454	9 24%	422	0 02040	0 4364	YES
B 2 18 6 1 2	P-3 UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	2 08%	530					
B 2 18 6 2 1	P-3 UNE ISDN/>=10 circuits/Dispatch/FL(%)	ISDN - BRI							
B 2 18 6 2 2	P-3 UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI							
B 2 18 7 1 1	P-3 Line Sharrng/<10 circuits/Dispatch/FL(%)	ADSL to Retail	7 25%	16,850	28 57%	7	0 09801	-2 1758	NO
B 2 18 7 1 2	P-3 Line Sharrng/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 15%	683	0 00%	101	0 00408	0 3592	YES
B 2 18 7 2 1	P-3 Line Sharrng/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	0 00%	7					
B 2 18 7 2 2	P-3 Line Sharrng/>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail							
B 2 18 8 1 1	P-3 2W Analog Loop Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4 54%	82,059	1 34%	224	0 01393	2 2994	YES
B 2 18 8 1 2	P-3 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4 54%	82,059					
B 2 18 8 2 1	P-3 2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5 44%	478	0 00%	4	0 11387	0 4777	YES
B 2 18 8 2 2	P-3 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 44%	478					
B 2 18 9 1 1	P-3 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	4 52%	81,390	3 39%	560	0 00881	1 2822	YES
B 2 18 9 1 4	P-3 2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 13%	281,331	0 00%	10	0 01158	0 1160	YES
B 2 18 9 2 1	P-3 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5 73%	436	25 00%	8	0 08295	-2 3227	NO
B 2 18 9 2 4	P-3 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 00%	14					
B 2 18 10 1 1	P-3 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	4 54%	82,059	0 00%	6	0 08502	0 5344	YES
B 2 18 10 1 2	P-3 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	4 54%	82,059					
B 2 18 10 2 1	P-3 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	5 44%	478					
B 2 18 10 2 2	P-3 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 44%	478					

BellSouth Monthly State Summary
Florida, July 2001

B 2 18 11 1 1	P-3	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)
B 2 18 11 1 4	P-3	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch Inv/FL(%)
B 2 18 11 2 1	P-3	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 18 11 2 4	P-3	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch Inv/FL(%)
B 2 18 12 1 1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)
B 2 18 12 1 2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)
B 2 18 12 2 1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)
B 2 18 12 2 2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 13 1 1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)
B 2 18 13 1 4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch Inv/FL(%)
B 2 18 13 2 1	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 18 13 2 4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch Inv/FL(%)
B 2 18 14 1 1	P-3	Other Design/<10 circuits/Dispatch/FL(%)
B 2 18 14 1 2	P-3	Other Design/<10 circuits/Non-Dispatch/FL(%)
B 2 18 14 2 1	P-3	Other Design/>=10 circuits/Dispatch/FL(%)
B 2 18 14 2 2	P-3	Other Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 15 1 1	P-3	Other Non-Design/<10 circuits/Dispatch/FL(%)
B 2 18 15 1 2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)
B 2 18 15 2 1	P-3	Other Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 18 15 2 2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 16 1 1	P-3	INP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 18 16 1 2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 18 16 2 1	P-3	INP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 18 16 2 2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 17 1 1	P-12	LNP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 18 17 1 2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 18 17 2 1	P-12	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 18 17 2 2	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 18 1 1	P-3	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)
B 2 18 18 1 2	P-3	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 18 18 2 1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)
B 2 18 18 2 2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)
B 2 18 19 1 1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)
B 2 18 19 1 2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 18 19 2 1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)
B 2 18 19 2 2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
R&B (POTS) excl SB Or	4.52%	81,390	0.00%	6		0.08484	0.5331	YES
R&B (POTS) excl SB Or	0.13%	281,331	0.00%	4		0.01832	0.0734	YES
R&B (POTS) excl SB Or	5.73%	436						
R&B (POTS) excl SB Or	0.00%	14						
R&B - Disp	4.54%	82,059	1.59%	691		0.00796	3.7120	YES
R&B - Disp	4.54%	82,059						
R&B - Disp	5.44%	478	0.00%	27		0.04486	1.2125	YES
R&B - Disp	5.44%	478						
R&B (POTS) excl SB Or	4.52%	81,390	3.04%	626		0.00834	1.7784	YES
R&B (POTS) excl SB Or	0.13%	281,331						
R&B (POTS) excl SB Or	5.73%	436	0.00%	19		0.05449	1.0524	YES
R&B (POTS) excl SB Or	0.00%	14						
Design	4.97%	4,929	3.65%	192		0.01599	0.8286	YES
Design	3.93%	483						
Design	12.50%	8	0.00%	2		0.26146	0.4781	YES
Design	0.00%	63						
R&B	4.54%	82,059	2.44%	41		0.03253	0.6468	YES
R&B	0.06%	655,410	0.00%	8		0.00853	0.0683	YES
R&B	5.44%	478	0.00%	3		0.13135	0.4141	YES
R&B	0.00%	227						
R&B (POTS)	4.52%	81,425						
R&B (POTS)	0.06%	653,366						
R&B (POTS)	5.73%	436						
R&B (POTS)	0.00%	21						
R&B (POTS)	4.52%	81,425	0.00%	6		0.08484	0.5331	YES
R&B (POTS)	0.06%	653,366	0.23%	2,569		0.00047	-3.6375	NO
R&B (POTS)	5.73%	436	0.00%	1		0.23276	0.2463	YES
R&B (POTS)	0.00%	21	0.00%	25		0.00000		YES
Digital Loop < DS1	9.80%	551	5.11%	901		0.01608	2.9198	YES
Digital Loop < DS1	0.00%	676						
Digital Loop < DS1								
Digital Loop < DS1								
Digital Loop >= DS1	9.41%	85	8.05%	236		0.03694	0.3684	YES
Digital Loop >= DS1	5.56%	18						
Digital Loop >= DS1								
Digital Loop >= DS1								

% Provisioning Troubles within 30 Days

B 2 19 1 1 1	P-9	Switch Ports/<10 circuits/Dispatch/FL(%)
B 2 19 1 1 2	P-9	Switch Ports/<10 circuits/Non-Dispatch/FL(%)
B 2 19 1 2 1	P-9	Switch Ports/>=10 circuits/Dispatch/FL(%)
B 2 19 1 2 2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 2 1 1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)
B 2 19 2 1 2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)
B 2 19 2 2 1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)
B 2 19 2 2 2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 3 1 1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)
B 2 19 3 1 2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)
B 2 19 3 1 3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)
B 2 19 3 1 4	P-9	Loop + Port Combinations/<10 circuits/Dispatch Inv/FL(%)
B 2 19 3 2 1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)
B 2 19 3 2 2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 3 2 3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)
B 2 19 3 2 4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch Inv/FL(%)
B 2 19 4 1 1	P-9	Combo Other/<10 circuits/Dispatch/FL(%)
B 2 19 4 1 4	P-9	Combo Other/<10 circuits/Dispatch Inv/FL(%)
B 2 19 4 2 1	P-9	Combo Other/>=10 circuits/Dispatch/FL(%)
B 2 19 4 2 4	P-9	Combo Other/>=10 circuits/Dispatch Inv/FL(%)
B 2 19 5 1 1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)
B 2 19 5 1 2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)
B 2 19 5 2 1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)

R&B (POTS)	6.15%	80,517						
R&B (POTS)	3.60%	714,657						
R&B (POTS)	6.36%	472						
R&B (POTS)	1.49%	67						
DS1/DS3			5.26%	19				
DS1/DS3								
DS1/DS3								
DS1/DS3								
R&B	6.11%	81,168	3.72%	698		0.00911	2.6215	YES
R&B	3.59%	717,054	3.04%	10,241		0.00185	2.9689	YES
R&B								
R&B								
<i>Data included in B.2.19.3.1.2</i>								
R&B	5.97%	519	20.00%	15		0.06207	-2.2600	NO
R&B	0.21%	471	0.00%	5		0.02069	0.1026	YES
R&B								
<i>Data included in B.2.19.3.2.2</i>								
R&B								
R&B								
R&B&D - Disp	5.91%	87,031						
R&B&D - Disp								
R&B&D - Disp	5.67%	547						
R&B&D - Disp								
ADSL to Retail	6.95%	16,771	4.83%	476		0.01182	1.7894	YES
ADSL to Retail	6.66%	871						
ADSL to Retail	25.00%	4						

BellSouth Monthly State Summary
Florida, July 2001

B 2 19 5 2 2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 6 1 1	P-9	UNE ISDN/<10 circuits/Dispatch/FL(%)
B 2 19 6 1 2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)
B 2 19 6 2 1	P-9	UNE ISDN/>=10 circuits/Dispatch/FL(%)
B 2 19 6 2 2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 7 1 1	P-9	Line Sharing/<10 circuits/Dispatch/FL(%)
B 2 19 7 1 2	P-9	Line Sharing/<10 circuits/Non-Dispatch/FL(%)
B 2 19 7 2 1	P-9	Line Sharing/>=10 circuits/Dispatch/FL(%)
B 2 19 7 2 2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 8 1 1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)
B 2 19 8 1 2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 8 2 1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)
B 2 19 8 2 2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 9 1 1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 9 1 4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/In/FL(%)
B 2 19 9 2 1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 9 2 4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/In/FL(%)
B 2 19 10 1 1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)
B 2 19 10 1 2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 10 2 1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)
B 2 19 10 2 2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 11 1 1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 11 1 4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/In/FL(%)
B 2 19 11 2 1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 11 2 4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/In/FL(%)
B 2 19 12 1 1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)
B 2 19 12 1 2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 12 2 1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)
B 2 19 12 2 2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 13 1 1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 13 1 4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/In/FL(%)
B 2 19 13 2 1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 13 2 4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/In/FL(%)
B 2 19 14 1 1	P-9	Other Design/<10 circuits/Dispatch/FL(%)
B 2 19 14 1 2	P-9	Other Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 14 2 1	P-9	Other Design/>=10 circuits/Dispatch/FL(%)
B 2 19 14 2 2	P-9	Other Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 15 1 1	P-9	Other Non-Design/<10 circuits/Dispatch/FL(%)
B 2 19 15 1 2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)
B 2 19 15 2 1	P-9	Other Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 19 15 2 2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 16 1 1	P-9	INP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 19 16 1 2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 19 16 2 1	P-9	INP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 19 16 2 2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 17 1 1	P-9	LNP (Standalone)/<10 circuits/Dispatch/FL(%)
B 2 19 17 1 2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B 2 19 17 2 1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2 19 17 2 2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 18 1 1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)
B 2 19 18 1 2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 19 18 2 1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)
B 2 19 18 2 2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 19 1 1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)
B 2 19 19 1 2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)
B 2 19 19 2 1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)
B 2 19 19 2 2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)

Average Completion Notice Interval - Mechanized

B 2 21 1 1 1	P-5	Switch Ports/<10 circuits/Dispatch/FL(hours)
B 2 21 1 1 2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
ADSL to Retail								
ISDN - BRI	0.00%	491	9.98%	401		0.00000		NO
ISDN - BRI	0.00%	446						
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	6.95%	16,771	0.00%	2		0.17979	0.3864	YES
ADSL to Retail	6.66%	871	13.04%	69		0.03118	-2.0465	NO
ADSL to Retail	25.00%	4						
ADSL to Retail								
R&B - Disp	6.11%	81,168	0.25%	401		0.01199	4.8888	YES
R&B - Disp	6.11%	81,168						
R&B - Disp	5.97%	519	0.00%	5		0.10649	0.5609	YES
R&B - Disp	5.97%	519						
R&B (POTS) excl SB Or	6.15%	80,517	0.00%	432		0.01159	5.3077	YES
R&B (POTS) excl SB Or			0.00%	2				
R&B (POTS) excl SB Or	6.36%	472	0.00%	4		0.12250	0.5189	YES
R&B (POTS) excl SB Or								
R&B - Disp	6.11%	81,168	0.00%	5		0.10713	0.5705	YES
R&B - Disp	6.11%	81,168						
R&B - Disp	5.97%	519						
R&B - Disp	5.97%	519						
R&B (POTS) excl SB Or	6.15%	80,517	0.00%	13		0.06665	0.9231	YES
R&B (POTS) excl SB Or			0.00%	1				
R&B (POTS) excl SB Or	6.36%	472						
R&B (POTS) excl SB Or								
R&B - Disp	6.11%	81,168	3.53%	1,133		0.00717	3.6030	YES
R&B - Disp	6.11%	81,168						
R&B - Disp	5.97%	519	0.00%	22		0.05159	1.1579	YES
R&B - Disp	5.97%	519						
R&B (POTS) excl SB Or	6.15%	80,517	0.00%	700		0.00912	6.7452	YES
R&B (POTS) excl SB Or			0.00%	1,096				
R&B (POTS) excl SB Or	6.36%	472	0.00%	56		0.03448	1.8433	YES
R&B (POTS) excl SB Or			0.00%	11				
Design	3.17%	5,863	2.35%	85		0.01915	0.4280	YES
Design	0.48%	3,352						
Design	0.00%	28						
Design	0.00%	43						
R&B	6.11%	81,168	0.00%	13		0.06644	0.9199	YES
R&B	3.59%	717,054	0.00%	12		0.05368	0.6681	YES
R&B	5.97%	519	0.00%	6		0.09731	0.6138	YES
R&B	0.21%	471						
R&B (POTS)	6.15%	80,517						
R&B (POTS)	3.60%	714,657						
R&B (POTS)	6.36%	472						
R&B (POTS)	1.49%	67						
R&B (POTS)	6.15%	80,517						
R&B (POTS)	3.60%	714,657						
R&B (POTS)	6.36%	472						
R&B (POTS)	1.49%	67						
Digital Loop < DS1	0.00%	598	7.26%	813		0.00000		NO
Digital Loop < DS1	0.00%	9						
Digital Loop < DS1								
Digital Loop < DS1								
Digital Loop >= DS1	0.00%	76	11.71%	222		0.00000		NO
Digital Loop >= DS1	0.00%	16						
Digital Loop >= DS1								
Digital Loop >= DS1								
R&B (POTS)	6.62	42,725			39.237			
R&B (POTS)	1.50	497,006			8.558			

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 221 1 2 1	P-5 Switch Ports/>=10 circuits/Dispatch/FL(hours)	R&B (POTS)	7 56	269		23 038			
B 221 1 2 2	P-5 Switch Ports/>=10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	1 06	15		1 206			
B 221 2 1 1	P-5 Local Interoffice Transport/<10 circuits/Dispatch/FL(hours)	DS1/ DS3 - Interoffice							
B 221 2 1 2	P-5 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(hours)	DS1/ DS3 - Interoffice							
B 221 2 2 1	P-5 Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)	DS1/ DS3 - Interoffice							
B 221 2 2 2	P-5 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)	DS1/ DS3 - Interoffice							
B 221 3 1 1	P-5 Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)	R&B	6 65	43,194	1 27	227	39 186	2 60767	2 0631
B 221 3 1 2	P-5 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)	R&B	1 50	498,627	0 89	6,883	8 574	0 10405	5 8723
B 221 3 1 3	P-5 Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)	R&B	1 97	262,221	0 84	2,676	10 345	0 20099	5 6613
B 221 3 1 4	P-5 Loop + Port Combinations/<10 circuits/Dispatch In/FL(hours)	R&B	0 97	236,324	0 92	4,207	5 962	0 09273	0 5250
B 221 3 2 1	P-5 Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)	R&B	7 34	295	0 99	7	22 380	8 55842	0 7417
B 221 3 2 2	P-5 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)	R&B	1 84	186	0 75	4	3 143	1 58811	0 6870
B 221 3 2 3	P-5 Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)	R&B	2 76	49	0 42	1	4 124	4 16547	0 5620
B 221 3 2 4	P-5 Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)	R&B	1 51	137	0 86	3	2 656	1 55020	0 4206
B 221 4 1 1	P-5 Combo Other/<10 circuits/Dispatch/FL(hours)	R&B&D - Disp	16 44	46,429			139 220		
B 221 4 1 4	P-5 Combo Other/<10 circuits/Dispatch In/FL(hours)	R&B&D - Disp							
B 221 4 2 1	P-5 Combo Other/>=10 circuits/Dispatch/FL(hours)	R&B&D - Disp	7 20	301			22 194		
B 221 4 2 4	P-5 Combo Other/>=10 circuits/Dispatch In/FL(hours)	R&B&D - Disp							
B 221 5 1 1	P-5 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)	ADSL to Retail	14 80	13,392			40 665		
B 221 5 1 2	P-5 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	2 56	555			21 642		
B 221 5 2 1	P-5 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)	ADSL to Retail	13 67	7			35 164		
B 221 5 2 2	P-5 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail							
B 221 6 1 1	P-5 UNE ISDN/<10 circuits/Dispatch/FL(hours)	ISDN - BRI	35 47	316	0 02	1	73 684	73 80020	0 4803
B 221 6 1 2	P-5 UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)	ISDN - BRI	5 74	393			24 125		
B 221 6 2 1	P-5 UNE ISDN/>=10 circuits/Dispatch/FL(hours)	ISDN - BRI							
B 221 6 2 2	P-5 UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours)	ISDN - BRI							
B 221 7 1 1	P-5 Line Sharing/<10 circuits/Dispatch/FL(hours)	ADSL to Retail	14 80	13,392			40 665		
B 221 7 1 2	P-5 Line Sharing/<10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	2 56	555			21 642		
B 221 7 2 1	P-5 Line Sharing/>=10 circuits/Dispatch/FL(hours)	ADSL to Retail	13 67	7			35 164		
B 221 7 2 2	P-5 Line Sharing/>=10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail							
B 221 8 1 1	P-5 2W Analog Loop Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	6 65	43,194	20 02	125	39 186	3 50994	-3 8112
B 221 8 1 2	P-5 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6 65	43,194			39 186		
B 221 8 2 1	P-5 2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	7 34	295	53 29	3	22 380	12 98634	-3 5384
B 221 8 2 2	P-5 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	7 34	295			22 380		
B 221 9 1 1	P-5 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	6 62	42,696	1 05	389	39 237	1 99844	2 7888
B 221 9 1 4	P-5 2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0 96	235,382	0 01	2	5 927	4 19097	0 2274
B 221 9 2 1	P-5 2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7 56	269	2 38	6	23 038	9 50937	0 5446
B 221 9 2 4	P-5 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	1 13	11			1 417		
B 221 10 1 1	P-5 2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	6 65	43,194			39 186		
B 221 10 1 2	P-5 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6 65	43,194			39 186		
B 221 10 2 1	P-5 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	7 34	295			22 380		
B 221 10 2 2	P-5 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	7 34	295			22 380		
B 221 11 1 1	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	6 62	42,696			39 237		
B 221 11 1 4	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0 96	235,382			5 927		
B 221 11 2 1	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7 56	269			23 038		
B 221 11 2 4	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	1 13	11			1 417		
B 221 12 1 2	P-5 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	6 65	43,194	20 63	277	39 186	2 36198	-5 9204
B 221 12 1 2	P-5 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	6 65	43,194			39 186		
B 221 12 2 1	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	7 34	295	11 31	11	22 380	6 87233	-0 5774
B 221 12 2 2	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	7 34	295			22 380		
B 221 13 1 1	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	6 62	42,696	13 53	276	39 237	2 36942	-2 9149
B 221 13 1 4	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0 96	235,382			5 927		
B 221 13 2 1	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	7 56	269	43 94	5	23 038	10 39804	-3 4989
B 221 13 2 4	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	1 13	11			1 417		
B 221 14 1 1	P-5 Other Design/<10 circuits/Dispatch/FL(hours)	Design	145 96	3,264			465 613		
B 221 14 1 2	P-5 Other Design/<10 circuits/Non-Dispatch/FL(hours)	Design	694 98	285			2082 695		
B 221 14 2 1	P-5 Other Design/>=10 circuits/Dispatch/FL(hours)	Design	0 13	6			0 174		
B 221 14 2 2	P-5 Other Design/>=10 circuits/Non-Dispatch/FL(hours)	Design	3 99	54			4 475		
B 221 15 1 1	P-5 Other Non-Design/<10 circuits/Dispatch/FL(hours)	R&B	6 65	43,194			39 186		
B 221 15 1 2	P-5 Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)	R&B	1 50	498,627			8 574		
B 221 15 2 1	P-5 Other Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B	7 34	295			22 380		

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 22 10 2 1	P-5 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 10 2 2	P-5 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 11 1 1	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic		19.51	6				Diagnostic
B 2 22 11 1 4	P-5 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)	Diagnostic		19.90	1				Diagnostic
B 2 22 11 2 1	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 11 2 4	P-5 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	Diagnostic							Diagnostic
B 2 22 12 1 1	P-5 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)	Diagnostic		42.51	180				Diagnostic
B 2 22 12 1 2	P-5 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 12 2 1	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic		91.21	8				Diagnostic
B 2 22 12 2 2	P-5 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 13 1 1	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic		30.00	237				Diagnostic
B 2 22 13 1 4	P-5 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)	Diagnostic							Diagnostic
B 2 22 13 2 1	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic		34.41	10				Diagnostic
B 2 22 13 2 4	P-5 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)	Diagnostic							Diagnostic
B 2 22 14 1 1	P-5 Other Design/<10 circuits/Dispatch/FL(hours)	Diagnostic		38.18	27				Diagnostic
B 2 22 14 1 2	P-5 Other Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 14 2 1	P-5 Other Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 14 2 2	P-5 Other Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 15 1 1	P-5 Other Non-Design/<10 circuits/Dispatch/FL(hours)	Diagnostic		40.07	16				Diagnostic
B 2 22 15 1 2	P-5 Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic		42.28	2				Diagnostic
B 2 22 15 2 1	P-5 Other Non-Design/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 15 2 2	P-5 Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 16 1 1	P-5 INP (Standalone)/<10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 16 1 2	P-5 INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 16 2 1	P-5 INP (Standalone)/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 16 2 2	P-5 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 17 1 1	P-5 LNP (Standalone)/<10 circuits/Dispatch/FL(hours)	Diagnostic		28.03	61				Diagnostic
B 2 22 17 1 2	P-5 LNP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic		27.91	788				Diagnostic
B 2 22 17 2 1	P-5 LNP (Standalone)/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 17 2 2	P-5 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic		38.01	6				Diagnostic
B 2 22 18 1 1	P-5 Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)	Diagnostic		63.86	523				Diagnostic
B 2 22 18 1 2	P-5 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 18 2 1	P-5 Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 18 2 2	P-5 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 19 1 1	P-5 Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)	Diagnostic		56.54	126				Diagnostic
B 2 22 19 1 2	P-5 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 19 2 1	P-5 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)	Diagnostic							Diagnostic
B 2 22 19 2 2	P-5 Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic							Diagnostic
Total Service Order Cycle Time - Mechanized									
B 2 24 1 1 1	P-10 Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 1 1 2	P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 1 2 1	P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 1 2 2	P-10 Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 2 1 1	P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 2 1 2	P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 2 2 1	P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 2 2 2	P-10 Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 3 1 1	P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic		3.33	61				Diagnostic
B 2 24 3 1 2	P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic		1.10	3,761				Diagnostic
B 2 24 3 2 1	P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic		3.00	2				Diagnostic
B 2 24 3 2 2	P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic		3.00	2				Diagnostic
B 2 24 4 1 1	P-10 Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 4 1 2	P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 4 2 1	P-10 Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 4 2 2	P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 5 1 1	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 5 1 2	P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 5 2 1	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 5 2 2	P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							Diagnostic
B 2 24 6 1 1	P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic							Diagnostic

BellSouth Monthly State Summary Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 2 25 17 1 2	P-14	LNP (Standalone)<10 circuits/Non-Dispatch/FL(days)			6 39	482				Diagnostic
B 2 25 17 2 1	P-14	LNP (Standalone)>=10 circuits/Dispatch/FL(days)			6 00	1				Diagnostic
B 2 25 17 2 2	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/FL(days)			2 00	1				Diagnostic
B 2 25 18 1 1	P-10	Digital Loop < DS1<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 25 18 1 2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 25 18 2 1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 25 18 2 2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 25 19 1 1	P-10	Digital Loop >= DS1<10 circuits/Dispatch/FL(days)			8 08	13				Diagnostic
B 2 25 19 1 2	P-10	Digital Loop >= DS1<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 25 19 2 1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 25 19 2 2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
		Total Service Order Cycle Time - Non-Mechanized								
B 2 26 1 1 1	P-10	Switch Ports<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 1 1 2	P-10	Switch Ports<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 1 2 1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 1 2 2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 2 1 1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)			25 14	7				Diagnostic
B 2 26 2 1 2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 2 2 1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 2 2 2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 3 1 1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)			4 97	35				Diagnostic
B 2 26 3 1 2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)			2 94	739				Diagnostic
B 2 26 3 2 1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 3 2 2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 4 1 1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 4 1 2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 4 2 1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 4 2 2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 5 1 1	P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/FL(days)			7 91	116				Diagnostic
B 2 26 5 1 2	P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 5 2 1	P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 5 2 2	P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 6 1 1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)			12 86	258				Diagnostic
B 2 26 6 1 2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 6 2 1	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 6 2 2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 7 1 1	P-10	Line Sharnng/<10 circuits/Dispatch/FL(days)			6 50	4				Diagnostic
B 2 26 7 1 2	P-10	Line Sharnng/<10 circuits/Non-Dispatch/FL(days)			6 05	56				Diagnostic
B 2 26 7 2 1	P-10	Line Sharnng/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 7 2 2	P-10	Line Sharnng/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 8 1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)			10 38	29				Diagnostic
B 2 26 8 1 2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 8 2 1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)			19 00	1				Diagnostic
B 2 26 8 2 2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 9 1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)			7 66	50				Diagnostic
B 2 26 9 1 2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 9 2 1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)			9 50	2				Diagnostic
B 2 26 9 2 2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 10 1 1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)			6 50	2				Diagnostic
B 2 26 10 1 2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 10 2 1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 10 2 2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 11 1 1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)			8 40	5				Diagnostic
B 2 26 11 1 2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)			5 00	1				Diagnostic
B 2 26 11 2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)								Diagnostic
B 2 26 11 2 2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 12 1 1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)			8 65	55				Diagnostic
B 2 26 12 1 2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)								Diagnostic
B 2 26 12 2 1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)			8 00	1				Diagnostic
B 2 26 12 2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)								Diagnostic

BellSouth Monthly State Summary
Florida, July 2001

Benchmark / Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

% Cooperative Test Attempts for xDSL

B 2 33 1	P-8	xDSL (ADSL, HDSL and UCL)/FL(%)
B 2 33 2	P-8	xDSL Other/FL(%)

>= 95% of requests
>= 95% of requests

	97 31%	409					YES
--	--------	-----	--	--	--	--	-----

Service Order Accuracy

B 2 34 1 1 1	P-11	Design (Specials)/<10 circuits/Dispatch/FL(%)
B 2 34 1 1 2	P-11	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)
B 2 34 1 2 1	P-11	Design (Specials)/>=10 circuits/Dispatch/FL(%)
B 2 34 1 2 2	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)
B 2 34 2 1 1	P-11	Loops Non-Design/<10 circuits/Dispatch/FL(%)
B 2 34 2 1 2	P-11	Loops Non-Design/<10 circuits/Non-Dispatch/FL(%)
B 2 34 2 2 1	P-11	Loops Non-Design/>=10 circuits/Dispatch/FL(%)
B 2 34 2 2 2	P-11	Loops Non-Design/>=10 circuits/Non-Dispatch/FL(%)

>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%
>= 95%

	62 30%	61					NO
	60 20%	98					NO
	50 00%	2					NO
	0 00%	4					NO
	60 78%	51					NO
	100 00%	3					YES
	0 00%	2					NO

Unbundled Network Elements - Maintenance and Repair

Missed Repair Appointments

B 3 1 1 1	M&R-1	Switch Ports/Dispatch/FL(%)
B 3 1 1 2	M&R-1	Switch Ports/Non-Dispatch/FL(%)
B 3 1 2 1	M&R-1	Local Interoffice Transport/Dispatch/FL(%)
B 3 1 2 2	M&R-1	Local Interoffice Transport/Non-Dispatch/FL(%)
B 3 1 3 1	M&R-1	Loop + Port Combinations/Dispatch/FL(%)
B 3 1 3 2	M&R-1	Loop + Port Combinations/Non-Dispatch/FL(%)
B 3 1 4 1	M&R-1	Combo Other/Dispatch/FL(%)
B 3 1 4 2	M&R-1	Combo Other/Non-Dispatch/FL(%)
B 3 1 5 1	M&R-1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)
B 3 1 5 2	M&R-1	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)
B 3 1 6 1	M&R-1	UNE ISDN/Dispatch/FL(%)
B 3 1 6 2	M&R-1	UNE ISDN/Non-Dispatch/FL(%)
B 3 1 7 1	M&R-1	Line Sharing/Dispatch/FL(%)
B 3 1 7 2	M&R-1	Line Sharing/Non-Dispatch/FL(%)
B 3 1 8 1	M&R-1	2W Analog Loop Design/Dispatch/FL(%)
B 3 1 8 2	M&R-1	2W Analog Loop Design/Non-Dispatch/FL(%)
B 3 1 9 1	M&R-1	2W Analog Loop Non-Design/Dispatch/FL(%)
B 3 1 9 2	M&R-1	2W Analog Loop Non-Design/Non-Dispatch/FL(%)
B 3 1 10 1	M&R-1	Other Design/Dispatch/FL(%)
B 3 1 10 2	M&R-1	Other Design/Non-Dispatch/FL(%)
B 3 1 11 1	M&R-1	Other Non-Design/Dispatch/FL(%)
B 3 1 11 2	M&R-1	Other Non-Design/Non-Dispatch/FL(%)
B 3 1 12 1	M&R-1	LNP (Standalone)/Dispatch/FL(%)
B 3 1 12 2	M&R-1	LNP (Standalone)/Non-Dispatch/FL(%)

R&B (POTS)
R&B (POTS)
DS1/DS3
DS1/DS3
R&B
R&B
R&B&D - Disp
R&B&D - Disp
ADSL to Retail
ADSL to Retail
ISDN - BRI
ISDN - BRI
ADSL to Retail
ADSL to Retail
R&B - Disp
R&B - Disp
R&B (POTS) excl SB FT
R&B (POTS) excl SB FT
Design
Design
R&B
R&B
R&B (POTS)
R&B (POTS)

12 67%	99,788							
2 13%	56,786							
1 19%	1,093	0 00%	2			0 07673	0 1550	YES
0 00%	702	0 00%	8			0 00000		YES
12 83%	101,333	7 40%	1,271			0 00944	5 7537	YES
2 22%	57,756	1 41%	567			0 00622	1 3031	YES
12 47%	104,233	0 00%	11			0 09961	1 2517	YES
12 47%	104,233	0 00%	6			0 13487	0 9244	YES
19 51%	1,148	7 46%	67			0 04981	2 4192	YES
2 10%	381	0 00%	14			0 03902	0 5382	YES
6 09%	361	11 86%	118			0 02537	-2 2747	NO
3 09%	291	2 63%	76			0 02230	0 2068	YES
19 51%	1,148	40 00%	10			0 12586	-1 6278	YES
2 10%	381	16 22%	37			0 02469	-5 7178	NO
12 83%	101,333	5 84%	1,216			0 00965	7 2434	YES
12 83%	101,333	3 01%	299			0 01937	5 0686	YES
12 61%	99,503	10 82%	638			0 01319	1 3647	YES
1 81%	47,644	3 57%	28			0 02521	-0 6982	YES
2 70%	3,185	0 00%	15			0 04195	0 6437	YES
0 84%	3,340	0 00%	7			0 03450	0 2430	YES
12 83%	101,333	15 22%	46			0 04931	-0 4849	YES
2 22%	57,756	4 88%	41			0 02302	-1 1538	YES
12 67%	99,788							
2 13%	56,786							

Customer Trouble Report Rate

B 3 2 1 1	M&R-2	Switch Ports/Dispatch/FL(%)
B 3 2 1 2	M&R-2	Switch Ports/Non-Dispatch/FL(%)
B 3 2 2 1	M&R-2	Local Interoffice Transport/Dispatch/FL(%)
B 3 2 2 2	M&R-2	Local Interoffice Transport/Non-Dispatch/FL(%)
B 3 2 3 1	M&R-2	Loop + Port Combinations/Dispatch/FL(%)
B 3 2 3 2	M&R-2	Loop + Port Combinations/Non-Dispatch/FL(%)
B 3 2 4 1	M&R-2	Combo Other/Dispatch/FL(%)
B 3 2 4 2	M&R-2	Combo Other/Non-Dispatch/FL(%)
B 3 2 5 1	M&R-2	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)
B 3 2 5 2	M&R-2	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)
B 3 2 6 1	M&R-2	UNE ISDN/Dispatch/FL(%)
B 3 2 6 2	M&R-2	UNE ISDN/Non-Dispatch/FL(%)
B 3 2 7 1	M&R-2	Line Sharing/Dispatch/FL(%)
B 3 2 7 2	M&R-2	Line Sharing/Non-Dispatch/FL(%)
B 3 2 8 1	M&R-2	2W Analog Loop Design/Dispatch/FL(%)
B 3 2 8 2	M&R-2	2W Analog Loop Design/Non-Dispatch/FL(%)
B 3 2 9 1	M&R-2	2W Analog Loop Non-Design/Dispatch/FL(%)
B 3 2 9 2	M&R-2	2W Analog Loop Non-Design/Non-Dispatch/FL(%)

R&B (POTS)
R&B (POTS)
DS1/DS3
DS1/DS3
R&B
R&B
R&B&D - Disp
R&B&D - Disp
ADSL to Retail
ADSL to Retail
ISDN - BRI
ISDN - BRI
ADSL to Retail
ADSL to Retail
R&B - Disp
R&B - Disp
R&B (POTS) excl SB FT
R&B (POTS) excl SB FT

1 73%	5,784,806	0 00%	2			0 09287	0 1857	YES
0 98%	5,784,806	0 00%	2			0 07006	0 1401	YES
2 19%	49,965	0 17%	1,144			0 00442	4 5509	YES
1 40%	49,965	0 70%	1,144			0 00354	1 9910	YES
1 64%	6,161,701	1 19%	107,094			0 00040	11 5811	YES
0 94%	6,161,701	0 53%	107,094			0 00030	13 6692	YES
1 49%	6,985,135	0 88%	1,249			0 00346	1 7690	YES
1 49%	6,985,135	0 48%	1,249			0 00346	2 9271	YES
0 56%	204,581	1 14%	5,902			0 00099	-5 8042	NO
0 19%	204,581	0 24%	5,902			0 00057	-0 8946	YES
1 42%	25,389	1 95%	5,954			0 00172	-3 2614	NO
1 15%	25,389	1 28%	5,954			0 00154	-0 8452	YES
0 56%	204,581	1 13%	884			0 00252	-2 2578	NO
0 19%	204,581	4 19%	884			0 00145	-27 4943	NO
1 64%	6,161,701	1 64%	74,301			0 00047	0 1685	YES
1 64%	6,161,701	0 40%	74,301			0 00047	26 2447	YES
1 72%	5,784,806	1 73%	36,891			0 00069	-0 1364	YES
0 82%	5,784,806	0 08%	36,891			0 00047	15 7743	YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 3 2 1 0 1	M&R-2 Other Design/Dispatch/FL(%)	Design	0.39%	823,434	0.76%	1,972	0.00140	-2.6662	NO
B 3 2 1 0 2	M&R-2 Other Design/Non-Dispatch/FL(%)	Design	0.41%	823,434	0.35%	1,972	0.00144	0.3527	YES
B 3 2 1 1 1	M&R-2 Other Non-Design/Dispatch/FL(%)	R&B	1.64%	6,161,701	6.50%	708	0.00482	-10.0680	NO
B 3 2 1 1 2	M&R-2 Other Non-Design/Non-Dispatch/FL(%)	R&B	0.94%	6,161,701	5.79%	708	0.00364	-13.3386	NO
B 3 2 1 2 1	M&R-2 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	1.73%	5,784,806					
B 3 2 1 2 2	M&R-2 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	0.98%	5,784,806					
Maintenance Average Duration									
B 3 3 1 1	M&R-3 Switch Ports/Dispatch/FL(hours)	R&B (POTS)	24.03	99,788			24.742		
B 3 3 1 2	M&R-3 Switch Ports/Non-Dispatch/FL(hours)	R&B (POTS)	8.73	56,786			13.644		
B 3 3 2 1	M&R-3 Local Interoffice Transport/Dispatch/FL(hours)	DS1/DS3	4.00	1,093	4.88	2	2.91375	-0.3006	YES
B 3 3 2 2	M&R-3 Local Interoffice Transport/Non-Dispatch/FL(hours)	DS1/DS3	1.96	702	5.39	8	2.552	0.90739	-3.7860
B 3 3 3 1	M&R-3 Loop + Port Combinations/Dispatch/FL(hours)	R&B	23.99	101,333	16.76	1,271	24.769	0.69910	10.3419
B 3 3 3 2	M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours)	R&B	8.67	57,756	3.62	567	13.593	0.57365	8.7904
B 3 3 4 1	M&R-3 Combo Other/Dispatch/FL(hours)	R&B&D - Disp	23.43	104,233	6.55	11	25.425	7.66645	2.2017
B 3 3 4 2	M&R-3 Combo Other/Non-Dispatch/FL(hours)	R&B&D - Disp	23.43	104,233	4.39	6	25.425	10.36017	1.8347
B 3 3 5 1	M&R-3 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours)	ADSL to Retail	71.47	1,148	9.16	67	48.912	6.14747	10.1358
B 3 3 5 2	M&R-3 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(hours)	ADSL to Retail	30.53	381	2.97	14	43.729	11.89985	2.3156
B 3 3 6 1	M&R-3 UNE ISDN/Dispatch/FL(hours)	ISDN - BRI	8.03	361	11.22	118	9.691	1.02759	-3.1060
B 3 3 6 2	M&R-3 UNE ISDN/Non-Dispatch/FL(hours)	ISDN - BRI	3.48	291	5.59	76	9.230	1.18904	-1.7772
B 3 3 7 1	M&R-3 Line Sharing/Dispatch/FL(hours)	ADSL to Retail	71.47	1,148	55.36	10	48.912	15.53458	1.0371
B 3 3 7 2	M&R-3 Line Sharing/Non-Dispatch/FL(hours)	ADSL to Retail	30.53	381	22.60	37	43.729	7.52999	1.0521
B 3 3 8 1	M&R-3 2W Analog Loop Design/Dispatch/FL(hours)	R&B - Disp	23.99	101,333	8.60	1,216	24.769	0.71454	21.5405
B 3 3 8 2	M&R-3 2W Analog Loop Design/Non-Dispatch/FL(hours)	R&B - Disp	23.99	101,333	5.15	299	24.769	1.43453	13.1303
B 3 3 9 1	M&R-3 2W Analog Loop Non-Design/Dispatch/FL(hours)	R&B (POTS) excl SB FT	24.02	99,503	17.34	638	24.736	0.96245	8.7956
B 3 3 9 2	M&R-3 2W Analog Loop Non-Design/Non-Dispatch/FL(hours)	R&B (POTS) excl SB FT	8.91	47,644	7.91	28	13.962	2.63931	0.3792
B 3 3 10 1	M&R-3 Other Design/Dispatch/FL(hours)	Design	6.20	3,185	5.35	15	37.164	9.61817	0.0882
B 3 3 10 2	M&R-3 Other Design/Non-Dispatch/FL(hours)	Design	2.69	3,340	5.89	7	21.881	8.27886	-0.3872
B 3 3 11 1	M&R-3 Other Non-Design/Dispatch/FL(hours)	R&B	23.99	101,333	29.51	46	24.769	3.65279	-1.5116
B 3 3 11 2	M&R-3 Other Non-Design/Non-Dispatch/FL(hours)	R&B	8.67	57,756	7.53	41	13.593	2.12362	0.5365
B 3 3 12 1	M&R-3 LNP (Standalone)/Dispatch/FL(hours)	R&B (POTS)	24.03	99,788			24.742		
B 3 3 12 2	M&R-3 LNP (Standalone)/Non-Dispatch/FL(hours)	R&B (POTS)	8.73	56,786			13.644		
% Repeat Troubles within 30 Days									
B 3 4 1 1	M&R-4 Switch Ports/Dispatch/FL(%)	R&B (POTS)	22.41%	99,788					
B 3 4 1 2	M&R-4 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	19.62%	56,786					
B 3 4 2 1	M&R-4 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	37.60%	1,093	50.00%	2		0.34283	-0.3616
B 3 4 2 2	M&R-4 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	34.62%	702	37.50%	8		0.16916	-0.1705
B 3 4 3 1	M&R-4 Loop + Port Combinations/Dispatch/FL(%)	R&B	22.36%	101,333	19.98%	1,271		0.01176	2.0169
B 3 4 3 2	M&R-4 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	19.55%	57,756	21.69%	567		0.01674	-1.2830
B 3 4 4 1	M&R-4 Combo Other/Dispatch/FL(%)	R&B&D - Disp	22.92%	104,233	45.45%	11		0.12674	-1.7778
B 3 4 4 2	M&R-4 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	22.92%	104,233	0.00%	6		0.17161	1.3358
B 3 4 5 1	M&R-4 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	24.91%	1,148	28.36%	67		0.05436	-0.6338
B 3 4 5 2	M&R-4 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	26.77%	381	7.14%	14		0.12049	1.6291
B 3 4 6 1	M&R-4 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	32.41%	361	26.27%	118		0.04963	1.2369
B 3 4 6 2	M&R-4 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	27.49%	291	38.16%	76		0.05751	-1.8546
B 3 4 7 1	M&R-4 Line Sharing/Dispatch/FL(%)	ADSL to Retail	24.91%	1,148	50.00%	10		0.13737	-1.8263
B 3 4 7 2	M&R-4 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	26.77%	381	40.54%	37		0.07624	-1.8059
B 3 4 8 1	M&R-4 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	22.36%	101,333	19.82%	1,216		0.01202	2.1107
B 3 4 8 2	M&R-4 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	22.36%	101,333	26.42%	299		0.02413	-1.6848
B 3 4 9 1	M&R-4 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	22.39%	99,503	19.44%	638		0.01656	1.7816
B 3 4 9 2	M&R-4 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	19.87%	47,644	21.43%	28		0.07543	-0.2063
B 3 4 10 1	M&R-4 Other Design/Dispatch/FL(%)	Design	41.79%	3,185	20.00%	15		0.12765	1.7070
B 3 4 10 2	M&R-4 Other Design/Non-Dispatch/FL(%)	Design	38.86%	3,340	42.86%	7		0.18443	-0.2166
B 3 4 11 1	M&R-4 Other Non-Design/Dispatch/FL(%)	R&B	22.36%	101,333	28.26%	46		0.06144	-0.9610
B 3 4 11 2	M&R-4 Other Non-Design/Non-Dispatch/FL(%)	R&B	19.55%	57,756	21.95%	41		0.06195	-0.3882
B 3 4 12 1	M&R-4 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	22.41%	99,788					
B 3 4 12 2	M&R-4 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	19.62%	56,786					
Out of Service > 24 hours									
B 3 5 1 1	M&R-5 Switch Ports/Dispatch/FL(%)	R&B (POTS)	26.91%	66,154					
B 3 5 1 2	M&R-5 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	10.86%	18,520					

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 3 5 2 1	M&R-5	Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1 19%	1,093	0.00%	2	0 07673	0 1550	YES
B 3 5 2 2	M&R-5	Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0 00%	702	0 00%	8	0 00000		YES
B 3 5 3 1	M&R-5	Loop + Port Combinations/Dispatch/FL(%)	R&B	26 88%	67,212	13 84%	831	0 01547	8 4299	YES
B 3 5 3 2	M&R-5	Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	10 80%	18,938	2 37%	253	0 01965	4 2920	YES
B 3 5 4 1	M&R-5	Combo Other/Dispatch/FL(%)	R&B&D - Disp	25 77%	70,367	0 00%	11	0 13189	1 9542	YES
B 3 5 4 2	M&R-5	Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	25 77%	70,367	0 00%	6	0 17857	1,4433	YES
B 3 5 5 1	M&R-5	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	100.00%	2	7.46%	67	0 00000		YES
B 3 5 5 2	M&R-5	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail			0 00%	14			
B 3 5 6 1	M&R-5	UNE ISDN/Dispatch/FL(%)	ISDN - BRI	5 83%	360	11 86%	118	0 02486	-2 4259	NO
B 3 5 6 2	M&R-5	UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	3 09%	291	2 63%	76	0 02230	0 2068	YES
B 3 5 7 1	M&R-5	Line Sharing/Dispatch/FL(%)	ADSL to Retail	100 00%	2	100 00%	1	0 00000		YES
B 3 5 7 2	M&R-5	Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail							
B 3 5 8 1	M&R-5	2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	26 88%	67,212	5 84%	1,216	0 01283	16 4046	YES
B 3 5 8 2	M&R-5	2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	26 88%	67,212	3 01%	299	0 02570	9 2905	YES
B 3 5 9 1	M&R-5	2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	26 90%	66,124	16 87%	498	0 01994	5 0283	YES
B 3 5 9 2	M&R-5	2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	10 77%	18,415	10 53%	19	0 07115	0 0340	YES
B 3 5 10 1	M&R-5	Other Design/Dispatch/FL(%)	Design	2 70%	3,185	0.00%	15	0 04195	0 6437	YES
B 3 5 10 2	M&R-5	Other Design/Non-Dispatch/FL(%)	Design	0 81%	3,340	0 00%	7	0 03388	0 2386	YES
B 3 5 11 1	M&R-5	Other Non-Design/Dispatch/FL(%)	R&B	26 88%	67,212	44 00%	25	0 08869	-1 9300	NO
B 3 5 11 2	M&R-5	Other Non-Design/Non-Dispatch/FL(%)	R&B	10 80%	18,938	11.11%	18	0 07320	-0.0420	YES
B 3 5 12 1	M&R-5	LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	26 91%	66,154					
B 3 5 12 2	M&R-5	LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	10 86%	18,520					

Unbundled Network Elements - Billing

			BST - State							
B 4 1	Invoice Accuracy									
	B-1	FL(%)		98 79%	\$493,225,276	99 74%	\$4,856,831	0 00005	-190 9581	YES
B 4 2	Mean Time to Deliver Invoices - CRIS									
	B-2	Region(business days)	BST - Region	5 04	1	3 46	1,191			YES

BellSouth Monthly State Summary
Florida, July 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
Local Interconnection Trunks - Ordering										
% Rejected Service Requests										
C 11	O-7 Local Interconnection Trunks/FL(%)	Diagnostic		59.90%	202				Diagnostic	
Reject Interval										
C 12	O-8 Local Interconnection Trunks/FL(%)	>= 85% w in 4 days		93.39%	113				YES	
FOC Timeliness										
C 13	O-9 Local Interconnection Trunks/FL(%)	>= 95% w in 10 days		95.30%	154				YES	
FOC & Reject Response Completeness										
C 14	O-11 Local Interconnection Trunks/FL(%)	>= 95%		95.35%	172				YES	
C 15	O-11 Local Interconnection Trunks/FL(%)	>= 95%								
Local Interconnection Trunks - Provisioning										
Order Completion Interval										
C 21	P-4 Local Interconnection Trunks/FL(days)	Parity w Retail	20.82	82	24.92	37	25.068	4.96451	-0.8262	YES
Held Orders										
C 22	P-1 Local Interconnection Trunks/FL(days)	Parity w Retail	Not Applicable for Trunks							
% Jeopardies										
C 23	P-2 Local Interconnection Trunks/FL(%)	Parity w Retail	Not Applicable for Trunks							
Average Jeopardy Notice Interval										
C 24	P-2 Local Interconnection Trunks/FL(hours)	95% >= 48 hrs	Not Applicable for Trunks							
% Missed Installation Appointments										
C 25	P-3 Local Interconnection Trunks/FL(%)	Parity w Retail	1.20%	83	2.13%	47		0.01992	-0.4634	YES
% Provisioning Troubles within 30 Days										
C 26	P-9 Local Interconnection Trunks/FL(%)	Parity w Retail	2.76%	10,432	0.00%	3,040		0.00308	8.1752	YES
Average Completion Notice Interval										
C 27	P-5 Local Interconnection Trunks/FL(hours)	Parity w Retail	150.59	57	114.13	30	364.436	82.20202	0.4436	YES
Total Service Order Cycle Time										
C 28	P-10 Local Interconnection Trunks/FL(days)	Diagnostic	Under development							
Total Service Order Cycle Time (offered)										
C 29	P-10 Local Interconnection Trunks/FL(days)	Diagnostic	Under development							
% Completions w/o Notice or < 24 hours										
C 210 1	P-6 Local Interconnection Trunks/Dispatch/FL(%)	Diagnostic		100.00%	37					Diagnostic
C 210 2	P-6 Local Interconnection Trunks/Non-Dispatch/FL(%)	Diagnostic								Diagnostic
1st Accuracy										
C 211 1 1	P-11 Local Interconnection Trunks/<10 circuits/Dispatch/FL(%)	>= 95%		100.00%	6					YES
C 211 1 2	P-11 Local Interconnection Trunks/<10 circuits/Non-Dispatch/FL(%)	>= 95%		100.00%	3					YES
C 211 2 1	P-11 Local Interconnection Trunks/>=10 circuits/Dispatch/FL(%)	>= 95%		94.12%	17					NO
C 211 2 2	P-11 Local Interconnection Trunks/>=10 circuits/Non-Dispatch/FL(%)	>= 95%		100.00%	20					YES
Local Interconnection Trunks - Maintenance and Repair										
Missed Repair Appointments										
C 311	M&R-1 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	33.33%	3	0.00%	1		0.54433	0.6124	YES
C 312	M&R-1 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.37%	270	0.00%	79		0.00777	0.4767	YES
Customer Trouble Report Rate										
C 321	M&R-2 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0.00%	363,217	0.00%	133,484		0.00001	0.0835	YES
C 322	M&R-2 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0.07%	363,217	0.06%	133,484		0.00009	1.7364	YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Maintenance Average Duration										
C 3 3 1	M&R-3	Local Interconnection Trunks/Dispatch/FL(hours)	20 18	3	5 23	1	28 359	32 74638	0 4565	YES
C 3 3 2	M&R-3	Local Interconnection Trunks/Non-Dispatch/FL(hours)	0 97	270	0 26	79	1 933	0 24719	2 8926	YES
% Repeat Troubles within 30 Days										
C 3 4 1	M&R-4	Local Interconnection Trunks/Dispatch/FL(%)	33 33%	3	100 00%	1		0 54433	-1 2247	YES
C 3 4 2	M&R-4	Local Interconnection Trunks/Non-Dispatch/FL(%)	2 59%	270	1 27%	79		0 02033	0 6627	YES
Out of Service > 24 hours										
C 3 5 1	M&R-5	Local Interconnection Trunks/Dispatch/FL(%)	33 33%	3	0 00%	1		0 54433	0 6124	YES
C 3 5 2	M&R-5	Local Interconnection Trunks/Non-Dispatch/FL(%)	0 37%	270	0 00%	79		0 00777	0 4767	YES
Local Interconnection Trunks - Billing										
Invoice Accuracy										
C 4 1	B-1	FL(%)	98.79%	\$493,225,276	99.74%	\$7,500,946		0 00004	-237 2543	YES
Mean Time to Deliver Invoices - CABS										
C 4 2	B-2	Region(calendar days)	5.08	1	4.78	3.199				YES
LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING										
Trunk Group Performance - Aggregate										
C 5 1	TGP-1	FL	>0.5% dif 2 consec. Hrs		0					YES

BellSouth Monthly State Summary
Florida, July 2001

Benchmark / Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

Operations Support Systems - Pre-Ordering

D 1 1 1	OSS-2 EDI/Region(%)	>= 99.5%	100.00%				YES
D 1 1 2	OSS-2 HAL/Region(%)	>= 99.5%	99.78%				YES
D 1 1 3	OSS-2 LENS/Region(%)	>= 99.5%	99.96%				YES
D 1 1 4	OSS-2 LEO MAINFRAME/Region(%)	>= 99.5%	100.00%				YES
D 1 1 5	OSS-2 LEO UNIX/Region(%)	>= 99.5%					
D 1 1 6	OSS-2 LESOG/Region(%)	>= 99.5%	100.00%				YES
D 1 1 7	OSS-2 TAG/Region(%)	>= 99.5%	99.95%				YES
D 1 1 8	OSS-2 PSIMS/Region(%)	>= 99.5%	99.80%				YES

% Interface Availability - BST & CLEC

D 1 2 1	OSS-2 ATLAS/COFFI/Region(%)	>= 99.5%	99.92%				YES
D 1 2 2	OSS-2 BOCRIS/Region(%)	>= 99.5%	99.93%				YES
D 1 2 3	OSS-2 DSAP/Region(%)	>= 99.5%	99.88%				YES
D 1 2 4	OSS-2 RSAG/Region(%)	>= 99.5%	99.91%				YES
D 1 2 5	OSS-2 SOCS/Region(%)	>= 99.5%	99.91%				YES
D 1 2 6	OSS-2 SONGS/Region(%)	>= 99.5%	99.93%				YES
D 1 2 7	OSS-2 DOE/Region(%)	>= 99.5%	100.00%				YES
D 1 2 8	OSS-2 LNP Gateway/Region(%)	>= 99.5%	100.00%				YES
D 1 2 9	OSS-2 COG/Region(%)	>= 99.5%	100.00%				YES
D 1 2 10	OSS-2 DOM/Region(%)	>= 99.5%	100.00%				YES
D 1 2 11	OSS-2 SOG/Region(%)	>= 99.5%	100.00%				YES

Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)

D 1 3 1 1	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	2.88	3,237,125	1.80	246,086	YES
D 1 3 1 2	OSS-1 RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.28	7,266	1.80	246,086	YES
D 1 3 2 1	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	3.00	9,608,635	1.62	156,550	YES
D 1 3 2 2	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	5.57	610,414	1.62	156,550	YES
D 1 3 3 1	OSS-1 ATLAS/Region (seconds)	RNS - ATLAS + 2 Sec			1.07	59,062	
D 1 3 3 2	OSS-1 ATLAS/Region (seconds)	ROS - ATLAS + 2 Sec	2.65	293,697	1.07	59,062	YES
D 1 3 4 1	OSS-1 DSAP/Region (seconds)	RNS - DSAP + 2 Sec			0.53	1,593	
D 1 3 4 2	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.67	294,671	0.53	1,593	YES
D 1 3 5 1	OSS-1 HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.50	5,503,374	13.31	464,511	NO
D 1 3 5 2	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.19	461,218	13.31	464,511	NO
D 1 3 6 1	OSS-1 COFFI/USOC/Region (seconds)	RNS - OASISBIG + 2 Sec	4.25	11,605,575	0.78	34,324	YES
D 1 3 6 2	OSS-1 COFFI/USOC/Region (seconds)	ROS - OASISBIG + 2 Sec	4.41	605,022	0.78	34,324	YES
D 1 3 7 1	OSS-1 PSIMS/ORB/Region (seconds)	RNS - OASISBIG + 2 Sec	4.25	11,605,575	0.09	71,569	YES
D 1 3 7 2	OSS-1 PSIMS/ORB/Region (seconds)	ROS - OASISBIG + 2 Sec	4.41	605,022	0.09	71,569	YES

Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)

D 1 4 1 1	OSS-1 RSAG, by TN/Region (seconds)	RNS - RSAG, by TN + 2 Sec	2.88	3,237,125	2.32	187,104	YES	
D 1 4 1 2	OSS-1 RSAG, by TN/Region (seconds)	ROS - RSAG, by TN + 2 Sec	3.28	7,266	2.32	187,104	YES	
D 1 4 2 1	OSS-1 RSAG, by ADDR/Region (seconds)	RNS - RSAG, by ADDR + 2 Sec	3.00	9,608,635	2.50	72,690	YES	
D 1 4 2 2	OSS-1 RSAG, by ADDR/Region (seconds)	ROS - RSAG, by ADDR + 2 Sec	5.57	610,414	2.50	72,690	YES	
D 1 4 3 1	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic					Diagnostic	
D 1 4 3 2	OSS-1 ATLAS - MLH/Region (seconds)	Diagnostic					Diagnostic	
D 1 4 4 1	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			1.61	4	Diagnostic	
D 1 4 4 2	OSS-1 ATLAS - DID/Region (seconds)	Diagnostic			1.61	4	Diagnostic	
D 1 4 5 1	OSS-1 ATLAS - TN/Region (seconds)	RNS - ATLAS - TN + 2 Sec			1.73	10,074		
D 1 4 5 2	OSS-1 ATLAS - TN/Region (seconds)	ROS - ATLAS - TN + 2 Sec	2.65	293,697	1.73	10,074	YES	
D 1 4 6 1	OSS-1 DSAP/Region (seconds)	RNS - DSAP + 2 Sec			2.28	66,065		
D 1 4 6 2	OSS-1 DSAP/Region (seconds)	ROS - DSAP + 2 Sec	2.67	294,671	2.28	66,065	YES	
D 1 4 7 1	OSS-1 HAL/CRIS/Region (seconds)	RNS - CRSACCTS + 2 Sec	3.50	5,503,374	3.60	133,650	NO	
D 1 4 7 2	OSS-1 HAL/CRIS/Region (seconds)	ROS - CRSOCSR + 2 Sec	3.19	461,218	3.60	133,650	NO	
D 1 4 8 1	OSS-1 RNS/CRSEINT/Region(seconds)	RNS - CRSACCTS + 2 sec	<i>This data not applicable after 5-1-2001; see D.1.4.7.1</i>					
D 1 4 8 2	OSS-1 ROS/CRSEINT/Region(seconds)	ROS - CRSOCSR + 2 sec	<i>This data not applicable after 5-1-2001; see D.1.4.7.2</i>					
D 1 4 9 1	OSS-1 RNS/CRSECSRL/Region(seconds)	RNS - CRSACCTS + 2 sec	<i>This data not applicable after 7-1-2001; see D.1.4.7.1</i>					
D 1 4 9 2	OSS-1 ROS/CRSECSRL/Region(seconds)	ROS - CRSOCSR + 2 sec	<i>This data not applicable after 7-1-2001; see D.1.4.7.2</i>					

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity	
Operations Support Systems - Maintenance and Repair											
% Interface Availability - BST											
D 2 1 1	OSS-3	TAFI/Region(%)	>= 99.5%	99.99%						YES	
% Interface Availability - CLEC											
D 2 2 1	OSS-3	CLEC TAFI/Region(%)	>= 99.5%		100.00%					YES	
D 2 2 2	OSS-3	CLEC ECTA/Region(%)	>= 99.5%		100.00%					YES	
% Interface Availability - BST & CLEC											
D 2 3 1	OSS-3	CRIS/Region(%)	>= 99.5%		99.93%					YES	
D 2 3 2	OSS-3	LMOS HOST/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 3	OSS-3	LNP/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 4	OSS-3	MARCH/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 5	OSS-3	OSPCM/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 6	OSS-3	Predictor/Region(%)	>= 99.5%		100.00%					YES	
D 2 3 7	OSS-3	SOCS/Region(%)	>= 99.5%		99.91%					YES	
Average Response Interval											
D 2 4 1 1	OSS-4	CRIS/Region(%) <= 4 Seconds	Party w Retail	95.82%	1,946,965	95.04%	92,271		0.00067	11,5962	NO
D 2 4 1 2	OSS-4	CRIS/Region(%) <= 10 Seconds	Party w Retail	98.81%	1,946,965	99.19%	92,271		0.00037	-10,4334	YES
D 2 4 1 3	OSS-4	CRIS/Region(%) > 10 Seconds	Party w Retail	1.19%	1,946,965	0.81%	92,271		0.00037	10,4334	YES
D 2 4 2 1	OSS-4	DLETH/Region(%) <= 4 Seconds	Party w Retail	10.69%	47,554	11.48%	932		0.01022	-0.7748	YES
D 2 4 2 2	OSS-4	DLETH/Region(%) <= 10 Seconds	Party w Retail	81.73%	47,554	89.38%	932		0.01278	-5.9848	YES
D 2 4 2 3	OSS-4	DLETH/Region(%) > 10 Seconds	Party w Retail	18.27%	47,554	10.62%	932		0.01278	5.9848	YES
D 2 4 3 1	OSS-4	DLR/Region(%) <= 4 Seconds	Party w Retail	6.56%	35,497	22.13%	29,083		0.00196	-79.5474	YES
D 2 4 3 2	OSS-4	DLR/Region(%) <= 10 Seconds	Party w Retail	88.73%	35,497	98.14%	29,083		0.00250	-37.6471	YES
D 2 4 3 3	OSS-4	DLR/Region(%) > 10 Seconds	Party w Retail	11.27%	35,497	1.86%	29,083		0.00250	37.6471	YES
D 2 4 4 1	OSS-4	LMOS/Region(%) <= 4 Seconds	Party w Retail	99.88%	1,946,930	99.85%	92,311		0.00012	2.3442	NO
D 2 4 4 2	OSS-4	LMOS/Region(%) <= 10 Seconds	Party w Retail	99.94%	1,946,930	99.93%	92,311		0.00008	2.1646	NO
D 2 4 4 3	OSS-4	LMOS/Region(%) > 10 Seconds	Party w Retail	0.06%	1,946,930	0.07%	92,311		0.00008	-2.1646	NO
D 2 4 5 1	OSS-4	LMOSupd/Region(%) <= 4 Seconds	Party w Retail	97.88%	1,463,874	97.31%	56,552		0.00062	9.1600	NO
D 2 4 5 2	OSS-4	LMOSupd/Region(%) <= 10 Seconds	Party w Retail	99.83%	1,463,874	99.67%	56,552		0.00018	9.3826	NO
D 2 4 5 3	OSS-4	LMOSupd/Region(%) > 10 Seconds	Party w Retail	0.17%	1,463,874	0.33%	56,552		0.00018	-9.3826	NO
D 2 4 6 1	OSS-4	LNP/Region(%) <= 4 Seconds	Party w Retail	99.62%	134,687	99.43%	5,486		0.00085	2.2146	NO
D 2 4 6 2	OSS-4	LNP/Region(%) <= 10 Seconds	Party w Retail	99.80%	134,687	99.84%	5,486		0.00061	-0.5469	YES
D 2 4 6 3	OSS-4	LNP/Region(%) > 10 Seconds	Party w Retail	0.20%	134,687	0.16%	5,486		0.00061	0.5469	YES
D 2 4 7 1	OSS-4	MARCH/Region(%) <= 4 Seconds	Party w Retail	29.87%	9,546	31.76%	403		0.02327	-0.8146	YES
D 2 4 7 2	OSS-4	MARCH/Region(%) <= 10 Seconds	Party w Retail	29.87%	9,546	31.76%	403		0.02327	-0.8146	YES
D 2 4 7 3	OSS-4	MARCH/Region(%) > 10 Seconds	Party w Retail	70.13%	9,546	68.24%	403		0.02327	0.8146	YES
D 2 4 8 1	OSS-4	OSPCM/Region(%) <= 4 Seconds	Party w Retail	45.00%	9,365	34.75%	118		0.04609	2.2245	NO
D 2 4 8 2	OSS-4	OSPCM/Region(%) <= 10 Seconds	Party w Retail	97.54%	9,365	96.61%	118		0.01434	0.6513	YES
D 2 4 8 3	OSS-4	OSPCM/Region(%) > 10 Seconds	Party w Retail	2.46%	9,365	3.39%	118		0.01434	-0.6513	YES
D 2 4 9 1	OSS-4	Predictor/Region(%) <= 4 Seconds	Party w Retail	16.89%	77,635	27.50%	3,898		0.00615	-17.2457	YES
D 2 4 9 2	OSS-4	Predictor/Region(%) <= 10 Seconds	Party w Retail	16.89%	77,635	27.50%	3,898		0.00615	-17.2457	YES
D 2 4 9 3	OSS-4	Predictor/Region(%) > 10 Seconds	Party w Retail	83.11%	77,635	72.50%	3,898		0.00615	17.2457	YES
D 2 4 10 1	OSS-4	SOCS/Region(%) <= 4 Seconds	Party w Retail	99.83%	250,355	99.81%	14,222		0.00036	0.5068	YES
D 2 4 10 2	OSS-4	SOCS/Region(%) <= 10 Seconds	Party w Retail	99.98%	250,355	99.96%	14,222		0.00013	0.8387	YES
D 2 4 10 3	OSS-4	SOCS/Region(%) > 10 Seconds	Party w Retail	0.02%	250,355	0.04%	14,222		0.00013	-0.8387	YES
D 2 4 11 1	OSS-4	NIW/Region(%) <= 4 Seconds	Party w Retail	82.85%	77,529	82.24%	3,818		0.00625	0.9734	YES
D 2 4 11 2	OSS-4	NIW/Region(%) <= 10 Seconds	Party w Retail	99.58%	77,529	99.63%	3,818		0.00108	-0.5352	YES
D 2 4 11 3	OSS-4	NIW/Region(%) > 10 Seconds	Party w Retail	0.42%	77,529	0.37%	3,818		0.00108	0.5352	YES

BellSouth Monthly State Summary
Florida, July 2001

Benchmark / Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error ZScore Equity

COLLOCATION - Collocation

E 1 1 1	C-1	Virtual/FL (calendar days)	<= 15 days						
E 1 1 2	C-1	Physical-Caged/FL (calendar days)	<= 15 days		8	11			YES
E 1 1 3	C-1	Physical-Cageless/FL (calendar days)	<= 15 days		8	16			YES
Average Arrangement Time									
E 1 2 1	C-2	Virtual/FL (calendar days)	<= 60 days						
E 1 2 2	C-2	Virtual-Augments/FL (calendar days)	<= 60 days		39	2			YES
E 1 2 3	C-2	Virtual-Augments - Additional Space Required/FL (calendar days)	<= 60 days						
E 1 2 4	C-2	Physical Caged-Ordinary/FL (calendar days)	<= 90 days						
E 1 2 5	C-2	Physical Caged-Augments/FL (calendar days)	<= 45 days		45	4			YES
E 1 2 6	C-2	Physical Caged-Augments - Additional Space Required/FL (calendar days)	<= 90 days						
E 1 2 7	C-2	Physical Cageless/FL (calendar days)	<= 90 days		62	12			YES
E 1 2 8	C-2	Physical Cageless-Augments/FL (calendar days)	<= 45 days		27	5			YES
E 1 2 9	C-2	Physical Cageless-Augments - Additional Space Required/FL (calendar days)	<= 90 days						
% Due Dates Missed									
E 1 3 1	C-3	Virtual/FL (%)	< 5% missed		0.00%	2			YES
E 1 3 2	C-3	Physical/FL (%)	< 5% missed		0.00%	21			YES

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
General - Flow Through										
% Flow Through Service Requests										
F 111	O-3	Summary/Region(%)	Diagnostic		87.38%	252,927				Diagnostic
F 112	O-3	Aggregate/Region(%)	Diagnostic		87.38%	252,927				Diagnostic
F 113	O-3	Residence/Region(%)	>= 95%		87.09%	183,387				NO
F 114	O-3	Business/Region(%)	>= 90%		69.92%	6,480				NO
F 115	O-3	UNE/Region(%)	>= 85%		90.00%	63,060				YES
% Flow Through Service Requests - Achieved										
F 121	O-3	Summary/Region(%)	Diagnostic		71.33%	309,818				Diagnostic
F 122	O-3	Aggregate/Region(%)	Diagnostic		71.33%	309,818				Diagnostic
F 123	O-3	Residence/Region(%)	Diagnostic		75.18%	212,428				Diagnostic
F 124	O-3	Business/Region(%)	Diagnostic		49.41%	9,170				Diagnostic
F 125	O-3	UNE/Region(%)	Diagnostic		64.34%	88,220				Diagnostic
% Flow Through Service Requests - LNP										
F 131	O-3	Summary/Region(%)	>= 85%		86.36%	4,860				YES
F 132	O-3	Aggregate/Region(%)	>= 85%		86.36%	4,860				YES
F 133	O-3	Residence/Region(%)	Diagnostic							Diagnostic
F 134	O-3	Business/Region(%)	Diagnostic							Diagnostic
General - Pre-Ordering										
Loop Makeup Inquiry (Manual)										
F 211	PO-1	Loops/FL(%)	>= 95% w in 3 bus days		100.00%	172				YES
Loop Makeup Inquiry (Electronic)										
F 221	PO-2	Loops/FL(%)	>= 95% w in 5 min		100.00%	2,094				YES
General - Ordering										
Service Inquiry with Firm Order										
F 311	O-10	xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% w in 5 bus days		91.00%	298				NO
F 312	O-10	Local Interoffice Transport/FL(%)	>= 95% w in 5 bus days		100.00%	2				YES
General - Ordering										
Average Speed of Answer										
F 41	O-12	Region(seconds)	Parity w Retail	199.33	6,834,494	59.15	44,292			YES
General - Maintenance Center										
Average Answer Time										
F 51	M&R-6	Region(seconds)	Parity w Retail	144.73	1,777,813	27.37	96,407			YES
General - Operator Services (Toll)										
Average Speed to Answer										
F 61	OS-1	FL(seconds)	PBD		4.69					PBD
% Answered in 10 seconds										
F 62	OS-2	FL(%)	PBD		97.00%					PBD
General - Directory Assistance										
Average Speed to Answer										
F 71	DA-1	FL(seconds)	PBD		4.33					PBD
% Answered in 10 seconds										

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
F 7 2	DA-2 FL(%)	PBD			97.10%					PBD
General - E911										
<i>Mean Interval</i>										
F 8 1	E-3 FL(hours)	PBD		1.26	1,210					PBD
<i>% Accuracy</i>										
F 8 2	E-2 FL(%)	PBD		95.86%	618,739					PBD
<i>% Timeliness</i>										
F 8 3	E-1 FL(%)	PBD		100.00%	1,210					PBD
General - Billing										
F 9 1										
F 9 2										
F 9 3										
F 9 4										
F 9 5 1	B-7 Resale/FL(%)	Parity w Retail	83.29%	\$20,734,379	97.11%	\$764,425		0.00106	-130.0705	YES
F 9 5 2	B-7 UNE/FL(%)	>= 90%			56.41%	\$864,775				NO
F 9 5 3	B-7 Interconnection/FL(%)	>= 90%			82.27%	\$12,673				NO
F 9 6 1	B-8 Resale/FL(%)	Parity w Retail	92.07%	\$24,421,913	98.34%	\$737,551		0.00113	-55.3521	YES
F 9 6 2	B-8 UNE/FL(%)	>= 90%			98.07%	\$1,586,157				YES
F 9 6 3	B-8 Interconnection/FL(%)	>= 90%			86.94%	\$710,888				NO
General - Change Management										
<i>% Software Release Notices Sent On Time</i>										
F 10 1	CM-1 FL(%)	>= 98% w in 30 days								
<i>Average Software Release Notice Delay Days</i>										
F 10 2	CM-2 FL(average)	>= 25 bus days prior to release								
<i>% Change Management Documentation Sent On Time</i>										
F 10 3	CM-3 FL(%)	>= 98% w in 30 days		50.00%	4					NO
<i>Average Documentation Release Delay Days</i>										
F 10 5	CM-4 FL(average)	>= 25 bus days prior to release		6	2					NO
<i>% CLEC Interface Outages Sent within 15 Minutes</i>										
F 10 6	CM-5 FL(%)	>= 97% w in 15 min		100.00%	32					YES
General - New Business Requests										
<i>% New Business Requests Processed within 30 Business Days</i>										
F 11 1	BFR-1 Region(%)	>= 90% w in 30 bus days		100.00%	5					YES
<i>% Quotes Provided within X Business Days</i>										
F 11 2 1	BFR-2A Region(%)	>= 90% w in 10 bus days		66.67%	3					NO
F 11 2 2	BFR-2B Region(%)	>= 90% w in 30 bus days								
F 11 2 3	BFR-2C Region(%)	>= 90% w in 60 bus days		100.00%	2					YES
General - Ordering										

BellSouth Monthly State Summary
Florida, July 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Acknowledgement Message Timeliness										
F 12 1 1	O-1	EDI/Region(%)			96.50%	78,663				YES
F 12 1 2	O-1	TAG/Region(%)	>= 90% w in 30 min		99.75%	194,073				YES
Acknowledgement Message Completeness										
F 12 2 1	O-2	EDI/Region(%)	100%		99.95%	78,663				NO
F 12 2 2	O-2	TAG/Region(%)	100%		99.75%	194,073				NO
General - Database Updates										
Average Database Update Interval										
F 13 1 1	D-1	LIDB/FL(hours)	PBD	1.43	22	1.43	22			PBD
F 13 1 2	D-1	Directory Listings/FL(hours)	PBD	0.11	25	0.11	25			PBD
F 13 1 3	D-1	Directory Assistance/FL(hours)	PBD	3.57	26	3.56	26			PBD
% Update Accuracy										
F 13 2 1	D-2	LIDB/FL(%)	>= 95%			84.75%	59			NO
F 13 2 2	D-2	Directory Listings/FL(%)	>= 95%			100.00%	85			YES
F 13 2 3	D-2	Directory Assistance/FL(%)	>= 95%			100.00%	85			YES
% NXXs / LRNs Loaded by LERG Effective Date										
F 13 3	D-3	Region(%)	100%			99.35%	153			NO
General - Network Outage Notification										
Mean Time to Notify CLEC of Major Network Outages										
F 14 1	M&R-7	Region (minutes)	Parity w Retail	4824.00	4	4582.00	4			YES

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (SUMMARY)
REPORT PERIOD: 07/01/2001 - 07/31/2001

		ACHIEVED FLOW-THROUGH %	ADJUSTED FLOW- THROUGH %
CLEC AGGREGATE			
REGION ALL SERVICES		71.33%	87.38%
		FLOW-THROUGH %	
BST AGGREGATE			
REGION			
- RETAIL RESIDENCE		93.90%	
- RETAIL BUSINESS*		TBD	
<p>*NOTE: BellSouth is reinstating the reporting of business retail flow through as directed by the Georgia Public Service Commission. BellSouth currently has no way to measure flow through for the Regional Operating System (ROS) interface used by business retail. BellSouth retail reports capture all business service requests submitted from all sources, including manually. BellSouth has initiated the development of an accurate report and will reflect this measure as soon as its development is complete</p>			
<p>**NOTE: BellSouth has identified an issue that may have an impact on the LSR count for Planned Manual Fallout. This is currently under assessment and it may result in a revised report being posted at a future date.</p>			

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
 REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

LESOG																
	RESH / OCN	LENS														
#1		4	0	0	4	1	2	0	1	0	0	0	1	50.00%	100.00%	100.00%
#2		435	0	0	435	19	30	2	384	60	54	6	324	81.61%	84.38%	85.71%
#3		108	0	0	108	6	10	0	92	1	1	0	91	92.86%	98.91%	98.91%
#4		17	0	0	17	4	2	0	11	0	0	0	11	73.33%	100.00%	100.00%
#5		1680	0	0	1680	160	61	2	1457	44	34	10	1413	87.93%	96.98%	97.65%
#6		0	0	16	16	4	0	0	12	5	2	3	7	53.85%	58.33%	77.78%
#7		0	0	732	732	96	81	1	554	43	23	20	511	81.11%	92.24%	95.69%
#8		0	0	84	84	7	5	1	71	9	3	6	62	86.11%	87.32%	95.38%
#9		0	0	358	358	62	43	2	251	57	18	39	194	70.80%	77.29%	91.51%
#10		0	0	301	301	57	23	0	221	28	20	8	193	71.48%	87.33%	90.61%
#11		0	0	253	253	56	42	1	154	51	41	10	103	51.50%	66.88%	71.53%
#12		6	0	0	6	0	1	0	5	0	0	0	5	100.00%	100.00%	100.00%
#13		467	0	0	467	43	28	7	389	30	19	11	359	85.27%	92.29%	94.97%
#14		125	0	0	125	10	9	2	104	5	4	1	99	87.61%	95.19%	96.12%
#15		182	0	0	182	31	9	3	139	17	11	6	122	74.39%	87.77%	91.73%
#16		811	0	0	811	102	86	15	608	68	36	32	540	79.65%	88.82%	93.75%
#17		307	0	0	307	52	71	13	171	48	41	7	123	56.94%	71.93%	75.00%
#18		47	0	0	47	13	4	0	30	1	0	1	29	69.05%	96.67%	100.00%
#19		47	0	0	47	10	6	0	31	2	1	1	29	72.50%	93.55%	96.67%
#20		180	0	0	180	22	8	4	146	26	14	12	120	76.92%	82.19%	89.55%
#21		17	0	0	17	0	3	2	12	5	4	1	7	63.64%	58.33%	63.64%
#22		0	0	3	3	0	0	1	2	2	1	1	0	0.00%	0.00%	0.00%
#23		32	0	0	32	7	8	0	17	5	1	4	12	60.00%	70.59%	92.31%
#24		43	0	0	43	2	10	0	31	11	6	5	20	71.43%	64.52%	76.92%
#25		10	0	0	10	0	0	3	7	1	0	1	6	100.00%	85.71%	100.00%
#26		25	0	0	25	0	1	1	23	6	4	2	17	80.95%	73.91%	80.95%
#27		32	0	0	32	0	0	11	21	19	14	5	2	12.50%	9.52%	12.50%
#28		127	0	0	127	2	15	8	102	60	10	50	42	77.78%	41.18%	80.77%
#29		73	0	0	73	3	6	1	63	13	11	2	50	78.13%	79.37%	81.97%
#30		18	0	0	18	0	6	0	12	5	5	0	7	58.33%	58.33%	58.33%
#31		147	0	0	147	42	12	5	88	24	19	5	64	51.20%	72.73%	77.11%
#32		11	0	0	11	1	0	0	10	4	4	0	6	54.55%	60.00%	60.00%
#33		634	0	0	634	67	51	27	489	130	117	13	359	66.11%	73.42%	75.42%
#34		56	0	0	56	4	2	0	50	16	13	3	34	66.67%	68.00%	72.34%
#35		15	0	0	15	4	6	0	5	0	0	0	5	55.56%	100.00%	100.00%
#36		149	0	0	149	13	47	0	89	6	1	5	83	85.57%	93.26%	98.81%
#37		4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING											FLOWTHROUGH			
Name	RESH / OCN	LESOG											Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
		Mechanized Interface Used				Manual	Rejects	Validated	Errors							
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout				Issued SO's
#38		22	0	0	22	13	2	0	7	4	4	0	3	15.00%	42.86%	42.86%
#39		8	0	0	8	0	4	0	4	4	4	0	0	0.00%	0.00%	0.00%
#40		0	228	0	228	99	48	2	79	14	11	3	65	37.14%	82.28%	85.53%
#41		55	0	0	55	5	2	0	48	28	23	5	20	41.67%	41.67%	46.51%
#42		117	0	0	117	35	3	0	79	3	2	1	76	67.26%	96.20%	97.44%
#43		625	0	0	625	65	32	0	528	3	3	0	525	88.53%	99.43%	99.43%
#44		0	17	0	17	4	1	0	12	3	2	1	9	60.00%	75.00%	81.82%
#45		0	43	0	43	19	7	0	17	6	3	3	11	33.33%	64.71%	78.57%
#46		0	4	0	4	0	1	0	3	0	0	0	3	100.00%	100.00%	100.00%
#47		0	20	0	20	8	0	5	7	4	4	0	3	20.00%	42.86%	42.86%
#48		0	203	0	203	155	13	2	33	6	3	3	27	14.59%	81.82%	90.00%
#49		0	146	0	146	81	14	3	48	10	7	3	38	30.16%	79.17%	84.44%
#50		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#51		51	0	0	51	3	25	1	22	8	7	1	14	58.33%	63.64%	66.67%
#52		393	0	0	393	29	22	0	342	9	7	2	333	90.24%	97.37%	97.94%
#53		693	0	0	693	28	15	3	647	56	56	0	591	87.56%	91.34%	91.34%
#54		410	0	0	410	49	13	4	344	54	53	1	290	73.98%	84.30%	84.55%
#55		387	0	0	387	33	16	2	336	119	118	1	217	58.97%	64.58%	64.78%
#56		188	0	0	188	21	13	1	153	5	3	2	148	86.05%	96.73%	98.01%
#57		117	0	0	117	26	8	1	82	8	5	3	74	70.48%	90.24%	93.67%
#58		2103	0	0	2103	250	85	9	1759	69	57	12	1690	84.63%	96.08%	96.74%
#59		0	0	608	608	7	63	0	538	16	12	4	522	96.49%	97.03%	97.75%
#60		459	0	0	459	127	55	4	273	13	9	4	260	65.66%	95.24%	96.65%
#61		0	55	0	55	2	4	0	49	5	4	1	44	88.00%	89.80%	91.67%
#62		169	0	0	169	1	2	0	166	102	100	2	64	38.79%	38.55%	39.02%
#63		178	0	0	178	12	15	0	151	9	8	1	142	87.65%	94.04%	94.67%
#64		0	0	1	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#65		1602	0	0	1602	102	134	1	1365	42	37	5	1323	90.49%	96.92%	97.28%
#66		236	0	0	236	39	17	9	171	37	33	4	134	65.05%	78.36%	80.24%
#67		169	0	0	169	15	12	1	141	17	12	5	124	82.12%	87.94%	91.18%
#68		0	1	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#69		56	0	0	56	4	5	4	43	4	3	1	39	84.78%	90.70%	92.86%
#70		2204	0	0	2204	498	208	32	1466	258	188	70	1208	63.78%	82.40%	86.53%
#71		7	0	0	7	0	2	0	5	0	0	0	5	100.00%	100.00%	100.00%
#72		10	0	0	10	1	1	0	8	0	0	0	8	88.89%	100.00%	100.00%
#73		983	0	0	983	121	70	4	788	94	90	4	694	76.69%	88.07%	88.52%
#74		0	0	2	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
 REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING										FLOWTHROUGH				
		LESOG														
		Mechanized Interface Used				Manual	Rejects		Validated	Errors						
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#75		0	0	1168	1168	170	178	0	820	95	49	46	725	76.80%	88.41%	93.67%
#76		0	0	826	826	98	104	2	622	62	36	26	560	80.69%	90.03%	93.96%
#77		0	0	8	8	0	1	0	7	4	0	4	3	100.00%	42.86%	100.00%
#78		0	0	1432	1432	238	225	1	968	115	52	63	853	74.63%	88.12%	94.25%
#79		0	0	861	861	117	177	2	565	81	44	37	484	75.04%	85.66%	91.67%
#80		0	0	689	689	102	90	4	493	71	27	44	422	76.59%	85.60%	93.99%
#81		0	0	2	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%
#82		151	0	0	151	34	42	1	74	11	4	7	63	62.38%	85.14%	94.03%
#83		14	0	0	14	0	1	0	13	1	1	0	12	92.31%	92.31%	92.31%
#84		34	0	0	34	8	1	0	25	5	5	0	20	60.61%	80.00%	80.00%
#85		15	0	0	15	0	1	0	14	9	9	0	5	35.71%	35.71%	35.71%
#86		20	0	0	20	0	5	0	15	1	1	0	14	93.33%	93.33%	93.33%
#87		5	0	0	5	0	2	0	3	1	1	0	2	66.67%	66.67%	66.67%
#88		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%
#89		4	0	0	4	0	0	0	4	1	1	0	3	75.00%	75.00%	75.00%
#90		236	0	0	236	4	6	0	226	24	19	5	202	89.78%	89.38%	91.40%
#91		29424	0	0	29424	3828	2320	91	23185	3655	3507	148	19530	72.70%	84.24%	84.78%
#92		17	0	0	17	4	1	0	12	4	3	1	8	53.33%	66.67%	72.73%
#93		117	0	0	117	8	18	0	91	6	5	1	85	86.73%	93.41%	94.44%
#94		0	537	0	537	294	120	12	111	40	22	18	71	18.35%	63.96%	76.34%
#95		0	150	0	150	68	21	1	60	11	7	4	49	39.52%	81.67%	87.50%
#96		2822	0	0	2822	1883	71	14	854	120	104	16	734	26.98%	85.95%	87.59%
#97		1226	0	0	1226	368	88	19	751	288	249	39	463	42.87%	61.65%	65.03%
#98		67	0	0	67	8	4	0	55	11	10	1	44	70.97%	80.00%	81.48%
#99		110	0	0	110	7	4	0	99	3	3	0	96	90.57%	96.97%	96.97%
#100		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#101		9	0	0	9	0	1	1	7	4	3	1	3	50.00%	42.86%	50.00%
#102		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%
#103		3386	0	0	3386	474	188	19	2705	523	473	50	2182	69.73%	80.67%	82.18%
#104		725	0	0	725	67	47	3	608	44	41	3	564	83.93%	92.76%	93.22%
#105		7	0	0	7	0	2	0	5	5	0	5	0	0.00%	0.00%	0.00%
#106		0	47	0	47	2	16	1	28	9	5	4	19	73.08%	67.86%	79.17%
#107		0	0	746	746	151	53	8	534	25	13	12	509	75.63%	95.32%	97.51%
#108		0	0	2347	2347	418	173	48	1708	249	203	46	1459	70.14%	85.42%	87.79%
#109		6529	0	0	6529	267	186	17	6059	33	12	21	6026	95.57%	99.46%	99.80%
#110		14509	0	0	14509	339	1453	13	12704	2547	2518	29	10157	78.05%	79.95%	80.13%
#111		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING										FLOWTHROUGH					
		LESOG															
		Mechanized Interface Used				Manual	Rejects		Validated	Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Erro Excluded Calculator	
#112		7609	0	0	7609	833	1558	1	5217	131	75	56	5086	84.85%	97.49%	98.55%	
#113		1114	0	0	1114	107	48	1	958	33	24	9	925	87.59%	96.56%	97.47%	
#114		0	6018	0	6018	923	1474	3	3618	399	345	54	3219	71.74%	88.97%	90.32%	
#115		0	8780	0	8780	1535	2098	4	5143	634	522	112	4509	68.67%	87.67%	89.62%	
#116		195	0	0	195	14	14	0	167	20	19	1	147	81.67%	88.02%	88.55%	
#117		322	0	0	322	19	17	3	283	78	76	2	205	68.33%	72.44%	72.95%	
#118		109	0	0	109	32	7	3	67	9	7	2	58	59.79%	86.57%	89.23%	
#119		17	0	0	17	4	5	0	8	0	0	0	8	66.67%	100.00%	100.00%	
#120		525	0	0	525	69	46	4	406	22	6	16	384	83.66%	94.58%	98.46%	
#121		74	0	0	74	4	10	3	57	1	1	0	56	91.80%	98.25%	98.25%	
#122		3	0	0	3	0	0	1	2	1	1	0	1	50.00%	50.00%	50.00%	
#123		464	0	0	464	61	52	1	350	34	30	4	316	77.64%	90.29%	91.33%	
#124		74	0	0	74	6	8	0	60	2	2	0	58	87.88%	96.67%	96.67%	
#125		0	0	414	414	17	33	0	364	9	9	0	355	93.18%	97.53%	97.53%	
#126		29	0	0	29	0	3	3	23	1	0	1	22	100.00%	95.65%	100.00%	
#127		0	0	100	100	28	3	0	69	7	6	1	62	64.58%	89.86%	91.18%	
#128		8	0	0	8	0	1	3	4	3	3	0	1	25.00%	25.00%	25.00%	
#129		16	0	0	16	0	0	0	16	3	3	0	13	81.25%	81.25%	81.25%	
#130		22	0	0	22	5	2	0	15	0	0	0	15	75.00%	100.00%	100.00%	
#131		50	0	0	50	8	8	2	32	6	4	2	26	68.42%	81.25%	86.67%	
#132		0	0	51	51	19	9	0	23	4	3	1	19	46.34%	82.61%	86.36%	
#133		0	0	73	73	19	7	0	47	21	16	5	26	42.62%	55.32%	61.90%	
#134		116	0	0	116	15	14	1	86	5	3	2	81	81.82%	94.19%	96.43%	
#135		59	0	0	59	21	6	1	31	13	13	0	18	34.62%	58.06%	58.06%	
#136		61	0	0	61	5	2	0	54	5	4	1	49	84.48%	90.74%	92.45%	
#137		153	0	0	153	24	19	3	107	25	9	16	82	71.30%	76.64%	90.11%	
#138		83	0	0	83	2	3	7	71	56	30	26	15	31.91%	21.13%	33.33%	
#139		51	0	0	51	3	10	2	36	6	4	2	30	81.08%	83.33%	88.24%	
#140		270	0	0	270	24	33	2	211	23	20	3	188	81.03%	89.10%	90.38%	
#141		1805	0	0	1805	215	225	0	1365	160	114	46	1205	78.55%	88.28%	91.36%	
#142		0	2336	0	2336	959	255	14	1108	319	200	119	789	40.50%	71.21%	79.78%	
#143		226	0	0	226	60	25	3	138	34	29	5	104	53.89%	75.36%	78.20%	
#144		377	0	0	377	43	41	1	292	8	1	7	284	86.59%	97.26%	99.65%	
#145		481	0	0	481	44	32	1	404	31	31	0	373	83.26%	92.33%	92.33%	
#146		22	0	0	22	5	1	0	16	2	2	0	14	66.67%	87.50%	87.50%	
#147		8	0	0	8	1	1	0	6	0	0	0	6	85.71%	100.00%	100.00%	
#148		512	0	0	512	57	30	12	413	44	42	2	369	78.85%	89.35%	89.78%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info					LSR PROCESSING								FLOWTHROUGH			
					LESOG											
Mechanized Interface Used					Manual	Rejects	Validated		Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#149		1415	0	0	1415	117	61	3	1234	120	109	11	1114	83.13%	90.28%	91.09%
#150		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#151		41	0	0	41	2	3	0	36	9	9	0	27	71.05%	75.00%	75.00%
#152		6906	0	0	6906	447	498	5	5956	325	243	82	5631	89.08%	94.54%	95.86%
#153		3859	0	0	3859	441	206	18	3194	556	537	19	2638	72.95%	82.59%	83.09%
#154		0	67	0	67	36	24	3	4	2	1	1	2	5.13%	50.00%	66.67%
#155		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#156		21	0	0	21	6	1	0	14	3	3	0	11	55.00%	78.57%	78.57%
#157		8	0	0	8	3	1	0	4	0	0	0	4	57.14%	100.00%	100.00%
#158		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%
#159		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#160		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#161		85	0	0	85	21	8	1	55	18	16	2	37	50.00%	67.27%	69.81%
#162		334	0	0	334	33	9	2	290	6	5	1	284	88.20%	97.93%	98.27%
#163		1382	0	0	1382	173	104	2	1103	28	21	7	1075	84.71%	97.46%	98.08%
#164		112	0	0	112	30	13	0	69	3	2	1	66	67.35%	95.65%	97.06%
#165		128	0	0	128	54	25	1	48	11	7	4	37	37.76%	77.08%	84.09%
#166		162	0	0	162	24	16	1	121	18	15	3	103	72.54%	85.12%	87.29%
#167		14	0	0	14	0	1	0	13	0	0	0	13	100.00%	100.00%	100.00%
#168		28	0	0	28	0	0	0	28	12	11	1	16	59.26%	57.14%	59.26%
#169		9	0	0	9	3	1	0	5	0	0	0	5	62.50%	100.00%	100.00%
#170		7	0	0	7	0	2	1	4	1	0	1	3	100.00%	75.00%	100.00%
#171		197	0	0	197	28	9	0	160	10	10	0	150	79.79%	93.75%	93.75%
#172		1693	0	0	1693	137	81	1	1474	32	23	9	1442	90.01%	97.83%	98.43%
#173		28	0	0	28	7	5	1	15	1	1	0	14	63.64%	93.33%	93.33%
#174		737	0	0	737	85	16	5	631	68	67	1	563	78.74%	89.22%	89.37%
#175		635	0	0	635	60	109	1	465	12	9	3	453	86.78%	97.42%	98.05%
#176		1686	0	0	1686	209	128	2	1347	32	26	6	1315	84.84%	97.62%	98.06%
#177		339	0	0	339	14	24	1	300	46	45	1	254	81.15%	84.67%	84.95%
#178		502	0	0	502	57	58	4	383	11	9	2	372	84.93%	97.13%	97.64%
#179		0	4021	0	4021	532	958	1	2530	256	219	37	2274	75.17%	89.88%	91.22%
#180		113	0	0	113	6	16	1	90	28	27	1	62	65.26%	68.89%	69.66%
#181		77	0	0	77	5	4	0	68	5	5	0	63	86.30%	92.65%	92.65%
#182		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#183		26	0	0	26	1	2	0	23	5	5	0	18	75.00%	78.26%	78.26%
#184		1642	0	0	1642	131	89	3	1419	53	45	8	1366	88.59%	96.26%	96.81%
#185		0	0	836	836	5	38	0	793	72	71	1	721	90.46%	90.92%	91.04%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
Attachment 2B

AGGREGATE ORDER TYPES																	
Company Info					LSR PROCESSING					FLOWTHROUGH							
					LESOG												
Mechanized Interface Used					Manual	Rejects	Validated		Errors								
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculator	
#186		47	0	0	47	1	2	1	43	11	9	2	32	76.19%	74.42%	78.05%	
#187		995	0	0	995	76	32	6	881	51	51	0	830	86.73%	94.21%	94.21%	
#188		0	0	905	905	116	6	89	694	367	312	55	327	43.31%	47.12%	51.17%	
#189		74	0	0	74	13	20	1	40	0	0	0	40	75.47%	100.00%	100.00%	
#190		480	0	0	480	69	35	3	373	47	40	7	326	74.94%	87.40%	89.07%	
#191		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#192		173	0	0	173	21	8	0	144	5	2	3	139	85.80%	96.53%	98.58%	
#193		1124	0	0	1124	133	159	0	832	16	15	1	816	84.65%	98.08%	98.19%	
#194		3543	0	0	3543	475	267	35	2766	97	66	31	2669	83.15%	96.49%	97.59%	
#195		0	0	133	133	46	40	1	46	6	2	4	40	45.45%	86.96%	95.24%	
#196		0	0	14	14	3	5	0	6	1	0	1	5	62.50%	83.33%	100.00%	
#197		0	0	21	21	2	6	1	12	1	1	0	11	78.57%	91.67%	91.67%	
#198		0	0	17	17	3	1	1	12	6	1	5	6	60.00%	50.00%	85.71%	
#199		0	0	17	17	4	4	0	9	4	2	2	5	45.45%	55.56%	71.43%	
#200		0	0	26	26	12	0	0	14	3	2	1	11	44.00%	78.57%	84.62%	
#201		122	0	0	122	15	33	3	71	10	4	6	61	76.25%	85.92%	93.85%	
#202		25	0	0	25	0	1	0	24	1	0	1	23	100.00%	95.83%	100.00%	
#203		31	0	0	31	2	3	0	26	4	0	4	22	91.67%	84.62%	100.00%	
#204		28	0	0	28	4	2	0	22	2	1	1	20	80.00%	90.91%	95.24%	
#205		48	0	0	48	20	10	0	18	5	3	2	13	36.11%	72.22%	81.25%	
#206		42	0	0	42	2	8	1	31	6	3	3	25	83.33%	80.65%	89.29%	
#207		0	0	157	157	3	15	0	139	24	20	4	115	83.33%	82.73%	85.19%	
#208		8	0	0	8	1	0	0	7	1	1	0	6	75.00%	85.71%	85.71%	
#209		854	0	0	854	80	22	5	747	65	60	5	682	82.97%	91.30%	91.91%	
#210		141	0	0	141	13	0	0	128	2	2	0	126	89.36%	98.44%	98.44%	
#211		6	0	0	6	2	0	0	4	0	0	0	4	66.67%	100.00%	100.00%	
#212		6	0	0	6	0	0	0	6	1	1	0	5	83.33%	83.33%	83.33%	
#213		0	0	1636	1636	28	166	0	1442	70	56	14	1372	94.23%	95.15%	96.08%	
#214		165	0	0	165	10	12	3	140	9	5	4	131	89.73%	93.57%	96.32%	
#215		189	0	0	189	13	10	0	166	21	20	1	145	81.46%	87.35%	87.88%	
#216		108	0	0	108	24	24	0	60	5	3	2	55	67.07%	91.67%	94.83%	
#217		5305	0	0	5305	439	767	17	4082	2008	192	1816	2074	76.67%	50.81%	91.53%	
#218		52	0	0	52	8	2	0	42	5	5	0	37	74.00%	88.10%	88.10%	
#219		14	0	0	14	3	2	0	9	2	2	0	7	58.33%	77.78%	77.78%	
#220		3005	0	0	3005	215	404	3	2383	118	107	11	2265	87.55%	95.05%	95.49%	
#221		0	0	145	145	51	33	3	58	28	24	4	30	28.57%	51.72%	55.56%	
#222		8185	0	0	8185	1431	540	111	6103	1184	1080	104	4919	66.20%	80.60%	82.00%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
Attachment 2B

AGGREGATE ORDER TYPES		LSR PROCESSING											FLOWTHROUGH			
Company Info		LESOG														
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Pending Supps (Z Status)	Validated	Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Excl Excluded Calculator
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout						
#223		4	0	0	4	2	0	0	2	0	0	0	2	50.00%	100.00%	100.00%
#224		10	0	0	10	2	3	1	4	1	0	1	3	60.00%	75.00%	100.00%
#225		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#226		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#227		0	0	4213	4213	75	88	21	4029	652	621	31	3377	82.91%	83.82%	84.47%
#228		8867	0	0	8867	767	337	3	7760	1000	977	23	6760	79.49%	87.11%	87.37%
#229		56	0	0	56	4	5	0	47	2	1	1	45	90.00%	95.74%	97.83%
#230		68	0	0	68	23	7	0	38	4	4	0	34	55.74%	89.47%	89.47%
#231		312	0	0	312	11	49	2	250	21	19	2	229	88.42%	91.60%	92.34%
#232		0	0	27	27	20	1	2	4	1	1	0	3	12.50%	75.00%	75.00%
#233		43	0	0	43	7	3	0	33	10	9	1	23	58.97%	69.70%	71.88%
#234		0	0	4	4	1	0	0	3	1	1	0	2	50.00%	66.67%	66.67%
#235		8	0	0	8	1	0	0	7	4	2	2	3	50.00%	42.86%	60.00%
#236		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#237		748	0	0	748	337	53	7	351	27	14	13	324	48.00%	92.31%	95.86%
#238		70	0	0	70	3	2	0	65	1	0	1	64	95.52%	98.46%	100.00%
#239		2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#240		0	0	4	4	0	1	0	3	1	1	0	2	66.67%	66.67%	66.67%
#241		5	0	0	5	1	0	0	4	0	0	0	4	80.00%	100.00%	100.00%
#242		0	0	5	5	0	0	1	4	0	0	0	4	100.00%	100.00%	100.00%
#243		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#244		0	0	25	25	0	2	4	19	8	6	2	11	64.71%	57.89%	64.71%
#245		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#246		66	0	0	66	20	3	3	40	6	4	2	34	58.62%	85.00%	89.47%
#247		0	0	3776	3776	11	457	50	3258	1212	1025	187	2046	66.39%	62.80%	66.62%
#248		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#249		12	0	0	12	1	1	0	10	1	1	0	9	81.82%	90.00%	90.00%
#250		87	0	0	87	14	11	1	61	16	12	4	45	63.38%	73.77%	78.95%
#251		0	0	2	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%
#252		1873	0	0	1873	172	173	10	1518	76	49	27	1442	86.71%	94.99%	96.71%
#253		2175	0	0	2175	282	226	40	1627	146	62	84	1481	81.15%	91.03%	95.98%
#254		312	0	0	312	62	35	2	213	31	21	10	182	68.68%	85.45%	89.66%
#255		338	0	0	338	52	14	2	270	39	35	4	231	72.64%	85.56%	88.84%
#256		0	0	19	19	14	1	1	3	2	0	2	1	6.67%	33.33%	100.00%
#257		0	0	12	12	9	1	0	2	0	0	0	2	18.18%	100.00%	100.00%
#258		0	0	5	5	5	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#259		144	0	0	144	39	46	0	59	11	3	8	48	53.33%	81.36%	94.12%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
 REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING										FLOWTHROUGH				
		LESOG														
		Mechanized Interface Used				Manual	Rejects	Pending Supps (Z Status)	Validated	Errors						
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#260		24	0	0	24	11	2	0	11	2	1	1	9	42.86%	81.82%	90.00%
#261		6	0	0	6	0	0	1	5	3	2	1	2	50.00%	40.00%	50.00%
#262		124	0	0	124	24	18	0	82	13	5	8	69	70.41%	84.15%	93.24%
#263		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%
#264		15	0	0	15	1	7	0	7	3	1	2	4	66.67%	57.14%	80.00%
#265		19	0	0	19	6	2	1	10	6	3	3	4	30.77%	40.00%	57.14%
#266		105	0	0	105	6	21	0	78	13	11	2	65	79.27%	83.33%	85.53%
#267		393	0	0	393	79	47	1	266	36	11	25	230	71.88%	86.47%	95.44%
#268		44	0	0	44	12	7	4	21	3	3	0	18	54.55%	85.71%	85.71%
#269		11	0	0	11	0	0	3	8	0	0	0	8	100.00%	100.00%	100.00%
#270		302	0	0	302	20	29	0	253	33	31	2	220	81.18%	86.96%	87.65%
#271		0	28842	0	28842	5361	3263	5	20213	2553	2160	393	17660	70.13%	87.37%	89.10%
#272		20	0	0	20	4	4	1	11	8	6	2	3	23.08%	27.27%	33.33%
#273		4	0	0	4	0	0	0	4	2	0	2	2	100.00%	50.00%	100.00%
#274		0	2164	0	2164	235	136	0	1793	257	203	54	1536	77.81%	85.67%	88.33%
#275		0	9989	0	9989	381	1843	0	7765	2185	1369	816	5580	76.13%	71.86%	80.30%
#276		74	0	0	74	13	7	1	53	5	5	0	48	72.73%	90.57%	90.57%
#277		840	0	0	840	198	142	1	499	6	6	0	493	70.73%	98.80%	98.80%
#278		89	0	0	89	10	11	0	68	16	5	11	52	77.61%	76.47%	91.23%
#279		9	0	0	9	5	1	0	3	2	1	1	1	14.29%	33.33%	50.00%
#280		57	0	0	57	6	10	1	40	7	7	0	33	71.74%	82.50%	82.50%
#281		149	0	0	149	23	25	8	93	9	5	4	84	75.00%	90.32%	94.38%
#282		137	0	0	137	17	11	0	109	41	37	4	68	55.74%	62.39%	64.76%
#283		160	0	0	160	21	16	0	123	14	12	2	109	76.76%	88.62%	90.08%
#284		0	0	23	23	2	0	0	21	1	1	0	20	86.96%	95.24%	95.24%
#285		19	0	0	19	4	0	1	14	1	1	0	13	72.22%	92.86%	92.86%
#286		9	0	0	9	4	1	0	4	3	2	1	1	14.29%	25.00%	33.33%
#287		38	0	0	38	2	3	0	33	1	1	0	32	91.43%	96.97%	96.97%
#288		18	0	0	18	4	0	0	14	2	2	0	12	66.67%	85.71%	85.71%
#289		31	0	0	31	6	1	0	24	0	0	0	24	80.00%	100.00%	100.00%
#290		261	0	0	261	48	18	19	176	135	112	23	41	20.40%	23.30%	26.80%
#291		675	0	0	675	147	47	5	476	51	30	21	425	70.60%	89.29%	93.41%
#292		2184	0	0	2184	497	214	14	1459	145	64	81	1314	70.08%	90.06%	95.36%
#293		80	0	0	80	0	30	3	47	18	4	14	29	87.88%	61.70%	87.88%
#294		1029	0	0	1029	209	150	19	651	260	196	64	391	49.12%	60.06%	66.61%
#295		0	45	0	45	16	8	0	21	11	9	2	10	28.57%	47.62%	52.63%
#296		0	291	0	291	84	42	2	163	33	22	11	130	55.08%	79.75%	85.53%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES		LSR PROCESSING											FLOWTHROUGH			
Company Info		LESOG														
Name	RESH / OCN	Mechanized Interface Used			Manual	Rejects	Pending Supps (Z Status)	Validated	Errors				Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
		LENS	EDI	TAG	Total Mech LSR's	Auto Clarification		LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's				
#297		875	0	0	875	160	98	20	597	179	138	41	418	58.38%	70.02%	75.18%
#298		2104	0	0	2104	340	237	31	1496	277	183	94	1219	69.98%	81.48%	86.95%
#299		0	1179	0	1179	329	120	38	692	224	192	32	468	47.32%	67.63%	70.91%
#300		427	0	0	427	57	48	21	301	113	79	34	188	58.02%	62.46%	70.41%
#301		626	0	0	626	85	76	20	445	128	98	30	317	63.40%	71.24%	76.39%
#302		4	0	0	4	1	1	0	2	0	0	0	2	66.67%	100.00%	100.00%
#303		319	0	0	319	32	33	8	246	99	42	57	147	66.52%	59.76%	77.78%
#304		0	0	8899	8899	44	280	14	8561	1021	982	39	7540	88.02%	88.07%	88.48%
#305		3053	0	0	3053	411	154	59	2429	391	352	39	2038	72.76%	83.90%	85.27%
#306		720	0	0	720	34	54	4	628	73	66	7	555	84.73%	88.38%	89.37%
#307		84	0	0	84	14	4	0	66	8	8	0	58	72.50%	87.88%	87.88%
#308		0	0	17	17	2	2	1	12	9	2	7	3	42.86%	25.00%	60.00%
#309		21	0	0	21	3	4	0	14	5	5	0	9	52.94%	64.29%	64.29%
#310		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#311		0	2339	0	2339	1278	174	1	886	180	152	28	706	33.05%	79.68%	82.28%
#312		2747	0	0	2747	329	150	25	2243	573	525	48	1670	66.16%	74.45%	76.08%
#313		952	0	0	952	118	78	2	754	42	40	2	712	81.84%	94.43%	94.68%
#314		8	0	0	8	0	0	0	8	0	0	0	8	100.00%	100.00%	100.00%
#315		16	0	0	16	2	3	0	11	0	0	0	11	84.62%	100.00%	100.00%
#316		129	0	0	129	12	8	0	109	1	1	0	108	89.26%	99.08%	99.08%
#317		83	0	0	83	13	2	2	66	19	18	1	47	60.26%	71.21%	72.31%
#318		2419	0	0	2419	282	165	11	1961	87	72	15	1874	84.11%	95.56%	96.30%
#319		407	0	0	407	34	56	5	312	60	46	14	252	75.90%	80.77%	84.56%
		19	0	0	19	0	2	0	17	17	9	8	0	0.00%	0.00%	0.00%
		372	0	0	372	10	14	1	347	13	9	4	334	94.62%	96.25%	97.38%
#322		358	0	0	358	12	42	1	303	22	21	1	281	89.49%	92.74%	93.05%
#323		272	0	0	272	46	12	0	214	6	5	1	208	80.31%	97.20%	97.65%
#324		832	0	0	832	155	80	7	590	62	53	9	528	71.74%	89.49%	90.88%
#325		196	0	0	196	19	14	1	162	2	1	1	160	88.89%	98.77%	99.38%
#326		531	0	0	531	64	7	0	460	17	15	2	443	84.87%	96.30%	96.72%
#327		7	0	0	7	0	1	0	6	3	0	3	3	100.00%	50.00%	100.00%
#328		82	0	0	82	26	13	0	43	11	10	1	32	47.06%	74.42%	76.19%
#329		6	0	0	6	1	1	0	4	0	0	0	4	80.00%	100.00%	100.00%
#330		90	0	0	90	20	12	0	58	2	1	1	56	72.73%	96.55%	98.25%
#331		78	0	0	78	11	6	1	60	5	5	0	55	77.46%	91.67%	91.67%
#332		169	0	0	169	33	5	0	131	6	6	0	125	76.22%	95.42%	95.42%
#333		20	0	0	20	9	4	0	7	1	1	0	6	37.50%	85.71%	85.71%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING											FLOWTHROUGH			
Name	RESH / OCN	Mechanized Interface Used				LESOG			Validated	Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Manual	Rejects	Pending Supps (Z Status)		Total System Fallout	BST Caused Fallout	CLEC Caused Fallout				
#334		0	0	39	39	3	16	7	13	3	3	0	10	62.50%	76.92%	76.92%
#335		0	0	21	21	4	4	6	7	2	1	1	5	50.00%	71.43%	83.33%
#336		917	0	0	917	129	181	13	594	75	48	27	519	74.57%	87.37%	91.53%
#337		256	0	0	256	13	48	33	162	117	63	54	45	37.19%	27.78%	41.67%
#338		48	0	0	48	4	16	0	28	7	7	0	21	65.63%	75.00%	75.00%
#339		105	0	0	105	18	10	0	77	8	8	0	69	72.63%	89.61%	89.61%
#340		811	0	0	811	105	52	0	654	35	30	5	619	82.10%	94.65%	95.38%
#341		37	0	0	37	2	2	1	32	18	15	3	14	45.16%	43.75%	48.28%
#342		543	0	0	543	44	23	3	473	82	71	11	391	77.27%	82.66%	84.63%
#343		110	0	0	110	8	21	0	81	1	1	0	80	89.89%	98.77%	98.77%
#344		1084	0	0	1084	91	83	2	908	17	16	1	891	89.28%	98.13%	98.24%
#345		293	0	0	293	35	45	8	205	64	46	18	141	63.51%	68.78%	75.40%
#346		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#347		1321	0	0	1321	199	36	8	1078	240	229	11	838	66.19%	77.74%	78.54%
#348		75	0	0	75	13	7	1	54	12	6	6	42	68.85%	77.78%	87.50%
#349		0	0	3	3	0	1	0	2	1	1	0	1	50.00%	50.00%	50.00%
#350		89	0	0	89	13	2	6	68	21	13	8	47	64.38%	69.12%	78.33%
#351		37	0	0	37	5	1	0	31	2	2	0	29	80.56%	93.55%	93.55%
#352		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#353		360	0	0	360	34	33	1	292	19	15	4	273	84.78%	93.49%	94.79%
#354		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%
#355		29278	0	0	29278	7791	3983	340	17164	5181	4163	1018	11983	50.06%	69.81%	74.22%
#356		259	0	0	259	43	21	1	194	8	1	7	186	80.87%	95.88%	99.47%
#357		22	0	0	22	3	3	1	15	2	0	2	13	81.25%	86.67%	100.00%
#358		24	0	0	24	3	2	0	19	2	1	1	17	80.95%	89.47%	94.44%
#359		395	0	0	395	33	21	3	338	144	130	14	194	54.34%	57.40%	59.88%
#360		16	0	0	16	2	0	0	14	6	4	2	8	57.14%	57.14%	66.67%
#361		0	0	772	772	1	49	0	722	87	85	2	635	88.07%	87.95%	88.19%
#362		68	0	0	68	4	2	2	60	10	9	1	50	79.37%	83.33%	84.75%
#363		1090	0	0	1090	153	91	7	839	34	32	2	805	81.31%	95.95%	96.18%
#364		0	0	1539	1539	176	109	23	1231	278	254	24	953	68.91%	77.42%	78.96%
#365		31	0	0	31	0	9	0	22	3	3	0	19	86.36%	86.36%	86.36%
#366		30	0	0	30	3	2	1	24	5	3	2	19	76.00%	79.17%	86.36%
#367		230	0	0	230	3	9	2	216	71	68	3	145	67.13%	67.13%	68.08%
#368		698	0	0	698	58	49	1	590	34	16	18	556	88.25%	94.24%	97.20%
#369		0	404	0	404	275	28	6	95	57	40	17	38	10.76%	40.00%	48.72%
#370		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
Attachment 2B

AGGREGATE ORDER TYPES																	
Company Info					LSR PROCESSING								FLOWTHROUGH				
					LESOG												
Mechanized Interface Used					Manual	Rejects		Validated		Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
#371		745	0	0	745	118	72	11	544	100	64	36	444	70 93%	81 62%	87 40%	
#372		0	0	1142	1142	3	54	0	1085	128	124	4	957	88 28%	88 20%	88 53%	
#373		129	0	0	129	6	5	7	111	27	27	0	84	71.79%	75.68%	75 68%	
#374		390	0	0	390	52	8	2	328	21	18	3	307	81 43%	93.60%	94 46%	
#375		0	0	324	324	2	28	0	294	25	21	4	269	92 12%	91 50%	92 76%	
#376		12	0	0	12	4	1	0	7	0	0	0	7	63.64%	100 00%	100.00%	
#377		29	0	0	29	3	3	1	22	5	4	1	17	70 83%	77.27%	80.95%	
#378		329	0	0	329	47	17	0	265	23	12	11	242	80.40%	91.32%	95.28%	
#379		296	0	0	296	19	2	1	274	24	24	0	250	85 32%	91 24%	91 24%	
#380		446	0	0	446	27	13	0	406	44	44	0	362	83.60%	89 16%	89 16%	
#381		147	0	0	147	23	12	0	112	2	1	1	110	82 09%	98 21%	99 10%	
#382		187	0	0	187	50	5	0	132	7	4	3	125	69 83%	94 70%	96 90%	
#383		0	0	11047	11047	8304	1040	30	1673	360	90	270	1313	13 53%	78 48%	93 59%	
#384		0	0	144	144	63	23	2	56	29	16	13	27	25.47%	48.21%	62 79%	
#385		9426	0	0	9426	453	459	24	8490	950	705	245	7540	86.69%	88.81%	91 45%	
#386		700	0	0	700	116	117	10	457	150	121	29	307	56 43%	67.18%	71.73%	
#387		14	0	0	14	1	1	0	12	4	4	0	8	61.54%	66 67%	66 67%	
#388		87	0	0	87	9	1	3	74	19	17	2	55	67 90%	74.32%	76.39%	
#389		10	0	0	10	1	2	0	7	0	0	0	7	87 50%	100 00%	100.00%	
#390		15	0	0	15	3	4	1	7	2	2	0	5	50 00%	71.43%	71 43%	
#391		12	0	0	12	7	1	0	4	0	0	0	4	36 36%	100.00%	100.00%	
#392		0	281	0	281	32	38	12	199	56	42	14	143	65 90%	71 86%	77 30%	
#393		1814	0	0	1814	110	132	9	1563	224	208	16	1339	80 81%	85 67%	86 55%	
#394		10	0	0	10	0	7	0	3	2	2	0	1	33.33%	33.33%	33 33%	
#395		257	0	0	257	42	28	0	187	21	19	2	166	73.13%	88 77%	89.73%	
#396		51	0	0	51	17	6	0	28	5	3	2	23	53 49%	82 14%	88 46%	
#397		290	0	0	290	18	23	0	249	38	33	5	211	80.53%	84.74%	86 48%	
#398		68	0	0	68	7	10	2	49	7	6	1	42	76.36%	85.71%	87 50%	
#399		15	0	0	15	1	1	0	13	6	3	3	7	63.64%	53.85%	70.00%	
#400		1793	0	0	1793	78	171	3	1541	113	54	59	1428	91.54%	92 67%	96 36%	
#401		958	0	0	958	89	26	4	839	86	84	2	753	81 32%	89 75%	89.96%	
#402		17	0	0	17	2	8	0	7	1	0	1	6	75 00%	85.71%	100 00%	
#403		630	0	0	630	27	49	2	552	43	40	3	509	88 37%	92.21%	92 71%	
#404		915	0	0	915	497	112	7	299	20	9	11	279	35.54%	93 31%	96 88%	
#405		2	0	0	2	0	0	1	1	1	0	1	0	0 00%	0 00%	0 00%	
#406		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#407		1	0	0	1	0	0	0	1	1	0	1	0	0.00%	0 00%	0 00%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 07/01/2001 - 07/31/2001

Exhibit July PM Data
 Attachment 2B

AGGREGATE ORDER TYPES																
Company Info					LSR PROCESSING								FLOWTHROUGH			
					LESOG											
					Mechanized Interface Used			Manual	Rejects	Validated	Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#408		7	0	0	7	1	2	0	4	1	1	0	3	60.00%	75.00%	75.00%
#409		44	0	0	44	13	5	2	24	2	1	1	22	61.11%	91.67%	95.65%
#410		726	0	0	726	61	38	11	616	76	75	1	540	79.88%	87.66%	87.80%
#411		361	0	0	361	65	27	3	266	33	25	8	233	72.14%	87.59%	90.31%
#412		0	49	0	49	14	13	0	22	4	3	1	18	51.43%	81.82%	85.71%
#413		0	562	0	562	329	83	5	145	33	15	18	112	24.56%	77.24%	88.19%
#414		0	137	0	137	39	42	0	56	11	3	8	45	51.72%	80.36%	93.75%
#415		36	0	0	36	4	8	0	24	9	5	4	15	62.50%	62.50%	75.00%
#416		8	0	0	8	0	1	0	7	3	3	0	4	57.14%	57.14%	57.14%
#417		441	0	0	441	88	59	12	282	66	43	23	216	62.25%	76.60%	83.40%
#418		0	114	0	114	30	23	0	61	12	2	10	49	60.49%	80.33%	96.08%
#419		0	1379	0	1379	306	301	0	772	500	36	464	272	44.30%	35.23%	88.31%
#420		0	12	0	12	0	2	0	10	1	0	1	9	100.00%	90.00%	100.00%
#421		0	11	0	11	6	1	0	4	2	0	2	2	25.00%	50.00%	100.00%
#422		0	65	0	65	18	9	0	38	9	1	8	29	60.42%	76.32%	96.67%
#423		0	66	0	66	21	7	0	38	13	3	10	25	51.02%	65.79%	89.29%
#424		63	0	0	63	7	11	0	45	11	5	6	34	73.91%	75.56%	87.18%
#425		1081	0	0	1081	95	115	11	860	121	66	55	739	82.11%	85.93%	91.80%
#426		12	0	0	12	0	0	1	11	3	2	1	8	80.00%	72.73%	80.00%
#427		6	0	0	6	2	1	0	3	0	0	0	3	60.00%	100.00%	100.00%
#428		12	0	0	12	3	2	0	7	2	1	1	5	55.56%	71.43%	83.33%
#429		52	0	0	52	2	3	2	45	12	3	9	33	86.84%	73.33%	91.67%
#430		26	0	0	26	4	2	0	20	3	2	1	17	73.91%	85.00%	89.47%
#431		9	0	0	9	1	0	0	8	3	0	3	5	83.33%	62.50%	100.00%
#432		28	0	0	28	2	4	1	21	2	1	1	19	86.36%	90.48%	95.00%
LENS Subtotal		239417	0	0	239417	32740	21282	1598	183797	27664	22019	5645	156133	74.03%	84.95%	87.64%
EDI Subtotal		0	70602	0	70602	13472	11187	120	45823	7859	5607	2252	37964	66.55%	82.85%	87.13%
TAG Subtotal		0	0	47048	47048	10679	3782	328	32259	5360	4305	1055	26899	64.22%	83.38%	86.20%
TOTAL INTERFACES		239417	70602	47048	357067	56891	36251	2046	261879	40883	31931	8952	220996	71.33%	84.39%	87.38%

AGGREGATE ORDER TYPES																
Company info																
LSR PROCESSING																
LESOG																
Mechanized Interface Used																
Manual																
Rejects																
Validated																
Errors																
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#1		3	0	0	3	1	2	0	0	0	0	0	0	0 00%	0.00%	0 00%
#2		435	0	0	435	19	30	2	384	60	54	6	324	81.81%	84.38%	85.71%
#3		106	0	0	106	6	9	0	91	1	1	0	90	92 78%	98.90%	98.90%
#4		17	0	0	17	4	2	0	11	0	0	0	11	73 33%	100 00%	100 00%
#5		1677	0	0	1677	159	60	2	1456	44	34	10	1412	87 98%	96 98%	97 65%
#6		0	0	217	217	48	31	1	137	43	38	5	94	52.22%	68 61%	71 21%
#7		1	0	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0 00%
#8		4	0	0	4	0	2	0	2	0	0	0	2	100 00%	100 00%	100.00%
#9		211	0	0	211	18	42	9	142	40	34	6	102	66 23%	71.83%	75 00%
#10		156	0	0	156	15	6	4	131	23	11	12	108	80 60%	82 44%	90.76%
#11		11	0	0	11	1	0	0	10	4	4	0	6	54 55%	60.00%	60.00%
#12		345	0	0	345	36	23	8	278	54	47	7	224	72 96%	80 58%	82 66%
#13		2	0	0	2	0	1	0	1	0	0	0	1	100 00%	100 00%	100 00%
#14		2	0	0	2	0	2	0	0	0	0	0	0	0 00%	0.00%	0 00%
#15		111	0	0	111	6	35	0	70	4	0	4	66	91.67%	94 29%	100.00%
#16		12	0	0	12	7	2	0	3	2	2	0	1	10 00%	33 33%	33 33%
#17		117	0	0	117	35	3	0	79	3	2	1	76	67 26%	96 20%	97 44%
#18		625	0	0	625	65	32	0	528	3	3	0	525	88 53%	99 43%	99 43%
#19		0	9	0	9	0	4	0	5	3	0	3	2	100 00%	40.00%	100 00%
#20		372	0	0	372	20	21	0	331	9	7	2	322	92 26%	97 28%	97 87%
#21		693	0	0	693	28	15	3	647	56	56	0	591	87 56%	91.34%	91 34%
#22		410	0	0	410	49	13	4	344	54	53	1	290	73.98%	84.30%	84 55%
#23		387	0	0	387	33	16	2	336	119	118	1	217	58.97%	64.58%	64 78%
#24		186	0	0	186	21	13	1	151	5	3	2	146	85 88%	96.69%	97 99%
#25		117	0	0	117	26	8	1	82	8	5	3	74	70.48%	90 24%	93.67%
#26		2091	0	0	2091	248	82	9	1752	69	57	12	1683	84.66%	96 06%	96 72%
#27		0	0	608	608	7	63	0	538	16	12	4	522	96 49%	97.03%	97.75%
#28		459	0	0	459	127	55	4	273	13	9	4	260	65.66%	95.24%	96 65%
#29		0	55	0	55	2	4	0	49	5	4	1	44	88.00%	89.80%	91 67%
#30		169	0	0	169	1	2	0	166	102	100	2	64	38.79%	38 55%	39 02%
#31		178	0	0	178	12	15	0	151	9	8	1	142	87.65%	94.04%	94 67%
#32		0	0	1	1	0	0	0	1	1	1	0	0	0 00%	0.00%	0 00%
#33		1584	0	0	1584	96	131	1	1356	41	36	5	1315	90 88%	96 98%	97 34%
#34		120	0	0	120	9	9	0	102	18	17	1	84	76.36%	82 35%	83 17%
#35		169	0	0	169	15	12	1	141	17	12	5	124	82 12%	87 94%	91 18%
#36		35	0	0	35	2	3	0	30	2	2	0	28	87 50%	93 33%	93 33%

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING											FLOWTHROUGH				
Name	RESH / OCN	Mechanized Interface Used				LESOG			Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Manual Total Manual Fallout	Rejects Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout					
		#37		7	0	0	7	0	2	0	5	0	0				
#38		7	0	0	7	0	1	0	6	0	0	0	6	100.00%	100.00%	100.00%	
#39		979	0	0	979	118	69	4	788	94	90	4	694	76.94%	88.07%	88.52%	
#40		136	0	0	136	32	39	1	64	9	2	7	55	61.80%	85.94%	96.49%	
#41		236	0	0	236	4	6	0	226	24	19	5	202	89.78%	89.38%	91.40%	
#42		29424	0	0	29424	3828	2320	91	23185	3655	3507	148	19530	72.70%	84.24%	84.78%	
#43		17	0	0	17	4	1	0	12	4	3	1	8	53.33%	66.67%	72.73%	
#44		117	0	0	117	8	18	0	91	6	5	1	85	86.73%	93.41%	94.44%	
#45		0	1	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#46		4	0	0	4	0	3	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#47		115	0	0	115	17	15	4	79	17	14	3	62	66.67%	78.48%	81.58%	
#48		67	0	0	67	8	4	0	55	11	10	1	44	70.97%	80.00%	81.48%	
#49		110	0	0	110	7	4	0	99	3	3	0	96	90.57%	96.97%	96.97%	
#50		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#51		3386	0	0	3386	474	188	19	2705	523	473	50	2182	69.73%	80.67%	82.18%	
#52		724	0	0	724	67	47	3	607	44	41	3	563	83.90%	92.75%	93.21%	
#53		0	0	3	3	1	2	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#54		0	0	2320	2320	413	170	46	1691	237	201	36	1454	70.31%	85.98%	87.85%	
#55		60	0	0	60	17	36	1	6	2	0	2	4	19.05%	66.67%	100.00%	
#56		14477	0	0	14477	333	1441	11	12692	2545	2516	29	10147	78.08%	79.95%	80.13%	
#57		7609	0	0	7609	833	1558	1	5217	131	75	56	5086	84.85%	97.49%	98.55%	
#58		1114	0	0	1114	107	48	1	958	33	24	9	925	87.59%	96.56%	97.47%	
#59		0	6018	0	6018	923	1474	3	3618	399	345	54	3219	71.74%	88.97%	90.32%	
#60		0	8780	0	8780	1535	2098	4	5143	634	522	112	4509	68.67%	87.67%	89.62%	
#61		195	0	0	195	14	14	0	167	20	19	1	147	81.67%	88.02%	88.55%	
#62		322	0	0	322	19	17	3	283	78	76	2	205	68.33%	72.44%	72.95%	
#63		457	0	0	457	58	51	1	347	33	29	4	314	78.30%	90.49%	91.55%	
#64		65	0	0	65	1	7	0	57	2	2	0	55	94.83%	96.49%	96.49%	
#65		0	0	414	414	17	33	0	364	9	9	0	355	93.18%	97.53%	97.53%	
#66		29	0	0	29	0	3	3	23	1	0	1	22	100.00%	95.65%	100.00%	
#67		0	0	62	62	13	2	0	47	3	3	0	44	73.33%	93.62%	93.62%	
#68		20	0	0	20	5	2	0	13	0	0	0	13	72.22%	100.00%	100.00%	
#69		50	0	0	50	8	8	2	32	6	4	2	26	68.42%	81.25%	86.67%	
#70		0	0	5	5	0	2	0	3	1	1	0	2	66.67%	66.67%	66.67%	
#71		7	0	0	7	1	3	0	3	0	0	0	3	75.00%	100.00%	100.00%	
#72		16	0	0	16	4	3	0	9	1	1	0	8	61.54%	88.89%	88.89%	

AGGREGATE ORDER TYPES		LSR PROCESSING											FLOWTHROUGH			
Company Info		LESOG														
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Validated			Errors			Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's			
#73		61	0	0	61	5	2	0	54	5	4	1	49	84.48%	90.74%	92.45%
#74		5	0	0	5	0	0	0	5	1	1	0	4	80.00%	80.00%	80.00%
#75		51	0	0	51	3	10	2	36	6	4	2	30	81.08%	83.33%	88.24%
#76		265	0	0	265	24	32	2	207	22	19	3	185	81.14%	89.37%	90.69%
#77		1805	0	0	1805	215	225	0	1365	160	114	46	1205	78.55%	88.28%	91.36%
#78		0	139	0	139	9	44	2	84	20	14	6	64	73.56%	76.19%	82.05%
#79		8	0	0	8	0	1	0	7	1	1	0	6	85.71%	85.71%	85.71%
#80		377	0	0	377	43	41	1	292	8	1	7	284	86.59%	97.26%	99.65%
#81		481	0	0	481	44	32	1	404	31	31	0	373	83.26%	92.33%	92.33%
#82		22	0	0	22	5	1	0	16	2	2	0	14	66.67%	87.50%	87.50%
#83		508	0	0	508	57	29	12	410	42	41	1	368	78.97%	89.76%	89.98%
#84		1414	0	0	1414	117	61	3	1233	119	108	11	1114	83.20%	90.35%	91.16%
#85		41	0	0	41	2	3	0	36	9	9	0	27	71.05%	75.00%	75.00%
#86		6906	0	0	6906	447	498	5	5956	325	243	82	5631	89.08%	94.54%	95.86%
#87		3859	0	0	3859	441	206	18	3194	556	537	19	2638	72.95%	82.59%	83.09%
#88		25	0	0	25	6	3	1	15	4	3	1	11	55.00%	73.33%	78.57%
#89		334	0	0	334	33	9	2	290	6	5	1	284	88.20%	97.93%	98.27%
#90		1382	0	0	1382	173	104	2	1103	28	21	7	1075	84.71%	97.46%	98.08%
#91		70	0	0	70	39	4	0	27	6	4	2	21	32.81%	77.78%	84.00%
#92		197	0	0	197	28	9	0	160	10	10	0	150	79.79%	93.75%	93.75%
#93		1690	0	0	1690	136	80	1	1473	32	23	9	1441	90.06%	97.83%	98.43%
#94		28	0	0	28	7	5	1	15	1	1	0	14	63.64%	93.33%	93.33%
#95		729	0	0	729	85	14	5	625	66	65	1	559	78.84%	89.44%	89.58%
#96		631	0	0	631	60	108	1	462	12	9	3	450	86.71%	97.40%	98.04%
#97		1686	0	0	1686	209	128	2	1347	32	26	6	1315	84.84%	97.62%	98.06%
#98		339	0	0	339	14	24	1	300	46	45	1	254	81.15%	84.67%	84.95%
#99		502	0	0	502	57	58	4	383	11	9	2	372	84.93%	97.13%	97.64%
#100		0	4021	0	4021	532	958	1	2530	256	219	37	2274	75.17%	89.88%	91.22%
#101		113	0	0	113	6	16	1	90	28	27	1	62	65.26%	68.89%	69.66%
#102		77	0	0	77	5	4	0	68	5	5	0	63	86.30%	92.65%	92.65%
#103		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%
#104		1642	0	0	1642	131	89	3	1419	53	45	8	1366	88.59%	96.26%	96.81%
#105		0	0	836	836	5	38	0	793	72	71	1	721	90.46%	90.92%	91.04%
#106		47	0	0	47	1	2	1	43	11	9	2	32	76.19%	74.42%	78.05%
#107		976	0	0	976	72	27	5	872	50	50	0	822	87.08%	94.27%	94.27%
#108		74	0	0	74	13	20	1	40	0	0	0	40	75.47%	100.00%	100.00%

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING											FLOWTHROUGH				
Name	RESH / OCN	Mechanized Interface Used				Manual		Rejects		Validated		Errors			Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's				
		#109		480	0	0	480	69	35	3	373	47	40	7			
#110		173	0	0	173	21	8	0	144	5	2	3	139	85.80%	96.53%	98.58%	
#111		1124	0	0	1124	133	159	0	832	16	15	1	816	84.65%	98.08%	98.19%	
#112		3543	0	0	3543	475	267	35	2766	97	66	31	2669	83.15%	96.49%	97.59%	
#113		0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0.00%	
#114		0	0	157	157	3	15	0	139	24	20	4	115	83.33%	82.73%	85.19%	
#115		8	0	0	8	1	0	0	7	1	1	0	6	75.00%	85.71%	85.71%	
#116		847	0	0	847	78	21	5	743	65	60	5	678	83.09%	91.25%	91.87%	
#117		136	0	0	136	13	0	0	123	2	2	0	121	88.97%	98.37%	98.37%	
#118		4	0	0	4	0	0	0	4	0	0	0	4	100.00%	100.00%	100.00%	
#119		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%	
#120		0	0	1636	1636	28	166	0	1442	70	56	14	1372	94.23%	95.15%	96.08%	
#121		165	0	0	165	10	12	3	140	9	5	4	131	89.73%	93.57%	96.32%	
#122		186	0	0	186	13	10	0	163	18	18	0	145	82.39%	88.96%	88.96%	
#123		81	0	0	81	15	19	0	47	3	1	2	44	73.33%	93.62%	97.78%	
#124		5302	0	0	5302	439	767	17	4079	2007	191	1816	2072	76.68%	50.80%	91.56%	
#125		52	0	0	52	8	2	0	42	5	5	0	37	74.00%	88.10%	88.10%	
#126		3005	0	0	3005	215	404	3	2383	118	107	11	2265	87.55%	95.05%	95.49%	
#127		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#128		397	0	0	397	94	34	1	268	40	33	7	228	64.23%	85.07%	87.36%	
#129		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#130		0	0	4212	4212	75	88	21	4028	652	621	31	3376	82.91%	83.81%	84.46%	
#131		8867	0	0	8867	767	337	3	7760	1000	977	23	6760	79.49%	87.11%	87.37%	
#132		55	0	0	55	4	5	0	46	2	1	1	44	89.80%	95.65%	97.78%	
#133		64	0	0	64	23	6	0	35	2	2	0	33	56.90%	94.29%	94.29%	
#134		288	0	0	288	10	38	0	240	19	18	1	221	88.76%	92.08%	92.47%	
#135		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#136		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%	
#137		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%	
#138		19	0	0	19	8	3	0	8	0	0	0	8	50.00%	100.00%	100.00%	
#139		70	0	0	70	3	2	0	65	1	0	1	64	95.52%	98.46%	100.00%	
#140		16	0	0	16	0	1	0	15	2	0	2	13	100.00%	86.67%	100.00%	
#141		0	0	3744	3744	11	446	48	3239	1200	1013	187	2039	66.57%	62.95%	66.81%	
#142		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#143		52	0	0	52	10	7	0	35	5	4	1	30	68.18%	85.71%	88.24%	
#144		413	0	0	413	41	53	4	315	27	14	13	288	83.97%	91.43%	95.36%	

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING										FLOWTHROUGH				
LESOG																
Mechanized Interface Used																
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Validated		Errors				Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's			
#145		43	0	0	43	7	18	0	18	2	1	1	16	66.67%	88.89%	94.12%
#146		253	0	0	253	48	22	2	181	22	13	9	159	72.27%	87.85%	92.44%
#147		338	0	0	338	52	14	2	270	39	35	4	231	72.64%	85.56%	86.84%
#148		105	0	0	105	6	21	0	78	13	11	2	65	79.27%	83.33%	85.53%
#149		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#150		5	0	0	5	3	0	0	2	1	1	0	1	20.00%	50.00%	50.00%
#151		302	0	0	302	20	29	0	253	33	31	2	220	81.18%	86.96%	87.65%
#152		0	178	0	178	14	15	0	149	11	10	1	138	85.19%	92.62%	93.24%
#153		4	0	0	4	1	0	1	2	2	2	0	0	0.00%	0.00%	0.00%
#154		0	2164	0	2164	235	136	0	1793	257	203	54	1536	77.81%	85.67%	88.33%
#155		0	9989	0	9989	381	1843	0	7765	2185	1369	816	5580	76.13%	71.86%	80.30%
#156		74	0	0	74	13	7	1	53	5	5	0	48	72.73%	90.57%	90.57%
#157		840	0	0	840	198	142	1	499	6	6	0	493	70.73%	98.80%	98.80%
#158		49	0	0	49	5	6	1	37	6	6	0	31	73.81%	83.78%	83.78%
#159		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#160		137	0	0	137	17	11	0	109	41	37	4	68	55.74%	62.39%	64.76%
#161		137	0	0	137	20	13	0	104	2	2	0	102	82.26%	98.08%	98.08%
#162		34	0	0	34	2	0	0	32	1	1	0	31	91.18%	96.88%	96.88%
#163		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#164		31	0	0	31	6	1	0	24	0	0	0	24	80.00%	100.00%	100.00%
#165		188	0	0	188	7	10	17	154	124	107	17	30	20.83%	19.48%	21.90%
#166		22	0	0	22	3	8	0	11	5	0	5	6	66.67%	54.55%	100.00%
#167		183	0	0	183	54	16	1	112	26	14	12	86	55.84%	76.79%	86.00%
#168		0	4	0	4	3	0	0	1	0	0	0	1	25.00%	100.00%	100.00%
#169		240	0	0	240	47	15	3	175	22	19	3	153	69.86%	87.43%	88.95%
#170		10	0	0	10	3	2	0	5	0	0	0	5	62.50%	100.00%	100.00%
#171		71	0	0	71	9	7	5	50	19	15	4	31	56.36%	62.00%	67.39%
#172		2	0	0	2	0	0	1	1	1	1	0	0	0.00%	0.00%	0.00%
#173		96	0	0	96	4	9	2	81	23	10	13	58	80.56%	71.60%	85.29%
#174		0	0	8899	8899	44	280	14	8561	1021	982	39	7540	88.02%	88.07%	88.48%
#175		3053	0	0	3053	411	154	59	2429	391	352	39	2038	72.76%	83.90%	85.27%
#176		716	0	0	716	33	54	4	625	72	65	7	553	84.95%	88.48%	89.48%
#177		83	0	0	83	14	4	0	65	7	7	0	58	73.42%	89.23%	89.23%
#178		0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#179		13	0	0	13	0	4	0	9	1	1	0	8	88.89%	88.89%	88.89%
#180		0	2339	0	2339	1278	174	1	886	180	152	28	706	33.05%	79.68%	82.28%

AGGREGATE ORDER TYPES																
Company Info																
LSR PROCESSING																
LESOG																
Mechanized Interface Used																
Manual																
Rejects																
Validated																
Errors																
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
#181		2747	0	0	2747	329	150	25	2243	573	525	48	1670	66.16%	74.45%	76.08%
#182		946	0	0	946	115	78	2	751	42	40	2	709	82.06%	94.41%	94.66%
#183		129	0	0	129	12	8	0	109	1	1	0	108	89.26%	99.08%	99.08%
#184		83	0	0	83	13	2	2	66	19	18	1	47	60.26%	71.21%	72.31%
#185		2416	0	0	2416	282	165	11	1958	85	71	14	1873	84.14%	95.66%	96.35%
#186		392	0	0	392	29	51	5	307	60	46	14	247	76.71%	80.46%	84.30%
#187		366	0	0	366	9	14	1	342	9	8	1	333	95.14%	97.37%	97.65%
#188		358	0	0	358	12	42	1	303	22	21	1	281	89.49%	92.74%	93.05%
#189		272	0	0	272	46	12	0	214	6	5	1	208	80.31%	97.20%	97.65%
#190		825	0	0	825	154	79	6	586	59	51	8	527	71.99%	89.93%	91.18%
#191		196	0	0	196	19	14	1	162	2	1	1	160	88.89%	98.77%	99.38%
#192		530	0	0	530	64	7	0	459	17	15	2	442	84.84%	96.30%	96.72%
#193		7	0	0	7	0	1	0	6	3	0	3	3	100.00%	50.00%	100.00%
#194		82	0	0	82	26	13	0	43	11	10	1	32	47.06%	74.42%	76.19%
#195		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%
#196		66	0	0	66	8	9	0	49	1	0	1	48	85.71%	97.96%	100.00%
#197		75	0	0	75	11	6	1	57	5	5	0	52	76.47%	91.23%	91.23%
#198		169	0	0	169	33	5	0	131	6	6	0	125	76.22%	95.42%	95.42%
#199		20	0	0	20	9	4	0	7	1	1	0	6	37.50%	85.71%	85.71%
#200		0	0	24	24	2	15	1	6	2	2	0	4	50.00%	66.67%	66.67%
#201		0	0	4	4	0	2	1	1	0	0	0	1	100.00%	100.00%	100.00%
#202		856	0	0	856	118	168	11	559	66	47	19	493	74.92%	88.19%	91.30%
#203		22	0	0	22	1	10	0	11	3	0	3	8	88.89%	72.73%	100.00%
#204		48	0	0	48	4	16	0	28	7	7	0	21	65.63%	75.00%	75.00%
#205		105	0	0	105	18	10	0	77	8	8	0	69	72.63%	89.61%	89.61%
#206		811	0	0	811	105	52	0	654	35	30	5	619	82.10%	94.65%	95.38%
#207		466	0	0	466	41	14	2	409	60	50	10	349	79.32%	85.33%	87.47%
#208		110	0	0	110	8	21	0	81	1	1	0	80	89.89%	98.77%	98.77%
#209		1084	0	0	1084	91	83	2	908	17	16	1	891	89.28%	98.13%	98.24%
#210		47	0	0	47	3	9	3	32	14	11	3	18	56.25%	56.25%	62.07%
#211		2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%
#212		1321	0	0	1321	199	36	8	1078	240	229	11	838	66.19%	77.74%	78.54%
#213		14	0	0	14	3	1	0	10	0	0	0	10	76.92%	100.00%	100.00%
#214		0	0	3	3	0	1	0	2	1	1	0	1	50.00%	50.00%	50.00%
#215		37	0	0	37	5	1	0	31	2	2	0	29	80.56%	93.55%	93.55%
#216		359	0	0	359	34	33	1	291	19	15	4	272	84.74%	93.47%	94.77%

AGGREGATE ORDER TYPES																
Company Info																
LESOG																
Mechanized Interface Used																
Name	RESH / OCN	Mechanized Interface Used			Manual	Rejects	Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout					CLEC Caused Fallout
#217		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#218		27603	0	0	27603	7566	3705	287	16045	4734	3816	918	11311	49.84%	70.50%	74.77%
#219		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#220		122	0	0	122	6	8	0	108	23	23	0	85	74.56%	78.70%	78.70%
#221		10	0	0	10	0	0	0	10	5	3	2	5	62.50%	50.00%	62.50%
#222		0	0	772	772	1	49	0	722	87	85	2	635	88.07%	87.95%	88.19%
#223		68	0	0	68	4	2	2	60	10	9	1	50	79.37%	83.33%	84.75%
#224		1086	0	0	1086	150	91	7	838	34	32	2	804	81.54%	95.94%	96.17%
#225		0	0	1539	1539	176	109	23	1231	278	254	24	953	68.91%	77.42%	78.96%
#226		31	0	0	31	0	9	0	22	3	3	0	19	86.36%	86.36%	86.36%
#227		30	0	0	30	3	2	1	24	5	3	2	19	76.00%	79.17%	86.36%
#228		230	0	0	230	3	9	2	216	71	68	3	145	67.13%	67.13%	68.08%
#229		697	0	0	697	57	49	1	590	34	16	18	556	88.39%	94.24%	97.20%
#230		0	0	1142	1142	3	54	0	1085	128	124	4	957	88.28%	88.20%	88.53%
#231		129	0	0	129	6	5	7	111	27	27	0	84	71.79%	75.68%	75.68%
#232		390	0	0	390	52	8	2	328	21	18	3	307	81.43%	93.60%	94.46%
#233		0	0	324	324	2	28	0	294	25	21	4	269	92.12%	91.50%	92.76%
#234		8	0	0	8	2	0	0	6	0	0	0	6	75.00%	100.00%	100.00%
#235		28	0	0	28	2	3	1	22	5	4	1	17	73.91%	77.27%	80.95%
#236		329	0	0	329	47	17	0	265	23	12	11	242	80.40%	91.32%	95.28%
#237		296	0	0	296	19	2	1	274	24	24	0	250	85.32%	91.24%	91.24%
#238		446	0	0	446	27	13	0	406	44	44	0	362	83.60%	89.16%	89.16%
#239		147	0	0	147	23	12	0	112	2	1	1	110	82.09%	98.21%	99.10%
#240		187	0	0	187	50	5	0	132	7	4	3	125	69.83%	94.70%	96.90%
#241		0	0	4	4	2	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#242		0	0	57	57	21	10	0	26	11	7	4	15	34.88%	57.69%	68.18%
#243		50	0	0	50	2	37	1	10	2	2	0	8	66.67%	80.00%	80.00%
#244		388	0	0	388	36	69	8	275	89	69	20	186	63.92%	67.64%	72.94%
#245		14	0	0	14	1	1	0	12	4	4	0	8	61.54%	66.67%	66.67%
#246		54	0	0	54	6	1	0	47	9	8	1	38	73.08%	80.85%	82.61%
#247		10	0	0	10	1	2	0	7	0	0	0	7	87.50%	100.00%	100.00%
#248		10	0	0	10	2	2	1	5	2	2	0	3	42.86%	60.00%	60.00%
#249		10	0	0	10	7	1	0	2	0	0	0	2	22.22%	100.00%	100.00%
#250		1794	0	0	1794	109	132	9	1544	219	204	15	1325	80.89%	85.82%	86.66%
#251		10	0	0	10	0	7	0	3	2	2	0	1	33.33%	33.33%	33.33%
#252		235	0	0	235	35	15	0	185	20	18	2	165	75.69%	89.19%	90.16%

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING										FLOWTHROUGH					
		LESOG															
		Mechanized Interface Used				Manual		Rejects	Validated		Errors						
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
#253		9	0	0	9	1	0	0	8	1	1	0	7	77.78%	87.50%	87.50%	
#254		290	0	0	290	18	23	0	249	38	33	5	211	80.53%	84.74%	86.48%	
#255		32	0	0	32	2	4	2	24	2	2	0	22	84.62%	91.67%	91.67%	
#256		1793	0	0	1793	78	171	3	1541	113	54	59	1428	91.54%	92.67%	96.36%	
#257		933	0	0	933	88	25	4	816	82	80	2	734	81.37%	89.95%	90.17%	
#258		17	0	0	17	2	8	0	7	1	0	1	6	75.00%	85.71%	100.00%	
#259		630	0	0	630	27	49	2	552	43	40	3	509	88.37%	92.21%	92.71%	
#260		2	0	0	2	0	0	1	1	1	0	1	0	0.00%	0.00%	0.00%	
#261		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#262		1	0	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%	
#263		718	0	0	718	59	38	11	610	76	75	1	534	79.94%	87.54%	87.68%	
#264		209	0	0	209	22	12	3	172	10	8	2	162	84.38%	94.19%	95.29%	
#265		5	0	0	5	0	1	0	4	1	1	0	3	75.00%	75.00%	75.00%	
#266		0	8	0	8	1	1	0	6	0	0	0	6	85.71%	100.00%	100.00%	
#267		0	38	0	38	1	2	0	35	17	1	16	18	90.00%	51.43%	94.74%	
#268		0	2	0	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%	
#269		0	1	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%	
#270		0	5	0	5	1	0	0	4	1	0	1	3	75.00%	75.00%	100.00%	
#271		5	0	0	5	0	4	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#272		1	0	0	1	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%	
<i>LENS Subtotal</i>		183318	0	0	183318	23253	16816	883	142366	21231	17317	3914	121135	74.91%	85.09%	87.49%	
<i>EDI Subtotal</i>		0	33751	0	33751	4915	6753	11	22072	3970	2839	1131	18102	70.01%	82.01%	86.44%	
<i>TAG Subtotal</i>		0	0	26988	26988	873	1606	155	24354	3883	3523	360	20471	82.32%	84.06%	85.32%	
TOTAL INTERFACES		183318	33751	26988	244057	29041	25175	1049	188792	29084	23679	5405	159708	75.18%	84.59%	87.09%	

AGGREGATE ORDER TYPES																		
Company Info		LSR PROCESSING											FLOWTHROUGH					
		LESOG																
		Mechanized Interface Used				Manual	Rejects	Validated			Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation		
#1		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%		
#2		3	0	0	3	1	1	0	1	0	0	0	1	50.00%	100.00%	100.00%		
#3		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%		
#4		0	0	36	36	8	11	0	17	8	3	5	9	45.00%	52.94%	75.00%		
#5		10	0	0	10	2	1	0	7	1	0	1	6	75.00%	85.71%	100.00%		
#6		9	0	0	9	0	1	0	8	0	0	0	8	100.00%	100.00%	100.00%		
#7		4	0	0	4	1	0	0	3	1	0	1	2	66.67%	66.67%	100.00%		
#8		42	0	0	42	0	3	2	37	5	0	5	32	100.00%	86.49%	100.00%		
#9		66	0	0	66	24	19	1	22	6	5	1	16	35.56%	72.73%	76.19%		
#10		6	0	0	6	2	0	0	4	0	0	0	4	66.67%	100.00%	100.00%		
#11		24	0	0	24	7	2	0	15	3	3	0	12	54.55%	80.00%	80.00%		
#12		32	0	0	32	7	8	0	17	5	1	4	12	60.00%	70.59%	92.31%		
#13		43	0	0	43	2	10	0	31	11	6	5	20	71.43%	64.52%	76.92%		
#14		16	0	0	16	0	0	1	15	5	4	1	10	71.43%	66.67%	71.43%		
#15		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%		
#16		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%		
#17		68	0	0	68	3	6	1	58	11	11	0	47	77.05%	81.03%	81.03%		
#18		15	0	0	15	0	3	0	12	5	5	0	7	58.33%	58.33%	58.33%		
#19		147	0	0	147	42	12	5	88	24	19	5	64	51.20%	72.73%	77.11%		
#20		289	0	0	289	31	28	19	211	76	70	6	135	57.20%	63.98%	65.85%		
#21		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#22		26	0	0	26	5	8	0	13	2	1	1	11	64.71%	84.62%	91.67%		
#23		4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%		
#24		10	0	0	10	6	0	0	4	2	2	0	2	20.00%	50.00%	50.00%		
#25		8	0	0	8	0	4	0	4	4	4	0	0	0.00%	0.00%	0.00%		
#26		55	0	0	55	5	2	0	48	28	23	5	20	41.67%	41.67%	46.51%		
#27		0	8	0	8	0	2	0	6	3	3	0	3	50.00%	50.00%	50.00%		
#28		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%		
#29		51	0	0	51	3	25	1	22	8	7	1	14	58.33%	63.64%	66.67%		
#30		21	0	0	21	9	1	0	11	0	0	0	11	55.00%	100.00%	100.00%		
#31		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%		
#32		12	0	0	12	2	3	0	7	0	0	0	7	77.78%	100.00%	100.00%		
#33		18	0	0	18	6	3	0	9	1	1	0	8	53.33%	88.89%	88.89%		
#34		116	0	0	116	30	8	9	69	19	16	3	50	52.08%	72.46%	75.76%		
#35		0	1	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#36		4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%		

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING										FLOWTHROUGH					
		LESOG															
		Mechanized Interface Used				Manual	Rejects		Validated		Errors						
		RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
#37		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%	
#38		4	0	0	4	3	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#39		0	0	17	17	0	2	0	15	2	2	0	13	86.67%	86.67%	86.67%	
#40		0	0	6	6	0	0	0	6	0	0	0	6	100.00%	100.00%	100.00%	
#41		0	0	10	10	0	0	0	10	1	0	1	9	100.00%	90.00%	100.00%	
#42		0	0	3	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%	
#43		0	0	3	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%	
#44		15	0	0	15	2	3	0	10	2	2	0	8	66.67%	80.00%	80.00%	
#45		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%	
#46		4	0	0	4	0	0	0	4	1	1	0	3	75.00%	75.00%	75.00%	
#47		0	149	0	149	68	21	1	59	11	7	4	48	39.02%	81.36%	87.27%	
#48		27	0	0	27	5	13	0	9	3	3	0	6	42.86%	66.67%	66.67%	
#49		1068	0	0	1068	327	71	15	655	271	235	36	384	40.59%	58.63%	62.04%	
#50		9	0	0	9	0	1	1	7	4	3	1	3	50.00%	42.86%	50.00%	
#51		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#52		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#53		6	0	0	6	1	0	0	5	1	1	0	4	66.67%	80.00%	80.00%	
#54		7	0	0	7	0	0	1	6	0	0	0	6	100.00%	100.00%	100.00%	
#55		15	0	0	15	3	5	0	7	0	0	0	7	70.00%	100.00%	100.00%	
#56		10	0	0	10	0	2	0	8	0	0	0	8	100.00%	100.00%	100.00%	
#57		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#58		7	0	0	7	3	1	0	3	1	1	0	2	33.33%	66.67%	66.67%	
#59		9	0	0	9	5	1	0	3	0	0	0	3	37.50%	100.00%	100.00%	
#60		0	0	36	36	15	1	0	20	2	1	1	18	52.94%	90.00%	94.74%	
#61		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%	
#62		0	0	68	68	19	5	0	44	20	15	5	24	41.38%	54.55%	61.54%	
#63		11	0	0	11	2	6	0	3	0	0	0	3	60.00%	100.00%	100.00%	
#64		43	0	0	43	17	3	1	22	12	12	0	10	25.64%	45.45%	45.45%	
#65		146	0	0	146	24	19	3	100	23	8	15	77	70.64%	77.00%	90.59%	
#66		4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100.00%	100.00%	
#67		5	0	0	5	0	1	0	4	1	1	0	3	75.00%	75.00%	75.00%	
#68		0	757	0	757	292	94	6	365	149	93	56	216	35.94%	59.18%	69.90%	
#69		82	0	0	82	14	9	1	58	20	18	2	38	54.29%	65.52%	67.86%	
#70		4	0	0	4	0	1	0	3	2	1	1	1	50.00%	33.33%	50.00%	
#71		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%	
#72		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%	

AGGREGATE ORDER TYPES																	
Company Info					LSR PROCESSING								FLOWTHROUGH				
					LESOG												
Mechanized Interface Used					Manual	Rejects	Validated			Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
#73		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#74		21	0	0	21	6	1	0	14	3	3	0	11	55.00%	78.57%	78.57%	
#75		8	0	0	8	3	1	0	4	0	0	0	4	57.14%	100.00%	100.00%	
#76		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%	
#77		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#78		60	0	0	60	15	5	0	40	14	13	1	26	48.15%	65.00%	66.67%	
#79		33	0	0	33	15	3	1	14	3	3	0	11	37.93%	78.57%	78.57%	
#80		6	0	0	6	0	2	0	4	0	0	0	4	100.00%	100.00%	100.00%	
#81		12	0	0	12	0	0	0	12	0	0	0	12	100.00%	100.00%	100.00%	
#82		28	0	0	28	0	0	0	28	12	11	1	16	59.26%	57.14%	59.26%	
#83		3	0	0	3	1	1	0	1	0	0	0	1	50.00%	100.00%	100.00%	
#84		8	0	0	8	0	2	0	6	2	2	0	4	66.67%	66.67%	66.67%	
#85		4	0	0	4	0	1	0	3	0	0	0	3	100.00%	100.00%	100.00%	
#86		24	0	0	24	1	2	0	21	4	4	0	17	77.27%	80.95%	80.95%	
#87		19	0	0	19	4	5	1	9	1	1	0	8	61.54%	88.89%	88.89%	
#88		0	0	19	19	2	17	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#89		0	0	8	8	2	1	0	5	3	0	3	2	50.00%	40.00%	100.00%	
#90		0	0	15	15	4	3	0	8	3	1	2	5	50.00%	62.50%	83.33%	
#91		0	0	3	3	0	0	0	3	2	1	1	1	50.00%	33.33%	50.00%	
#92		2	0	0	2	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#93		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#94		48	0	0	48	20	10	0	18	5	3	2	13	36.11%	72.22%	81.25%	
#95		6	0	0	6	0	3	0	3	0	0	0	3	100.00%	100.00%	100.00%	
#96		7	0	0	7	2	1	0	4	0	0	0	4	66.67%	100.00%	100.00%	
#97		5	0	0	5	0	0	0	5	0	0	0	5	100.00%	100.00%	100.00%	
#98		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#99		5	0	0	5	0	0	0	5	0	0	0	5	100.00%	100.00%	100.00%	
#100		3	0	0	3	0	0	0	3	3	2	1	0	0.00%	0.00%	0.00%	
#101		27	0	0	27	9	5	0	13	2	2	0	11	50.00%	84.62%	84.62%	
#102		3	0	0	3	0	0	0	3	1	1	0	2	66.67%	66.67%	66.67%	
#103		14	0	0	14	3	2	0	9	2	2	0	7	58.33%	77.78%	77.78%	
#104		0	0	86	86	28	20	3	35	25	22	3	10	16.67%	28.57%	31.25%	
#105		1301	0	0	1301	560	114	32	595	155	134	21	440	38.80%	73.95%	76.66%	
#106		4	0	0	4	2	0	0	2	0	0	0	2	50.00%	100.00%	100.00%	
#107		9	0	0	9	2	3	1	3	1	0	1	2	50.00%	66.67%	100.00%	
#108		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	

AGGREGATE ORDER TYPES																
Company Info		LSR PROCESSING										FLOWTHROUGH				
Name	RESH / OCN	Mechanized Interface Used				LESOG		Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Manual	Rejects	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout				
						Total Manual Fallout	Auto Clarification									
#109		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#110		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#111		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#112		4	0	0	4	0	1	0	3	2	2	0	1	33.33%	33.33%	33.33%
#113		0	0	5	5	2	1	0	2	0	0	0	2	50.00%	100.00%	100.00%
#114		43	0	0	43	7	3	0	33	10	9	1	23	58.97%	69.70%	71.88%
#115		0	0	3	3	1	0	0	2	1	1	0	1	33.33%	50.00%	50.00%
#116		5	0	0	5	1	0	0	4	3	1	2	1	33.33%	25.00%	50.00%
#117		2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#118		5	0	0	5	1	0	0	4	0	0	0	4	80.00%	100.00%	100.00%
#119		50	0	0	50	20	2	3	25	4	4	0	21	46.67%	84.00%	84.00%
#120		0	0	32	32	0	11	2	19	12	12	0	7	36.84%	36.84%	36.84%
#121		12	0	0	12	1	1	0	10	1	1	0	9	81.82%	90.00%	90.00%
#122		35	0	0	35	4	4	1	26	11	8	3	15	55.56%	57.69%	65.22%
#123		24	0	0	24	2	11	0	11	0	0	0	11	84.62%	100.00%	100.00%
#124		34	0	0	34	10	3	0	21	8	8	0	13	41.94%	61.90%	61.90%
#125		0	0	3	3	0	1	1	1	1	0	1	0	0.00%	0.00%	0.00%
#126		0	0	5	5	5	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#127		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%
#128		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#129		11	0	0	11	5	1	0	5	4	3	1	1	11.11%	20.00%	25.00%
#130		6	0	0	6	2	0	0	4	0	0	0	4	66.67%	100.00%	100.00%
#131		23	0	0	23	2	4	4	13	2	2	0	11	73.33%	84.62%	84.62%
#132		11	0	0	11	0	0	3	8	0	0	0	8	100.00%	100.00%	100.00%
#133		4	0	0	4	1	0	0	3	1	0	1	2	66.67%	66.67%	100.00%
#134		5	0	0	5	3	1	0	1	1	0	1	0	0.00%	0.00%	0.00%
#135		8	0	0	8	1	4	0	3	1	1	0	2	50.00%	66.67%	66.67%
#136		13	0	0	13	2	2	2	7	3	1	2	4	57.14%	57.14%	80.00%
#137		23	0	0	23	1	3	0	19	12	10	2	7	38.89%	36.84%	41.18%
#138		9	0	0	9	4	1	0	4	3	2	1	1	14.29%	25.00%	33.33%
#139		4	0	0	4	0	3	0	1	0	0	0	1	100.00%	100.00%	100.00%
#140		16	0	0	16	4	0	0	12	2	2	0	10	62.50%	83.33%	83.33%
#141		73	0	0	73	41	8	2	22	11	5	6	11	19.30%	50.00%	68.75%
#142		8	0	0	8	1	4	0	3	0	0	0	3	75.00%	100.00%	100.00%
#143		51	0	0	51	10	7	1	33	9	1	8	24	68.57%	72.73%	96.00%
#144		735	0	0	735	131	82	17	505	230	181	49	275	46.85%	54.46%	60.31%

AGGREGATE ORDER TYPES																			
Company Info		LSR PROCESSING										FLOWTHROUGH							
		LESOG																	
		Mechanized Interface Used				Manual		Rejects		Validated		Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation			
#145		0	41	0	41	13	8	0	20	11	9	2	9	29.03%	45.00%	50.00%			
#146		562	0	0	562	102	61	10	389	154	117	37	235	51.76%	60.41%	66.76%			
#147		56	0	0	56	9	19	1	27	3	1	2	24	70.59%	88.89%	96.00%			
#148		312	0	0	312	43	34	12	223	78	62	16	145	58.00%	65.02%	70.05%			
#149		115	0	0	115	25	5	4	81	31	25	6	50	50.00%	61.73%	66.67%			
#150		4	0	0	4	1	1	0	2	0	0	0	2	66.67%	100.00%	100.00%			
#151		223	0	0	223	28	24	6	165	76	32	44	89	59.73%	53.94%	73.55%			
#152		4	0	0	4	1	0	0	3	1	1	0	2	50.00%	66.67%	66.67%			
#153		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%			
#154		0	0	13	13	1	1	0	11	9	2	7	2	40.00%	18.18%	50.00%			
#155		8	0	0	8	3	0	0	5	4	4	0	1	12.50%	20.00%	20.00%			
#156		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%			
#157		6	0	0	6	3	0	0	3	0	0	0	3	50.00%	100.00%	100.00%			
#158		8	0	0	8	0	0	0	8	0	0	0	8	100.00%	100.00%	100.00%			
#159		16	0	0	16	2	3	0	11	0	0	0	11	84.62%	100.00%	100.00%			
#160		3	0	0	3	0	0	0	3	2	1	1	1	50.00%	33.33%	50.00%			
#161		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%			
#162		7	0	0	7	1	1	1	4	3	2	1	1	25.00%	25.00%	33.33%			
#163		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%			
#164		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%			
#165		24	0	0	24	12	3	0	9	1	1	0	8	38.10%	88.89%	88.89%			
#166		3	0	0	3	0	0	0	3	0	0	0	3	100.00%	100.00%	100.00%			
#167		0	0	15	15	1	1	6	7	1	1	0	6	75.00%	85.71%	85.71%			
#168		40	0	0	40	9	7	1	23	3	1	2	20	66.67%	86.96%	95.24%			
#169		37	0	0	37	2	2	1	32	18	15	3	14	45.16%	43.75%	48.28%			
#170		77	0	0	77	3	9	1	64	22	21	1	42	63.64%	65.63%	66.67%			
#171		246	0	0	246	32	36	5	173	50	35	15	123	64.74%	71.10%	77.85%			
#172		61	0	0	61	10	6	1	44	12	6	6	32	66.67%	72.73%	84.21%			
#173		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%			
#174		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%			
#175		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%			
#176		1666	0	0	1666	224	278	53	1111	447	347	100	664	53.77%	59.77%	65.68%			
#177		10	0	0	10	1	2	0	7	5	0	5	2	66.67%	28.57%	100.00%			
#178		2	0	0	2	0	0	1	1	0	0	0	1	100.00%	100.00%	100.00%			
#179		261	0	0	261	26	9	3	223	118	104	14	105	44.68%	47.09%	50.24%			
#180		6	0	0	6	2	0	0	4	1	1	0	3	50.00%	75.00%	75.00%			

AGGREGATE ORDER TYPES		LSR PROCESSING											FLOWTHROUGH			
Company Info		LESOG														
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Validated			Errors			Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's			
#181		4	0	0	4	3	0	0	1	0	0	0	1	25.00%	100.00%	100.00%
#182		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#183		4	0	0	4	2	1	0	1	0	0	0	1	33.33%	100.00%	100.00%
#184		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#185		0	0	13	13	6	3	1	3	1	0	1	2	25.00%	66.67%	100.00%
#186		0	0	83	83	41	13	1	28	18	9	9	10	16.67%	35.71%	52.63%
#187		50	0	0	50	4	8	0	38	3	2	1	35	85.37%	92.11%	94.59%
#188		217	0	0	217	30	21	2	164	55	47	8	109	58.60%	66.46%	69.87%
#189		33	0	0	33	3	0	3	27	10	9	1	17	58.62%	62.96%	65.38%
#190		5	0	0	5	1	2	0	2	0	0	0	2	66.67%	100.00%	100.00%
#191		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%
#192		20	0	0	20	1	0	0	19	5	4	1	14	73.68%	73.68%	77.78%
#193		22	0	0	22	7	13	0	2	1	1	0	1	11.11%	50.00%	50.00%
#194		42	0	0	42	16	6	0	20	4	2	2	16	47.06%	80.00%	88.89%
#195		36	0	0	36	5	6	0	25	5	4	1	20	68.97%	80.00%	83.33%
#196		13	0	0	13	1	0	0	12	6	3	3	6	60.00%	50.00%	66.67%
#197		25	0	0	25	1	1	0	23	4	4	0	19	79.17%	82.61%	82.61%
#198		7	0	0	7	1	2	0	4	1	1	0	3	60.00%	75.00%	75.00%
#199		44	0	0	44	13	5	2	24	2	1	1	22	61.11%	91.67%	95.65%
#200		8	0	0	8	2	0	0	6	0	0	0	6	75.00%	100.00%	100.00%
#201		140	0	0	140	41	10	0	89	22	17	5	67	53.60%	75.28%	79.76%
#202		36	0	0	36	4	8	0	24	9	5	4	15	62.50%	62.50%	75.00%
#203		3	0	0	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33.33%
<i>LENS Subtotal</i>		9971	0	0	9971	2181	1188	236	6366	2246	1767	479	4120	51.07%	64.72%	69.98%
<i>EDI Subtotal</i>		0	956	0	956	374	125	7	450	174	112	62	276	36.22%	61.33%	71.13%
<i>TAG Subtotal</i>		0	0	484	484	135	91	14	244	109	70	39	135	39.71%	55.33%	65.85%
TOTAL INTERFACES		9971	956	484	11411	2690	1404	257	7060	2529	1949	580	4531	49.41%	64.18%	69.92%

AGGREGATE ORDER TYPES		LSR PROCESSING											FLOWTHROUGH			
Company Info		LESOG														
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout				
#1		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#2		0	0	16	16	4	0	0	12	5	2	3	7	53.85%	58.33%	77.78%
#3		0	0	732	732	96	81	1	554	43	23	20	511	81.11%	92.24%	95.69%
#4		0	0	84	84	7	5	1	71	9	3	6	62	86.11%	87.32%	95.38%
#5		0	0	357	357	62	43	2	250	57	18	39	193	70.70%	77.20%	91.47%
#6		0	0	301	301	57	23	0	221	28	20	8	193	71.48%	87.33%	90.61%
#7		6	0	0	6	0	1	0	5	0	0	0	5	100.00%	100.00%	100.00%
#8		457	0	0	457	41	27	7	382	29	19	10	353	85.47%	92.41%	94.89%
#9		116	0	0	116	10	8	2	96	5	4	1	91	86.67%	94.79%	95.79%
#10		177	0	0	177	30	8	3	136	16	11	5	120	74.53%	88.24%	91.60%
#11		765	0	0	765	102	81	13	569	63	36	27	506	78.57%	88.93%	93.36%
#12		30	0	0	30	10	10	3	7	2	2	0	5	29.41%	71.43%	71.43%
#13		41	0	0	41	11	4	0	26	1	0	1	25	69.44%	96.15%	100.00%
#14		47	0	0	47	10	6	0	31	2	1	1	29	72.50%	93.55%	96.67%
#15		17	0	0	17	0	3	2	12	5	4	1	7	63.64%	58.33%	63.64%
#16		0	0	3	3	0	0	1	2	2	1	1	0	0.00%	0.00%	0.00%
#17		10	0	0	10	0	0	3	7	1	0	1	6	100.00%	85.71%	100.00%
#18		9	0	0	9	0	1	0	8	1	0	1	7	100.00%	87.50%	100.00%
#19		31	0	0	31	0	0	11	20	19	14	5	1	6.67%	5.00%	6.67%
#20		124	0	0	124	1	15	8	100	60	10	50	40	78.43%	40.00%	80.00%
#21		5	0	0	5	0	0	0	5	2	0	2	3	100.00%	60.00%	100.00%
#22		3	0	0	3	0	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#23		53	0	0	53	3	1	0	49	16	13	3	33	67.35%	67.35%	71.74%
#24		13	0	0	13	4	4	0	5	0	0	0	5	55.56%	100.00%	100.00%
#25		12	0	0	12	2	4	0	6	0	0	0	6	75.00%	100.00%	100.00%
#26		0	228	0	228	99	48	2	79	14	11	3	65	37.14%	82.28%	85.53%
#27		0	17	0	17	4	1	0	12	3	2	1	9	60.00%	75.00%	81.82%
#28		0	26	0	26	19	1	0	6	0	0	0	6	24.00%	100.00%	100.00%
#29		0	4	0	4	0	1	0	3	0	0	0	3	100.00%	100.00%	100.00%
#30		0	20	0	20	8	0	5	7	4	4	0	3	20.00%	42.86%	42.86%
#31		0	203	0	203	155	13	2	33	6	3	3	27	14.59%	81.82%	90.00%
#32		0	146	0	146	81	14	3	48	10	7	3	38	30.16%	79.17%	84.44%
#33		21	0	0	21	2	2	4	13	2	1	1	11	78.57%	84.62%	91.67%
#34		2200	0	0	2200	498	206	32	1464	258	188	70	1206	63.74%	82.38%	86.51%
#35		0	0	2	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%
#36		0	0	1151	1151	170	176	0	805	93	47	46	712	76.64%	88.45%	93.81%

AGGREGATE ORDER TYPES																		
Company Info					LSR PROCESSING								FLOWTHROUGH					
					LESOG													
Mechanized Interface Used					Manual		Rejects		Validated		Errors							
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation		
#37		0	0	820	820	98	104	2	616	62	36	26	554	80.52%	89.94%	93.90%		
#38		0	0	8	8	0	1	0	7	4	0	4	3	100.00%	42.86%	100.00%		
#39		0	0	1422	1422	238	225	1	958	114	52	62	844	74.43%	88.10%	94.20%		
#40		0	0	858	858	117	177	2	562	81	44	37	481	74.92%	85.59%	91.62%		
#41		0	0	686	686	102	90	4	490	71	27	44	419	76.46%	85.51%	93.95%		
#42		0	0	2	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%		
#43		14	0	0	14	0	1	0	13	1	1	0	12	92.31%	92.31%	92.31%		
#44		34	0	0	34	8	1	0	25	5	5	0	20	60.61%	80.00%	80.00%		
#45		15	0	0	15	0	1	0	14	9	9	0	5	35.71%	35.71%	35.71%		
#46		20	0	0	20	0	5	0	15	1	1	0	14	93.33%	93.33%	93.33%		
#47		5	0	0	5	0	2	0	3	1	1	0	2	66.67%	66.67%	66.67%		
#48		0	537	0	537	294	120	12	111	40	22	18	71	18.35%	63.96%	76.34%		
#49		2791	0	0	2791	1878	55	14	844	117	101	16	727	26.87%	86.14%	87.80%		
#50		43	0	0	43	24	2	0	17	0	0	0	17	41.46%	100.00%	100.00%		
#51		7	0	0	7	0	2	0	5	5	0	5	0	0.00%	0.00%	0.00%		
#52		0	47	0	47	2	16	1	28	9	5	4	19	73.08%	67.86%	79.17%		
#53		0	0	743	743	150	51	8	534	25	13	12	509	75.74%	95.32%	97.51%		
#54		0	0	27	27	5	3	2	17	12	2	10	5	41.67%	29.41%	71.43%		
#55		6469	0	0	6469	250	150	16	6053	31	12	19	6022	95.83%	99.49%	99.80%		
#56		32	0	0	32	6	12	2	12	2	2	0	10	55.56%	83.33%	83.33%		
#57		102	0	0	102	32	7	2	61	9	7	2	52	57.14%	85.25%	88.14%		
#58		2	0	0	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%		
#59		515	0	0	515	69	44	4	398	22	6	16	376	83.37%	94.47%	98.43%		
#60		73	0	0	73	3	10	3	57	1	1	0	56	93.33%	98.25%	98.25%		
#61		3	0	0	3	0	0	1	2	1	1	0	1	50.00%	50.00%	50.00%		
#62		0	0	2	2	0	0	0	2	2	2	0	0	0.00%	0.00%	0.00%		
#63		8	0	0	8	0	1	3	4	3	3	0	1	25.00%	25.00%	25.00%		
#64		16	0	0	16	0	0	0	16	3	3	0	13	81.25%	81.25%	81.25%		
#65		0	0	51	51	19	9	0	23	4	3	1	19	46.34%	82.61%	86.36%		
#66		98	0	0	98	12	5	1	80	5	3	2	75	83.33%	93.75%	96.15%		
#67		2	0	0	2	0	0	0	2	1	0	1	1	100.00%	50.00%	100.00%		
#68		79	0	0	79	2	1	7	69	56	30	26	13	28.89%	18.84%	30.23%		
#69		0	1440	0	1440	658	117	6	659	150	93	57	509	40.40%	77.24%	84.55%		
#70		136	0	0	136	46	15	2	73	13	10	3	60	51.72%	82.19%	85.71%		
#71		8	0	0	8	1	1	0	6	0	0	0	6	85.71%	100.00%	100.00%		
#72		0	67	0	67	36	24	3	4	2	1	1	2	5.13%	50.00%	66.67%		

AGGREGATE ORDER TYPES		LSR PROCESSING												FLOWTHROUGH			
Company Info		LESOG															
Name	RESH / OCN	Mechanized Interface Used				Manual	Rejects	Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation	
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout					
#73		2	0	0	2	0	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#74		112	0	0	112	30	13	0	69	3	2	1	66	67.35%	95.65%	97.06%	
#75		25	0	0	25	0	18	0	7	2	0	2	5	100.00%	71.43%	100.00%	
#76		156	0	0	156	24	14	1	117	18	15	3	99	71.74%	84.62%	86.84%	
#77		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#78		9	0	0	9	3	1	0	5	0	0	0	5	62.50%	100.00%	100.00%	
#79		7	0	0	7	0	2	1	4	1	0	1	3	100.00%	75.00%	100.00%	
#80		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#81		0	0	905	905	116	6	89	694	367	312	55	327	43.31%	47.12%	51.17%	
#82		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#83		0	0	114	114	44	23	1	46	6	2	4	40	46.51%	86.96%	95.24%	
#84		0	0	14	14	3	5	0	6	1	0	1	5	62.50%	83.33%	100.00%	
#85		0	0	21	21	2	6	1	12	1	1	0	11	78.57%	91.67%	91.67%	
#86		0	0	9	9	1	0	1	7	3	1	2	4	66.67%	57.14%	80.00%	
#87		0	0	23	23	12	0	0	11	1	1	0	10	43.48%	90.91%	90.91%	
#88		120	0	0	120	14	32	3	71	10	4	6	61	77.22%	85.92%	93.85%	
#89		25	0	0	25	0	1	0	24	1	0	1	23	100.00%	95.83%	100.00%	
#90		31	0	0	31	2	3	0	26	4	0	4	22	91.67%	84.62%	100.00%	
#91		26	0	0	26	2	2	0	22	2	1	1	20	86.96%	90.91%	95.24%	
#92		36	0	0	36	2	5	1	28	6	3	3	22	81.48%	78.57%	88.00%	
#93		0	0	58	58	23	13	0	22	3	2	1	19	43.18%	86.36%	90.48%	
#94		6487	0	0	6487	777	392	78	5240	989	913	76	4251	71.55%	81.13%	82.32%	
#95		24	0	0	24	1	11	2	10	2	1	1	8	80.00%	80.00%	88.89%	
#96		0	0	22	22	18	0	2	2	1	1	0	1	5.00%	50.00%	50.00%	
#97		729	0	0	729	329	50	7	343	27	14	13	316	47.95%	92.13%	95.76%	
#98		0	0	4	4	0	1	0	3	1	1	0	2	66.67%	66.67%	66.67%	
#99		0	0	5	5	0	0	1	4	0	0	0	4	100.00%	100.00%	100.00%	
#100		0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#101		0	0	25	25	0	2	4	19	8	6	2	11	64.71%	57.89%	64.71%	
#102		0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%	
#103		0	0	2	2	1	0	0	1	0	0	0	1	50.00%	100.00%	100.00%	
#104		1460	0	0	1460	131	120	6	1203	49	35	14	1154	87.42%	95.93%	97.06%	
#105		2108	0	0	2108	273	197	40	1598	144	61	83	1454	81.32%	90.99%	95.97%	
#106		25	0	0	25	4	10	0	11	1	0	1	10	71.43%	90.91%	100.00%	
#107		0	0	16	16	14	0	0	2	1	0	1	1	6.67%	50.00%	100.00%	
#108		0	0	12	12	9	1	0	2	0	0	0	2	18.18%	100.00%	100.00%	

AGGREGATE ORDER TYPES																	
Company Info					LSR PROCESSING								FLOWTHROUGH				
Name	RESH / OCN	Mechanized Interface Used			Manual		Rejects		Validated		Errors			Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout					
#109		142	0	0	142	39	45	0	58	11	3	8	47	52.81%	81.03%	94.00%	
#110		24	0	0	24	11	2	0	11	2	1	1	9	42.86%	81.82%	90.00%	
#111		6	0	0	6	0	0	1	5	3	2	1	2	50.00%	40.00%	50.00%	
#112		122	0	0	122	24	18	0	80	13	5	8	67	69.79%	83.75%	93.06%	
#113		3	0	0	3	1	0	0	2	0	0	0	2	66.67%	100.00%	100.00%	
#114		15	0	0	15	1	7	0	7	3	1	2	4	66.67%	57.14%	80.00%	
#115		8	0	0	8	1	1	1	5	2	0	2	3	75.00%	60.00%	100.00%	
#116		386	0	0	386	77	47	1	261	36	11	25	225	71.88%	86.21%	95.34%	
#117		16	0	0	16	7	3	0	6	0	0	0	6	46.15%	100.00%	100.00%	
#118		0	28664	0	28664	5347	3248	5	20064	2542	2150	392	17522	70.03%	87.33%	89.07%	
#119		16	0	0	16	3	4	0	9	6	4	2	3	30.00%	33.33%	42.86%	
#120		4	0	0	4	0	0	0	4	2	0	2	2	100.00%	50.00%	100.00%	
#121		85	0	0	85	9	11	0	65	15	5	10	50	78.13%	76.92%	90.91%	
#122		4	0	0	4	2	0	0	2	1	1	0	1	25.00%	50.00%	50.00%	
#123		134	0	0	134	21	23	6	84	6	4	2	78	75.73%	92.86%	95.12%	
#124		0	0	23	23	2	0	0	21	1	1	0	20	86.96%	95.24%	95.24%	
#125		19	0	0	19	4	0	1	14	1	1	0	13	72.22%	92.86%	92.86%	
#126		667	0	0	667	146	43	5	473	51	30	21	422	70.57%	89.22%	93.36%	
#127		2111	0	0	2111	484	199	13	1415	131	63	68	1284	70.13%	90.74%	95.32%	
#128		80	0	0	80	0	30	3	47	18	4	14	29	87.88%	61.70%	87.88%	
#129		111	0	0	111	24	52	1	34	4	1	3	30	54.55%	88.24%	96.77%	
#130		0	291	0	291	84	42	2	163	33	22	11	130	55.08%	79.75%	85.53%	
#131		73	0	0	73	11	22	7	33	3	2	1	30	69.77%	90.91%	93.75%	
#132		2038	0	0	2038	328	216	30	1464	274	182	92	1190	70.00%	81.28%	86.73%	
#133		0	1179	0	1179	329	120	38	692	224	192	32	468	47.32%	67.63%	70.91%	
#134		44	0	0	44	5	7	4	28	16	2	14	12	63.16%	42.86%	85.71%	
#135		509	0	0	509	60	71	15	363	96	72	24	267	66.92%	73.55%	78.76%	
#136		0	0	3	3	0	1	1	1	0	0	0	1	100.00%	100.00%	100.00%	
#137		15	0	0	15	5	5	0	5	0	0	0	5	50.00%	100.00%	100.00%	
#138		19	0	0	19	0	2	0	17	17	9	8	0	0.00%	0.00%	0.00%	
#139		4	0	0	4	0	0	0	4	4	1	3	0	0.00%	0.00%	0.00%	
#140		0	0	17	17	4	2	5	6	2	1	1	4	44.44%	66.67%	80.00%	
#141		21	0	0	21	2	6	1	12	6	0	6	6	75.00%	50.00%	100.00%	
#142		234	0	0	234	12	38	33	151	114	63	51	37	33.04%	24.50%	37.00%	
#143		89	0	0	89	13	2	6	68	21	13	8	47	64.38%	69.12%	78.33%	
#144		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%	

AGGREGATE ORDER TYPES																	
Company Info		LSR PROCESSING											FLOWTHROUGH				
Name	RESH / OCN	Mechanized Interface Used				LESOG			Validated			Errors			Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		LENS	EDI	TAG	Total Mech LSR's	Manual	Rejects	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's				
						Total Manual Fallout	Auto Clarification										
#145		9	0	0	9	1	0	0	8	0	0	0	8	88.89%	100.00%	100.00%	
#146		247	0	0	247	42	18	1	186	3	1	2	183	80.97%	98.39%	99.46%	
#147		20	0	0	20	3	3	0	14	2	0	2	12	80.00%	85.71%	100.00%	
#148		24	0	0	24	3	2	0	19	2	1	1	17	80.95%	89.47%	94.44%	
#149		12	0	0	12	1	4	0	7	3	3	0	4	50.00%	57.14%	57.14%	
#150		0	404	0	404	275	28	6	95	57	40	17	38	10.76%	40.00%	48.72%	
#151		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#152		745	0	0	745	118	72	11	544	100	64	36	444	70.93%	81.62%	87.40%	
#153		0	0	11030	11030	8296	1036	29	1669	358	90	268	1311	13.52%	78.55%	93.58%	
#154		0	0	4	4	1	0	1	2	0	0	0	2	66.67%	100.00%	100.00%	
#155		9326	0	0	9326	447	414	23	8442	945	701	244	7497	86.72%	88.81%	91.45%	
#156		95	0	0	95	50	27	0	18	6	5	1	12	17.91%	66.67%	70.59%	
#157		0	281	0	281	32	38	12	199	56	42	14	143	65.90%	71.86%	77.30%	
#158		2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#159		915	0	0	915	497	112	7	299	20	9	11	279	35.54%	93.31%	96.88%	
#160		12	0	0	12	2	5	0	5	1	0	1	4	66.67%	80.00%	100.00%	
#161		0	49	0	49	14	13	0	22	4	3	1	18	51.43%	81.82%	85.71%	
#162		0	562	0	562	329	83	5	145	33	15	18	112	24.56%	77.24%	88.19%	
#163		0	137	0	137	39	42	0	56	11	3	8	45	51.72%	80.36%	93.75%	
#164		441	0	0	441	88	59	12	282	66	43	23	216	62.25%	76.60%	83.40%	
#165		0	106	0	106	29	22	0	55	12	2	10	43	58.11%	78.18%	95.56%	
#166		0	1341	0	1341	305	299	0	737	483	35	448	254	42.76%	34.46%	87.89%	
#167		0	10	0	10	0	2	0	8	0	0	0	8	100.00%	100.00%	100.00%	
#168		0	10	0	10	6	1	0	3	1	0	1	2	25.00%	66.67%	100.00%	
#169		0	65	0	65	18	9	0	38	9	1	8	29	60.42%	76.32%	96.67%	
#170		0	61	0	61	20	7	0	34	12	3	9	22	48.89%	64.71%	88.00%	
#171		63	0	0	63	7	11	0	45	11	5	6	34	73.91%	75.56%	87.18%	
#172		1076	0	0	1076	95	111	11	859	121	66	55	738	82.09%	85.91%	91.79%	
#173		12	0	0	12	0	0	1	11	3	2	1	8	80.00%	72.73%	80.00%	
#174		6	0	0	6	2	1	0	3	0	0	0	3	60.00%	100.00%	100.00%	
#175		12	0	0	12	3	2	0	7	2	1	1	5	55.56%	71.43%	83.33%	
#176		51	0	0	51	2	3	2	44	11	3	8	33	86.84%	75.00%	91.67%	
#177		26	0	0	26	4	2	0	20	3	2	1	17	73.91%	85.00%	89.47%	
#178		9	0	0	9	1	0	0	8	3	0	3	5	83.33%	62.50%	100.00%	
#179		28	0	0	28	2	4	1	21	2	1	1	19	86.36%	90.48%	95.00%	
LENS Subtotal		46128	0	0	46128	7306	3278	479	35065	4187	2935	1252	30878	75.09%	88.06%	91.32%	

AGGREGATE ORDER TYPES																
Company Info					LSR PROCESSING								FLOWTHROUGH			
LESOG																
Mechanized Interface Used					Manual	Rejects	Validated			Errors						
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
<i>EDI Subtotal</i>		0	35895	0	35895	8183	4309	102	23301	3715	2656	1059	19586	64.37%	84.06%	88.06%
<i>TAG Subtotal</i>		0	0	19576	19576	9671	2085	159	7661	1368	712	656	6293	37.74%	82.14%	89.84%
TOTAL INTERFACES		46128	35895	19576	101599	25160	9672	740	66027	9270	6303	2967	56757	64.34%	85.96%	90.00%

	ACHIEVED FLOW-THROUGH %	ADJUSTED FLOW-THROUGH %
CLEC AGGREGATE		
REGION ALL SERVICES	37.50%	86.36%

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH		
Company Info														
Name	RESH / OCN	Mechanized Interface Used			Manual	Rejects	Validated	Errors				Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation
		EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's			
#1		729	0	729	347	91	291	166	58	108	125	23.58%	42.96%	68.31%
#2		277	0	277	177	3	97	52	43	9	45	16.98%	46.39%	51.14%
#3		143	0	143	126	2	15	9	2	7	6	4.48%	40.00%	75.00%
#4		719	0	719	470	31	218	76	28	48	142	22.19%	65.14%	83.53%
#5		25	0	25	3	5	17	2	0	2	15	83.33%	88.24%	100.00%
#6		0	95	95	17	6	72	32	32	0	40	44.94%	55.56%	55.56%
#7		3	0	3	3	0	0	0	0	0	0	0.00%	0.00%	0.00%
#8		54	0	54	43	6	5	3	1	2	2	4.35%	40.00%	66.67%
#9		0	1555	1555	653	118	784	226	120	106	558	41.92%	71.17%	82.30%
#10		2	0	2	0	1	1	0	0	0	1	100.00%	100.00%	100.00%
#11		0	47	47	26	10	11	5	2	3	6	17.65%	54.55%	75.00%
#12		0	151	151	82	22	47	10	4	6	37	30.08%	78.72%	90.24%
#13		0	6	6	6	0	0	0	0	0	0	0.00%	0.00%	0.00%
#14		0	46	46	19	8	19	15	11	4	4	11.76%	21.05%	26.67%
#15		0	1674	1674	1573	101	0	0	0	0	0	0.00%	0.00%	0.00%
#16		708	0	708	203	7	498	58	18	40	440	66.57%	88.35%	96.07%
#17		1963	0	1963	251	33	1679	110	21	89	1569	85.23%	93.45%	98.68%
#18		19	0	19	7	9	3	2	0	2	1	12.50%	33.33%	100.00%
#19		0	13	13	8	0	5	2	2	0	3	23.08%	60.00%	60.00%
#20		925	0	925	740	113	72	40	24	16	32	4.02%	44.44%	57.14%
#21		10	0	10	2	0	8	1	1	0	7	70.00%	87.50%	87.50%
#22		0	29	29	13	2	14	10	4	6	4	19.05%	28.57%	50.00%
#23		0	60	60	42	10	8	6	0	6	2	4.55%	25.00%	100.00%
#24		2286	0	2286	963	243	1080	300	185	115	780	40.46%	72.22%	80.83%
#25		4	0	4	3	0	1	1	0	1	0	0.00%	0.00%	0.00%
#26		628	0	628	246	69	313	68	50	18	245	45.29%	78.27%	83.05%
#27		182	0	182	94	11	77	18	11	7	59	35.98%	76.62%	84.29%
#28		167	0	167	71	2	94	60	38	22	34	23.78%	36.17%	47.22%
#29		211	0	211	143	8	60	20	8	12	40	20.94%	66.67%	83.33%
EDI Subtotal		9055	0	9055	3892	634	4529	986	488	498	3543	44.72%	78.23%	87.89%
TAG Subtotal		0	3676	3676	2439	277	960	306	175	131	654	20.01%	68.13%	78.89%
TOTAL INTERFACES		9055	3676	12731	6331	911	5489	1292	663	629	4197	37.50%	76.46%	86.36%

