Lisa S Foshee General Attorney

BellSouth Telecommunications, Inc. 150 South Monroe Street Room 400 Tallahassee, Florida 32301 (404) 335-0754

January 25, 2002

Mrs. Blanca S. Bayó
Director, Division of the Commission Clerk
and Administrative Services
Florida Public Service Commission
2540 Shumard Oak Boulevard
Tallahassee, FL 32399-0850

Re: 960786-B-TL (Section 271)

Dear Ms. Bayó:

Enclosed please find the original and six copies of BellSouth Telecommunications, Inc.'s Notice of Filing with attached Affidavit of Alphonso J. Varner which we ask that you file in the captioned docket.

A copy of this letter is enclosed. Please mark it to indicate that the original was filed and return the copy to me. Copies have been served to the parties by Federal Express as shown on the attached Certificate of Service.

Sincerely,

Lisa S. Foshee

Enclosures

cc: All Parties of Record Marshall M. Criser III Fred J. McCallum

### CERTIFICATE OF SERVICE DOCKET NO. 960786-B-TL

I HEREBY CERTIFY that a true and correct copy of the foregoing was served by

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Lisa Foshee

(+) Signed Protective Agreement

#### BEFORE THE FLORIDA PUBLIC SERVICE COMMISSION

In Re: Consideration of BellSouth	)	
Telecommunications, Inc.'s entry into	)	Docket No. 960786-B-TL
interLATA services pursuant to Section	)	
271 of the Federal Telecommunications	)	
Act of 1996.	)	
	)	Filed: January 25, 2002

#### BELLSOUTH TELECOMMUNICATIONS, INC.'S NOTICE OF FILING

BellSouth Telecommunications, Inc. ("BellSouth") hereby files the Affidavit of Alphonso J. Varner that attaches BellSouth's performance data reflecting performance for the month of November 2001. The Affidavit and the accompanying attachments describe the performance data and explain the conclusions that can be drawn from it.

Respectfully submitted this 25th day of January 2002.

BELLSOUTH TELECOMMUNICATIONS, INC.

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## Before the Florida Public Service Commission Tallahassee, Florida

# AFFIDAVIT OF ALPHONSO J. VARNER ON BEHALF OF BELLSOUTH TELECOMMUNICATIONS, INC. FILED JANUARY 25, 2002

- I, Alphonso J. Varner, being of lawful age and duly sworn upon my oath, depose and state:
- My name is Alphonso J. Varner. I am employed by BellSouth as Senior Director in Interconnection Services. My business address is 675 West Peachtree Street, Atlanta, Georgia 30375.

#### PROFESSIONAL AND EDUCATIONAL BACKGROUND

- 2. I graduated from Florida State University in 1972 with a Bachelor of Engineering Science degree in systems design engineering. I immediately joined Southern Bell in the division of revenues organization with the responsibility for preparation of all Florida investment separations studies for division of revenues and for reviewing interstate settlements.
- 3. Subsequently, I accepted an assignment in the rates and tariffs organization with responsibilities for administering selected rates and tariffs including preparation of tariff filings. In January 1994, I was appointed Senior Director of Pricing for the nine-state region. I was named Senior Director for Regulatory Policy and Planning in August 1994.

In April 1997, I was named Senior Director of Regulatory for the nine-state BellSouth region, and I accepted my current position in March 2001.

#### II. PURPOSE OF AFFIDAVIT

4. The purpose of my Affidavit is to provide data specific to BellSouth's operations in Florida. This filing reflects performance for the month of November 2001. Exhibit November PM Data and Attachments 1F though 3F that accompany this filing describe the data and explain the conclusions that can be drawn from it.

1	DISCU	JSSION OF PERFORMANCE MEASUREMENTS DATA	
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#### **DISCUSSION OF PERFORMANCE MEASUREMENTS DATA** 1 2 3 I. ANALYSIS OF PERFORMANCE MEASUREMENTS 4 5 A. Introduction 6 7 BellSouth is currently producing state level results based on the January 12, 8 2001, Georgia Order from Docket 7892-U. While there are some differences from the interim Service Quality Measurement (SQM) Version 3.0 approved 9 10 by this Commission on July 3, 2001, they are minor and should not cause any 11 difficulty in determining BellSouth's overall performance level. 12 Attachment 1F is the Monthly State Summary (MSS) for Florida for November 13 14 2001. The MSS contains 2,337 sub-metrics based on the Georgia Public 15 Service Commission (GPSC) Docket 7892-U. As shown in Attachment 1F, there were 901 sub-metrics for which there was CLEC activity in November 16 17 2001 and that were compared to either benchmarks or retail analogues. BellSouth met or exceeded the criteria for 716 of these 901 sub-metrics, or 18 19 79%. The remainder (1,436) of the sub-metrics were either diagnostic (916), 20 had no CLEC activity (484), were parity by design (10), are still under development (2) or were removed due to computational problems (24). 21

As explained in previous updates to this Exhibit, three of the measures have been identified by BellSouth as having deficiencies in their calculations over the September through November period and are being investigated and evaluated for appropriate program code corrections. These three measures are FOC & Reject Response Completeness and FOC & Reject Completeness (Multiple Responses), Average Jeopardy Notice Interval and LNP Disconnect Timeliness. As the program coding corrections were/are completed, the additional sub-metrics affected by the changes have and will be included in the Exhibit updates. As of this update for November data, corrections have been implemented for all of the sub-metrics in the FOC & Reject Response Completeness and FOC & Reject Completeness (Multiple Responses) measures, and these sub-metrics are included in the November "Met/Total" (716/901) percentage figure. The Average Jeopardy Notice Interval measures are still undergoing program coding changes. As these corrections are completed, the additional sub-metrics affected by the changes will be included in the Exhibit updates. The LNP Disconnect Timeliness measure is still under review by the Georgia PSC.

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During the three-month period of September through November 2001, there were a total of 723 sub-metrics that had CLEC activity for all three months and that were compared with either a benchmark or retail analogue. Of those 723 sub-metrics, 612 or 85% satisfied the comparison criteria for a minimum of two of the three months.

Two general issues can impact the degree to which BellSouth's performance data is meaningful. First, the extreme disaggregation of the data in the reports often dilutes the universe size of individual measurements, which in turn reduces the confidence level of each of the individual Z-test results. As a result, there are many performance measurements for which the results are statistically inconclusive due to the small number of observations. Second, in situations in which there are a large number of observations and the difference between the means is very small, the results can be misleading and not indicative of the absolute level of performance that BellSouth provides to CLECs.

With respect to the first issue, in many cases, the extensive levels of disaggregation leads to numerous sub-metrics with fewer than 30 observations, which is generally accepted as the smallest number of observations for application of the Z-test. Despite this fact, BellSouth has reported results for all of the measures, even those with statistically inconclusive universe sizes.

The second issue arises in situations where BellSouth provides very high quality service to both BellSouth's retail units and the CLECs, where there are very large universe sizes, and the difference between the means is very small. This scenario can cause an apparent missed condition from a

Installation Appointments (%MIA), for Resale Residence / Non-Dispatch / < 10 Circuits (A.2.11.1.1.2) showed that BellSouth retail had 0.04% missed appointments for the 669,232 scheduled orders. The CLEC %MIA for the same period is 0.15% missed appointments for 46,311 scheduled orders. While there is very little difference in the results, only eleven one hundredths of a percentage point, the universe is so large that the Z-test becomes overly sensitive to any difference. As a result, the statistical test shows that the submetric missed the standard criteria, but BellSouth's actual performance is at a very high level for both the CLECs and BellSouth retail, in this case, greater than 99.8%. From a practical point of view, the CLECs' ability to compete has not been hindered, even though the statistical result does not technically meet the retail analogue.

In reviewing the data, the Florida Public Service Commission (Commission) should use the data as a tool in analyzing whether BellSouth has met its commitments. It is not a substitute for the qualitative evaluation of BellSouth's performance. The commission will still need to conduct a qualitative assessment of the data that considers, among other things, universe size, distributional properties of the data, as well as overall performance.

Each sub-metric designated as having not satisfied the benchmark or BellSouth retail analogue requirement for September, October and/or November 2001 is included in this Exhibit. Each sub-metric discussed is labeled being missed in any one or more of the months (September/October/November) included in this filing. The following paragraphs will address specific performance measurements associated with each checklist item. B. CHECKLIST ITEM 1 - INTERCONNECTION 1. Collocation BellSouth provides three separate collocation reports: 1) Average Response Time; 2) Average Arrangement Time; and 3) Percent of Due Dates Missed. Section E in Attachment 1F, Items E.1.1.1 through E.1.3.2, provides these results. BellSouth met the approved benchmarks for all 10 of the 10 submetrics that had CLEC activity in September, October and November 2001. For the three-month period, September through November 2001, there were 8 sub-metrics for which there was CLEC activity in all three months and were compared to retail analogues or benchmarks. All 8 of these sub-metrics met the retail analogue/benchmark comparisons in all three months.

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#### 1 2. Local Interconnection Trunking 2 Trunking Reports 3 Attachment 1F, Section C, Items C.1.1 to C.4.2 of the MSS contains data for ordering, provisioning, maintenance and repair, and billing associated with 4 5 Local Interconnection Trunks. 6 7 In September 2001, BellSouth met 20 of 25 sub-metrics or 80% and in 8 October, met 19 of the 25 sub-metrics or 76% of the applicable 9 benchmarks/analogues for all local interconnection trunking measures having 10 CLEC activity. In November, BellSouth met 21 of the 25 sub-metrics or 84% 11 of the benchmarks/retail analogues having CLEC activity. The sub-metrics 12 that did not meet the benchmarks/retail analogues for September, October 13 and/or November 2001 are as follows: 14 Reject Interval / Local Interconnection Trunks (C.1.2) (September/October) 15 16 BellSouth met the benchmark interval for 47 of the 57 rejected ASRs for this 17 sub-metric in September and 57 of the 72 rejected ASRs in October 2001. The 85% benchmark required that 49 of the 57 September rejects and 62 of 18 the 72 rejected ASRs in October be returned within the 4-day interval. 19 BellSouth met the benchmark for this sub-metric in November 2001. 20 21 22 FOC Timeliness / Local Interconnection Trunks (C.1.3) (November)

1 BellSouth met the 10-day benchmark interval for 142 of the 153 FOCs 2 (91.03%) returned for this sub-metric in November 2001. 3 benchmark required that 146 FOCs be returned bases on the number of 4 orders in the period. BellSouth met the benchmark for this sub-metric in 5 September and October 2001. 6 7 FOC & Reject Response Completeness / Local Interconnection Trunks 8 (C.1.4) (October/November) 9 BellSouth met the standard criteria for 99 of the 111 responses returned for 10 this sub-metric in October and for 113 of the 120 responses returned in 11 November 2001. The 95% benchmark required that 106 of the 111 of the 12 October responses and 114 of the 120 November responses meet the 13 criteria. BellSouth met the benchmark for this sub-metric in September 2001. 14 Order Completion Interval / Local Interconnection Trunks (C.2.1) 15 16 (September/October) Investigation has identified that a significant number of the orders for this sub-17 18 metric are for new trunk groups. These orders have a normal installation 19 interval of 30 business days. Trunk group augment orders receive a 20 business day completion interval unless the customer requests a longer 20 21 interval. These intervals are consistent with the 21 to 27-day OCI intervals for 22 CLEC orders for this sub-metric. BellSouth met the retail analogue 23 comparison for this sub-metric in November 2001.

1 2 Provisioning Troubles within 30 Days/ Local Interconnection Trunks (C.2.6) 3 (October) 4 Analysis of the result for this sub-metric revealed that all 72 trouble reports 5 generated were involved with the same event. One CLEC, performing 6 provisioning activities, requested that the trunks be busied out while the work 7 was performed. The trouble ticket should have been entered as "info only" 8 and excluded from this measurement. With the proper coding, this sub-metric 9 would have met the retail analogue comparison for the month. BellSouth met 10 the retail analogue comparison for this sub-metric in September and 11 November 2001. 12 13 Service Order Accuracy / Local Interconnection Trunks / < 10 Circuits / Non-14 Dispatch (C.2.11.1.2) (November) 15 BellSouth met the standard for 24 of the 26 orders (92.31%) reviewed for 16 November 2001. The 95% benchmark set a requirement of 25 of the 26 17 orders for November based on the quantity of orders for this sub-metric. 18 Although BellSouth is within one order of the benchmark for this measure, BellSouth continues to focus on this measurement in order to improve results 19 20 to meet the benchmark. BellSouth met the benchmark for this sub-metric in 21 September and October 2001. 22

1 Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits / Non-2 Dispatch (C.2.11.2.2) (October) 3 BellSouth met the standard for 18 of the 19 orders reviewed for this sub-4 metric in October 2001. The 95% benchmark set a requirement of all 19 5 orders in October based on the quantity of orders for this sub-metric. 6 BellSouth continues to focus on this measurement in order to improve results 7 to meet the benchmark. BellSouth met the benchmark for this sub-metric in 8 September and November 2001. 9 10 Customer Trouble Report Rate / Local Interconnection Trunks / Non-Dispatch 11 (C.3.2.2) (September) 12 BellSouth provided over 99.8% trouble free service for both retail and the 13 CLECs for this sub-metric for the month of September. When BellSouth 14 provisions high quality service coupled with very large universe sizes, it can 15 cause an apparent out of equity condition from a quantitative viewpoint. In 16 these cases, there is very little variation and the universe size is so large that 17 the Z-test becomes overly sensitive to any difference. In other words, the 18 statistical test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual 19 20 performance for both CLECs and its own retail operations is at a very high level - in this case over 99%. From a practical point of view, the CLECs' 21 ability to compete has not been hindered even though the statistical results 22 23 may technically show that BellSouth failed to meet the benchmark/analogue.

1	BellSouth met the retail analogue for this sub-metric in October and
2	November 2001.
3	
4	Maintenance Average Duration / Local Interconnection Trunks / Dispatch
5	(C.3.3.1) (September)
6	There was only one order for this sub-metric in September 2001. The small
7	universe of orders for the month does not provide a statistically conclusive
8	comparison to the retail analogue. BellSouth met the retail analogue
9	comparison for this sub-metric in October and November 2001.
10	
11	% Repeat Troubles within 30 Days / Local Interconnection Trucks (C.3.4.2)
12	(October)
13	The results indicated that there were 72 repeat trouble reports for this sub-
14	metric in October 2001. All 72 of these repeat reports were associated with
15	one group of trunks being busied out multiple times during cooperative testing
16	with a CLEC during their switch modification work. These reports should
17	have been charged as "info only" and not counted against this measurement.
18	With proper coding, this sub-metric would have met the retail analogue
19	comparison for the month. BellSouth met the retail analogue comparison for
20	this sub-metric in September and November 2001.
21	
22	Invoice Accuracy - Interconnection (C.4.1) (September/November)
23	The CLECs experienced Local Interconnection invoice accuracy rates in
24	September and November 2001 that were less than for the invoices BellSouth

sends to its customers (98.61% accuracy for BellSouth versus 97.84% for the CLEC invoices for September and 98.32% accuracy for BellSouth versus 97.71% for the CLEC invoices in November). The difference in September performance was the result of provisioning and system errors that caused the over billing of one CLEC customer. The difference in November performance was the result of three different problems. The first problem involved the discovery by BellSouth that mileage quantities on numerous CLEC dedicated transport accounts were incorrectly understated. Service orders were issued to correct the billing. The second problem involved problems that BellSouth had in turning up SMARTRing service for one CLEC customer. Consequently, the due dates on the DS1 and DS0 orders were missed. Adjustments were given to waive the non-recurring charges associated with SMARTRing. The third problem involved adjustments for non-recurring charges that were billed in error to a CLEC customer who has a bill-and-keep arrangement for trunks and facilities. BellSouth met the retail analogue comparison for this sub-metric in October 2001. Trunk Blockage BellSouth has developed a trunk blocking report that compares BellSouth retail's trunk blockage rates to those of CLECs. The report, Trunk Group Performance Report (TGP), Attachment 3F, displays trunk blocking in a manner that accurately represents the customer experience. The TGP report tabulates actual call blocking as a percentage of call attempts for all comparable trunk groups administered by BellSouth that handle CLEC and

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BellSouth traffic, and provides a direct comparison of hour-by-hour blocking between CLEC and BellSouth trunk groups. The analogue/benchmark for the Trunk Group Performance measure is any consecutive two-hour period in 24 hours where CLEC blockage exceeds BellSouth blockage by more than 0.5%. BellSouth met or exceeded the benchmark for this sub-metric in September, October and November 2001.

#### C. CHECKLIST ITEM 2 – UNBUNDLED NETWORK ELEMENTS (UNE)

This section addresses the measures associated with UNEs under checklist item 2. Attachment 1F, Sections B1 – B3, provides data that is divided into Ordering, Provisioning and Maintenance & Repair operations. In general, the Ordering function is disaggregated into 17 sub-metrics, the Provisioning function has 19 sub-metrics, and there are 12 sub-metrics for the Maintenance & Repair function. All Ordering measures will be included in this checklist item because of the overall relationship of the mechanized, partially mechanized and manual processing of Local Service Requests (LSRs). The Provisioning and Maintenance & Repair measures for the following products are included in the checklist item as shown below:

20 Product Checklist Item:
21 Combo (Loop & Port) #2 – Unbundled Network Elements
22 Combo (Other) #2 – Unbundled Network Elements
23 Other Design #2 – Unbundled Network Elements

1	Other Non-Design	#2 – Unbundled Network Elements
2	xDSL Loop	#4 – Unbundled Local Loops
3	UNE ISDN Loop	#4 - Unbundled Local Loops
4	Line Sharing	#4 - Unbundled Local Loops
5	2w Analog Loop Design	#4 - Unbundled Local Loops
6	2w Analog Loop Non Design	#4 - Unbundled Local Loops
7	2w Analog Loop w/INP Design	#4 - Unbundled Local Loops
8	2w Analog Loop w/INP Non Design	#4 - Unbundled Local Loops
9	2w Analog Loop w/LNP Design	#4 - Unbundled Local Loops
10	2w Analog Loop w/LNP Non Design	#4 - Unbundled Local Loops
11	Digital Loop < DS1	#4 - Unbundled Local Loops
12	Digital Loop => DS1	#4 - Unbundled Local Loops
13	Local Interoffice Transport	#5 – Unbundled Local Transport
14	Switch Ports	#6 – Unbundled Local Switching
15	INP Standalone	#11 – Local Number Portability
16	LNP Standalone	#11 – Local Number Portability
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18	An overall review of the UNE sub-	metrics for Ordering, Provisioning,
19	Maintenance & Repair and Billing	indicates that BellSouth met the
20	benchmark/analogue for 84%, 81% and	1 78% of the sub-metrics during the
21	months of September, October and Nove	ember 2001, respectively.
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For the three-month period, September through November 2001, there were 387 sub-metrics in the UNE measurements for which there was CLEC activity in all three months and that were compared to retail analogues or benchmarks. Of those 387 sub-metrics, 324 sub-metrics (84%) met the retail analogue/benchmark comparisons in at least two of the three months.

#### 1. UNE Ordering Measures

Items B.1.1 – B.1.19 in Attachment 1F show data for Percent Rejected Service Requests, Reject Interval, FOC Timeliness and FOC & Reject Response Completeness. These reports are disaggregated by interface type (electronic, partial electronic and manual), as well as product type.

#### Reject Interval

Items B.1.4 - B.1.8 in Attachment 1F examine the Reject Interval for the month of November 2001. For orders submitted electronically, the benchmark is 97% within one hour. In September, October and November 2001, 90%, 80% and 78%, respectively, of all rejected electronic service requests were delivered within the one-hour benchmark interval. (See the write-up below for Items B.1.4.2 – B.1.4.17 for further discussion concerning electronically submitted orders.)

For partially mechanized orders, which are LSRs submitted electronically and requiring service representative intervention, the benchmark is 85% returned 3 within 10 hours. BellSouth exceeded this benchmarks in September, October 4 and November 2001, with 92%, 90% and 94%, respectively, of partially mechanized rejects being returned to the CLECs within the benchmark 6 interval. 7 For manual orders, the current benchmark is 85% within 24 hours. BellSouth 8 also exceeded this requirement, with 99% of the LSRs submitted manually 10 being returned to the CLECs within the 24-hour time period in each of the three months. 12 The following sub-metrics did not meet the established benchmarks in 13 14 September, October and/or November 2001: 15 16 Reject Interval / Combo (Loop & Port) / Electronic (B.1.4.3) 17 (September/October/November) Reject Interval / UNE ISDN / Electronic (B.1.4.6) (November) 18 19 Reject Interval / Line Sharing / Electronic (B.1.4.7) 20 (September/October/November) Reject Interval / 2w Analog Loop Design / Electronic (B.1.4.8) 21 22 (September/October/November)

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1 Reject Interval / 2w Analog Loop Non-Design / Electronic (B.1.4.9) 2 (September/October/November) 3 Reject Interval / 2w Analog Loop w/LNP Design / Electronic (B.1.4.12) 4 (September/October/November) 5 Reject Interval / 2w Analog Loop w/LNP Non-Design / Electronic (B.1.4.13) 6 (October/November) 7 Reject Interval / Other Design / Electronic (B.1.4.14) 8 (September/October/November) 9 Reject Interval / Other Non-Design / Electronic (B.1.4.15) 10 (September/October/November) 11 Reject Interval / LNP (Standalone) / Electronic (B.1.4.17) 12 (September/October/November) 13 The current benchmark for these sub-metrics is >= 97% within one hour. 14 BellSouth is conducting a detailed root cause analysis of the process for 15 electronic rejects. This analysis addresses the ordering systems (EDI, TAG, 16 and LENS) used by the CLECs and the back-end legacy applications, such 17 as SOCS, that are accessed by the ordering systems. 18 Thus far, the analysis has determined that many of the LSRs that did not 19 meet the one-hour benchmark in September were issued between 11:00 p.m. 20 21 and 4:30 a.m. Between these hours, the system is unable to process LSRs because certain of the back-end legacy systems are out of service. LSRs 22 23 submitted during these periods should have been excluded from the

1 measurement. BellSouth implemented a program coding change in 2 September to exclude these LSRs from this measure. 3 4 With the May 2001, data month, BellSouth was directed to change the time 5 stamp identification for the start and complete times of the interval for this measurement from the Local Exchange Ordering (LEO) System to the CLEC 6 7 ordering interface system (TAG or EDI). However, with this change, 8 BellSouth is currently unable to identify multiple issues of the same version of 9 LSRs that have been rejected (fatal rejects). These rejected LSRs should be 10 excluded from the measurement. If there are multiple issues of the same 11 version, the measure currently calculates the interval from the initial issue to 12 the final issue of the LSR returned to the CLEC, Reject or FOC. 13 Consequently, BellSouth's performance level is inappropriately understated. BellSouth is currently working to determine a fix for this issue. 14 15 Reject Interval / UNE ISDN / Partially Electronic (B.1.7.6) 16 17 (September/October) There were only three LSRs rejected for this sub-metric in September and 18 one rejected LSR in October 2001. The small universe of orders for these 19 20 months does not provide a conclusive benchmark comparison. BellSouth met the benchmark for this sub-metric in November 2001. 21 22

1 Reject Interval / Line Sharing / Partially Electronic (B.1.7.7) 2 (October/November) 3 There were only eleven LSRs rejected for this sub-metric in October and eight 4 LSRs rejected in November 2001. The small universe of orders for the month 5 does not provide a conclusive benchmark comparison. BellSouth met the 6 benchmark for this sub-metric in September 2001. 7 8 Reject Interval / 2w Analog Loop Non-Design / Partially Electronic (B.1.7.9) 9 (September/October/November) 10 In September, BellSouth met the 10-hour benchmark interval for 66 of the 78 11 or 84.62% of the rejected LSRs in this sub-metric. Normal rounding 12 convention would indicate that there is no significant difference between the 13 CLEC result and the 85% benchmark. In October 2001, BellSouth met the 14 benchmark interval for 123 of the 146 rejected LSRs - only one LSR short of meeting the benchmark for the sub-metric for the month. In November 2001, 15 16 BellSouth met the 10-hour benchmark interval for 141 of the 176 rejected 17 LSRs. The 85% benchmark required that 150 of the 176 orders be returned. 18 Reject Interval / 2w Analog Loop w/LNP Design / Partially Electronic 19 20 (B.1.7.12) (September) 21 BellSouth met the benchmark for 172 of the 203 or 84.73% of the LSRs rejected in this sub-metric for September 2001. Normal rounding convention 22 would indicate that there is no significant difference between the CLEC result 23

and the 85% benchmark. BellSouth met the benchmark for this sub-metric in
 October and November 2001.

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- Reject Interval / 2w Analog Loop w/LNP Non-Design / Partially Electronic
- 5 (B.1.6.13/B.1.7.13) (October/November)
- 6 BellSouth met the benchmark for 376 of the 460 rejected LSRs for this sub-
- 7 metric in October and 431 of the 547 rejected LSRs in November 2001. The
- 85 % benchmark required that 391 of the 460 orders for October and 465 of
- 9 the 547 orders for November be returned. BellSouth met the 10-hour
- benchmark for this sub-metric in September 2001.

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#### **FOC Timeliness**

For LSRs submitted electronically, the benchmark is 95% of the FOCs returned within 3 hours. BellSouth met the benchmark interval for 99% of the electronically submitted LSRs in September, October and November 2001. For partially mechanized LSRs, the benchmark is 85% of FOCs returned within 10 hours. BellSouth met the benchmark for 95%, 94% and 97% of partially electronic FOCs in September, October and November 2001, respectively. For LSRs submitted manually, the benchmark is 85% returned within 36 hours. BellSouth met the benchmark interval for 98%, 99% and 93% of the manual LSRs submitted in September, October and November 2001, respectively. The sub-metrics that did not meet the benchmark in September, October and /or November 2001 are as follows:

#### FOC Timeliness / xDSL / Electronic (B.1.9.5) (September/October)

BellSouth met the benchmark for 147 of the 160 LSRs that received a FOC in September and for 211 of the 223 FOCs in October 2001. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems. For further information, see the explanation included with the electronic reject interval measurement. BellSouth met the benchmark for this sub-metric in November 2001.

#### FOC Timeliness / 2w Analog Loop w/LNP Design / Electronic (B.1.9.12)

#### (November)

BellSouth met the benchmark for 36 of the 38 LSRs in November that received a FOC for this sub-metric. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems. For further information, see the explanation included with the electronic reject interval measurement, item B.1.4.x. BellSouth met the benchmark for this sub-metric in September and October 2001.

#### FOC Timeliness / LNP Standalone / Electronic (B.1.9.17) (November)

BellSouth met the benchmark for 2,024 of the 2,313 LSRs in November that received a FOC for this sub-metric. BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems. For further information, see the explanation included with the electronic reject interval measurement, item B.1.4.x. BellSouth met the benchmark for this sub-metric in September and October 2001. FOC Timeliness / 2w Analog Loop w/LNP Design / Partially Electronic (B.1.12.12) (November) BellSouth met the 10-hour benchmark for 313 of the 411 FOCs returned for this sub-metric in November 2001. The 85% benchmark required that 350 orders be returned based on the number of orders for this sub-metric. BellSouth met the benchmark for this sub-metric in September and October 2001. FOC Timeliness / Other Design / Partially Electronic (B.1.12.14) (October/November) BellSouth met the 10-hour benchmark interval for 117 of the 146 FOCs returned for this sub-metric in October and for 67 of the 84 FOCs returned in November 2001. BellSouth met the benchmark for this sub-metric in September 2001.

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2 FOC Timeliness / 2w Analog Loop w/INP Design / Manual (B.1.13.10)

3 (October)

BellSouth met the benchmark interval for 5 of the 6 FOCs returned for this sub-metric in October 2001. The small universe of orders for this sub-metric does not provide a conclusive benchmark comparison. BellSouth met the benchmark for this sub-metric in September and November 2001.

#### FOC & Reject Response Completeness and FOC & Reject Response

#### Completeness (Multiple Responses) Measures

BellSouth determined that the coding for the FOC & Reject Completeness and FOC & Reject Response Completeness (Multiple Responses) measures failed to include rejections that were classified as "auto clarifications." BellSouth has rewritten the code to correct this problem. Effective with the Exhibit update for September data, the program coding was corrected for all the FOC & Reject Completeness sub-metrics for Checklist Item No. 2, UNE Loop products with the exceptions of: xDSL, 2w Analog Loop w/INP Design, 2w Analog Loop w/INP Non-Design, 2w Analog Loop w/LNP Design, 2w Analog Loop w/LNP Non-Design, INP (Standalone) and LNP (Standalone). The corrected coding for these measures was implemented and effective with the October data. The individual sub-metrics with corrected coding that missed the required benchmarks in September, October and/or November 2001 are addressed separately following the next section. BellSouth did not

1 meet the benchmark in September 2001 for the FOC and Reject Response 2 Completeness and FOC & Reject Response Completeness (Multiple 3 Responses) metrics listed below: 4 5 FOC & Reject Response Completeness / xDSL / Electronic (B.1.14.5) 6 (September) 7 FOC & Reject Response Completeness / xDSL / Partial Electronic (B.1.15.5) 8 (September) 9 FOC & Reject Response Completeness / xDSL / Manual (B.1.16.5) 10 (September) 11 FOC & Reject Response Completeness (Multiple Responses) / xDSL / 12 Manual (B.1.19.5) (September) 13 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 14 Loop w/INP Design / Manual (B.1.19.10) (September) 15 BellSouth determined that the coding for the FOC & Reject Completeness 16 and FOC & Reject Response Completeness (Multiple Responses) measures 17 failed to include rejections that were classified as "auto clarifications." 18 BellSouth has rewritten the code to correct this problem. The coding changes were implemented for some products in August and for the remainder of the 19 The sub-metric "misses" listed above were for 20 products in September. 21 operations prior to the implementation of the coding modifications. 22

1 Effective with October 2001 data, each sub-metric in the Electronic and 2 Partial Electronic sections were disaggregated between LSRs submitted from 3 the EDI and TAG systems. The following FOC & Reject Response 4 Completeness sub-metrics, for which the program code has been corrected, did not meet the benchmarks for September, October and/or November 2001: 5 6 7 FOC & Reject Response Completeness / xDSL / EDI / Electronic (B.1.14.5.1) 8 (October/November) 9 There were only 10 orders for this sub-metric in October 2001. The small 10 universe of orders for this sub-metric does not provide a conclusive 11 benchmark comparison. BellSouth met the benchmark standard for 35 of the 12 39 responses for this sub-metric in November 2001. The 95% benchmark 13 required that the criteria be met for 38 of the 39 responses. BellSouth 14 continues to focus on this measurement in order to improve results to meet 15 the benchmark. 16 FOC & Reject Response Completeness / xDSL / TAG / Electronic 17 18 (B.1.14.5.2) (October/November) 19 BellSouth met the benchmark standard for 325 of the 390 responses for this sub-metric in October and for 194 of the 249 responses in November 2001. 20 The 95% benchmark required that the criteria be met for 371 of the 390 21 22 responses in October and for 237 of the 249 responses in November based

1 on the number of orders for this sub-metric. BellSouth continues to focus on 2 this measurement in order to improve results to meet the benchmark. 3 FOC & Reject Response Completeness / Line Sharing / TAG / Electronic 4 5 (B.1.14.7.2) (November) BellSouth met the benchmark standard for 67 of the 71 responses for this 6 7 sub-metric in November 2001. The 95% benchmark required that the criteria 8 be met for 68 of the 71 responses based on the number of orders for this sub-9 metric. BellSouth continues to focus on this measurement in order to improve 10 results to meet the benchmark. BellSouth met the benchmark for this sub-11 metric in October 2001. 12 FOC & Reject Response Completeness / 2w Analog Loop Design / EDI / 13 14 Electronic (B.1.14.8.1) (November) 15 BellSouth met the benchmark standard for 293 of the 316 responses for this 16 sub-metric in November 2001. The 95% benchmark required that the criteria 17 be met for 301 of the 316 responses based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to 18 improve results to meet the benchmark. BellSouth met the benchmark for 19 20 this sub-metric in October 2001. 21 FOC & Reject Response Completeness / 2w Analog Loop Non-Design / TAG 22 / Electronic (B.1.14.9.2) (November) 23

BellSouth met the benchmark standard for 466 of the 492 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 468 of the 492 responses based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in October 2001. FOC & Reject Response Completeness / 2w Analog Loop w/LNP Design / EDI / Electronic (B.1.14.12.1) (November) BellSouth met the benchmark standard for 33 of the 35 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 34 of the 35 responses based on the number of orders for this submetric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this submetric in October 2001. FOC & Reject Response Completeness / 2w Analog Loop w/LNP Design / TAG / Electronic (B.1.14.12.2) (November) BellSouth met the benchmark standard for 23 of the 26 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 25 of the 26 responses based on the number of orders for this submetric. BellSouth continues to focus on this measurement in order to improve

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1 results to meet the benchmark. BellSouth met the benchmark for this sub-2 metric in October 2001. 3 4 FOC & Reject Response Completeness / 2w Analog Loop w/LNP Non-5 Design / TAG / Electronic (B.1.14.13.2) (November) 6 BellSouth met the benchmark standard for 190 of the 232 responses for this 7 sub-metric in November 2001. The 95% benchmark required that the criteria 8 be met for 221 of the 232 responses based on the number of orders for this 9 sub-metric. BellSouth continues to focus on this measurement in order to 10 improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in October 2001. 11 12 13 FOC & Reject Response Completeness / Other Design / TAG / Electronic (B.1.14.14.2) (November) 14 15 BellSouth met the benchmark standard for 127 of the 140 responses for this 16 sub-metric in November 2001. The 95% benchmark required that the criteria 17 be met for 133 of the 140 responses based on the number of orders for this 18 sub-metric. BellSouth continues to focus on this measurement in order to 19 improve results to meet the benchmark. BellSouth met the benchmark for 20 this sub-metric in October 2001. 21 22 FOC & Reject Response Completeness / LNP Standalone / TAG / Electronic 23 (B.1.14.17.2) (November)

BellSouth met the benchmark standard for 293 of the 311 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 296 of the 311 responses based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in October 2001. FOC & Reject Response Completeness / xDSL / EDI / Partial Electronic (B.1.15.5.1) (November) There were only four orders for this sub-metric in November 2001. The small universe of orders for this sub-metric does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in October 2001. FOC & Reject Response Completeness / xDSL / TAG / Partial Electronic (B.1.15.5.2) (October/November) BellSouth met the benchmark standard for 20 of the 43 responses for this sub-metric in October and for 14 of the 29 responses in November 2001. The 95% benchmark required that the criteria be met for 41 of the 43 responses in October and for 28 of the 29 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark.

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1	FOC & Reject Response Completeness / Local Interoffice Transport / Manual
2	(B.1.16.2) (October/November)
3	BellSouth met the benchmark standard for 57 of the 62 responses for this
4	sub-metric in October and for 75 of the 81 responses in November 2001. The
5	95% benchmark required that the criteria be met for 59 of the 62 responses in
6	October and for 77 of the 81 responses in November based on the number of
7	orders for this sub-metric. BellSouth continues to focus on this measurement
8	in order to improve results to meet the benchmark. BellSouth met the
9	benchmark for this sub-metric in September 2001.
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1	FOC & Reject Response Completeness / Combo (Loop & Port) / Manual
12	(B.1.16.3) (October/November)
13	BellSouth met the benchmark standard for 812 of the 859 responses for this
14	sub-metric in October and for 802 of the 866 responses in November 2001.
15	The 95% benchmark required that the criteria be met for 817 of the 859
16	responses in October and for 823 of the 866 responses in November based
17	on the number of orders for this sub-metric. BellSouth continues to focus on
18	this measurement in order to improve results to meet the benchmark.
19	BellSouth met the benchmark for this sub-metric in September 2001.
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21	FOC & Reject Response Completeness / UNE ISDN / Manual (B.1.16.6)
22	(November)

BellSouth met the benchmark standard for 555 of the 595 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 566 of the 595 responses based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September and October 2001. FOC & Reject Response Completeness / Line Sharing / Manual (B.1.16.7) (September/October/November) BeliSouth met the benchmark for 192 of the 203 or 94.56% of the orders for this sub-metric in September 2001. Normal rounding convention would indicate that there was not a significant difference between the CLEC result and the 95% benchmark for this sub-metric in September. BellSouth met the benchmark standard for 142 of the 153 responses for this sub-metric in October and for 112 of the 120 responses in November 2001. The 95% benchmark required that the criteria be met for 146 of the 153 responses in October and for 114 of the 120 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. FOC & Reject Response Completeness / 2w Analog Loop Design / Manual (B.1.16.8) (September/November)

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BellSouth met the benchmark for 122 of the 130 responses for this sub-metric
in September and for 204 of the 228 responses in November 2001. The 95%
benchmark set a requirement of 124 responses in September and for 217 of
the 228 responses in November based on the number of orders for this sub-
metric. BellSouth continues to focus on this measurement in order to improve
results to meet the benchmark. BellSouth met the benchmark for this sub-
metric in October 2001.
FOC & Reject Response Completeness / 2w Analog Loop Non-Design /
Manual (B.1.16.9) (September/October/November)
BellSouth met the benchmark for 861 of the 928 responses for this sub-metric
in September, for 1,275 of the 1,378 responses in October and for 1,241 of
the 1,346 responses in November 2001. The 95% benchmark set a
requirement of 882 orders for September, for 1,310 orders in October and for
1,273 orders in November based on the number of orders for this sub-metric.
BellSouth continues to focus on this measurement in order to improve results
to meet the benchmark.
FOC & Reject Response Completeness / 2w Analog Loop w/iNP Non-Design
/ Manual (B.1.16.11) (November)
BellSouth met the benchmark standard for 11 of the 13 responses for this
sub-metric in November 2001. The 95% benchmark required that the criteria
be met for all 13 of the responses. BellSouth continues to focus on this

measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in October 2001. There was no CLEC activity for this sub-metric in September 2001. FOC & Reject Response Completeness / Other Design / Manual (B.1.16.14) (October/November) BellSouth met the benchmark standard for 410 of the 441 responses for this sub-metric in October and for 554 of the 603 responses in November 2001. The 95% benchmark required that the criteria be met for 419 of the 441 responses in October and for 573 of the 603 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September 2001. FOC & Reject Response Completeness / Other Non-Design / Manual (B.1.16.15) (November) BellSouth met the benchmark standard for 1, 423 of the 1,549 responses for this sub-metric in November 2001. The 95% benchmark required that the criteria be met for 1,472 of the 1,549 responses based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September and October 2001.

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1 FOC & Reject Response Completeness / INP Standalone / Manual 2 (B.1.16.16) (November) 3 BellSouth met the benchmark standard for 58 of the 63 responses for this 4 sub-metric in November 2001. The 95% benchmark required that the criteria 5 be met for 60 of the 63 responses based on the number of orders for this sub-6 metric. BellSouth continues to focus on this measurement in order to improve 7 results to meet the benchmark. BellSouth met the benchmark for this sub-8 metric in October 2001. 9 10 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop 11 & Port) / Electronic (B.1.17.3) (September) 12 BellSouth met the benchmark for 6,459 of the 7,056 orders for this sub-metric 13 in September 2001. The 95% benchmark set a requirement of 6,704 of the 7.056 orders based on the number of orders for this sub-metric. BellSouth 14 15 continues to focus on this measurement in order to improve results to meet 16 the benchmark. This sub-metric was replaced by Items B.1.17.3.1 and 17 B.1.17.3.2 effective with October 2001 data. 18 19 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop 20 & Port) / EDI / Electronic (B.1.17.3.1) (October/November) 21 BellSouth met the benchmark for 154 of the 237 responses for this sub-metric in October and for 610 of the 787 responses in November 2001. The 95% 22 23 benchmark set a requirement of 226 of the 237 responses in October and for

1	748 of the 787 responses in November based on the number of orders for this
2	sub-metric. BellSouth continues to focus on this measurement in order to
3	improve results to meet the benchmark.
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5	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
6	& Port) / TAG / Electronic (B.1.17.3.2) (October/November)
7	BellSouth met the benchmark for 8,765 of the 9,548 responses for this sub-
8	metric in October and for 8,093 of the 8,666 responses in November 2001.
9	The 95% benchmark set a requirement of 9,071 of the 9,548 responses in
10	October and for 8,233 of the 8,666 responses in November based on the
11	number of orders for this sub-metric. BellSouth continues to focus on this
12	measurement in order to improve results to meet the benchmark.
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14	FOC & Reject Response Completeness (Multiple Responses) / UNE ISDN /
15	TAG / Electronic (B.1.17.6.2) (October/November)
16	There were only 10 orders for this sub-metric in October and 6 orders in
17	November 2001. The small universe of orders for this sub-metric does not
18	provide a conclusive benchmark comparison.
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20	FOC & Reject Response Completeness (Multiple Responses) / Line Sharing
21	TAG / Electronic (B.1.17.7.2) (October)
22	BellSouth met the benchmark for 74 of the 78 (94.9%) orders for this sub-
23	metric in September 2001. Normal rounding convention would indicate that

1 there was not a significant difference between the CLEC result and the 95% 2 benchmark for this sub-metric in October. BellSouth met the benchmark for 3 this sub-metric in November 2001. 4 5 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 6 Loop Design / Electronic (B.1.17.8) (September) BellSouth met the benchmark for 342 of the 445 orders for this sub-metric in 7 September 2001. The 95% benchmark set a requirement of 423 of the 445 8 9 orders based on the number of orders for this sub-metric. This sub-metric 10 was replaced by Items B.1.17.8.1 and B.1.17.8.2 effective with October 2001 11 data. 12 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 13 Loop Design / EDI / Electronic (B.1.17.8.1) (October/November) 14 BellSouth met the benchmark for 180 of the 232 responses for this sub-metric 15 in October and for 189 of the 293 responses in November 2001. The 95% 16 17 benchmark set a requirement of 221 of the 232 responses in October and for 279 of the 293 responses in November based on the number of orders for this 18 sub-metric. BellSouth continues to focus on this measurement in order to 19 improve results to meet the benchmark. 20 21 22 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop Non-Design / TAG / Electronic (B.1.17.9.2) (October/November) 23

1 BellSouth met the benchmark for 495 of the 531 responses for this sub-metric 2 in October and for 441 of the 466 responses in November 2001. The 95% benchmark set a requirement of 505 of the 531 responses in October and for 3 4 443 of the 466 responses in November based on the number of orders for this 5 sub-metric. BellSouth continues to focus on this measurement in order to 6 improve results to meet the benchmark. 7 8 FOC & Reject Response Completeness (Multiple Responses) / Other Design 9 / Electronic (B.1.17.14) (September) 10 BellSouth met the benchmark for 77 of the 115 orders for this sub-metric in 11 September 2001. The 95% benchmark set a requirement of 110 of the 115 12 orders based on the number of orders for this sub-metric. This sub-metric 13 was replaced by Items B.1.17.14.1 and B.1.17.14.2 effective with October 14 2001 data. 15 16 FOC & Reject Response Completeness (Multiple Responses) / Other Design 17 / EDI / Electronic (B.1.17.14.1) (October/November) 18 BellSouth met the benchmark for 10 of the 17 responses for this sub-metric in 19 October and for 20 of the 29 responses in November 2001. The 95% 20 benchmark set a requirement of all 17 of the 17 responses in October and for 21 28 of the 29 responses in November based on the number of orders for this 22 sub-metric. BellSouth continues to focus on this measurement in order to 23 improve results to meet the benchmark.

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2	FOC & Reject Response Completeness (Multiple Responses) / Other Design
3	/ TAG / Electronic (B.1.17.14.2) (October/November)
4	BellSouth met the benchmark for 179 of the 230 responses for this sub-metric
5	in October and for 89 of the 127 responses in November 2001. The 95%
6	benchmark set a requirement of 219 of the 230 responses in October and for
7	121 of the 127 responses in November based on the number of orders for this
8	sub-metric. BellSouth continues to focus on this measurement in order to
9	improve results to meet the benchmark.
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11	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
12	Design / Electronic (B.1.17.15) (September)
13	BellSouth met the benchmark for 1,513 of the 3,193 orders for this sub-metric
14	in September 2001. The 95% benchmark set a requirement of 3,034 of the
15	3,193 orders based on the number of orders for this sub-metric. BellSouth
16	continues to focus on this measurement in order to improve results to meet
17	the benchmark. This sub-metric was replaced by Items B.1.17.15.1 and
18	B.1.17.15.2 effective with October 2001 data.
19	
20	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
21	Design / EDI / Electronic (B.1.17.15.1) (October/November)
22	BellSouth met the benchmark for 3,620 of the 6,900 responses for this sub-
23	metric in October and for 3,089 of the 7,159 responses in November 2001.

The 95% benchmark set a requirement of 6,555 of the 6,900 responses in 1 2 October and for 6,802 of the 7,159 responses in November based on the 3 number of orders for this sub-metric. BellSouth continues to focus on this 4 measurement in order to improve results to meet the benchmark. 5 6 FOC & Reject Response Completeness (Multiple Responses) / Other Non-7 Design / TAG / Electronic (B.1.17.15.2) (October/November) 8 BellSouth met the benchmark for 744 of the 947 responses for this sub-metric 9 in October and for 500 of the 590 responses in November 2001. The 95% 10 benchmark set a requirement of 900 of the 947 responses in October and for 11 561 of the 590 responses in November based on the number of orders for this 12 sub-metric. BellSouth continues to focus on this measurement in order to 13 improve results to meet the benchmark. 14 15 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop & Port) / Partial Electronic (B.1.18.3) (September) 16 BellSouth met the benchmark for 3,692 of the 4,018 orders for this sub-metric 17 18 in September 2001. The 95% benchmark set a requirement of 3,818 of the 4,018 orders based on the number of orders for this sub-metric. This sub-19 metric was replaced by Items B.1.18.3.1 and B.1.18.3.2 effective with October 20 21 2001 data. 22

1	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
2	& Port) / EDI / Partial Electronic (B.1.18.3.1) (October/November)
3	BellSouth met the benchmark for 232 of the 255 responses for this sub-metric
4	in October and for 317 of the 342 responses in November 2001. The 95%
5	benchmark set a requirement of 243 of the 255 responses in October and for
6	325 of the 342 responses in November based on the number of orders for this
7	sub-metric. BellSouth continues to focus on this measurement in order to
8	improve results to meet the benchmark.
9	
10	FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
11	& Port) / TAG / Partial Electronic (B.1.18.3.2) (October/November)
12	BellSouth met the benchmark for 5,610 of the 6,058 orders for this sub-metric
13	in October and for 5,364 of the 5,892 responses in November 2001. The
14	95% benchmark set a requirement of 5,756 of the 6,058 responses in
15	October and for 5,598 of the 5,892 responses in November based on the
16	number of orders for this sub-metric. BellSouth continues to focus on this
17	measurement in order to improve results to meet the benchmark.
18	
19	FOC & Reject Response Completeness (Multiple Responses) / UNE ISDN /
20	TAG / Partial Electronic (B.1.18.6.2) (November)
21	There was only one order for this sub-metric in November 2001. The small
22	universe of orders for this sub-metric does not provide a conclusive

1 benchmark comparison. BellSouth met the benchmark for this sub-metric in 2 October 2001. 3 4 FOC & Reject Response Completeness (Multiple Responses) / Line Sharing / 5 TAG / Partial Electronic (B.1.18.7.2) (October) 6 BellSouth met the benchmark for 59 of the 63 responses for this sub-metric in 7 October and for 44 of the 50 responses in November 2001. The 95% 8 benchmark set a requirement of 60 of the 63 responses in October and for 48 9 of the 50 responses in November based on the number of orders for this sub-10 metric. BellSouth continues to focus on this measurement in order to improve 11 results to meet the benchmark. 12 13 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 14 Loop Design / Partial Electronic (B.1.18.8) (September) 15 BellSouth met the benchmark for 348 of the 403 orders for this sub-metric in September 2001. The 95% benchmark set a requirement of 383 of the 403 16 orders based on the number of orders for this sub-metric. This sub-metric 17 was replaced by Items B.1.18.8.1 and B.1.18.8.2 effective with October 2001 18 19 data. 20 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 21 Loop Design / EDI / Partial Electronic (B.1.18.8.1) (October/November) 22

BellSouth met the benchmark for 170 of the 184 responses for this sub-metric in October and for 213 of the 225 responses in November 2001. The 95% benchmark set a requirement of 175 of the 184 responses in October and for 214 of the 225 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop Design / TAG / Partial Electronic (B.1.18.8.2) (October/November) BellSouth met the benchmark for 120 of the 133 responses for this sub-metric in October and for 127 of the 145 responses in November 2001. The 95% benchmark set a requirement of 127 of the 133 responses in October and for 138 of the 145 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop Non-Design / TAG / Partial Electronic (B.1.18.9.2) (October/November) BellSouth met the benchmark for 794 of the 847 responses for this sub-metric in October and for 767 of the 835 responses in November 2001. The 95% benchmark set a requirement of 805 of the 847 responses in October and for 794 of the 835 responses in November based on the number of orders for this

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1 sub-metric. BellSouth continues to focus on this measurement in order to 2 improve results to meet the benchmark. 3 4 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 5 Loop w/LNP Design / EDI / Partial Electronic (B.1.18.12.1) (October) 6 BellSouth met the benchmark for 561 of the 598 responses for this sub-metric 7 in October 2001. The 95% benchmark set a requirement of 569 of the 598 8 orders based on the number of orders for this sub-metric. BellSouth met the 9 benchmark for this sub-metric in November 2001. 10 11 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 12 Loop w/LNP Design / TAG / Partial Electronic (B.1.18.12.2) 13 (October/November) 14 BellSouth met the benchmark for 219 of the 231 responses for this sub-metric 15 in October and for 230 of the 253 responses in November 2001. The 95% benchmark set a requirement of 220 of the 231 responses in October and for 16 241 of the 253 responses in November based on the number of orders for this 17 sub-metric. BellSouth continues to focus on this measurement in order to 18 improve results to meet the benchmark. 19 20 21 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop w/LNP Non-Design / EDI / Partial Electronic (B.1.18.13.1) (October) 22

1 There were only 3 orders for this sub-metric in October 2001. The small 2 universe of orders for this sub-metric does not provide a conclusive 3 benchmark comparison. BellSouth met the benchmark for this sub-metric in 4 November 2001. 5 6 FOC & Reject Response Completeness (Multiple Responses) / Other Design 7 / Partial Electronic (B.1.18.14) (September) 8 BellSouth met the benchmark for 89 of the 119 orders for this sub-metric in 9 September 2001. The 95% benchmark set a requirement of 114 of the 119 10 orders based on the number of orders for this sub-metric. This sub-metric 11 was replaced by Items B.1.18.14.1 and B.1.18.14.2 effective with October 12 2001 data. 13 FOC & Reject Response Completeness (Multiple Responses) / Other Design 14 15 / EDI / Partial Electronic (B.1.18.14.1) (October/November) 16 BellSouth met the benchmark for 24 of the 26 responses for this sub-metric in 17 October and for 11 of the 12 responses in November 2001. The 95% benchmark set a requirement of 25 of the 26 responses in October and for all 18 12 of the 12 responses in November based on the number of orders for this 19 20 sub-metric. BellSouth continues to focus on this measurement in order to 21 improve results to meet the benchmark. 22

1	FOC & Reject Response Completeness (Multiple Responses) / Other Design
2	/ TAG / Partial Electronic (B.1.18.14.2) (October/November)
3	BellSouth met the benchmark for 148 of the 183 responses for this sub-metric
4	in October and for 96 of the 111 responses in November 2001. The 95%
5	benchmark set a requirement of 174 of the 183 responses in October and for
6	106 of the 111 responses in November based on the number of orders for this
7	sub-metric. BellSouth continues to focus on this measurement in order to
8	improve results to meet the benchmark.
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10	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
11	Design / Partial Electronic (B.1.18.15) (September)
12	BellSouth met the benchmark for 1,592 of the 1,688 orders for this sub-metric
13	in September 2001. The 95% benchmark set a requirement of 1,604 of the
14	1,688 orders based on the number of orders for this sub-metric. This sub-
15	metric was replaced by Items B.1.18.15.1 and B.1.18.15.2 effective with
16	October 2001 data.
17	
18	FOC & Reject Response Completeness (Multiple Responses) / Other Non-
19	Design / EDI / Partial Electronic (B.1.18.15.1) (October/November)
20	BellSouth met the benchmark for 1,801 of the 1,958 responses for this sub-
21	metric in October and for 2,262 of the 2,461 responses in November 2001.
22	The 95% benchmark set a requirement of 1,851 of the 1,958 responses in
23	October and for 2,338 of the 2,461 responses in November based on the

1 number of orders for this sub-metric. BellSouth continues to focus on this 2 measurement in order to improve results to meet the benchmark. 3 4 FOC & Reject Response Completeness (Multiple Responses) / Switch Ports / 5 Manual (B.1.19.1) (November) 6 There were only three orders for this sub-metric in November 2001. The small universe of orders for this sub-metric does not provide a conclusive 7 8 benchmark comparison. There was no CLEC activity for this sub-metric in 9 September 2001. BellSouth met the benchmark for this sub-metric in October 10 2001. 11 12 FOC & Reject Response Completeness (Multiple Responses) / Local 13 Interoffice Transport / Manual (B.1.19.2) (September/October/November) 14 BellSouth met the benchmark for 37 of the 41 responses for this sub-metric in 15 September, for 46 of the 57 responses in October and for 65 of the 75 16 responses in November 2001. The 95% benchmark set a requirement of 39 17 of the 41 orders in September, for 55 of the 57 orders in October and for 72 of 18 the 75 responses in November based on the number of orders for this sub-19 metric, BellSouth continues to focus on this measurement in order to improve 20 results to meet the benchmark. 21 22 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop 23 & Port) / Manual (B.1.19.3) (September/October/November)

BellSouth met the benchmark for 1,241 of the 1,334 responses for this submetric in September, for 757 of the 812 orders in October and for 740 of the 802 responses in November 2001. The 95% benchmark set a requirement of 1,268 of the 1,334 responses in September, for 772 of the 812 responses in October and 762 of the 802 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop / Manual (B.1.19.6) (September/November) BellSouth met the benchmark for 452 of the 485 responses for this sub-metric in September and for 518 of the 565 responses in November 2001. The 95% benchmark set a requirement of 461 of the 485 responses in September and for 528 of the 565 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in October 2001. FOC & Reject Response Completeness (Multiple Responses) / Line Sharing / Manual (B.1.19.7) (September) BellSouth met the benchmark for 182 of the 192 orders for this sub-metric in September 2001. The 95% benchmark set a requirement of 183 of the 192

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orders based on the number of orders for this sub-metric. BellSouth met the benchmark for this sub-metric in October and November 2001. FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop Design / Manual (B.1.19.8) (September/October/November) BellSouth met the benchmark for 115 of the 122 responses for this sub-metric in September, for 193 of the 204 responses in October and for 192 of the 204 responses in November 2001. The 95% benchmark set a requirement of 116 of the 122 orders in September, for 194 of the 204 orders in October and for 194 of the 204 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. 14 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog Loop Non-Design / Manual (B.1.19.9) (September/October/November) 16 BellSouth met the benchmark for 783 of the 815 responses for this sub-metric 17 in September, for 1,179 of the 1,275 responses in October and for 1,165 of the 1,241 responses in November 2001. The 95% benchmark set a 18 19 requirement of 817 of the 815 responses in September, for 1,212 of the 1,275 responses in October and for 1,179 of the 1,241 responses in November 20 21 based on the number of orders for this sub-metric. BellSouth continues to 22 focus on this measurement in order to improve results to meet the 23 benchmark.

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1 2 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 3 Loop w/INP Design / Manual (B.1.19.10) (October) There were only 6 orders for this sub-metric in October 2001. The small 4 5 universe of orders for this sub-metric does not provide a conclusive 6 benchmark comparison. BellSouth met the benchmark for this sub-metric in 7 November 2001. 8 9 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 10 Loop w/LNP Design / Manual (B.1.19.12) (October/November) 11 BellSouth met the benchmark for 265 of the 302 responses for this sub-metric 12 in October and for 58 of the 64 responses in November 2001. The 95% 13 benchmark set a requirement of 287 of the 302 responses in October and for 14 61 of the 64 responses in November based on the number of orders for this 15 sub-metric. BellSouth continues to focus on this measurement in order to 16 improve results to meet the benchmark. BellSouth met the benchmark for 17 this sub-metric in September 2001. 18 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog 19 20 Loop w/LNP Non-Design / Manual (B.1.19.13) (October/November) 21 BellSouth met the benchmark for 212 of the 244 responses for this sub-metric 22 in October and for 129 of the 143 responses in November 2001. The 95% 23 benchmark set a requirement of 232 of the 244 responses in October and for

136 of the 143 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September 2001. FOC & Reject Response Completeness (Multiple Responses) / Other Design / Manual (B.1.19.14) (September/October/November) BellSouth met the benchmark for 361 of the 395 responses for this sub-metric in September, for 375 of the 410 responses in October and for 526 of the 554 responses in November 2001. The 95% benchmark set a requirement of 376 of the 395 responses in September, for 390 of the 410 responses in October and 527 of the 554 responses in November based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. FOC & Reject Response Completeness (Multiple Responses) / Other Non-Design / Manual (B.1.19.15) (November) BellSouth met the benchmark for 1,350 of the 1,423 responses (94.87%) for this sub-metric in November 2001. The 95% benchmark set a requirement of 1,352 of the 1,423 orders based on the number of orders for this sub-metric. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. BellSouth met the benchmark for this sub-metric in September and October 2001.

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1 2 FOC & Reject Response Completeness (Multiple Responses) / LNP 3 (Standalone) / Manual (B.1.19.17) (October/November) 4 BellSouth met the benchmark for 924 of the 990 responses for this sub-metric 5 in October and for 855 of the 906 responses in November 2001. The 95% 6 benchmark set a requirement of 941 of the 990 responses in October and for 7 863 of the 906 responses in November based on the number of orders for this 8 sub-metric. BellSouth continues to focus on this measurement in order to 9 improve results to meet the benchmark. BellSouth met the benchmark for 10 this sub-metric in September 2001. 11 12 Flow-Through 13 14 Attachment 1F, Items F.1.1 - F.1.3, shows Flow-Through data disaggregated 15 by customer type and for the Summary/Aggregate. Detailed flow-through 16 results for individual CLECs are included in Attachment 2F. The following 17 table shows the Regional Flow-Through results for September, October and 18 November 2001 as compared with the Interim SQM benchmarks.

## % Flow-through Service Requests (F.1.1.1 – F.1.3.4)

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Customer Type	September 2001	October 2001	November 2001	Benchmark
Residence	90.39%	89.40%	89.40%	95%

Business	68.47%	70.17%	75.18%	90%
UNE	79.33%	76.74%	79.66%	85%
LNP	86.96%	89.09%	91.24%	85%

The table above excludes those LSRs designed to "fall out" for manual handling. Business flow-through rates are well below the 90% objective. Business LSRs are more complex than the typical LSRs and, as a result, there is a greater probability for error. For example, an LSR requesting 10 lines with series completion hunting that are located over multiple floors and have a variation of features on the lines presents many more opportunities for system mismatches than one that adds just lines and features.

BellSouth has established a Flow-Through Improvement Program Management process that includes seven different internal organizations. Ongoing analysis is being done to determine trends and identify flow-through problems. To date, fifteen system enhancements have been identified and are targeted for Encore releases. Three of the enhancements were implemented in August, five enhancements implemented in November and two enhancements implemented in January 2002. The remainder of the enhancements are scheduled for release during early 2002.

## 2. UNE Provisioning Measures

1 BellSouth met 87% of the overall UNE Provisioning measurements in the 2 month of September, 87% of these measurements in October and 84% in 3 November 2001. 4 5 The following sub-metrics did not meet the applicable retail analogues in the 6 months of September, October and/or November 2001: 7 8 Order Completion Interval / Combo (Loop & Port) / < 10 Circuits / Switch 9 Based Orders (B.2.1.3.1.3) (September/November) This sub-metric is a further disaggregation of Item B.2.1.3.1.2. 10 The 11 completion interval difference between the CLEC result and the result for the 12 BellSouth retail analogue for this sub-metric was only 0.01 days for 13 September and 0.03 days for November. Both measures were approximately one-third day. This indicates virtually identical service for both the CLECs 14 15 and the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in October 2001. 16 17 Order Completion Interval / Combo (Loop & Port) / >= 10 Circuits / Non-18 19 Dispatch (B.2.1.3.2.2) (November) There was only one order for this sub-metric in November 2001. The small 20 21 universe of orders for this sub-metric does not provide a statistically 22 conclusive comparison to the retail analogue. There was no CLEC activity for

1	this sub-metric in September 2001. BellSouth met the retail analogue
2	comparison for this sub-metric in October 2001.
3	
4	Order Completion Interval / Combo (Loop & Port) / >= 10 Circuits / Dispatch
5	In (B.2.1.3.2.4) (November)
6	There was only one order for this sub-metric in November 2001. The small
7	universe of orders for this sub-metric does not provide a statistically
8	conclusive comparison to the retail analogue. There was no CLEC activity for
9	this sub-metric in September 2001. BellSouth met the retail analogue
10	comparison for this sub-metric in October 2001.
11	
12	Order Completion Interval / Combo Other / < 10 Circuits / Dispatch
13	(B.2.1.4.1.1) (October/November)
14	The primary factor for the miss in this sub-metric is that the standard
15	installation interval for this product is 10 days. This is much longer than for
16	the retail analogue product. Even though the committed dates to the
17	customer are being met, the intervals are longer than for the retail analogue
18	product. There was no CLEC activity for this sub-metric in September 2001.
19	<u>-</u>
20	Order Completion Interval / Other Non-Design / < 10 Circuits / Dispatch
21	(B.2.1.15.1.1) (October)
22	The average order completion interval for CLEC orders in this sub-metric for
23	October was 4.29 days compared to an average of 3.81 days for the retail

analogue. The "standard" offered completion interval for this sub-metric is longer than for the retail analogue it is compared against. Nevertheless, the difference of less than one half day, on average, does not hinder the CLECs' ability to compete in this area. BellSouth met the retail analogue comparison for this sub-metric in September and November 2001. % Jeopardies / Other Non-Design (B.2.5.15) (September/October/November) There were a total of 3 jeopardies issued for the 119 orders that were scheduled for this sub-metric in September, 12 jeopardies issued for the 288 orders scheduled for October and 2 jeopardies issued for the 32 orders scheduled for November 2001. While the data indicates that BellSouth placed a higher percentage of CLEC orders in jeopardy status, all of the jeopardy orders in September were actually worked on time. All of the ieopardies except one in October and one in November were resolved prior to the due dates, and the orders were completed on time. % Jeopardy Notice Interval >= 48 hours / Combo (Loop & Port) / < 10 Circuits (B.2.10.3) (September) The calculations for this measure have been determined to be incorrect. A portion of the coding modifications required to correct this problem were implemented in September 2001. BellSouth is continuing to prepare and test the remainder of the modifications necessary to correct the calculations for this measure.

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1 2 % Missed Installation Appointments / Combo (Loop & Port) / < 10 Circuits / 3 Non-Dispatch (B.2.18.3.1.2) (September/October/November) 4 BellSouth missed 25 of the 9,659 scheduled appointments in this sub-metric 5 for September, missed 29 of the 10,375 appointments for October and 6 missed 12 of the 10,916 appointments for November 2001. BellSouth met 7 over 99% of the scheduled appointments for both retail and CLEC orders in 8 this sub-metric for all three months. When BellSouth provisions high quality 9 service coupled with very large universe sizes, it can cause an apparent out 10 of equity condition from a quantitative viewpoint. In these cases, there is 11 very little variation and the universe size is so large that the Z-test becomes 12 overly sensitive to any difference. In other words, the statistical test shows 13 that the measurement does not meet the fixed critical value when compared 14 with the retail analogue, but BellSouth's actual performance for both CLECs 15 and its own retail operations is at a very high level – in this case over 99%. 16 From a practical point of view, the CLECs' ability to compete has not been 17 hindered even though the statistical results may technically show that 18 BellSouth failed to meet the benchmark/analogue. 19 20 % Missed Installation Appointments / Combo (Loop & Port) / < 10 Circuits / Dispatch In (B.2.18.3.1.4) (September/October/November) 21 22 This is a further disaggregation of Item B.2.18.3.1.2, above. BellSouth 23 missed 25 of the 4,091 appointments in this sub-metric scheduled in

1	September, missed 29 of the 4,612 appointments scheduled in October and
2	missed 12 of the 5,253 appointments scheduled in November 2001.
3	BellSouth completed over 99% of the appointments as scheduled in
4	September, October and November 2001.
5	
6	% Missed Installation Appointments / Combo Other / < 10 Circuits / Dispatch
7	(B.2.18.4.1.1) (October)
8	BellSouth missed four of the thirty-seven installation appointments scheduled
9	for this sub-metric in October. None of these appointment misses resulted in
10	held orders. No systemic installation issues or patterns were identified for
11	these missed appointments. There was no CLEC activity for this sub-metric
12	in September 2001. BellSouth met the retail analogue comparison for this
13	sub-metric in November 2001.
14	
15	% Missed Installation Appointments / Other Non-Design / >= 10 Circuits /
16	Dispatch (B.2.18.15.2.1) (November)
17	There were only two orders for this sub-metric in November 2001. The small
18	universe of orders for this sub-metric does not provide a statistically
19	conclusive comparison to the retail analogue. BellSouth met the retail
20	analogue comparison for this sub-metric in September and October 2001.
21	
22	% Provisioning Troubles w/i 30 Days / Combo (Loop & Port) / >= 10 Circuits /
23	Dispatch (B.2.19.3.2.1) (September)

There were five troubles reported for the twenty-one orders completed in the 2 30 days prior to September for this sub-metric. No systemic problems were 3 identified for this small number of troubles. BellSouth met or exceeded the 4 retail analogue for this sub-metric in October and November 2001. 5 6 % Provisioning Troubles w/i 30 Days / Combo Other / < 10 Circuits / Dispatch 7 (B.2.19.4.1.1) (November) 8 There were 6 troubles reported for the 32 orders completed for this sub-metric 9 in the 30 days prior to November 2001. No patterns or systemic installation 10 issues were identified for any of these trouble reports. There was no CLEC 11 activity for this sub-metric in either September or October 2001. 12 13 % Provisioning Troubles w/i 30 Days / Other Design / < 10 Circuits / Dispatch (B.2.19.14.1.1) (September/October/November) 14 15 There were 44 troubles reported for the 725 orders that completed in the 30 16 days prior to September, 10 troubles reported for the 104 orders completed in 17 the 30 days prior to October and 27 troubles reported for the 375 orders completed in the 30 days prior to November 2001 for this sub-metric. In 18 19 September, 9 of the trouble reports, or 21%, were closed as "no trouble found." In October, one of the troubles was closed as "no trouble found." The 20 majority of the troubles in each month were for various facility and central 21 22 office problems with no patterns or systemic issues identified.

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1 % Provisioning Troubles w/i 30 Days / Other Design / >= 10 Circuits / 2 Dispatch (B.2.19.14.2.1) (September/October) 3 There were only ten orders completed for this sub-metric in the 30 days prior 4 to September and only one order completed in the 30 days prior to October 5 2001. The small universe of orders for this sub-metric does not provide a 6 statistically conclusive comparison to the retail analogue. BellSouth met the 7 retail analogue comparison for this sub-metric in November 2001. 8 9 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits / 10 Dispatch-In (B.2.21.3.1.4) (September) 11 The root cause analysis of this measure indicated that the only differences 12 between the performance between BellSouth retail and CLECs are the 13 mismatches found when the orders are compared with the original LSRs. 14 The start of the completion interval is the point at which the technician 15 completes the order, and the interval ends when the completion notice is 16 sent. Any change to a name, number of items, etc., occurring during the 17 provisioning process will generate inconsistencies with the original LSRs that must be resolved before a final completion notice can be sent. Any time to 18 resolve these inconsistencies with the original LSRs is included in the 19 Because of numerous CLEC changes and order updates, 20 average. 21 mismatches on CLECs orders exceed those for BellSouth retail orders. 22 Combining this with the smaller base for the CLECs' measurement raises the

average, which results in a miss. Specific Service Representatives within the

Work Management Centers have been assigned to resolve any completion issues that are required. Providing specific training and dedicating personnel to this task should reduce the difference between the CLEC and retail analogue results. BellSouth met the retail analogue comparison for this submetric in October and November 2001. Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch (B.2.34.1.1.1) (October) BellSouth met the standard for 36 of the 38 orders (94.74%) reviewed in this sub-metric in October 2001. Normal rounding conventions indicates that there is no significant difference between the CLEC result and the benchmark for October. BellSouth met the benchmark for this sub-metric in September and November 2001. Service Order Accuracy / Loops Non-Design / < 10 Circuits / Dispatch (B.2.34.2.1.1) (September/October) BellSouth met the standard for 23 of the 28 orders reviewed for this submetric in September and for 21 of the 32 orders reviewed in October 2001. The 95% benchmark set a requirement of 27 of the 28 orders reviewed in September and 31 of the 32 orders reviewed in October based on the quantity of orders in the sub-metric. BellSouth met the benchmark for this sub-metric in November 2001.

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1 Service Order Accuracy / Loops Non-Design / < 10 Circuits / Non-Dispatch 2 (B.2.34.2.1.2) (September/October/November) 3 BellSouth met the standard for 120 of the 200 orders reviewed in this sub-4 metric for September and for 128 of the 188 orders reviewed in October 2001. The 95% benchmark set a requirement of 190 orders for September and 179 5 6 orders in October based on the quantity of orders for this sub-metric. In 7 November 2001, BellSouth met the standard for 284 of the 300 orders 8 (94.67%) reviewed. Normal rounding convention indicates that there is no 9 significant difference between the CLEC result and the benchmark for 10 November. BellSouth continues to focus on this measurement in order to 11 improve results to meet the benchmark. 12 13 Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Non-Dispatch 14 (B.2.34.2.2.2) (September/October/November) 15 There were only 4 orders reviewed for this sub-metric in September and 11 orders reviewed in October 2001. The small universe of orders for this sub-16 17 metric combined with the 95% benchmark required that all orders reviewed in each month be trouble free. A problem with any order would cause a miss for 18 19 the entire sub-metric. BellSouth met the standard for 49 of the 58 orders reviewed for this sub-metric in November 2001. The 95% benchmark set a 20 requirement of 56 orders based on the number of orders for the sub-metric. 21 BellSouth continues to focus on this measurement in order to improve results 22 23 to meet the benchmark.

## 3. UNE Maintenance and Repair (M&R) Measures

- 3 BellSouth met the applicable performance standard for 90% in September,
- 4 87% in October and 89% in November 2001 of the overall UNE M&R
- 5 measurements. The sub-metrics that did not meet the fixed critical value for
- 6 this checklist item in September, October and/or November are as follows:

## % Missed Repair Appointments / Combo (Loop & Port / Non-Dispatch

9 (B.3.1.3.2) (September/November)

BellSouth completed 635 of the 662 repair appointments (96%) as scheduled for this sub-metric in September and completed 676 of the 697 (97%) of the repair appointments scheduled for November 2001. Eleven of the twenty-seven missed appointments in September were orders that were grouped together for one customer for the same trouble, and in November, 12 of the 21 missed appointments were grouped together for 4 customers. Even though the statistical test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, BellSouth's actual performance for both CLECs and its own retail operations is at a high level. From a practical point of view, the CLECs' ability to compete has not been hindered even though the statistical results may technically show that BellSouth failed to meet the retail analogue comparison. BellSouth met the retail analogue comparison for this sub-metric in October 2001.

1 % Missed Repair Appointments / Other Design / Non-Dispatch (B.3.1.10.2) 2 (September) 3 BellSouth missed 1 of the 11 repair appointments scheduled for this sub-4 metric in September 2001. No systemic problems were identified for the 5 missed appointment. BellSouth met the retail analogue comparison for this 6 sub-metric in October and November 2001. 7 8 Customer Trouble Report Rate / Combo Other / Dispatch (B.3.2.4.1) 9 (September/October) 10 Over 96% of the lines in service for this sub-metric for both CLECs and the 11 retail analogue provided trouble free service in September and October 2001. 12 Of the 31 troubles reported for this sub-metric in September, 5 (16%) were 13 closed as "no trouble found." In October, 8 (18%) of the 45 trouble reports were closed as "no trouble found." Major emphasis is being placed on 14 15 improving field documentation of test results during the closeout process. 16 BellSouth met or exceeded the retail analogue for this sub-metric in 17 November 2001. 18 19 Customer Trouble Report Rate / Combo Other / Non-Dispatch (B.3.2.4.2) 20 (October) There were 35 troubles reported for the 1,317 lines in service for this sub-21 22 metric in October. Both the CLECs and BellSouth retail had over 97% trouble 23 free service for the month. Of the 35 October trouble reports for this sub-

1 metric, 14 (40%) were closed as "no trouble found." With the exclusion of 2 these TOK/FOK reports. BellSouth would have met the retail analogue 3 comparison for October. BellSouth met the retail analogue comparison for 4 this sub-metric in September and November 2001. 5 6 Customer Trouble Report Rate / Other Design / Dispatch (B.3.2.10.1) 7 (October/November) 8 The difference between the retail analogue and the CLEC aggregate was 9 1.1% or less in both October and November 2001. Both the CLECs and 10 BellSouth retail had greater than 98% trouble free service for all in service lines in this sub-metric in both months. In October and November, 14% and 11 17%, respectively, of the trouble reports for this sub-metric were closed as 12 "no trouble found." From a practical point of view, the CLECs' ability to 13 compete has not been hindered even though the statistical results may 14 technically show that BellSouth failed to meet the benchmark/analogue. 15 BellSouth met the retail analogue comparison for this sub-metric in 16 17 September 2001. 18 Customer Trouble Report Rate / Other Design / Non-Dispatch (B.3.2.10.2) 19 20 (November) The difference between the retail analogue and the CLEC aggregate was only 21 0.3% for this sub-metric in November 2001. Both the CLECs and BellSouth 22 retail had greater than 99% trouble free service for all in service lines in this 23

1 sub-metric. Five of the nine trouble reports were closed as "no trouble found." 2 BellSouth met the retail analogue comparison for this sub-metric in 3 September and October 2001. 4 5 Customer Trouble Report Rate / Other Non-Design / Dispatch (B.3.2.11.1) 6 (September/October/November) 7 There were a total of 67 trouble reports for the 697 in service lines for this 8 sub-metric in September, 49 trouble reports for the 688 lines in service in 9 October and 68 trouble reports for the 656 lines in service in November 2001. 10 In September, 33 of the 67 total trouble reports (49%), in October, 34 of the 11 49 reports (69%) and in November, 57 of the 68 trouble reports (84%) were 12 identified as being BellSouth customers rather than CLEC customers. Of the 13 remaining 34 September reports, 17 reports (50%) were from the same 14 customer for the same trouble incident. There were no trends identified in an 15 analysis of the remaining 15 October reports. In November, 3 of the 11 CLEC 16 trouble reports were closed as "no trouble found." Continuing analysis is 17 underway to determine if any systemic issues exist with this sub-metric. 18 19 Customer Trouble Report Rate / Other Non-Design / Non-Dispatch 20 (B.3.2.11.2) (September/October/November) 21 There were a total of 45 troubles reports for the 697 in service lines for this sub-metric in September, 28 troubles reported for the 688 lines in service in 22 October and 53 troubles reported for the 656 in service lines for November 23

1 2001. An analysis revealed that 30 of the 45 trouble reports (67%) for September, 17 of the 28 reports (61%) for October and 25 of the 53 trouble 2 3 reports (47%) for November were closed out as "no trouble found," or about 4 half to two-thirds of the troubles reported had minimal impact on the end-user 5 customer. Continuing analysis is underway to determine any systemic issues with this sub-metric. 6 7 8 % Repeat Troubles within 30 Days / Combo Other / Dispatch (B.3.4.4.1) 9 (September) 10 There were 13 repeat trouble reports for this sub-metric in September 2001. 11 BellSouth is currently investigating this sub-metric to determine if all orders shown as repeats actually had trouble reports within the previous 30 days. 12 13 Five of the thirteen repeat reports in September should have been classified 14 as "information only" and not counted as a repeat report, and three of the 15 remaining reports were closed as 'no trouble found." The other reports revealed no patterns or systemic issues. BellSouth met the retail analogue 16 comparison for this sub-metric in October and November 2001. 17 18 19 Out of Service > 24 hours / Other Design / Non-Dispatch (B.3.5.10.2) 20 (September) 21 In September, 1 of the 11 repair orders was out of service longer than 24 22 hours. No systemic problems were identified for this repair order. BellSouth

1	met the retail analogue comparison for this sub-metric in October and
2	November 2001.
3	
4	Out of Service > 24 Hours / Other Non-Design / Dispatch (B.3.5.11.1)
5	(October)
6	14 of the 37 repair appointments scheduled for this sub-metric in October
7	2001 were out of service longer than 24 hours. Of these 14 trouble reports, 7
8	were identified as BST customers rather than CLEC customers. Of the
9	remaining 7 CLEC reports, 6 met the offered commitment repair interval (4 of
10	the 6 were taken on Friday or Saturday and scheduled due for Monday).
11	BellSouth met the retail analogue comparison for this sub-metric for
12	September and November 2001.
13	
14	4. Other UNE Measures
15	
16	Pre-Ordering
17	Service Inquiry for xDSL loops (F.3.1.1), Loop Makeup Manual (F.2.1) and
18	Loop Makeup Electronic (F.2.2) are included in the Pre-Ordering
19	measurements. The sub-metrics that did not meet the benchmarks in
20	September, October and/or November 2001 are as follows:
21	
22	Loop Makeup Inquiry (Manual) (F.2.1) (October)

BellSouth met the 3-business day benchmark interval for 45 of the 48 1 2 inquiries submitted in October 2001. This was one order short of the 46 3 required by the 95% benchmark. No ordering process issues were identified 4 for the longer interval orders. BellSouth met the benchmark for this sub-5 metric in September and November 2001. 6 7 Service Inquiry with Firm Order / xDSL (F.3.1.1) (September/November) 8 BellSouth met 6 of the 7 inquiries within the 5-day interval in September 2001. 9 The 95% benchmark for this quantity of orders required all 7 to be returned in 10 the benchmark period in September. In November 2001, BellSouth met the 11 5-day interval for 74 of the 78 inquiries. At 94.87%, normal rounding 12 convention indicates that there is no significant difference between the CLEC result and the benchmark level. BellSouth met the benchmark for this sub-13 14 metric in October 2001. 15 16 Operations Support Systems (OSS) 17 The OSS/Preordering measures for which BellSouth did not meet the 18 benchmark/retail analogue in September, October and/or November 2001 19 20 were: 21 Average Response Interval / COFFI / RNS / Region (D.1.3.6.1) (November) 22 Average Response Interval / COFFI / ROS / Region (D.1.3.6.2) (November) 23

The CLECs received slightly longer response times from this system in November 2001 than for the retail analogue standard (6+ seconds average for CLECS compared to 4+ to 5+ seconds for BellSouth). One November transaction was reported as having a duration of approximately three days, while the average for all the rest of the transactions was less than one second. BellSouth is investigating the cause of the reported long duration transaction. BellSouth met the retail analogue comparison for these submetrics in September and October 2001.

## Average Response Interval / CRIS / Region (D.2.4.1.1)

## (September/October/November)

The average response interval for this sub-metric is measured in three separate disaggregations -- the percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. The average response interval for the CLEC requests did not meet the retail analogue intervals for the less than 4-second disaggregation but exceeded both the less than 10 and greater than 10 seconds responses. For the 4-second interval, there was only approximately 1% difference between the CLEC responses as compared with the retail analogue in all three months. Both the CLECs and the retail analogue received approximately 99% within the less than 10 second response interval. Similarly, for the greater than 10 seconds interval measure, the CLECs and the BellSouth retail analogue received approximately 1% of responses in over 10 seconds. These very

1 small differences in response intervals indicate equivalent service levels for 2 the CLECs and BellSouth retail. 3 Average Response Interval / LMOS / Region (D.2.4.4.1, D.2.4.4.2, D.2.4.4.3) 4 5 (September/October/November) 6 The average response intervals for these sub-metrics are measured in three 7 separate disaggregations -- the percentage of queries that are responded to 8 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 9 For all three measurements, the results were virtually identical in September, 10 with all the measures being less than 1% apart. In October and November. 11 the difference in the less than 4-second interval responses was less than 2%, 12 while the differences in the less than 10-second and greater than 10-second 13 interval responses were less than 0.5%. These results indicate virtually 14 equivalent service levels for both the CLECs and BellSouth retail. 15 16 Average Response Interval / LMOSupd / Region (D.2.4.5.1, D.2.4.5.2, D.2.4.5.3) (September/October/November) 17 The average response interval for this sub-metric is measured in three 18 19 separate disaggregations. The percentage of gueries that are responded to 20 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. For each of the three sub-metrics, there was less than a 5% difference in the 21 responses received by the CLECs and BellSouth retail in each month. 22

1 Differences of about 5%, or less, for all of these intervals indicate virtually 2 equivalent service levels for both the CLECs and BellSouth retail. 3 Average Response Interval / LNP/ Region (D.2.4.6.1) (October/November) 4 5 Average Response Interval / LNP/ Region (D.2.4.6.2, D.2.4.6.3) 6 (September/November) 7 The average response interval for this measurement is measured in three 8 separate disaggregations -- the percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 9 10 In October, the average response interval for the CLEC requests did not meet 11 the retail analogue intervals for the less than 4-second disaggregation but 12 exceeded both the less than 10 and greater than 10 seconds responses. In 13 September, October and November 2001, both the CLECs and BellSouth 14 retail received over 98.8% of responses in less than 4 seconds and less than 0.3% in more than 10 seconds. The less than one percent difference for 15 these intervals indicates virtually equivalent service levels for the CLECs and 16 17 BellSouth retail. 18 Average Response Interval / MARCH / Region (D.2.4.7.1, D.2.4.7.2, 19 D.2.4.7.3) (November) 20 The average response interval for this sub-metric is measured in three 21 separate disaggregations -- the percentage of queries that are responded to 22 in less than 4 seconds, less than 10 seconds and greater than 10 seconds. 23

BellSouth missed the retail analogue comparison for this measure in November but met the retail analogue comparison for these sub-metrics in September and October 2001.

#### Average Response Interval / OSPCM / Region (D.2.4.8.2, D.2.4.8.3)

## (September)

The average response interval for these sub-metrics is measured in three separate disaggregations -- the percentage of queries that are responded to in less than 4 seconds, less than 10 seconds and greater than 10 seconds. In September 2001, the CLEC response interval was 44.19% within 4 seconds as compared to 42.76% for the retail analogue. For the less than 10 second response interval, the CLECs received 94.19% of their responses and the retail analogue received 97.18% in September. For the greater than 10 second response interval, the CLECs received 5.81% of their responses and the retail analogue received 2.82% in September. With activity levels of only 86 requests from this system for the month, only one to five additional responses within 10 seconds would have brought the sub-metric into parity with the retail analogue. BellSouth met the retail analogue comparison for all three of these sub-metrics in October and November 2001.

#### Average Response Interval / NIW / Region (D.2.4.11.1) (October)

The average response interval for this sub-metric is measured in three separate disaggregations -- the percentage of queries that are responded to

in less than 4 seconds, less than 10 seconds and greater than 10 seconds. In October, the average response interval for the CLEC requests did not meet the retail analogue intervals for the less than 4-second disaggregation but exceeded both the less than 10 and greater than 10 seconds responses. The CLEC response interval was 71.22% within 4 seconds in October, as compared with 72.73% for the retail analogue. The small difference between the CLEC and retail analogue results should not impede the CLECs' ability to compete in this area. BellSouth met the retail analogue comparison for this sub-metric in September and November 2001.

## General - Billing

Usage Data Delivery Timeliness / Region (F.9.2) (November)

This measure compares the percentage of recorded usage data delivered to the appropriate CLECs within six calendar days from the receipt of the initial recording to the BellSouth retail analog. In November 2001, the BellSouth result was 98.89% compared to the CLEC result of 98.37%. The difference in performance was the result of problems encountered by BellSouth in updating one CLEC customer's UNE accounts after the customer requested a bill period change and then changed their mind. While the CLEC measurement is slightly lower than the BellSouth results, the CLECs are provided with substantially the same opportunity to bill end users as is BellSouth. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

1 2 Usage Data Delivery Completeness / Region (F.9.3) (November) 3 This measure compares the percentage of complete and accurately recorded 4 usage data processed and transmitted to CLECs within thirty (30) days of the 5 message recording date to the BellSouth retail analog. The CLECs 6 experienced usage data delivery completeness rates that were slightly lower 7 than the rates for BellSouth customers during November 2001 (99.85% for 8 BellSouth versus 99.54% for CLECs). The difference in performance was the 9 result of problems encountered by BellSouth in updating one CLEC 10 customer's UNE accounts after the customer requested a bill period change 11 and then changed their mind. It is important to point out that the CLEC result 12 of 99.54% still provides the CLECs a meaningful opportunity to compete. BellSouth met the retail analog comparison for this sub-metric in September 13 14 and October 2001. 15 16 Recurring Charge Completeness / UNE (F.9.5.2) (September) 17 In September 2001, the result for this sub-metric was 86.34% against a benchmark of 90%. The benchmark was not met in September because of 18 19 problems encountered in correcting some service order problems in a timely 20 manner. The CLECs are provided with a meaningful opportunity to compete, 21 as this issue does not impede the ability to serve end users. BellSouth met the retail analogue comparison for this sub-metric in October and November 22

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2001.

Recurring Charge Completeness / Interconnection (F.9.5.3) (September) This measure tracks the ability of the ordering and billing systems to begin billing an CLEC recurring charges for local interconnection services on the next invoice after an order has "completed". A benchmark of 90% has been set as the level of performance to meet. In September 2001, the result for this measure was 38.01%. This result was negatively impacted by service orders issued to move billed amounts from one billing account to another connected with CLECs which have filed for bankruptcy. These orders were backdated several months to the date of the bankruptcy. None of these orders impacted the CLECs' total billed amounts but were issued to separate pre-bankruptcy billed amounts from post-bankruptcy amounts. The CLECs are provided with a meaningful opportunity to compete, as these issues do not impede the ability to serve end users. BellSouth met the benchmark for this sub-metric in October and November 2001. Non-Recurring Charge Completeness / Interconnection (F.9.6.3) (September/October/November) This measure tracks the ability of the ordering and billing systems to begin billing a CLEC non-recurring charges for local interconnection services on the next invoice after an order has "completed". A benchmark of 90% has been set as the level of performance to meet. In September, October and November 2001, BellSouth's performance was 87.61%, 63.16% and 73.99%, respectively. This measure was missed in all three months because of

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1	problems encountered in correcting service order errors in a timely manner.
2	The differences between the benchmark and the CLEC results do not impair
3	a CLEC's ability to support its own end users or to effect billing to those end
4	users in any meaningful way.
5	
6	General - Change Management
7	% Software Release Notices Sent On Time (F.10.1) (October)
8	Average Software Release Notice Delay Days (F.10.2) (October)
9	BellSouth met the specified benchmark intervals for one of the two software
10	releases issued in October 2001. BellSouth met the benchmark intervals for
11	all releases in September and November 2001.
12	
13	% Change Management Documentation Sent On Time (F.10.3) (November)
14	Average Documentation Release Delay Days (F.10.5) (November)
15	There was only one Change Management Documentation notice issued in
16	November 2001. This notice did not meet the standard notice interval. There
17	was no activity for these sub-metrics in September 2001. BellSouth met the
18	benchmark for these sub-metrics in October 2001.
19	
20	<u>General – New Business Requests</u>
21	% Quotes Provided in 10 Business Days (F.11.2.1) (September)
22	In September 2001, four items were inadvertently counted in this sub-metric
23	that were not appropriate. The removal of these items would meet the

1 benchmark requirement for September. There was no CLEC activity for this 2 sub-metric in either October or November 2001. 3 4 General - Ordering 5 % Acknowledgement Message Completeness / EDI (F.12.2.1) 6 (September/October) 7 In September 2001, there were only 2 failed messages (0.003%) of the 8 67,850 total messages returned for the month, and there were only 18 failed 9 messages (0.02%) of the 87,896 total messages in October 2001. A Stability 10 Plan to improve EDI availability has been put into effect. This plan includes 11 implementing both a manual application monitoring schedule (24 / 7) and 12 increased mechanized application alarms to more adequately monitor and 13 react to application outages. The database parameters have also been 14 adjusted to allow for maximum processing in the EDI system. BellSouth met 15 the 100% benchmark for this sub-metric in November 2001. 16 % Acknowledgement Message Completeness / TAG (F.12.2.2) 17 18 (September/October) BellSouth failed to deliver 5 (0.003%) of the 167,159 messages in September 19 20 and 4 (0.002%) of the 195,248 messages in October 2001 for this sub-metric. Analysis continues to identify any issues in this process. However, such a 21 small number of failed records have not revealed any systemic process 22

1 BellSouth met the 100% benchmark for this sub-metric in problems. 2 November 2001. 3 4 D. CHECKLIST ITEM 4 - UNBUNDLED LOCAL LOOPS 5 As discussed in Checklist Item 2, Sections B.2 and B.3 of Attachment 1F 6 provide data for provisioning and maintenance & repair measures for 7 unbundled local loops. 8 9 For purposes of discussion in this checklist item, the local loop sub-metrics 10 have been separated into two mode-of-entry groups, xDSL and 11 SL1/SL2/Digital. The xDSL group includes xDSL (ADSL, HDSL, UCL), ISDN 12 and Line Sharing sub-metrics. The SL1/SL2/Digital group includes the design 13 and non-design 2-wire analog loops, as well as the 2-wire and 4-wire digital 14 loop sub-metrics. 15 16 xDSL Group 17 1. Provisioning Measures 18 The xDSL group sub-metrics that did not meet the fixed critical value 19 comparison requirements for September, October and/or November 2001 are 20 as follows: 21 22 Order Completion Interval / xDSL / < 6 Circuits / Dispatch (B.2.1.5.3.1) 23 (November)

1 The average order completion interval for this sub-metric in November was 2 5.31 days for CLECs compared to 4.42 days for BellSouth' retail customers. 3 This sub-metric experienced a miss in November because 33 of the 117 4 orders had extended intervals requested by the customers which should have 5 been given an "L-code" and excluded from the measure. Without these 6 orders, this sub-metric would have met the retail analogue comparison for 7 November. BellSouth met the retail analogue for this sub-metric in 8 September and October 2001. 9 10 Order Completion Interval / Line Sharing / < 6 Circuits / Non-Dispatch 11 (B.2.1.7.3.2) (November) 12 There were only five orders for this sub-metric in November 2001. The small 13 universe of orders for this sub-metric does not provide a statistically 14 conclusive comparison to the retail analogue. BellSouth met the retail 15 analogue comparison for this sub-metric in September and October 2001. 16 17 Order Completion Interval within 14 Days / xDSL w/Conditioning / < 6 Circuits 18 (B.2.2.1) (November) 19 There was only one order for this sub-metric in November 2001. The small 20 universe of orders for this sub-metric does not provide a conclusive 21 benchmark comparison. BellSouth met the benchmark for this sub-metric in 22 September and October 2001. 23

1 Held Orders / UNE ISDN / < 10 Circuits / Facility (B.2.3.6.1.1) (November) 2 There were only five orders for this sub-metric in November 2001. The small 3 universe of orders for this sub-metric does not provide a statistically 4 conclusive comparison to the retail analogue. BellSouth met the retail 5 analogue comparison for this sub-metric in September and October 2001. 6 7 Held Orders / UNE ISDN / < 10 Circuits / Other (B.2.3.6.1.3) (November) 8 There were only two orders for this sub-metric in November 2001. The small 9 universe of orders for this sub-metric does not provide a statistically 10 conclusive comparison to the retail analogue. BellSouth met the retail 11 analogue comparison for this sub-metric in September and October 2001. 12 13 % Jeopardy Notice >= 48 Hours / xDSL (B.2.10.5) (September) 14 The calculations for this measure have been determined to be incorrect. A 15 portion of the coding modifications required to correct this problem were 16 implemented in September 2001. BellSouth is continuing to prepare and test 17 the remainder of the modifications necessary to correct the calculations for 18 this measure. 19 % Missed Installation Appointments / Line Sharing / < 10 Circuits / Dispatch 20 21 (B.2.18.7.1.1) (October) 22 There were only seven orders for this sub-metric in October 2001. Such a 23 small universe does not provide a statistically conclusive comparison to the

retail analogue. BellSouth met the retail analogue comparison for this sub-2 metric in September 2001. there was no CLEC activity for this sub-metric in 3 November 2001. 4 5 % Provisioning Troubles within 30 Days / UNE ISDN / < 10 Circuits / Dispatch 6 (B.2.19.6.1.1) (October) 7 In October, there were 24 troubles reported for orders that completed in the prior 30 days in this sub-metric. Five (21%) of the twenty-four trouble reports 8 9 were closed as "no trouble found." BellSouth has implemented an improved 10 procedure to document circuit test results in the order closeout narratives. 11 This initiative, along with added emphasis on cooperative testing procedures, 12 should improve the results for this sub-metric. BellSouth met the retail 13 analogue for this sub-metric in September and November 2001. 14 15 % Provisioning Troubles within 30 Days / Line Sharing / < 10 Circuits / 16 Dispatch (B.2.19.7.1.1) (September/November) There were only eight orders for this sub-metric in September and only seven 17 orders in November 2001. The small universe of orders for this sub-metric 18 does not provide a statistically conclusive comparison to the retail analogue. 19 BellSouth met the retail analogue comparison for this sub-metric in October 20 21 2001. 22

1 % Provisioning Troubles within 30 Days / Line Sharing / < 10 Circuits / Non-2 Dispatch (B.2.19.7.1.2) (September/October/November) 3 There were 20 troubles reported in this sub-metric for the 125 orders 4 completed in the 30 days prior to September, 16 trouble reports for the 77 5 orders completed in the 30 days prior to October and 6 troubles reported for 6 the 21 orders completed in the 30 days prior to November 2001. In both 7 September and October, 50% of the trouble reports were closed as "no 8 trouble found." In November, 5 of the 6 (83%) of the reports were closed as 9 "No trouble found." An analysis of the remainder of the reports did not reveal 10 any distinct patterns or systemic installation problems. 11 12 Average Completion Notice Interval / xDSL / < 10 Circuits / Dispatch 13 (B.2.21.5.1.1) (September) 14 The root cause analysis of this measure indicated that the only differences 15 between the performance between BellSouth retail and CLECs are the 16 mismatches found when the orders are compared with the original LSRs. 17 The start of the completion interval is the point at which the technician 18 completes the order, and the interval ends when the completion notice is 19 sent. Any change to a name, number of items, etc., occurring during the 20 provisioning process will generate inconsistencies with the original LSRs that 21 must be resolved before a final completion notice can be sent. Any time to

resolve these inconsistencies with the original LSRs is included in the

Because of numerous CLEC changes and order updates,

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average.

mismatches on CLECs orders exceed those for BellSouth retail orders. Combining this with the smaller base for the CLECs' measurement raises the average, which results in a miss. Specific Service Representatives within the 4 Work Management Centers have been assigned to resolve any completion issues that are required. Providing specific training and dedicating personnel to this task should reduce the difference between the CLEC and retail 7 analogue results. 2. Maintenance & Repair Measures The xDSL group sub-metrics that did not meet the fixed critical value comparison requirements for September, October and/or November 2001 are 12 as follows: 13 % Missed Repair Appointments / Line Sharing / Non-Dispatch (B.3.1.7.2) 14 15 (November) BellSouth missed five of thirty-six appointments scheduled for this sub-metric 16 17 in November 2001. An action plan has been implemented to cover central office technicians on proper handling of Line Sharing troubles. BellSouth met 18 19 the retail analogue comparison for this sub-metric in September and October 2001. 20 21 Customer Trouble Report Rate / xDSL Loops / Dispatch (B.3.2.5.1) 22 23 (September/October)

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There were a total of 57 troubles reported for the 5,448 in service lines for this sub-metric in September and 82 troubles reported for the 5,558 lines in service in October 2001. Both the CLECs and BellSouth retail had 98% or more trouble free service for all in service lines in this sub-metric in both months. Even though the measurement indicated that BellSouth did not meet the retail analogue, both BellSouth and the CLECs were being provided a high level of service for this sub-metric. BellSouth met the retail analogue comparison for this sub-metric in November 2001.

## Customer Trouble Report Rate / UNE ISDN / Dispatch (B.3.2.6.1)

## (September/October/November)

Both the CLECs and BellSouth retail had 97% to 98% trouble free service for all in service lines in this sub-metric in September, October and November 2001. Even though the measurement indicated that BellSouth did not meet the retail analogue, both BellSouth and the CLECs were being provided a high level of service for this sub-metric. BellSouth is developing an action plan to improve circuit testing and turn-up documentation. ISDN test jacks have been installed in each central office to facilitate improved testing and turn-up control procedures.

## Customer Trouble Report Rate / Line Sharing / Dispatch (B.3.2.7.1)

## 22 (November)

There were a total of 14 troubles reported for the 1,132 in service lines for this sub-metric in November 2001. Of the 14 November trouble reports, 4 (29%) were closed as "no trouble found." There were no distinctive trends or systemic problems identified for any of the troubles reported for this submetric. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001. Customer Trouble Report Rate / Line Sharing / Non-Dispatch (B.3.2.7.2) (October/November) There were a total of 33 troubles for the 1,051 in service lines for this submetric in October and 36 troubles reported for the 1,132 lines in service in November 2001. In October, 28 of the 33 troubles (85%) and in November, 29 of the 36 troubles (81%) were closed as "no trouble found." Even though the measurement indicated that BellSouth did not meet the retail analogue, both BellSouth and the CLECs were being provided a high level of service for this sub-metric. BellSouth met the retail analogue comparison for this submetric in September 2001. SL1/SL2/Digital Loop Group 1. Provisioning Measures The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed critical value comparison requirements for September, October and/or November 2001 are as follows: 23

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## **Order Completion Interval (OCI)**

A root cause analysis for OCI for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS orders, but the wholesale non-dispatched orders were receiving the same interval as "dispatched" orders. On June 2, 2001, a release was added to the due date calculator software to correct this error. However, due to problems with the software load, it had to be removed. In addition to the appointment interval issue, OCI is adversely affected by LSRs for which CLECs request intervals beyond the offered interval. When a CLEC requests an interval beyond the available interval offered by BellSouth, an "L" code is entered on the Service Order generated by BellSouth. "L" coded orders are excluded from the OCI metrics.

# Order Completion Interval / 2w Analog Loop Design / < 10 Circuits / Dispatch

## (B.2.1.8.1.1) (September/October/November)

There were a total of 209 orders completed for this sub-metric in September, 47 orders completed in October and 230 orders completed in November 2001. The primary factor for the misses in this sub-metric is that the standard installation interval for this product is 4 business days. Even though the committed dates to the customer are being met, the intervals are longer than for the retail analogue product. BellSouth continues to work to lower the

i	interval for this sub-metric to meet the 3 calendar day interval ordered for
2	the POTS type retail analogue services in Florida.
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4	Order Completion Interval / 2w Analog Loop Non-Design / < 10 Circuits /
5	Dispatch (B.2.1.9.1.1) (October/November)
6	The primary contributor to the miss in this sub-metric for both October and
7	November was that 58 (56%) of the 103 orders for October and 61(15%) of
8	the November orders had extended intervals requested by the customers.
9	These orders should have been given and "L" code and excluded from the
10	measurement. BellSouth met the retail analogue comparison for this sub-
11	metric in September 2001.
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13	Order Completion Interval / 2w Analog Loop Non-Design / < 10 Circuits /
14	Dispatch In (B.2.1.9.1.4) (November)
15	There were only nine orders for this sub-metric in November 2001. The small
16	universe of orders for this sub-metric does not provide a statistically
17	conclusive comparison to the retail analogue. BellSouth met the retail
18	analogue comparison for this sub-metric in September and October 2001.
19	
20	Order Completion Interval / 2w Analog Loop w/LNP Design / < 10 Circuits /
21	Dispatch (B.2.1.12.1.1) (September/October/November)
22	There were a total of 178 orders that completed for this sub-metric in
23	September, 225 orders that completed in October and 176 orders that

completed in November 2001. A detailed analysis indicated a significant number of orders with customer requested extended intervals were not "L coded" and should have been excluded from the measurement. BellSouth continues to work to lower the interval for this sub-metric to meet the "3 day" interval ordered for the POTS type retail analogue services in Florida. The current standard interval for orders in this sub-metric is four business days as compared to the three calendar day interval for the retail analogue. Order Completion Interval / 2w Analog Loop w/LNP Non Design / < 10 Circuits / Dispatch (B.2.1.13.1.1) (September/October/November) There were a total of 266 orders that completed for this sub-metric in September, 266 orders that completed in October and 204 orders that completed in November 2001. BellSouth continues to work to lower the interval for this sub-metric to meet the "3 calendar day" interval ordered for the POTS type retail analogue services in Florida. The current standard interval for this sub-metric is four business days as compared to the three-day interval for the retail analogue. Order Completion Interval / Digital Loop < DS1 / < 10 Circuits / Dispatch (B.2.1.18.1.1) (November) There were a total of 307 orders that completed for this sub-metric in November 2001. BellSouth continues to work to lower the interval for this sub-metric to meet the "3 calendar day" interval ordered for the POTS type 23

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retail analogue services in Florida. Due to customer requests, 90 of the 307 orders were given due date intervals longer than 10 days. These orders should have been given "L-codes" and excluded from the measure. The current standard interval for this sub-metric is four business days as compared to the three-day interval for the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001. The remainder of the provisioning measures that did not meet the retail analogue for provisioning is as follows: Held Orders / 2w Analog Loop w/LNP Design / >= 10 Circuits / Facility (B.2.3.12.2.1) (October) There were only four orders for this sub-metric in October 2001. The small universe size for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and November 2001. Held Orders / Digital Loop >= DS1 / < 10 Circuits / Facility (B.2.3.19.1.1) (November) There was only one order associated with this sub-metric in November 2001. The small universe size for this sub-metric does not provide a statistically

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conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

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## % Jeopardies / 2w Analog Loop Design (B.2.5.8)

## (September/October/November)

In September 2001, there were a total of 33 jeopardies issued for the 292 orders that were scheduled for this sub-metric. All but 6 of the jeopardies were resolved prior to the due date and the orders worked as scheduled. Only one of the missed appointments resulted in a held order – which was resolved and completed in 3 days. In October 2001, there were a total of 9 ieopardies issued for the 44 orders that were scheduled for this sub-metric. All but 5 of the jeopardies were resolved prior to the due date and the orders worked as scheduled. None of these jeopardies or missed appointments resulted in held orders in October. In November 2001, there were a total of 24 jeopardies issued for the 230 orders that were scheduled for this submetric. All but 5 of the jeopardies were resolved prior to the due date and the orders worked as scheduled. Only two of the missed appointments resulted in held orders - which were resolved and completed in less than 3 days. There were no missed appointments for BellSouth company reasons in September or October and only two missed appointments for BellSouth company reasons in November.

## % Jeopardies / 2w Analog Loop Non-Design (B.2.5.9)

## (September/October/November)

In September 2001, there were a total of 31 jeopardies issued for the 463 orders that were scheduled for this sub-metric. All but 10 of the jeopardies were resolved and the orders were worked as scheduled. Only 3 of the 31 jeopardies in this sub-metric resulted in a held order that were resolved and completed in an average of 4 days. In October 2001, there were a total of 4 jeopardies issued for the 64 orders that were scheduled for this sub-metric. None of the 4 October jeopardies resulted in a missed installation appointment. In November 2001, there were a total of 6 jeopardies issued for the 177 orders that were scheduled for this sub-metric. None of the 6 November jeopardies resulted in a misses installation appointment.

#### % Jeopardies / 2w Analog Loop w/LNP Design (B.2.5.12)

#### (September/November)

In September 2001, there were a total of 82 jeopardies issued for the 3,707 orders that were scheduled for this sub-metric. All but 5 of the jeopardies were resolved and the orders were worked as scheduled. In November 2001, there were a total of 24 jeopardies issued for the 476 orders that were scheduled for this sub-metric. None of the November jeopardies resulted in missed installation appointments. BellSouth met the retail analogue comparison for this sub-metric in October 2001.

1 % Jeopardies / 2w Analog Loop w/LNP Non-Design (B.2.5.13) (November) 2 In November 2001, there were a total of 44 jeopardies issued for the 396 3 orders that were scheduled for this sub-metric. Only 2 of the 44 November 4 jeopardies resulted in missed installation appointments. One of these two 5 misses was due to customer reasons. BellSouth met the retail analogue 6 comparison for this sub-metric in September and October 2001. 7 8 % Jeopardies / Digital Loop >= DS1 (B.2.5.19) 9 (September/October/November) 10 There were a total of 37 jeopardies issued for the 168 installation 11 appointments that were scheduled for this sub-metric in September. 48 12 jeopardies for the 101 appointments scheduled for October and 71 jeopardies 13 issued for the 120 orders scheduled for November 2001. While the data 14 indicates that BellSouth placed a higher percentage of CLEC orders in 15 jeopardy status, all but 19 of the orders that were placed in jeopardy in 16 September, all 48 of the jeopardy orders in October and all but 8 of the 17 jeopardies for November were resolved prior to the due date, and the orders 18 were completed on time. 19 % Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/LNP Non-20 21 Design (B.2.10.13) (October) The calculations for this measure have been determined to be incorrect. A 22 23 portion of the coding modifications required to correct this problem were implemented in September 2001. BellSouth is continuing to prepare and test the remainder of the modifications necessary to correct the calculations for this measure. % Missed Installation Appointments / 2w Analog Loop w/INP Non-Design / < 10 Circuits / Dispatch (B.2.18.11.1.1) (November) There was only one order for this sub-metric in November 2001. The small universe of orders for this sub-metric does not provide a statistically conclusive comparison to the retail analogue. BellSouth met the retail analogue comparison for this sub-metric in September and October 2001. % Missed Installation Appointments / Digital Loop >= DS1 / < 10 Circuits / Dispatch (B.2.18.19.1.1) (September/October) BellSouth completed 208 of the 227 installation appointments as scheduled for this sub-metric in September and 263 of the 282 installation appointments scheduled in October 2001. In September, nine of the nineteen missed appointments, and in October ten of the nineteen missed appointments were due to unavailability of facilities. The remainder of the missed appointments in both months were due to various scheduling and prioritization problems. BellSouth is refocusing its efforts on this area to improve its performance on these orders. BellSouth met the retail analogue comparison for this submetric in November 2001.

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1 % Provisioning Troubles w/i 30 Days / 2w Analog Loop Design / < 10 Circuits 2 / Dispatch (B.2.19.8.1.1) (September/November) 3 In September 2001, 29 troubles were reported for the 302 orders completed 4 in the prior 30 days. Ten of the twenty-nine troubles were closed as "no 5 trouble found" in September. Without these reports, the CLEC result would 6 have been virtually identical to the result for the retail analogue. Twenty of 7 the twenty-nine trouble reports in September for this sub-metric came from 8 one CLEC. In November 2001, there were 11 troubles reported for the 85 9 orders completed in the prior 30 days. The majority of the troubles were due 10 to defective cable facilities and serving wire. An analysis of the remainder of 11 the troubles revealed no specific patterns or trends. BellSouth met the retail 12 analogue comparison for this sub-metric in October 2001. 13 14 % Provisioning Troubles w/i 30 Days / 2w Analog Loop Design / >= 10 15 Circuits / Dispatch (B.2.19.8.2.1) (October) 16 There was only one order associated with this sub-metric in October 2001. 17 This small universe of orders does not provide a statistically conclusive BellSouth met the retail analogue 18 comparison to the retail analogue. comparison for this sub-metric in September and November 2001. 19 20 % Provisioning Troubles w/i 30 Days / 2w Analog Loop w/INP Non-Design / 21 22 >= 10 Circuits / Dispatch (B.2.19.11.2.1) (November)

1 There was only one order associated with this sub-metric in November 2001. 2 This small universe of orders does not provide a statistically conclusive 3 comparison to the retail analogue. BellSouth met the retail analogue 4 comparison for this sub-metric in September 2001. There was no CLEC 5 activity for this sub-metric in October 2001. 6 % Provisioning Troubles w/i 30 Days / Digital Loops >= DS1 / < 10 Circuits / 7 8 Dispatch (B.2.19.19.1.1) (September/October/November) 9 There were a total of 15 troubles reported for this sub-metric for the 251 10 orders that completed in the 30 days prior to September, 12 troubles reported 11 for the 227 orders that completed in the 30 days prior to October and 18 12 troubles reported for the 282 orders that completed in the 30 days prior to 13 November 2001. In September, October and November, 44%, 25% and 14 33%, respectively, of the trouble reports in this sub-metric were closed as "no 15 trouble found" indicating minimal impact on the end user. BellSouth is 16 currently investigating this sub-metric. There were less than 1% trouble 17 reports indicated for the retail analogue for this sub-metric in each of the three 18 months -- which is also being reviewed. 19 Average Completion Notice Interval / 2w Analog Loop Design / < 10 Circuits / 20 Dispatch (B.2.21.8.1.1) (September/October/November) 21 Average Completion Notice Interval / 2w Analog Loop w/LNP Design / < 10 22 Circuits / Dispatch (B.2.21.12.1.1) (September/October/November) 23

1 Average Completion Notice Interval / 2w Analog Loop w/LNP Design / >= 10 2 Circuits / Dispatch (B.2.21.12.2.1) (November) 3 Average Completion Notice Interval / 2w Analog Loop w/LNP Non-Design / < 4 10 Circuits / Dispatch (B.2.21.13.1.1) (September/October) 5 The root cause analysis of these measures indicated that the only differences 6 between the performance between BellSouth retail and CLECs are the 7 mismatches found when the orders are compared with the original LSRs. 8 The start of the completion interval is the point at which the technician 9 completes the order, and the interval ends when the completion notice is 10 sent. Any change to a name, number of items, etc., occurring during the 11 provisioning process will generate inconsistencies with the original LSRs that 12 must be resolved before a final completion notice can be sent. Any time to resolve these inconsistencies with the original LSRs is included in the 13 Because of numerous CLEC changes and order updates, 14 average. 15 mismatches on CLECs orders exceed those for BellSouth retail orders. 16 Combining this with the smaller base for the CLECs' measurement raises the 17 average, which results in a miss. Specific Service Representatives within the Work Management Centers have been assigned to resolve any completion 18 issues that are required. Providing specific training and dedicating personnel 19 to this task should reduce the difference between the CLEC and retail 20 21 analogue results.

## 2. Maintenance & Repair Measures

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1 The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed 2 critical value comparison requirements for September, October and/or 3 November 2001 are as follows: 4 5 % Missed Repair Appointments / 2W Analog Loop Non-Design / Non-6 Dispatch (B.3.1.9.2) (September/October/November) 7 BellSouth completed 34 of the 36 repair appointments as scheduled in 8 September, 49 of the 57 appointments scheduled for October and 26 of the 9 30 repair appointments scheduled for November 2001. All 4 of the November 10 missed appointments were finally closed as "no trouble found." There were 11 no distinct patterns or systemic maintenance problems identified for any of 12 the missed appointments in these three months. 13 14 Maintenance Average Duration / 2w Analog Loop Non-Design / Non-Dispatch 15 (B.3.3.9.2) (October) 16 There were 57 repair orders completed for this sub-metric in October 2001. 17 Of the 57 total October reports, 33 (58%) were finally closed as "no trouble 18 found." Reports closed as TOK/FOK often have longer duration intervals due to multiple and time consuming test procedures and investigations without 19 20 finding any cause for a problem. Excluding the reports closed to "no trouble found" in October, this sub-metric would have met the retail analogue 21 comparison for the month. BellSouth met the retail analogue comparison for 22 23 this sub-metric in September and November 2001.

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2	% Repeat Reports w/i 30 Days / 2W Analog Loop Non-Design / Non-Dispatch
3	(B.3.4.9.2) (October)
4	There were a total of 57 trouble reports of which 16 were repeats in this sub-
5	metric for October 2001. Of the 16 repeat reports for October, 11 (69%) were
6	closed as "no trouble found." Excluding these TOK/FOK reports, this sub-
7	metric would have met the retail analogue comparison for the month.
8	BellSouth met the retail analogue comparison for this sub-metric in
9	September and November 2001.
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11	Out of Service > 24 Hours / 2W Analog Loop Non-Design / Non-Dispatch
12	(B.3.5.9.2) (October)
13	Of the 12 troubles classified as "out of service" for this sub-metric in October
14	2001, only 5 caused out of service conditions longer than 24 hours. All 5 of
15	these troubles for October were associated with a central office failure.
16	BellSouth met the retail analogue comparison for this sub-metric in
17	September and November 2001.
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20	E. CHECKLIST ITEM 5 - UNBUNDLED LOCAL TRANSPORT
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1	The Provisioning and Maintenance & Repair sub-metrics that did not meet the
2	retail analogue in September, October and/or November 2001 associated
3	with Checklist Item 5 are as follows:
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5	% Missed Installation Appointments / Local Interoffice Transport / < 10
6	Circuits / Dispatch (B.2.18.2.1.1) (September)
7	BellSouth completed 24 of the 26 installation appointments for this sub-metric
8	as scheduled in September 2001. There were no systemic installation issues
9	identified for the two missed appointments. BellSouth met the retail analogue
10	comparison for this sub-metric in October and November 2001.
11	
12	Maintenance Average Duration / Local Interoffice Transport / Dispatch
13	(B.3.3.2.1) (November)
14	There were only two troubles reported for this sub-metric in November 2001.
15	This small universe does not provide a statistically conclusive comparison
16	with the retail analogue. BellSouth met the retail analogue comparison for
17	this sub-metric in September and October 2001.
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19	F. CHECKLIST ITEM 6 - UNBUNDLED LOCAL SWITCHING
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21	The data in these measures indicate that BellSouth met the
22	benchmark/analogue requirements for all measurements in Checklist Item 6
23	for September, October and November 2001.

1 2 G. CHECKLIST ITEM 7a - 911 AND E911 SERVICES 3 H. CHECKLIST ITEM 7b - DIRECTORY ASSISTANCE/OPERATOR 4 SERVICES 5 As indicated in Attachment 1F, Sections F.6, F.7 and F.8, BellSouth met the 6 7 benchmark/analogue requirements of Checklist Items 7a and 7b in 8 September, October and November 2001. Even though BellSouth tracks and reports these measures, the processes used in providing these services are 9 10 designed to provide parity for all users. 11 I. CHECKLIST ITEM 10 - ACCESS TO DATABASES AND ASSOCIATED 12 13 SIGNALING BellSouth met the benchmarks for one out of four sub-metrics in this Checklist 14 Item in September, three out of four sub-metrics in October and all four out of 15 four sub-metrics in November 2001. See items F.13.1.1 through F.13.3 in 16 17 Attachment 1F for further details of the November data. The items that did 18 not meet the appropriate benchmark in September and/or October 2001 are 19 as follows: 20 21 % Update Accuracy / LIDB (F.13.2.1) (September) The results in this sub-metric are based on a statistical sample of LSRs and 22 service orders which are manually checked for the accuracy of information 23

that impacts the LIDB database. In September, all but 23 of the 174 orders were error free for this sub-metric. BellSouth has refocused its effort on all LSRs processed in the partial mechanized and manual categories to eliminate basic errors made by the representatives that should meet the benchmark for this sub-metric. BellSouth met the benchmark for this submetric in October and November 2001. % Update Accuracy / Directory Listings / Region (F.13.2.2) (September) The results in this sub-metric are based on a statistical sample of LSRs and service orders, which are manually checked for the accuracy of information that impacts the Directory Listings database. The September 2001 results were based on a sample size of 89 orders, of which 23 orders were found to contain errors. BellSouth has refocused its effort on all LSRs processed in the partial mechanized and manual categories to eliminate basic errors made by the representatives that should meet the benchmark for this sub-metric. BellSouth met the benchmark for this sub-metric in October and November 2001. % NXXs / LRNs Loaded by LERG Effective Date (Region) (F.13.3) (September/October) The measure indicated that 39 of 40 NXXs were loaded by their effective date in September and 45 of 48 NXXs were loaded by their effective date in

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October 2001 across the BellSouth region. All NXXs were completed as

scheduled in Florida for September, October and November. BellSouth met the benchmark for this sub-metric in November 2001.

## J. CHECKLIST ITEM 11 - NUMBER PORTABILITY

All the measurements in this Checklist Item were met or exceeded for September, October and/or November 2001 except for the following:

% Missed Installation Appointments / LNP (Standalone) / < 10 Circuits / Non-

Dispatch (B.2.18.17.1.2) (September/October)

BellSouth missed only 4 of the 1,381 appointments scheduled for this submetric in September and missed only 3 of the 2,219 appointments scheduled in October 2001. BellSouth met over 99% of the scheduled appointments for both retail and the CLECs in this sub-metric for September and October. When BellSouth provisions high quality service coupled with very large universe sizes, it can cause an apparent out of equity condition from a quantitative viewpoint. In these cases, there is very little variation and the universe size is so large that the Z-test becomes overly sensitive to any difference. In other words, the statistical test shows that the measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual performance for both CLECs and its own retail operations is at a very high level – in this case over 99%. From a practical point of view, the CLECs' ability to compete has not been hindered even

1 though the statistical results may technically show that BellSouth failed to

2 meet the benchmark/analogue. BellSouth met the retail analogue

3 comparison for this sub-metric in November 2001.

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- 5 Average Completion Notice Interval / LNP (Standalone) / < 10 Circuits / Non-
- 6 <u>Dispatch (B.2.21.17.1.2) (September/October)</u>
- 7 Average Completion Notice Interval / LNP (Standalone) / >= 10 Circuits /
- 8 Non-Dispatch (B.2.21.17.2.2) (October)
  - The root cause analysis of these measures indicated that the only differences between the performance between BellSouth retail and CLECs are the mismatches found when the orders are compared with the original LSRs. The start of the completion interval is the point at which the technician completes the order, and the interval ends when the completion notice is sent. Any change to a name, number of items, etc., occurring during the provisioning process will generate inconsistencies with the original LSRs that must be resolved before a final completion notice can be sent. Any time to resolve these inconsistencies with the original LSRs is included in the Because of numerous CLEC changes and order updates, average. mismatches on CLECs orders exceed those for BellSouth retail orders. Combining this with the smaller base for the CLECs' measurement raises the average, which results in a miss. Specific Service Representatives within the Work Management Centers have been assigned to resolve any completion issues that are required. Providing specific training and dedicating personnel

1 to this task should reduce the difference between the CLEC and retail 2 analogue results. 3 4 Disconnect Timeliness / LNP / < 10 Circuits (B.2.31) 5 The Disconnect Timeliness measure is supposed to track the time it takes to 6 disconnect a number in the central office switch after the message has been 7 received from the Local Number Portability (LNP) Gateway that it is ready. 8 However, this measurement does not track the relevant time to perform this 9 function. 10 11 On a great majority of LNP orders, BellSouth creates what is referred to as a 12 "trigger" in conjunction with the order. This trigger gives the end user 13 customer the ability to make and receive calls from other customers who are 14 served by the customer's host switch at the time of the LNP activation. This 15 ability is not dependent upon BellSouth working a disconnect order in the 16 central office switch. In other words, when a trigger is involved, an end user customer can receive calls from other customers served by the same host 17 switch before the disconnect order is ever worked. 18 19 As it currently exists, Performance Measure P-13 does not recognize the 20 importance of triggers and their effect on the LNP process. Rather, the 21 current measure calculates the end time of the LNP activity as the processing 22 of the actual disconnect order in the host switch, even though, from a 23

customer's perspective, this activity is totally meaningless on most LNP orders. It is the activation of the LNP and the routing function accomplished by the LSMS that ultimately determines whether the end user is back in full service and is able to make and receive calls when a trigger is used in porting a telephone number. So, while BellSouth may be missing this measure, the actual impact on CLECs and their end users, for a great majority of the orders is minimal, or nonexistent. The Georgia PSC is currently evaluating a change in this measure that more accurately reflects the LNP process and its impacts on end users, and, therefore, the measurements will be shown blank until a resolution is reached on this issue.

## K. CHECKLIST ITEM 14 - RESALE

BellSouth has met or exceeded the benchmarks/analogues for 86% of the 211 Resale metrics for the month of September, for 80% of the 223 metrics in October and for 83% of the 226 metrics in November 2001. The details are delineated in Attachment 1F, Items A.1.1.1 through A.4.2.

For the three-month period, September through November 2001, there were 185 sub-metrics in the Resale measurements for which there was CLEC activity in all three months and were compared to retail analogues or benchmarks. Of those 185 sub-metrics, 159 sub-metrics (86%) met the retail analogue/benchmark comparisons in at least two of the three months.

# 1. Resale Ordering Measures

## Reject Interval

The benchmark for electronic rejects is 97% within 1 hour. In September 2001, 14,963 resale LSRs were rejected, with 96% meeting the relevant benchmark or retail analogue. Of the 14,963 rejected LSRs, 60% were processed electronically with 95% of them meeting the 1-hour benchmark interval. In October 2001, there was a total of 23,820 resale LSRs rejected, with 94% meeting the relevant benchmark. Of the 23,820 rejected LSRs, 67% were processed electronically with 94% of them meeting the 1-hour benchmark interval. In November 2001, 21,375 resale LSRs were rejected, with 95% meeting the relevant benchmark or retail analogue. Of the 21,375 rejected LSRs, 62% were processed electronically with 95% of them meeting the 1-hour benchmark interval. See Attachment 1F, Items A.1.4 through A.1.8 for further details.

#### **FOC Timeliness**

In September, BellSouth issued FOCs for 48,475 resale LSRs and met the relevant benchmark for 99% of them. Of the 48,475 FOCs returned, 36,875 were fully mechanized with 99% meeting the 3-hour benchmark interval. In October, BellSouth issued FOCs for 71,611 resale LSRs and met the relevant benchmark for 98% of them. Of the 71,611 FOCs returned, 54,852 were fully mechanized with 99% meeting the 3-hour benchmark interval. In November, BellSouth issued FOCs for 68,770 resale LSRs and met the relevant

1 benchmark for 98% of them. Of the 68,770 FOCs returned, 52,438 were fully 2 mechanized with 99.7% meeting the 3-hour benchmark interval. 3 Attachment 1F, Sections A.1.9 through A.1.13 for further details. 4 5 The Ordering sub-metrics for which BellSouth did not meet the 6 benchmarks/analogues for September, October and/or November 2001 were: 7 8 Reject Interval / Residence / Electronic (A.1.4.1) 9 (September/October/November) 10 The current benchmark for this sub-metric is >= 97% within one hour. In 11 September 2001, 7,954 of the 8,395 total rejected LSRs met the one-hour 12 benchmark, and in October, 14,285 of the 15,140 rejected LSRs in this sub-13 metric met the benchmark interval. In November 2001, 11,591 of the 12,177 14 total rejected LSRs for this sub-metric met the 1-hour benchmark interval. 15 BellSouth is conducting a detailed root cause analysis of the process for 16 electronic rejects. This analysis addresses the ordering systems (EDI, TAG, 17 and LENS) used by the CLECs and the back-end legacy applications, such 18 as SOCS, that are accessed by the ordering systems. 19 Thus far, the analysis has determined that many of the LSRs that did not 20 21 meet the one-hour benchmark in September were issued between 11:00 p.m. 22 and 4:30 a.m. Between these hours, the system is unable to process LSRs 23 because certain of the back-end legacy systems are out of service. LSRs submitted during these periods should have been excluded from the measurement. BellSouth implemented a program coding change in September to exclude these LSRs from this measure.

With the May 2001, data month, BellSouth was directed to change the time stamp identification for the start and complete times of the interval for this measurement from the Local Exchange Ordering (LEO) System to the CLEC ordering interface system (TAG or EDI). However, with this change, BellSouth is currently unable to identify multiple issues of the same version of LSRs that have been rejected (fatal rejects). These rejected LSRs should be excluded from the measurement. If there are multiple issues of the same version, the measure currently calculates the interval from the initial issue to the final issue of the LSR returned to the CLEC, Reject or FOC. Consequently, BellSouth's performance level is inappropriately understated. BellSouth is currently working to determine a fix for this issue.

#### Reject Interval / Business / Electronic (A.1.4.2)

#### (September/October/November)

The current benchmark for this sub-metric is >= 97% within one hour. In September 2001, 533 of the 563 rejected LSRs for this sub-metric met the one-hour benchmark, and in October, 839 of the 892 rejected LSRs met the 1-hour benchmark. There were 1,160 LSRs rejected in this sub-metric in November 2001, with 1,099 or 95% meeting the one-hour benchmark.

BellSouth is conducting a detailed root cause analysis of the process for electronic ordering. This analysis addresses the ordering systems (EDI, TAG, and LENS) used by the CLECs and the back-end legacy applications, such as SOCS, that are accessed by the ordering systems. For further information see the explanation included with the electronic reject interval measurement, item A.1.4.1. Reject Interval / Design (Specials) / Electronic (A.1.4.3) (November) There were only two LSRs rejected for this sub-metric in November 2001. The small universe of orders for this sub-metric does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in either September or October 2001. Reject Interval / ISDN / Electronic (A.1.4.6) (October) There were only two LSRs rejected for this sub-metric in October 2001. This small universe does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in either September or November 2001. Reject Interval / ISDN / Partial Electronic (A.1.7.6) (October) There was only one LSR rejected for this sub-metric in October 2001. This small universe does not provide a conclusive benchmark comparison. There

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1 was no CLEC activity for this sub-metric in either September or November 2 2001. 3 4 Reject Interval / Centrex / Manual (A.1.8.5) (November) 5 BellSouth met the 24-hour benchmark interval for 22 of the 27 LSRs rejected 6 for this sub-metric in November 2001. This was only one response short of 7 the 23 required by the 85% benchmark. BellSouth met the benchmark for this 8 sub-metric in September and October 2001. 9 10 FOC Timeliness / Design (Specials) / Partial Electronic (A.1.12.3) 11 (October/November) 12 There was only one LSR rejected for this sub-metric in October and two LSRs rejected in November 2001. This small universe of orders does not provide a 13 14 conclusive benchmark comparison. There was no CLEC activity for this sub-15 metric in September 2001. 16 17 FOC Timeliness / PBX / Partial Electronic (A.1.12.4) (September) There was only one order for which FOCs were returned in this sub-metric in 18 19 September 2001. Such a small universe does not provide a conclusive benchmark comparison. There was no CLEC activity for this sub-metric in 20 either October or November 2001. 21 22 23 FOC Timeliness / ISDN / Partial Electronic (A.1.12.6) (October)

There were only two LSRs rejected for this sub-metric in October 2001. This small universe does not provide a conclusive benchmark comparison. There 3 was no CLEC activity for this sub-metric in either September or November 4 2001. 5 6 FOC & Reject Response Completeness and FOC & Reject Response 7 Completeness (Multiple Responses) Measures 8 Effective with October 2001 data, each sub-metric in the Electronic and Partial Electronic sections for this measurement have been disaggregated 9 10 between LSRs submitted from the EDI and TAG systems. The following FOC & Reject Response Completeness sub-metrics did not meet the 11 benchmarks for September, October and/or November 2001: 12 13 FOC Reject & Response Completeness / Design (Specials) / TAG / Electronic 14 15 (A.1.14.3.2) (October) There was only one order associated with this sub-metric in October 2001. 16 This small universe does not provide a conclusive benchmark comparison. 17 BellSouth met the benchmark for this sub-metric in November 2001. 18 19 FOC Reject & Response Completeness / ISDN / Electronic (A.1.14.6) 20 21 (September) There was only one order for this sub-metric in September 2001. The small 22 universe size for this sub-metric does not provide a conclusive benchmark 23

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1 comparison. This sub-metric was replaced by Items A.1.14.6.1 and 2 A.1.14.6.2 effective with October 2001 data. 3 4 FOC Reject & Response Completeness / Residence / Manual (A.1.16.1) 5 (September/October/November) 6 BellSouth met the completeness criteria for 833 of the 922 responses for this 7 sub-metric in September, 1,114 of the 1,176 responses in October and for 8 1,165 of the 1,276 responses in November 2001. The 95% benchmark 9 required that 887 of 933 LSRs for September, 1,118 of the 1,176 LSRs in 10 October and 1,213 of the 1,276 LSRs in November meet the criteria. BellSouth continues to focus on this measurement in order to improve results 11 12 to meet the benchmark. 13 14 FOC Reject & Response Completeness / Business / Manual (A.1.16.2) 15 (September/October/November) 16 BellSouth met the completeness criteria for 903 of the 969 responses for this 17 sub-metric in September, for 1,168 of the 1,238 responses in October and for 18 1,158 of the 1,260 responses in November 2001. The 95% benchmark 19 required that 921 of 969 LSRs in September, 1,177 of 1,238 LSRs for 20 October and 1,197 of the 1,260 LSRs for November 2001 meet the criteria. 21 BellSouth continues to focus on this measurement in order to improve results 22 to meet the benchmark.

1 FOC Reject & Response Completeness / Design (Specials) / Manual 2 (A.1.16.3) (September/October/November) 3 BellSouth met the completeness criteria for 127 of the 139 responses for this 4 sub-metric in September, for 165 of the 177 responses in October and for 127 5 of the 146 responses in November 2001. The 95% benchmark required that 6 133 of 139 LSRs for September, 169 of the 177 LSRs for October and 139 of 7 146 LSRs for November meet the criteria. BellSouth continues to focus on 8 this measurement in order to improve results to meet the benchmark. 9 10 FOC Reject & Response Completeness / PBX / Manual (A.1.16.4) 11 (September/October/November) 12 BellSouth met the completeness criteria for 61 of the 66 responses for this sub-metric in September, for 79 of 84 orders in October and for 49 of the 59 13 14 responses in November 2001. The 95% benchmark required that 63 of 66 15 LSRs in September, 80 of 84 LSRs in October and 57 of 59 LSRs in 16 November meet the criteria. BellSouth continues to focus on this measurement in order to improve results to meet the benchmark. 17 18 19 FOC Reject & Response Completeness / Centrex / Manual (A.1.16.5) 20 (September/October) BellSouth met the completeness criteria for 16 of the 17 orders for this sub-21 metric in September and for 11 of the 14 orders in October 2001. The 95% 22 benchmark required that all 17 of 17 LSRs for September and all 14 of 14 23

1 LSRs in October meet the criteria. With universe sizes of only 17 or 14 2 orders and a 95% benchmark, a problem on even one order causes a miss 3 for the entire sub-metric. BellSouth met the benchmark for this sub-metric in November 2001. 4 5 6 FOC Reject & Response Completeness / PBX / Manual (A.1.16.6) 7 (September/November) 8 BellSouth met the completeness criteria for 30 of the 33 responses for this 9 sub-metric in September and for 40 of the 48 responses in November 2001. 10 The 95% benchmark required that 32 of 33 LSRs for September and 46 of 48 11 LSRs for November meet the criteria. BellSouth continues to focus on this 12 measurement in order to improve results to meet the benchmark. BellSouth 13 met the benchmark for this sub-metric in October 2001. 14 15 FOC Reject & Response Completeness (Multiple Responses) / Residence / 16 EDI / Electronic (A.1.17.1.1) (October/November) 17 BellSouth met the completeness criteria for 769 of the 965 responses for this 18 sub-metric in October and for 613 of the 777 responses in November 2001. The 95% benchmark required that 917 of 965 LSRs for October and 739 of 19 777 LSRs for November meet the criteria. BellSouth continues to focus on 20 this measurement in order to improve results to meet the benchmark. 21 22

1	FOC Reject & Response Completeness (Multiple Responses) / Business /
2	EDI / Electronic (A.1.17.2.1) (October/November)
3	BellSouth met the completeness criteria for 23 of the 47 responses for this
4	sub-metric in October and for 27 of the 40 responses in November 2001. The
5	95% benchmark required that 45 of 47 LSRs for October and 38 of 40 LSRs
6	for November meet the criteria. BellSouth continues to focus on this
7	measurement in order to improve results to meet the benchmark.
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9	FOC Reject & Response Completeness (Multiple Responses) / Design
10	(Specials) / TAG / Electronic (A.1.17.3.2) (November)
11	There were only two orders for this sub-metric in November 2001. The small
12	universe of orders for this sub-metric does not provide a conclusive
13	benchmark comparison.
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15	FOC Reject & Response Completeness (Multiple Responses) / Residence /
16	Partially Electronic (A.1.18.1) (September)
17	BellSouth met the completeness criteria for 11,829 of the 12,767 orders for
18	this sub-metric in September 2001. The 95% benchmark required that
19	12,129 of 12,767 LSRs be returned. This sub-metric was replaced by Items
20	A.1.18.1.1 and A.1.18.1.2 effective with October 2001 data.
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22	FOC Reject & Response Completeness (Multiple Responses) / Residence /
23	TAG / Partial Electronic (A.1.18.1.2) (October/November)

1 BellSouth met the completeness criteria for 16,528 of the 17,932 responses 2 for this sub-metric in October and for 16,532 for the 17,849 responses in 3 November 2001. The 95% benchmark required that 17,036 of 17,932 LSRs for October and 16,957 of the 17,849 LSRs for November meet the criteria. 4 5 BellSouth continues to focus on this measurement in order to improve results 6 to meet the benchmark. 7 8 FOC Reject & Response Completeness (Multiple Responses) / Business / 9 Partially Electronic (A.1.18.2) (September) BellSouth met the completeness criteria for 1,660 of the 1,861 orders for this 10 11 sub-metric in September 2001. The 95% benchmark required that 1,768 of 12 This sub-metric was replaced by Items A.1.18.2.1 1,861 LSRs be returned. 13 and A.1.18.2.2 effective with October 2001 data. 14 FOC Reject & Response Completeness (Multiple Responses) / Business / 15 EDI / Partial Electronic (A.1.18.2.1) (October/November) 16 BellSouth met the completeness criteria for 17 of the 19 responses for this 17 18 sub-metric in October and for 22 of the 34 responses in November 2001. The 19 95% benchmark required that all 19 of 19 LSRs for October and 33 of 34 LSRs for November meet the criteria. BellSouth continues to focus on this 20 21 measurement in order to improve results to meet the benchmark. 22

1	FOC Reject & Response Completeness (Multiple Responses) / Business /						
2	TAG / Partial Electronic (A.1.18.2.2) (October/November)						
3	BellSouth met the completeness criteria for 2,355 of the 2,628 responses for						
4	this sub-metric in October and for 1,747 of the 2,118 responses in November						
5	2001. The 95% benchmark required that 2,497 of 2,628 LSRs for October						
6	and 2,013 of 2,118 LSRs for November meet the criteria. BellSouth						
7	continues to focus on this measurement in order to improve results to meet						
8	the benchmark.						
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10	FOC Reject & Response Completeness (Multiple Responses) / ISDN / TAG /						
11	Partial Electronic (A.1.18.6.2) (October)						
12	There were only two orders for this sub-metric in October 2001. This small						
13	universe size does not provide a conclusive benchmark comparison. There						
14	was no CLEC activity for this sub-metric in November 2001.						
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16	FOC Reject & Response Completeness (Multiple Responses) / Residence /						
17	Manual (A.1.19.1) (September/October/November)						
18	BellSouth met the completeness criteria for 748 of the 833 responses for this						
19	sub-metric in September, for 1,001 of the 1,114 responses in October and for						
20	1,049 of the 1,165 responses in November 2001. The 95% benchmark						
21	required that 792 of 833 LSRs for September, 1,059 of 1,114 LSRs for						
22	October and 1,107 of the 1,165 LSRs for November meet the criteria.						

1 BellSouth continues to focus on this measurement in order to improve results 2 to meet the benchmark. 3 4 FOC Reject & Response Completeness (Multiple Responses) / Business / 5 Manual (A.1.19.2) (September/October/November) 6 BellSouth met the completeness criteria for 837 of the 903 responses for this 7 sub-metric in September, for 1,066 of the 1,168 responses in October and for 8 1,073 of the 1,158 responses in November 2001. The 95% benchmark 9 required that 858 of 903 LSRs for September, 1,110 of the 1,168 LSRs for 10 October and 1,101 of 1,158 LSRs for November meet the criteria. BellSouth 11 continues to focus on this measurement in order to improve results to meet 12 the benchmark. 13 14 FOC Reject & Response Completeness (Multiple Responses) / Centrex / Manual (A.1.19.5) (September/October) 15 BellSouth met the completeness criteria for 15 of the 16 orders for this sub-16 17 metric in September and for 10 of the 11 orders in October 2001. The 95% benchmark required that all 16 of 16 LSRs for September and all 11 of 11 18 19 LSRs in October meet the criteria. With universe sizes of 15 and 11 orders 20 and a 95% benchmark, problems with even one order causes a miss for the BellSouth met the benchmark for this sub-metric in 21 entire sub-metric. 22 November 2001.

## 2. Resale Provisioning Measures

For the months of September, October and November 2001, BellSouth met or exceeded the benchmark or retail analogue for 92%, 91% and 89%, respectively, of all Resale provisioning measures. The details supporting the November percentage are delineated in Items A.2.1.1.1.1 through A.2.25.3.2.2 of Attachment 1F.

The following are the Resale provisioning measures for which BellSouth did not meet the retail analogue in September, October and/or November 2001.

- % Missed Installation Appointments / Residence / < 10 Circuits / Non-
- 13 <u>Dispatch (A.2.11.1.1.2) (September/October/November)</u>

BellSouth missed only 32 of the 35,349 installation appointments scheduled for this sub-metric in September, missed 82 of the 54,436 appointments scheduled in October and missed 69 of the 46,311 installation appointments scheduled in November 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in September, October and November 2001. When BellSouth provisions high quality service coupled with very large universe sizes, it can cause an apparent out of equity condition from a quantitative viewpoint. In these cases, there is very little variation and the universe size is so large that the Z-test becomes overly sensitive to any difference. In other words, the statistical test shows that the

measurement does not meet the fixed critical value when compared with the retail analogue, but BellSouth's actual performance for both CLECs and its own retail operations is at a very high level - in this case over 99%. From a practical point of view, the CLECs' ability to compete has not been hindered even though the statistical results may technically show that BellSouth failed to meet the benchmark/analogue. % Missed Installation Appointments / Business / < 10 Circuits / Dispatch (A.2.11.2.1.1) (October) There were a total of 25 missed appointments out of the 636 appointments scheduled for this sub-metric in October 2001. Both BellSouth retail and the CLECs had over 96% of all scheduled appointments completed on time in October. BellSouth met the retail analogue comparison for this sub-metric in September and November 2001. % Missed Installation Appointments / Business / < 10 Circuits / Non-Dispatch (A.2.11.2.1.2) (September/October/November) BellSouth missed 7 of the 2,410 scheduled appointments for this sub-metric in September, missed 10 of the 3,375 appointments scheduled for October and missed 7 of the 2,818 installation appointments scheduled in November 2001. Both the CLECs and BellSouth retail had over 99% of all orders completed as scheduled in September, October and November 2001.

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1	% Missed Installation Appointments / PBX / < 10 Circuits / Dispatch
2	(A.2.11.4.1.1) (September)
3	BellSouth missed 3 of the 11 scheduled appointments for this sub-metric in
4	September 2001. The small universe of orders for this sub-metric does not
5	provide a statistically conclusive comparison to the retail analogue. BellSouth
6	met the retail analogue for this sub-metric in October and November 2001.
7	
8	% Missed Installation Appointments / PBX / >= 10 Circuits / Dispatch
9	(A.2.11.4.2.1) (November)
10	There was only one order for this sub-metric in November 2001. the small
11	universe of orders for this sub-metric does not provide a conclusive
12	benchmark comparison. There was no CLEC activity for this sub-metric in
13	September 2001. BellSouth met the retail analogue comparison for this sub-
14	metric in October 2001.
15	
16	% Missed Installation Appointments / Centrex / < 10 Circuits / Non-Dispatch
17	(A.2.11.5.1.2) (November)
18	BellSouth completed 21 of the 22 installation appointments as scheduled for
19	this sub-metric in November 2001. There were no systemic issues identified
20	for the one missed appointment. BellSouth met the retail analogue
21	comparison for this sub-metric in September and October 2001.
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1 % Missed Installation Appointments / ISDN / < 10 Circuits / Non-Dispatch 2 (A.2.11.6.1.2) (October) 3 BellSouth completed 24 of the 25 scheduled appointments for this sub-metric 4 in October 2001. Both the CLECs and BellSouth retail had 96% of all orders 5 completed as scheduled in October. BellSouth met the retail analogue 6 comparison for this sub-metric in September and November 2001. 7 8 % Provisioning Troubles w/i 30 days / Residence / < 10 Circuits / Non-9 Dispatch (A.2.12.1.1.2) (September/October/November) 10 In September 2001, there were 1,905 troubles reported for the 41,062 orders 11 that completed in the prior 30 days. Twenty-nine percent of the reported troubles were closed as "TOK/FOK." 12 In October 2001, there were 1,796 13 troubles reported for the 35,349 orders that completed in the prior 30 days. 33% of those troubles were closed as "TOK/FOK." The only significant trend 14 identified in the October data showed that 995, or 55%, of the total trouble 15 reports for this sub-metric were for one CLEC, with 55% of those troubles 16 17 being cleared as TOK/FOK. In November 2001, there were 2,640 troubles 18 reported for the 54,436 orders that completed in the prior 30 days. Thirty-four percent of the November trouble reports were closed as "TOK/FOK." With 19 the exclusion of the "no trouble found" reports, this sub-metric would have 20 met the retail analogue comparison in each of the three months. BellSouth is 21 22 conducting an analysis of the provisioning situation with this particular CLEC

1 and will conduct joint sessions to determine how to avoid the no trouble found 2 reports. 3 4 % Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Dispatch 5 (A.2.12.2.1.1) (September/October/November) 6 In September 2001, there were 39 troubles reported for the 572 orders that 7 completed in the prior 30 days. Of the 39 troubles reported, 21 (54%) were 8 closed as "no trouble found." There were 42 troubles reported for the 486 9 orders that completed for this sub-metric in the 30 days prior to October 2001. 10 Of the 42 troubles reported in October, 18 (43%) were closed as "no trouble 11 found." In November 2001, there were 33 troubles reported for the 639 12 orders that completed in the prior 30 days. Of the 33 troubles reported in 13 November, 14 (41%) were closed as "no trouble found." 14 % Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Non-Dispatch 15 16 (A.2.12.2.1.2) (November) 17 There were 192 troubles reported for the 3,375 orders that completed for this sub-metric in the 30 days prior to November 2001. Of the total November 18 trouble reports for this sub-metric, 36% were closed as "TOK/FOK." Without 19 these "no trouble found" reports, this sub-metric would have met the retail 20 analogue comparison for November. BellSouth met the retail analogue 21 22 comparison for this sub-metric in September and October 2001.

1 % Provisioning Troubles w/i 30 days / Business / >= 10 Circuits / Dispatch 2 (A.2.12.2.2.1) (November) Troubles were reported on 3 of the 12 orders completed for this sub-metric in 3 4 the 30 days prior to November 2001. No distinct patterns or systemic 5 installation issues were identified for these 3 orders. BellSouth met the retail 6 analogue comparison for this sub-metric in September and October 2001. 7 8 % Provisioning Troubles w/i 30 days / PBX / >= 10 Circuits / Dispatch 9 (A.2.12.4.2.1) (September) 10 There was only one order for this sub-metric in September 2001. The small 11 universe for this measurement does not provide a statistically conclusive 12 comparison with the retail analogue. BellSouth met the retail analogue 13 comparison for this sub-metric in November 2001. There was no CLEC 14 activity for this sub-metric in October 2001. 15 16 Service Order Accuracy / Business / < 10 Circuits / Dispatch (A.2.25.2.1.1) 17 (October) 18 BellSouth met the standard for 8 of the 13 orders reviewed in this sub-metric 19 for October 2001. The 95% benchmark required that all 13 of the 13 orders 20 meet the criteria. BellSouth met the benchmark for this sub-metric in 21 September and November 2001. 22

1 Service Order Accuracy / Business / < 10 Circuits / Non-Dispatch 2 (A.2.25.2.1.2) (September/October) 3 BellSouth met the standard for 204 of the 221 orders reviewed for this sub-4 metric in September and for 128 of the 145 orders reviewed in October 2001. 5 The 95% benchmark set requirements of 210 orders for September and 139 6 orders in October based on the quantity of orders for this sub-metric. 7 BellSouth met the benchmark for this sub-metric in November 2001. 8 9 Service Order Accuracy / Business / >= 10 Circuits / Dispatch (A.2.25.2.2.1) 10 (November) 11 BellSouth met the standard for 21 of the 23 orders reviewed for this sub-12 metric in November 2001. The 95% benchmark set a requirement of 22 of 13 the 23 orders based on the quantity of orders for this sub-metric. BellSouth 14 met the benchmark for this sub-metric in September 2001. There was no CLEC activity for this sub-metric in October 2001. 15 16 17 Service Order Accuracy / Business / >= 10 Circuits / Non-Dispatch 18 (A.2.25.2.2.2) (September/November) There were only seven orders reviewed for this sub-metric in September 19 20 2001. The small universe for this sub-metric does not provide a conclusive benchmark comparison. BellSouth met the standard for 29 of the 31 orders 21 22 reviewed for this sub-metric in November 2001. The 95% benchmark set a requirement of 30 of the 31 orders in November based on the quantity of 23

1 orders for this sub-metric. BellSouth met the benchmark for this sub-metric 2 in October 2001. 3 4 Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch 5 (A.2.25.3.1.1) (October/November) 6 There were only four orders reviewed for this sub-metric in October 2001. 7 This small universe size does not provide a conclusive benchmark 8 comparison. BellSouth met the standard for 45 of the 50 orders reviewed for 9 this sub-metric in November 2001. The 95% benchmark set a requirement of 10 48 of the 50 orders in November based on the quantity of orders for this sub-11 metric. BellSouth met the benchmark for this sub-metric in September 2001. 12 Service Order Accuracy / Design (Specials) / < 10 Circuits / Non-Dispatch 13 14 (A.2.25.3.1.2) (November) BellSouth met the standard for 45 of the 50 orders (94.65%) reviewed for this 15 16 sub-metric in November 2001. Normal rounding convention indicates that 17 there is no significant difference between the CLEC results for this sub-metric 18 and the benchmark requirement. BellSouth met the benchmark for this sub-19 metric in October 2001. There was no CLEC activity for this sub-metric in September 2001. 20 21 22 3. Resale Maintenance and Repair (M&R) Measures

1 BellSouth met the relevant retail analogues for 89%, 79% and 87% of all the 2 Resale Maintenance & Repair measurements in September, October and 3 November, respectively. The sub-metrics for which BellSouth did not meet 4 the retail analogues were: 5 6 Missed Repair Appointments / Design (Specials) / Non-Dispatch (A.3.1.3.2) 7 (September/November) 8 BellSouth completed 16 of the 22 repair appointments as scheduled for this 9 sub-metric in September and completed 18 of the 22 appointments scheduled 10 for November 2001. There were no maintenance issues or patterns identified 11 for any of the missed appointments in either months. BellSouth met the retail 12 analogue comparison for this sub-metric in October 2001. 13 14 Missed Repair Appointments / PBX / Dispatch (A.3.1.4.1) (October) 15 BellSouth completed 27 of the 40 repair appointments as scheduled for this 16 sub-metric in October 2001. There were no maintenance issues or patterns 17 identified for the 13 missed appointments. Six of the thirteen missed 18 appointments were dispatched on time but did not finish by the committed 19 time (all completed within 1.5 hours of the committed time). BellSouth met the retail analogue comparison for this sub-metric in September and 20 21 November 2001. 22 23 Missed Repair Appointments / ISDN / Non-Dispatch (A.3.1.6.2) (October)

1 There were only nine orders for this sub-metric in October 2001. The small 2 universe for this sub-metric does not provide a statistically conclusive 3 comparison to the retail analogue. BellSouth met the retail analogue 4 comparison for this sub-metric in September and November 2001. 5 6 Customer Trouble Report Rate / Residence / Dispatch (A.3.2.1.1) 7 (October/November) 8 There were 4,304 troubles reported for the approximately 173,600 in service 9 lines for this sub-metric in October and 3,650 trouble reports for the 190,100 10 lines in service in November 2001. Both the CLECs and BellSouth retail had 11 no trouble reports for over 97% of the in service lines in both October and 12 November. There was only about 0.1% difference in the report rates between 13 retail and resale results for this sub-metric in both months. BellSouth met the retail analogue for this sub-metric in September 2001. 14 15 16 Customer Trouble Report Rate / Residence / Non-Dispatch (A.3.2.1.2) 17 (November) There were 2,415 troubles reported for the approximately 190,100 lines in 18 19 service in November 2001. Both the CLECs and BellSouth retail had no trouble reports for over 98% of the in service lines in November. There was 20 less than 0.2% difference in the report rates between retail and resale results 21 22 for this sub-metric in November. Of the 2,415 total trouble reports, 1,779 reports (73%) were closed as "TOK/FOK." Without these "no trouble found" 23

reports, BellSouth would have met the retail analogue comparison for this sub-metric in November. One CLEC generated 1,998 (82%) of the November trouble reports for this sub-metric. BellSouth met the retail analogue for this sub-metric in September and October 2001. Customer Trouble Report Rate / Business / Dispatch (A.3.2.2.1) (September/October/November) There were 980 troubles reported for the approximately 56,000 in service lines for this sub-metric in September, 1,038 troubles reported for the 55,500 lines in service in October and 774 trouble reports for the 8,325 lines in service in November 2001. In September, October and November, 258 (26%), 145 (14%) and 132 (17%), respectively, of the trouble reports were closed as "TOK/FOK." BellSouth is still investigating this sub-metric to determine if any systemic maintenance issues are present. Customer Trouble Report Rate / Business / Non-Dispatch (A.3.2.2.2) (November) There were 510 troubles reported for the 8,325 in service lines for this submetric in November 2001. Of the 510 total trouble reports, 332 (65%) of the reports were closed as "TOK/FOK." BellSouth met the retail analogue comparison for this sub-metric in September and October 2001.

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1 Customer Trouble Report Rate / PBX / Dispatch (A.3.2.4.1) 2 (September/October) 3 There were only 26 trouble reports for the 3,995 in service lines for this sub-4 metric in September and 40 trouble reports for the 6,477 lines in service for October 2001. BellSouth provided over 99% trouble free service for both 5 6 retail and the CLECs for this sub-metric for the months of September and 7 October. From a practical point of view, the CLECs' ability to compete has 8 not been hindered even though the statistical results may technically show 9 that BellSouth failed to meet the benchmark/analogue. BellSouth met the 10 retail analogue comparison for this sub-metric in November 2001. 11 Customer Trouble Report Rate / Centrex / Non-Dispatch (A.3.2.5.2) (October) 12 There were only 14 trouble reports for the 2,145 in service lines for this sub-13 14 metric in October 2001. Of the 14 trouble reports in October, 8 (57%) were 15 closed as "no trouble found." BellSouth provided over 99% trouble free service for both retail and the CLECs for this sub-metric for the month. From 16 a practical point of view, the CLECs' ability to compete has not been hindered 17 even though the statistical results may technically show that BellSouth failed 18 19 to meet the benchmark/analogue. BellSouth met the retail analogue comparison for this sub-metric in September and November 2001. 20 21 Customer Trouble Report Rate / ISDN / Dispatch (A.3.2.6.1) 22 23 (October/November)

There were only 13 trouble reports for the 5,484 in service lines for this submetric in October and 10 trouble reports for the 6,138 lines in service in November 2001. Of the 13 reports for October, 6 (46%) reports were closed as "no trouble found," and 3 of the 10 reports (30%) for November were closed as "No trouble found." BellSouth provided over 99% trouble free service for both retail and the CLECs for this sub-metric for both months. From a practical point of view, the CLECs' ability to compete has not been hindered even though the statistical results may technically show that BellSouth failed to meet the benchmark/analogue. BellSouth met the retail analogue comparison for this sub-metric in September 2001.

# Maintenance Average Duration / PBX / Dispatch (A.3.3.4.1) (October)

Of the 40 total trouble reports for this sub-metric in October, 19 exceeded the average maintenance duration time for the retail analogue. However, 12 of the 19 longer duration repair reports met the offered commitment intervals. Five of these twelve reports were received late on a Friday afternoon, and were committed and completed before noon on Monday. Six of the twelve reports were taken late on a weekday afternoon and were completed the following day. One report could not be completed because the technician could not gain access to the customer's equipment location. The remaining seven longer duration reports were due to cable facility problems (four at the same customer location). BellSouth met the retail analogue comparison for this sub-metric in September and November 2001.

1 2 Maintenance Average Duration / Centrex / Dispatch (A.3.3.5.1) (September) 3 There were only nine orders for this sub-metric in September 2001. The 4 small universe for this sub-metric does not provide a statistically conclusive 5 comparison to the retail analogue. BellSouth met the retail analogue for this 6 sub-metric in October and November 2001. 7 8 Maintenance Average Duration / ISDN / Non-Dispatch (A.3.3.6.2) 9 (October/November) 10 There were only nine orders for this sub-metric in October and six orders in 11 November 2001. The small universe for this sub-metric does not provide a 12 statistically conclusive comparison to the retail analogue. BellSouth met the 13 retail analogue for this sub-metric in September 2001. 14 15 % Repeat Troubles within 30 Days / PBX / Dispatch (A.3,4.4.1) 16 (September/October) 17 In September 2001, there were 12 repeat trouble reports, 10 of which were by 18 the same customer for the same trouble. Nine of the repeat reports were closed as "No trouble found." In October 2001, there were 13 repeat reports 19 20 for this sub-metric. Of the 13 October repeats, 5 were from one customer due to facilities problems, 5 were from another customer due to service wire 21 problems, 2 were closed as "no trouble found," and 1 was from an unrelated 22 23 incident. There were only three actual different trouble situations for the

1 month. BellSouth met the retail analogue for this sub-metric in November 2 2001. 3 4 Out of Service > 24 Hours / Design (Specials) / Non-Dispatch (A.3.5.3.2) 5 (September/November) 6 Of the 22 trouble reports for this sub-metric in September 2001, 6 of the 7 troubles caused out of service conditions longer than 24 hours. In November 2001, 4 of the 22 trouble reports were out of service longer than 24 hours. 8 9 None of these situations revealed any systemic maintenance issues. 10 BellSouth met the retail analogue for this sub-metric in October 2001. 11 12 Out of Service > 24 Hours / PBX / Dispatch (A.3.5.4.1) (October) Of the 28 "out of service" reports for this sub-metric in October, 11 of the 13 14 reports were out of service longer than 24 hours. Of these 11 reports, 5 were 15 for one customer received late on a Friday afternoon, committed and completed before noon of Monday. The remaining 6 reports out of service 16 longer than 24 hours were due to wet cable facilities that had to be repaired 17 by a cable technician. BellSouth met the retail analogue comparison for this 18 sub-metric in September and November 2001. 19 20 Out of Service > 24 Hours / Centrex / Dispatch (A.3.5.5.1) (October) 21 There were only six orders for this sub-metric in October 2001. The small 22 universe for this sub-metric does not provide a statistically conclusive 23

1	comparison to the retail analogue. BellSouth met the retail analogue for this
2	sub-metric in September and November 2001.
3	
4	Out of Service > 24 Hours / ISDN / Non-Dispatch (A.3.5.6.2) (October)
5	There were only nine orders for this sub-metric in October 2001. The small
6	universe for this sub-metric does not provide a statistically conclusive
7	comparison to the retail analogue. BellSouth met the retail analogue for this
8	sub-metric in September and November 2001.
9	
10	Resale – Billing
11	Invoice Accuracy / Resale (A.4.1) (September)
12	The CLECs experienced Resale invoice rates that were slightly less than the
13	invoices BellSouth sends to its retail customers during September 2001
14	(98.61% accuracy for BellSouth versus 97.84% for the CLEC invoices). The
15	difference in performance was the result of provisioning and system errors
16	that caused the over billing of one CLEC customer. BellSouth met the retail
17	analogue for this sub-metric in October and November 2001.
18	
19	II. Summary
20	
21	As stated in the Introduction to the Analysis of Performance Measurements
22	section, BellSouth met or exceeded the criteria for 687 of the 816 sub-metrics
23	(84%) for which there was CLEC activity in September, for 733 of 901 sub-

1 metrics (81%) in October and for 716 of 901 sub-metrics (79%) in November 2 2001. 3 4 During the three-month period of September through November 2001, there were a total of 723 sub-metrics that had CLEC activity for all three months 5 6 and that were compared with either a benchmark or retail analogue. Of those 723 sub-metrics, 612 or 85% satisfied the comparison criteria for a minimum 7 8 of two of the three months. 9

# BellSouth Monthly State Summary Florida. November 2001

St. Repeted Survice Requests - Mechanized	FI	lorida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Desprecise	Re	sale - Ordering						·····			
Description	%	Rejected Service Requests - Mechanized						*			
Dagnostic   Dagn			Diagnostic			19 38%	62.648				Diagnostic
Daymonic											
Degreed   Degr						100 00%	2				
Dagrosto									1 december		
September Service Requests - Partially Mechanized   Diagnosic		7 Centrex/FL(%)					••••				Diagnostic
Desproach   Desp	-		Diagnosiic								Diagnostic
Open			D			00.440/	40.000				
Dagnostic   Dagn											Diagnostic
Degrostic											
Degrosts		7 PBX/FL(%)				3,00,0	· · · · · · · · · · · · · · · · · · ·	-			
Degrostic   Degr	o-	7 Centrex/FL(%)	Diagnostic								
Degrossic   Degr	0-	7 [ISDN/FL(%)	Diagnostic							1	Diagnostic
Deginosic   1.11%   1.260   Deginosic   0-7   Deginosic   0-7   PEWFL(%)   Deginosic   Deginosic   PEWFL(%)   Deginosic	%										
Design Specials/FL(%)											
Dagnostic   45.78%   59   Dagnostic   0.7   PROVERLY(%)   Dagnostic   92.99%   27   Dagnostic   92.99%   48   Dagnostic   92.99%   92.											
Desproach   Desproach   Desproach   September   Sept		7 Design (Specials)/FL(%)									
Diagnosic   Sc 08%   48   Diagnosic   Sc 08%   48   Diagnosic   Sc 08%   48   Diagnosic   Sc 08%   S								-			
Reject Interval - Mechanized		7 (SDN/FL(%)						200 Co.			
C-8											
Des   Design (Specials)FL(%)   >= 97% w in 1 hr   50,00%   2   NO			>= 97% w in 1 hr			95 19%	12,177				NO
Design   D		B Business/FL(%)						"			
O-8   CentrevFL(%)   >= 97% win 1 hr						50.00%	2				NO
Reject Interval - Partially Mechanized - 10 hours		B PBX/FL(%)									
Reject Interval - Partially Mechanized - 10 hours	멎	B   Centrex/FL(%)						_			
D-8   Residence/FL(%)   Sessions/FL(%)	-		37 70 W III T III			L					L
O-8   Business/FL(%)			>= 85% wan 10 hrs			92.46%	5.610				VES
O-8         Design (Specials)/FL(%)         >= 85% w in 10 hrs           O-8         PBX/FL(%)         >= 85% w in 10 hrs           O-8         Centrex/FL(%)         >= 85% w in 10 hrs           O-8         ISDN/FL(%)         >= 85% w in 10 hrs           CB         Sepice Interval - Non-Mechanized           O-8         Residence/FL(%)         >= 85% w in 24 hrs           O-8         Business/FL(%)         >= 85% w in 24 hrs         99 54%         659         YES           O-8         Design (Specials)/FL(%)         >= 85% w in 24 hrs         90 74%         54         YES           O-8         Design (Specials)/FL(%)         >= 85% w in 24 hrs         96 77%         31         YES           O-8         Centrex/FL(%)         >= 85% w in 24 hrs         96 77%         31         YES           O-8         ISDN/FL(%)         >= 85% w in 24 hrs         81.48%         27         NO           O-8         ISDN/FL(%)         >= 85% w in 3 hrs         92 00%         25         YES           O-9         Residence/FL(%)         >= 95% w in 3 hrs         98 94%         2.357         YES           O-9         Design (Specials)/FL(%)         >= 95% w in 3 hrs         98 94%         2.357         YES				•							
D-8   PBXFL[%]   >= 85% win 10 hrs   >= 85% win 24 hrs   99.30%   573   YES   >= 85% win 24 hrs   99.54%   659   YES   >= 85% win 24 hrs   99.54%   659   YES   >= 85% win 24 hrs   99.74%   659   YES   >= 85% win 24 hrs   99.74%   659   YES   >= 85% win 24 hrs   96.77%   31   YES   >= 85% win 24 hrs   99.20%   25   YES   >= 85% win 24 hrs   99.20%   25   YES   >= 85% win 24 hrs   99.20%   25   YES   >= 85% win 34 hrs   99.20%   25   YES   >= 95% win 34 hrs   99.20%   2.357   YES   >= 95% win 34 hrs   >=											
C-8   ISDN/FL(%)   >= 85% win 10 hrs	O.	B PBX/FL(%)									
Residence/FL(%)											
O-8       Residence/FL(%)       >= 85% w in 24 hrs       99 30%       573       YES         O-8       Business/FL(%)       >= 85% w in 24 hrs       99 54%       659       YES         O-8       Design (Specials)/FL(%)       >= 85% w in 24 hrs       90 74%       54       YES         O-8       PBX/FL(%)       >= 85% w in 24 hrs       96 77%       31       YES         O-8       Centrex/FL(%)       >= 85% w in 24 hrs       81 48%       27       NO         O-8       ISDN/FL(%)       >= 85% w in 24 hrs       92 00%       25       YES         FOC Timeliness - Mechanized         O-9       Residence/FL(%)       >= 95% w in 3 hrs       99 74%       50.081       YES         O-9       Design (Specials)/FL(%)       >= 95% w in 3 hrs       98 94%       2.357       YES         O-9       PBX/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95%			>= 85% WIN 10 N/S			LL					
O-8         Business/FL(%)         >= 85% w in 24 hrs         99 54%         659         YES           O-8         Design (Specials)/FL(%)         >= 85% w in 24 hrs         90 74%         54         YES           O-8         PBX/FL(%)         >= 85% w in 24 hrs         96 77%         31         YES           O-8         ISDN/FL(%)         >= 85% w in 24 hrs         91 44 hrs         92 00%         25         NO           O-8         ISDN/FL(%)         >= 85% w in 24 hrs         92 00%         25         YES           FOC Timeliness - Mechanized           O-9         Residence/FL(%)         >= 95% w in 3 hrs         99 74%         50.081         YES           O-9         Design (Specials)/FL(%)         >= 95% w in 3 hrs         98 94%         2.357         YES           O-9         ISBN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs			. 05% . 044			00.000/					
O-8         Design (Specials)/FL(%)         >= 85% w in 24 hrs         90 74%         54         YES           O-8         PBX/FL(%)         >= 85% w in 24 hrs         96 77%         31         YES           O-8         Centrex/FL(%)         >= 85% w in 24 hrs         96 77%         31         YES           ISDN/FL(%)         >= 85% w in 24 hrs         92 00%         25         YES           FOC Timeliness - Mechanized           O-9         Residence/FL(%)         >= 95% w in 3 hrs         99 74%         50,081         YES           O-9         Business/FL(%)         >= 95% w in 3 hrs         98 94%         2,357         YES           O-9         Design (Specials)/FL(%)         >= 95% w in 3 hrs         99 94%         2,357         YES           O-9         PBX/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         Centrex/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         <											
O-8       PBX/FL(%)       >= 85% w in 24 hrs       96 77%       31       YES         O-8       Centrex/FL(%)       >= 85% w in 24 hrs       81.48%       27       NO         O-8       ISDN/FL(%)       >= 85% w in 24 hrs       92.00%       25       YES         FOC Timeliness - Mechanized         O-9       Residence/FL(%)       >= 95% w in 3 hrs       99.74%       50.081       YES         O-9       Business/FL(%)       >= 95% w in 3 hrs       98.94%       2.357       YES         O-9       Design (Specials)/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       PBX/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs											
Q-8       Centrex/FL(%)       >= 85% w in 24 hrs       81.48%       27       NO         Q-8       ISDN/FL(%)       92.00%       25       YES         FOC Timeliness - Mechanized         Q-9       Residence/FL(%)       >= 95% w in 3 hrs       99.74%       50.081       YES         Q-9       Design (Specials)/FL(%)       >= 95% w in 3 hrs       98.94%       2.357       YES         Q-9       PBX/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs       >= 95% w in 3 hrs         Q-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         Q-9       ISDN/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         FOC Timeliness - Partially Mechanized - 10 hours       >= 95% w in 3 hrs       >= 95% w in 3 hrs								Spanol			
O-8   ISDN/FL(%)   >= 85% w in 24 hrs   92 00%   25   YES	o	B Centrex/FL(%)					27	1.60 i.			
O-9         Residence/FL(%)         >= 95% w in 3 hrs         99 74%         50.081         YES           O-9         Business/FL(%)         >= 95% w in 3 hrs         98 94%         2.357         YES           O-9         Design (Specials)/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         PBX/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           FOC Timeliness - Partially Mechanized - 10 hours         >= 95% w in 3 hrs         >= 95% w in 3 hrs		B ISDN/FL(%)					25		3000	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	
O-9         Residence/FL(%)         >= 95% w in 3 hrs         99 74%         50.081         YES           O-9         Business/FL(%)         >= 95% w in 3 hrs         98 94%         2.357         YES           O-9         Design (Specials)/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         PBX/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs         >= 95% w in 3 hrs           FOC Timeliness - Partially Mechanized - 10 hours         >= 95% w in 3 hrs         >= 95% w in 3 hrs	FO	C Timeliness - Mechanized		-							
O-9       Business/FL(%)       >= 95% w in 3 hrs       98 94%       2.357       YES         O-9       Design (Specials)/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       Centrex/FL(%)       >= 95% w in 3 hrs       >= 95% w in 3 hrs         O-9       ISDN/FL(%)       >= 95% w in 3 hrs         FOC Timeliness - Partially Mechanized - 10 hours	0-9	9 Residence/FL(%)									YÉS
O-9         PBX/FL(%)         >= 95% w in 3 hrs           O-9         Centrex/FL(%)         >= 95% w in 3 hrs           O-9         ISDN/FL(%)         >= 95% w in 3 hrs           FOC Timeliness - Partially Mechanized - 10 hours						98 94%	2,357				
C-9         Centrex/FL(%)         >= 95% w in 3 hrs           C-9         ISDN/FL(%)         >= 95% w in 3 hrs           FOC Timeliness - Partially Mechanized - 10 hours											
O-9 ISDN/FL(%) >= 95% w in 3 hrs  FOC Timeliness - Partially Mechanized - 10 hours						<b></b>		Y80860 11111			
FOC Timeliness - Partially Mechanized - 10 hours				38030		<b>}</b>	·		944		
	-		- 0070 # 111 0 1115			·		*			
0-3   Thesities   10/10   10/050   15/0			>= 85% w in 10 has			Q1 75% T	13 505				VES
	0-9	9   R#SIUERICE/FL(70)	~- 05% WIN 10 HIS			31/0%	13,393			1, 3	159

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	Fiorida, November 2001	benchmark i	801	B21	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
A 1.12.2	O-9 Buşiness/FL(%)	>= 85% w in 10 hrs			93 60%	1,437	The state of the s			YES
A.1 12.3	O-9 Design (Specials)/FL(%)	>= 85% w in 10 hrs			0 00%	2				NO
A.1.12.4	O-9 PBX/FL(%)	>= 85% w in 10 hrs								
A.1 12.5	O-9 Centrex/FL(%)	>= 85% w in 10 hrs					_			
A.1.12.6	0-9 ISDN/FL(%)	>= 85% w in 10 hrs								
***************************************		0070 11 11 10 1110							***	
	FOC Timeliness - Non-Mechanized									
A.1 13.1	O-9 Residence/FL(%)	>= 85% w in 36 hrs			98 73%	629				YES
A.1.13.2	O-9 Business/FL(%)	>= 85% w in 36 hrs			99 25%	533				YES
A.1 13.3	O-9 Design (Specials)/FL(%)	>= 85% w in 36 hrs			98.91%	92	1			YES
A.1 13.4	O-9 PBX/FL(%)	>= 85% w in 36 hrs			88.46%	26				YES
A.1 13 5	O-9 Centrex/FL(%)	>= 85% w in 36 hrs			100 00%	2				YES
A.1.13.6	0-9   SDN/FL(%)	>= 85% w in 36 hrs			93 75%	16				YES
761.15.0		65 /6 47 111 66 1116	<b>———</b>		33 13 70		ــــــــــــــــــــــــــــــــــــــ			16.5
	FOC & Reject Response Completeness - Mechanized									
A.1 14 1 1	O-11 Residence/EDI/FL(%)	>= 95%			99 74%	779				YES
A.1.14 1.2	O-11 Residence/TAG/FL(%)	>= 95%			99 05%	61,869				YES
A.1 14.2.1	O-11 Business/EDI/FL(%)	>= 95%			100.00%	40			yaasi ka	YES
A.1.14.2 2	O-11 Business/TAG/FL(%)	>= 95%			96 36%	3,570	-			YES
A.1 14.3.1	O-11 Design (Specials)/EDVFL(%)	>= 95%			30 30 10	3,370	7000			153
A.1.14.3.1	O-11 Design (Specials)/TAG/FL(%)	>= 95%			100 00%	2	-			YES
		>= 95%			100 00%		-			YES
A.1 14.4.1							-			···
A.1.14 4.2	O-11 PBX/TAG/FL(%)	>= 95%				<del></del>	-			
A.1.14.5.1	O-11 Centrex/EDVFL(%)	>= 95%					- 3000			
A.1.14 5 2	O-11 Centrex/TAG/FL(%)	>= 95%								
A.1.14 6.1	O-11  SDN/EDVFL(%)	>= 95%	48							
A.1 14.6.2	O-11  ISDN/TAG/FL(%)	>= 95%					5 4-1		7 200	
	FOC & Reject Response Completeness - Partially Mechanized									
444544	O-11 Residence/EDI/FL(%)	>= 95%			100 00%	496				VEC
A.1.15.1.1							-			YES
A.1.15.1.2	O-11 Residence/TAG/FL(%)	>= 95%			99 88%	17,870				YES
A.1 15 2.1	O-11 Business/EDI/FL(%)	>= 95%			100 00%	34	_		÷ .	YES
A.1.15 2 2	O-11 Business/TAG/FL(%)	>= 95%			99 53%	2,128	- 1000 0			YES
A.1 15.3.1	O-11 Design (Specials)/EDI/FL(%)	>= 95%					2000			
A.1 15 3.2	O-11 Design (Specials)/TAG/FL(%)	>= 95%			100 00%	1			55000	YES
A.1.15.4.1	O-11 PBX/EDI/FL(%)	>= 95%								
A.1.15 4 2	O-11 PBX/TAG/FL(%)	>= 95%					50,500,000			
A.1 15 5 1	O-11   Centrex/EDVFL(%)	>= 95%	- A							
A.1 15 5.2	O-11 Centrex/TAG/FL(%)	>= 95%								
A.1 15.6.1	Q-11 ISDN/EDI/FL(%)	>= 95%								
A.1.15.6.2	O-11 ISDN/TAG/FL(%)	>= 95%							8	
			•							
	FOC & Reject Response Completeness - Non-Mechanized									
A.1.16.1	O-11 Residence/FL(%)	>= 95%			91 30%	1,276				NO
A.1 16.2	O-11 Business/FL(%)	>= 95%			91 90%	1,260	100		٠.	NO
A.1.16.3	O-11 Design (Specials)/FL(%)	>≈ 95%			86 99%	146			- 1, 180 P	NO
A.1.16.4	O-11 PBX/FL(%)	>= 95%			83.05%	59	1. 1.			NO
A.1.16.5	O-11 Centrex/FL(%)	>= 95%	210000		96.30%	27	* 1875.			YES
A.1.16.6	0-11 ISDN/FL(%)	>= 95%	100		83 33%	48			1,000 00 0	NO
			-9 Sr			<u></u>				
	FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
A.1.17.1.1	O-11 Residence/EDI/FL(%)	>= 95%			78 89%	777				NO
A.1.17.1.2	O-11 Residence/TAG/FL(%)	>= 95%			99 39%	61,280				YES
A.1 17 2 1	O-11 Business/EDI/FL(%)	>= 95%			67.50%	40				NO
A.1.17.22	O-11 Business/TAG/FL(%)	>= 95%			97 67%	3,440				YEŞ
A.1 17 3 1	O-11 Design (Specials)/EDI/FL(%)	>= 95%			2. 3	· · · · · ·	N 60 M			
A.1 17.3 2	O-11 Design (Specials)/TAG/FL(%)	>= 95%			0 00%	2				NO
	O-11 PBX/EDV/FL(%)	>= 95%				<u>t</u>			\$ 4 / S X	
A.1.17.4.1	O-11   PBX/EDVFL(%)	>= 95% >= 95%			<del>-</del> +					
A.1 17.4.2					<del>                                     </del>		- The second		7	———
A.1 17 5.1	O-11 Centrex/EDVFL(%)	>= 95%	10.8000		<u> </u>		Mar- Sex	and a		
A.1.17.5.2	O-11 Centrex/TAG/FL(%)	>= 95%						render (Street)	Strain Sec. 3.	

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Standard Standard

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A.1 17 6.1	Standard Standard Deviation Error	ZScore Equity  YES NO NO
A.1 17 6 1		ZScore Equity  YES NO
A.1 17 6 1	Deviation Cirror	YES NO
A 1.17.6.2 O-11		NO
A 1.17.6.2 O-11		NO
## FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized  ## A.1 18 1.1		NO
A.1 18 1.1		NO
A 1 18 1 2		NO
A.1 18 1 2 O-11 Residence/TAG/FL(%) >= 95% 92 62% 17,849 A.1 18 2 1 O-11 Business/EDVFL(%) >= 95% 64 71% 34 A.1 18 2 2 O-11 Business/TAG/FL(%) >= 95% 82.48% 2,118 A.1 18 3 1 O-11 Design (Specials)/EDVFL(%) >= 95% A.1 18 3 2 O-11 Design (Specials)/TAG/FL(%) >= 95% A.1 18 3 2 O-11 Design (Specials)/TAG/FL(%) >= 95% A.1 18 4 2 O-11 PBX/FDVFL(%) >= 95% A.1 18 5 1 O-11 Centrex/EDVFL(%) >= 95% A.1 18 5 1 O-11 Centrex/EDVFL(%) >= 95%		NO
A 1 18 2 1		
A.1.18.2.2		
A 1 18 3 1 O-11 Design (Specials)/EDVFL(%) >= 95% A 1 18 3 2 O-11 Design (Specials)/TAG/FL(%) >= 95% A 1.18 4.1 O-11 PBX/FBD/FL(%) >= 95% A 18 4.2 O-11 PBX/FAG/FL(%) >= 95% A 18.5 1 O-11 Centrex/EDVFL(%) >= 95% >= 95%		NO
A.1 18 3 2 O-11 Design (Specials)/TAG/FL(%) >= 95% 100 00% 1 A.1 18 4.1 O-11 PBX/EDV/FL(%) >= 95%		, NO
A 1.18.4.1		YES
A 18 4 2		TES
A 18.5 1 O-11 Centrex/EDVFL(%) >= 95%		
A. 18.5.2 O-11 Centrex/TAG/FL(%) >= 95%		
	A000000	
		20
A. 18.6.2 O-11   ISDN/TAG/FL(%) >= 95%		
FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized		
A. 19.1 O-11 Residence/FL(%) >= 95% 90.04% 1.165		NO
A 19.2 Q-11 Business/FL(%) >= 95% 92.66% 1.158		NO NO
A.1.19.3 O-11 Design (Specials)/FL(%) >= 95% 95 06% 127	Tarent	YES
A.1.19 4 O-11 PBX/FL(%) >= 95% 49		
A.1.19.5 (O-11   Centrew/FL(%) >= 95% 100.00% 26		YES
A.1.18.6 O-11 ISDN/FL(%) >= 95% 10000% 26		YES YES
7- 6376 40		YES
Resale - Provisioning		1
Order Completion Interval		
A.2 1.1 1.1 P-4 Residence/<10 circuits/Dispatch/FL(days) Res 4.75 46,411 3.46 2,983	5.467 0 10326	12 4834 YES
A.2.1.1 12 P-4 Residence/<10 circuits/Non-Dispatch/FL(days) Res 0.87 613,440 0.53 44,418	1 729 0 00849	39 6476 YES
A.2 1 1 2 1 P-4 Residence/>=10 circuits/Dispatch/FL(days) Res 4 58 104 2 00 2	2 581 1 84215	1 4005 YES
A 2 1 1 2.2 P-4 Residence/>=10 circuits/Non-Dispatch/FL(days) Res		
A 2.1.2 1.1 P-4 Business/<10 circuits/Dispatch/FL(days) Bus 2.78 38,826 3.01 405	6 085 0 30394	-0 7430 YES
A 2 1 2.1.2 P-4 Business/<10 circuits/Non-Dispatch/FL(days) Bus 1.61 40,676 0 78 2,337	4 326 0 09203	9 0842 YES
A.2.1.2.2.1 P-4 Business/>=10 circuits/Dispatch/FL(days) Bus 10 70 314 5 17 6	12.830 5 28771	1 0460 YES
A2.1.2.2.2 P-4 Business/>=10 circuits/Non-Dispatch/FL(days)  Bus 2.69 16	2.878	
A.2 1.3.1.1 P-4 Design (Specials)/<10 circuits/Dispatch/FL(days) Design 22.55 1,690 4.08 4	21.808 10.91680	1 6919 YES
	19,462 13,96860	
A.2.1.3.1.2 P-4 Design (Specials)/<10 circuits/Non-Dispatch/FL(days) Design 17.94 66 3.00 2	13,402 13.50000	
A.2.1.3.1.2       P-4       Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	11 957	
A.2.1.3.2.1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days) Design 33.83 6	11 957	1 0695 YES
A.2.1.3.2.1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days) Design 33.83 6  A.2.1.3.2.2 P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days) Design Calcuits/Non-Dispatch/FL(days) Design Calcuits/Dispatch/FL(days) PBX 14.46 63 10.75 4	11 957 17 001 8 76610	1 0695 YES 0 4233 YES
A 2.1 3 2 1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days)  A 2.1 3.2 2 P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 4.1 1 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4.1 2 P-4 PBX/<10 circuits/Non-Dispatch/FL(days)  PBX 14 46 63 10 75 4  A 2.1 4 1.2 P-4 PBX/<10 circuits/Non-Dispatch/FL(days)  PBX 4 64 159 2 95 13	11 957 17 001 8 76610 9 396 2 71054	1 0695 YES 0 4233 YES 0 6238 YES
A 2.1 3 2 1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days)  A 2.1 3.2.2 P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 4.1.1 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4 1.2 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4 2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 4 2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 4 2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  B 2.1 4 2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  B 33 83 6  Design	11 957 17 001 8 76610 9 396 2 71054 6 148 6 73502	1 0695 YES 0 4233 YES 0 6238 YES 1 0987 YES
A 2.1 3 2 1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days)  A 2.1 3.2.2 P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 4.1.1 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4.1.2 P-4 PBX/<10 circuits/Dispatch/FL(days)  PBX 14 46 63 10 75 4  A 2.1 4 2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  PBX 8 40 5 100 1  A 2.1 4 2.2 P-4 PBX/>=10 circuits/Non-Dispatch/FL(days)  PBX 8 40 5 100 1  A 2.1 4 2.2 P-4 PBX/>=10 circuits/Non-Dispatch/FL(days)  PBX 1.39 35 193 5	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552	1 0695 YES 0 4233 YES 0 6238 YES
A 2.1 3.2 1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days) A 2.1 3.2.2 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days) Design A 2.1 4.1.1 P-4 PBX/<10 circuits/Dispatch/FL(days) A 2.1 4.1.2 P-4 PBX/<10 circuits/Dispatch/FL(days) PBX 14.46 63 10.75 4 A 2.1 4.2.1 P-4 PBX/<-10 circuits/Non-Dispatch/FL(days) PBX 464 159 2.95 13 A 2.1 4.2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days) PBX 8.40 5 1.00 1 A 2.1 4.2.2 P-4 PBX/>=10 circuits/Dispatch/FL(days) PBX 1.3.9 3.5 1.93 5 A 2.1 5.1.1 P-4 Centrex/<10 circuits/Dispatch/FL(days) Centrex 7.12 5.73	11 957 17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428	1 0695 YES 0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Dispatch/FL(days)	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536	1 0695 YES 0 4233 YES 0 6238 YES 1 0987 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Non-Dispatch/FL(days)         A.2.1.3.2.2       P-4       Design (Specials)>=10 circuits/Non-Dispatch/FL(days)       Design (Specials)>=10 circuits/Non-Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Non-Dispatch/FL(days)	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865	1 0695 YES 0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES
A 2.1 3 2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A 2.1 3.2.2       P-4       Design (Specials)>=10 circuits/Non-Dispatch/FL(days)       Design (Specials)         A 2.1 4.1.1       P-4       PBX/F10 circuits/Dispatch/FL(days)       PBX         A 2.1 4.1.2       P-4       PBX/F10 circuits/Dispatch/FL(days)       PBX         A 2.1 4.2.1       P-4       PBX/>=10 circuits/Dispatch/FL(days)       PBX         A 2.1 4.2.2       P-4       PBX/>=10 circuits/Non-Dispatch/FL(days)       PBX         A 2.1 5.1.1       P-4       Centrex/<10 circuits/Dispatch/FL(days)	11 957 17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)       14 46 63 10.75 4       Design (Specials)       Design (Specials)       PBX       464 159 295 13       Design (Specials)       Design (Specials)       PBX       464 159 295 13       Design (Specials)       Destign (Specials)       Destign (Specials)       PBX       464 159 295 13       Destign (Specials)       Destign (Specials)       Destign (Specials)       Destign (Specials)       Destign (Specials)       Destign	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES 2 2410 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.3.2.2       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Dispatch/FL(days)	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 3 05266	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.3.2.2       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Dispatch/FL(days)	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 0 000	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES 2 2410 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.3.2.2       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Dispatch/FL(days)	17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 3 05266	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES 2 2410 YES
A.2.1.3.2.1       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.3.2.2       P-4       Design (Specials)>=10 circuits/Dispatch/FL(days)       Design (Specials)>=10 circuits/Dispatch/FL(days)         A.2.1.4.1.1       P-4       PBX/<10 circuits/Dispatch/FL(days)	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 0 000	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES 2 2410 YES
A 2.1 3 2 1 P-4 Design (Specials)/>=10 circuits/Dispatch/FL(days) A 2.1 3.2.2 P-4 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days) Design	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63636 6 865 1 139 38 491 10 76239 10 057 3 05266 0 000 3.462	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES  2 2410 YES -0 0922 YES
A 2.1 3 2 1 P-4 Design (Specials)>=10 circuits/Dispatch/FL(days)  A 2.1 3.2.2 P-4 Design (Specials)>=10 circuits/Dispatch/FL(days)  A 2.1 4.1.1 P-4 PBX/<-10 circuits/Dispatch/FL(days)  A 2.1 4.1.2 P-4 PBX/<-10 circuits/Dispatch/FL(days)  A 2.1 4.1.2 P-4 PBX/>-10 circuits/Dispatch/FL(days)  A 2.1 4.2.1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 4.2.2 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 5.1.1 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.1.2 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.2.1 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.2.2 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.2.1 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.2.2 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 6.1.2 P-4 ISDN/<-10 circuits/Dispatch/FL(days)  A 2.1 6.1.2 P-4 ISDN/>=10 circuits/Dispatch/FL(days)  A 2.1 6.2.2 P-4 ISDN/>=10 circuits/Dispatch/FL(days)  A 2.1 6.2.2 P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 6.2 P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days)	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 0 000	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES  2 2410 YES -0 0922 YES
A 2.1 3 2 1 P-4 Design (Specials)>=10 circuits/Dispatch/FL(days)  A 2.1 3.2 2 P-4 Design (Specials)>=10 circuits/Non-Dispatch/FL(days)  A 2.1 4.1 1 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4.1 2 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4.1 2 P-4 PBX/<10 circuits/Dispatch/FL(days)  A 2.1 4.2 1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 4.2 1 P-4 PBX/>=10 circuits/Dispatch/FL(days)  A 2.1 5.1 1 P-4 Centrex/<10 circuits/Dispatch/FL(days)  A 2.1 5.1 2 P-4 Centrex/>=10 circuits/Dispatch/FL(days)  A 2.1 5.2 1 P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 5.2 1 P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 6.1 2 P-4 ISDN/<10 circuits/Dispatch/FL(days)  A 2.1 6.2 1 P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days)  A 2.1 6.2 1 P-4 ISDN/>=10	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 3 05266 0 000 3.462	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES 2 2410 YES -0 0922 YES  -0 2029 YES YES
A2.1.3.2.1   P-4   Design (Specials)/>=10 circuits/Dispatch/FL(days)   Design   33.83   6     A2.1.3.2.2   P-4   Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)   Design     A2.1.4.1.1   P-4   PBX/<-10 circuits/Dispatch/FL(days)   PBX   14.46   63   10.75   4     A2.1.4.1.2   P-4   PBX/>=10 circuits/Dispatch/FL(days)   PBX   4.64   159   2.95   13     A2.1.4.2.1   P-4   PBX/>=10 circuits/Dispatch/FL(days)   PBX   8.40   5   1.00   1     A2.1.5.1.1   P-4   Centrex/-10 circuits/Dispatch/FL(days)   PBX   1.3.9   3.5   1.93   5     A2.1.5.1.2   P-4   Centrex/-10 circuits/Dispatch/FL(days)   Centrex   1.68   9.33   2.70   10     A2.1.5.1.2   P-4   Centrex/>=10 circuits/Dispatch/FL(days)   Centrex   1.68   9.33   2.70   10     A2.1.5.2.2   P-4   Centrex/>=10 circuits/Dispatch/FL(days)   Centrex   1.60   3.7     A2.1.6.1.1   P-4   ISDN/<-10 circuits/Non-Dispatch/FL(days)   ISDN   31.45   796   7.33   13     A2.1.6.1.2   P-4   ISDN/<-10 circuits/Dispatch/FL(days)   ISDN   3.145   796   7.33   13     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.2   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-1   Residence/<10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-1   Residence/<10 circuits/Non-Dispatch/FL(days)   Res   5.23   8.4   6.50   2	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63636 6 865 1 139 38 491 10 76239 10 057 3 05266 0 000 3.462	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES  2 2410 YES -0 0922 YES  -0 2029 YES -0 1499 YES
A 2.1 3 2 1 P-4 Design (Specials)>=10 circuts/Dispatch/FL(days) A 2.1 3.2 2 P-4 Design (Specials)>=10 circuts/Non-Dispatch/FL(days) Design A 2.1 4.1.1 P-4 PBX/-10 circuits/Non-Dispatch/FL(days) A 2.1 4.1.2 P-4 PBX/-10 circuits/Non-Dispatch/FL(days) A 2.1 4.2.1 P-4 PBX/-=10 circuits/Dispatch/FL(days) A 2.1 4.2.1 P-4 PBX/==10 circuits/Dispatch/FL(days) A 2.1 4.2.2 P-4 PBX/==10 circuits/Dispatch/FL(days) A 2.1 5.1.1 P-4 Centrex/<10 circuits/Dispatch/FL(days) A 2.1 5.1.2 P-4 Centrex/<10 circuits/Non-Dispatch/FL(days) Centrex A 2.1 5.2.1 P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days) Centrex A 2.1 5.2.1 P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days) Centrex A 2.1 5.2.1 P-4 Centrex/>=10 circuits/Non-Dispatch/FL(days) Centrex B 9 06 31 A 2.1 6.1 P-4 ISDN/<10 circuits/Non-Dispatch/FL(days) B ISDN B 13 45 796 733 13 A 2.1 6.1 P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days) B ISDN B 12 00 1 A 2.1 6.2 P-4 ISDN/>=10 circuits/Dispatch/FL(days) B ISDN B 12 00 1 B A 2.1 6.2 P-4 ISDN/>=10 circuits/Non-Dispatch/FL(days) B ISDN B 12 00 1 B A 2.1 6.2 P-1 Residence/<10 circuits/Facility/FL(days) B Res B 7 92 506 8 37 19 B A 2.2 111 P-1 Residence/<10 circuits/Facility/FL(days) B Res B 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 3 05266 0 000 3.462	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES  2 2410 YES -0 0922 YES -0 1499 YES YES YES YES
A2.1.3.2.1   P-4   Design (Specials)/>=10 circuits/Dispatch/FL(days)   Design   33.83   6     A2.1.3.2.2   P-4   Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)   Design     A2.1.4.1.1   P-4   PBX/<-10 circuits/Dispatch/FL(days)   PBX   14.46   63   10.75   4     A2.1.4.1.2   P-4   PBX/>=10 circuits/Dispatch/FL(days)   PBX   4.64   159   2.95   13     A2.1.4.2.1   P-4   PBX/>=10 circuits/Dispatch/FL(days)   PBX   8.40   5   1.00   1     A2.1.5.1.1   P-4   Centrex/-10 circuits/Dispatch/FL(days)   PBX   1.3.9   3.5   1.93   5     A2.1.5.1.2   P-4   Centrex/-10 circuits/Dispatch/FL(days)   Centrex   1.68   9.33   2.70   10     A2.1.5.1.2   P-4   Centrex/>=10 circuits/Dispatch/FL(days)   Centrex   1.68   9.33   2.70   10     A2.1.5.2.2   P-4   Centrex/>=10 circuits/Dispatch/FL(days)   Centrex   1.60   3.7     A2.1.6.1.1   P-4   ISDN/<-10 circuits/Non-Dispatch/FL(days)   ISDN   31.45   796   7.33   13     A2.1.6.1.2   P-4   ISDN/<-10 circuits/Dispatch/FL(days)   ISDN   3.145   796   7.33   13     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.2   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-4   ISDN/>=10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-1   Residence/<10 circuits/Non-Dispatch/FL(days)   ISDN   3.24   4.9     A2.1.6.2.1   P-1   Residence/<10 circuits/Non-Dispatch/FL(days)   Res   5.23   8.4   6.50   2	11 957  17 001 8 76610 9 396 2 71054 6 148 6 73502 1 350 0 64552 10 428 1 999 0 63536 6 865 1 139 38 491 10 76239 10 057 3 05266 0 000 3.462	1 0695 YES  0 4233 YES 0 6238 YES 1 0987 YES -0 8410 YES -1 6093 YES  2 2410 YES -0 0922 YES  -0 2029 YES -0 1499 YES

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	Flor	ida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
A.2 2 2 1 1	P-1	Business/<10 circuits/Facility/FL(days)	Bus	9 79	112	1 00	1	13 952	14 01455	0 6275	YES
A 2.2.2.1.2	P-1	Business/<10 circuits/Equipment/FL(days)	Bus	0 00	0	0.00	0		1101.00	0 02/0	YES
A.2.2.2 1.3	P-1	Business/<10 circuits/Other/FL(days)	Bus	11 20	10	0 00	0	23 266			YES
A 2.2.2.2.1	P-1	Business/>=10 circuits/Facility/FL(days)	Bus	15 00	1	0 00	0	0 000			YES
A22222	P-1	Business/>=10 circuits/Equipment/FL(days)	Bus	0 00	0	0 00	0				YES
A 2.2 2 2 3	P-1	Business/>=10 circuits/Other/FL(days)	Bus	0 00	0	0 00	0	L			YES
A22311	P-1	Design (Specials)/<10 circuits/Facility/FL(days)	Design	22 00	1	5 00	1	0 000	0 00000		YES
A22312	P-1	Design (Specials)/<10 circuits/Equipment/FL(days)	Design	0 00	Ö	0 00	0				YES
A 2 2 3 1.3	P-1	Design (Specials)/<10 circuits/Other/FL(days)	Design	5 00	4	0 00	0	3 464			YES
A 2.2.3.2.1	P-1	Design (Specials)/>=10 circuits/Facility/FL(days)	Design	0 00	0						
A 2.2.3.2.2 A.2 2 3 2 3	P-1	Design (Specials)/>=10 circuits/Equipment/FL(days)  Design (Specials)/>=10 circuits/Other/FL(days)	Design	0 00	0						
A.2 2.4.1 1	P-1	PBX/<10 circuits/Facility/FL(days)	Design	0 00	0						
A 2.2 4 1.2	P-1	PBX/<10 circuits/Facility/rL(days) PBX/<10 circuits/Equipment/FL(days)	PBX	5 00	1	0 00	0	0 000			YES
A 2 2 4 1.3	P-1	PBX/<10 circuits/Other/FL(days)	PBX	0.00	0	0 00	0				YES
A.2.2.4 2.1	P-1	PBX/>=10 circuits/Facility/FL(days)	PBX PBX	0 00	0	0 00	0	<u> </u>			YES
A.2.2.4 2 2	P-1	PBX/>=10 circuits/Equipment/FL(days)	PBX	0.00	0	0 00	0				YES
A.2 2.4 2.3	P-1	PBX/>=10 circuits/Other/FL(days)	PBX	0.00	<u> </u>	0.00	0				YES
A.2.2.5.1 1	P-1	Centrex/<10 circuits/Facility/FL(days)	Centrex	15 80	5	0 00	0	0.057			YES
A.2 2 5.1 2	P-1	Centrex/<10 circuits/Equipment/FL(days)	Centrex	0.00	0	0 00		9 257			YES
A 2.2.5 1 3	P-1	Centrex/<10 circuits/Other/FL(days)	Centrex	0.00	0	0.00	0	<del> </del>			YES
A.2 2 5 2.1	P-1	Centrex/>=10 circuits/Facility/FL(days)	Centrex	10.50	2	0 00	<u>0</u>	7 778			YES
A.2 2.5.2 2	P-1	Centrex/>=10 circuits/Equipment/FL(days)	Centrex	0 00	- 2	0.00	0	1 / / / 9 1			YES
A.2.2 5.2 3	P-1	Centrex/>=10 circuits/Other/FL(days)	Centrex	0 00	Ö	0.00	· <del>·</del>	<del> </del>			YES
A.2 2 6.1 1	P-1	ISDN/<10 circuits/Facility/FL(days)	ISDN	0.00	<del>- 0</del>	0.00	0				YES YES
A.2.2.6 1 2	P-1	ISDN/<10 circuits/Equipment/FL(days)	ISDN	0.00	0	0.00	<del>- ŏ</del>				YES
A.2 2 6.1.3	P-1	ISDN/<10 circuits/Other/FL(days)	ISDN	0 00	ó	0 00	0				YES
A.2 2 6 2.1	P-1	ISDN/>=10 circuits/Facility/FL(days)	ISDN	0.00	0	0.00	ō				YES
A.2.2.6 2.2	P-1	ISDN/>=10 circuits/Equipment/FL(days)	ISDN	0 00	0	0.00	0				YES
A.2.2 6.2.3	P-1	ISDN/>=10 circuits/Other/FL(days)	ISDN	0 00	0	0.00	0				YES
	% Jeo	pardies - Mechanized									
A.2.4.1	P-2	Residence/FL(%)	Res	0.48%	728,682	0 36%	38,359		0 00036	3 2480	YES
A.2.4 2	P-2	Business/FL(%)	Bus	1.01%	82,087	0.59%	2,028		0 00224	1 8434	YES
A 2.4 3	P-2	Design (Specials)/FL(%)	Design	9 88%	2,420	0.00%	1		0 29840	0 3310	YES
A.2.4 4	P-2	PBX/FŁ(%)	PBX	6 19%	339	0.00%	6		0 09928	0 6240	YES
A.2.4.5	P-2	Centrex/FL(%)	Centrex	5 46%	1,739	0.00%	19	PS994	0 05242	1 0421	YES
A.2.4.6	P-2	ISDN/FL(%)	ISDN	6.13%	2,429	0 00%	11		0 07251	0 8459	YES
		pardies - Non-Mechanized									
A.2.5.1	P-2	Residence/FL(%)	Diagnostic			1 45%	413				Diagnostic
A.2 5.2	P-2	Business/FL(%)	Diagnostic			0.86%	349				Diagnostic
A.2.5.3	P-2	Design (Specials)/FL(%)	Diagnostic			0 00%	8				Diagnostic
A.2.5 4	P-2	PBX/FL(%)	Diagnostic	1 × × ×		0.00%	27				Diagnostic
A.2 5 5	P-2 P-2	Centrex/FL(%)	Diagnostic		** *	0.00%	10			100	Diagnostic
A.2.5.6		ISDN/FL(%)	Diagnostic			0.00%	24	SESSECTION OF THE			Diagnostic
4074	P-2	ge Jeopardy Notice Interval - Mechanized	40.5								
A 2.7.1		Residence/FL(hours)	>= 48 hrs			134.74	140			7	YES
A.2 7 2 A.2 7 3	P-2 P-2	Business/FL(hours)	>= 48 hrs			120 00	12				YES
		Design (Specials)/FL(hours)	>= 48 hrs							1.44	
A 2 7 4 A 2 7 5	P-2 P-2	PBX/FL(hours) Centrex/FL(hours)	>= 48 hrs >= 48 hrs								
A.2.76	P-2	ISDN/FL(hours)	>= 48 nrs >= 48 hrs							eces <sup>55</sup> 5	
razir o			Z- 40 1112			<u></u>				3.3%	
A 2 B 1	P-2	re Jeopardy Notice Interval - Non-Mechanized [Residence/FL(hours)	Diagrants			040.00					
A 2 8.1 A 2.8.2	P-2	Business/FL(hours)	Diagnostic Diagnostic			216 00	6				Diagnostic
A 2.8.2 A 2.8.3	P-2	Design (Specials)/FL(hours)	Diagnostic Diagnostic			104 00	3				Diagnostic
A284	P-2	PBX/FL(hours)	Diagnostic								Diagnostic
A.2.8.5	P-2	Centrex/FL(hours)	Diagnostic						526		Diagnostic
7.2.0.0	1 -2	Tooman Elinonol	Diagnosiic					*			Diagnostic

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A.2 8.6	P-2  SDN/FL(nours)	Diagnostic								Diagnostic
	% Jeopardy Notice >= 48 hours - Mechanized	•							**	Diagnostic
A.2.9.1	P-2   Residence/FL(%)	95% >= 48 hrs			100.00%	440				
A.2.9 2	P-2 Business/FL(%)	95% >= 48 hrs			100.00%	140				YES
A 2.9 3	P-2 Design (Specials)/FL(%)	95% >= 48 hrs			100 00%	12				YES
A.2.9.4	P-2 PBX/FL(%)	95% >= 48 hrs								
A 2.9.5	P-2 Centrex/FL(%) P-2 ISDN/FL(%)	95% >= 48 hrs								
A.2.9 6	P-2   ISDN/FL(%)	95% >= 48 hrs					- 19 mail 1			
	% Jeopardy Notice >= 48 hours - Non-Mechanized									
A.2 10 1	P-2 Residence/FL(%)	Diagnostic			100 00%	6				Diagnostic
A.2 10.2	P-2 Business/FL(%)	Diagnostic			100 00%	3				Diagnostic
A.2.10.3	P-2 Design (Specials)/FL(%)	Diagnostic					*			Diagnostic
A.2.10.4	P-2 PBX/FL(%)	Diagnostic					1.5			Diagnostic
A.2.10.5	P-2 Centrex/FL(%) P-2 ISDN/FL(%)	Diagnostic	17.		1					Diagnostic
A.2.10.6	P-2  ISDN/FL(%)	Diagnostic					1 1 20 0			Diagnostic
	% Missed installation Appointments	i								
A.2.11.1.1.1	P-3 Residence/<10 circuits/Dispatch/FL(%)	Res	5 38%	55,551	3 17%	3,466		0.00395	5 5794	YES
A.2.11.1.1.2	P-3 Residence/<10 circuits/Non-Dispatch/FL(%)	Res	0 04%	669,232	0 15%	46,311		0 00009	-12 2060	NO
A.2.11.1 2 1	P-3 Residence/>=10 circuits/Dispatch/FL(%)	Res	6 15%	130	0 00%	4	i oboo.	0 12199	0 5044	YES
A.2.11.1.2.2	P-3 Residence/>=10 circuits/Non-Dispatch/FL(%)	Res					1.0000			
A.2.11 2 1.1	P-3   Business/<10 circuits/Dispatch/FL(%) P-3   Business/<10 circuits/Non-Dispatch/FL(%)	Bus	1 46%	39,646	1 80%	610	5,000	0 00489	-0 7120	YES
A.2.11 2 1 2 A.2.11.2.2 1	P-3 Business/<10 circuits/Non-Dispatch/FL(%) P-3 Business/>=10 circuits/Dispatch/FL(%)	Bus	0 10%	41,111	0 25%	2,818	1.00	0 00062	-2 3508	NO
A.2.11.2.2.2	P-3 Business/>=10 circuits/Dispatch/FL(%)	Bus	7 52%	359	18 18%	11		0 08073	-1 3206	YES
A.2.11.2.2.2 A.2.11 3.1.1	P-3 Design (Specials)/<10 circuits/Dispatch/FL(%)	Bus Design	0 00% 3.89%	1.748	0.00%	1		0 00000		YES
A.2.11.3.1.2	P-3 Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	Design Design	4.23%	71	0 00%	5 4	- 1000	0 08660	0 4492	YES
A.2 11.3.2.1	P-3 Design (Specials)/>=10 circuits/Dispatch/FL(%)	Design	0.00%	7	0.00%	4		0 10338	0 4087	YES
A 2 11.3.2.2	P-3 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	Design	0 00 76							
A 2 11 4 1.1	P-3 PBX/<10 circuits/Dispatch/FL(%)	PBX	7 89%	76	0 00%	7		0 10651	0 7412	YES
A.2.11 4 1.2	P-3 PBX/<10 circuits/Non-Dispatch/FL(%)	PBX	1 70%	176	0 00%	19	87807 1	0 03126	0 5453	YES
A 2.11 4 2.1	P-3 PBX/>=10 circuits/Dispatch/FL(%)	PBX	20 00%	5	100 00%	1	at on the second	0.43818	-1 8257	NO NO
A.2.11.4.2.2	P-3 PBX/>=10 circuits/Non-Dispatch/FL(%)	PBX	0.00%	38	0.00%	6		0.00000	-1 0237	YES
A.2.11.5 1.1	P-3 Centrex/<10 circuits/Dispatch/FL(%)	Centrex	3 92%	638	0 00%	<del></del> 6		0 07959	0 4924	YES
A 2 11.5 1.2	P-3 Centrex/<10 circuits/Non-Dispatch/FL(%)	Centrex	0 00%	955	4 55%	22		0 00000	0 4324	NO
A.2 11.5.2.1	P-3 Centrex/>=10 circuits/Dispatch/FL(%)	Centrex	5.71%	35						
A.2 11.5.2.2	P-3 Centrex/>=10 circuits/Non-Dispatch/FL(%)	Centrex	0.00%	40	0 00%	1	3.000	0 00000		YES
A.2.11 6 1.1	P-3 ISDN/<10 circuits/Dispatch/FL(%)	ISDN	3 85%	832	0 00%	21		0 04249	0 9052	YES
A.2 11 6.1.2	P-3 ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN	1 43%	839	0 00%	18		0 02828	0 5057	YES
A.2 11.6.2.1	P-3 ISDN/>=10 circuits/Dispatch/FL(%)	ISDN	0 00%	1						
A.2.11.6.2.2	P-3 ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN	0.00%	50	0.00%	1		0 00000		YES
	% Provisioning Troubles within 30 Days									
A.2.12.1.1.1	P-9 Residence/<10 circuits/Dispatch/FL(%)	Res	7 46%	62,441	7 56%	3,851		0 00436	-0 2289	YES
A.2.12.1.1 2	P-9 Residence/<10 circuits/Non-Dispatch/FL(%)	Res	3.81%	745,483	4 85%	54,436		0 00085	-12 1789	NO
A.2.12.1.2 1	P-9 Residence/>=10 circuits/Dispatch/FL(%)	Res	14 17%	120	33 33%	6		0 14587	-1 3139	YES
A.2.12 1.2.2	P-9 Residence/>=10 circuits/Non-Dispatch/FL(%)	Res					2000			
A.2.12.2.1.1	P-9 Business/<10 circuits/Dispatch/FL(%)	Bus	3 11%	40,271	5 16%	639	André Rundez L'époène	0 00692	-2 9654	NO
A.2.12.2.1.2	P-9 Business/<10 circuits/Non-Dispatch/FL(%)	Bus	4.85%	49,460	5 69%	3,375		0 00382	-2 1939	NO
A 2.12.2.2.1	P-9 Business/>=10 circuits/Dispatch/FL(%)	Bus	9.18%	316	25.00%	12	property of	0 08491	-1 8635	NO
A.2.12.2.2.2	P-9 Business/>=10 circuits/Non-Dispatch/FL(%)	Bus	11 11%	9						
A.2 12.3.1 1	P-9 Design (Specials)/<10 circuits/Dispatch/FL(%)	Design	3.28%	2,133	0 00%	11		0 05386	0 6094	YES
A 2.12.3.1 2	P-9 Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	Design	2 44%	82	0 00%	5	38 8 Y V	0 07106	0 3432	YES
A.2.12 3.2.1	P-9 Design (Specials)>=10 circuits/Dispatch/FL(%)	Design	0 00%	11						
A.2.12.3.2.2	P-9 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	Design	1000				110 25			
A.2 12.4.1.1	P-9 PBX/<10 circuits/Dispatch/FL(%) P-9 PBX/<10 circuits/Non-Dispatch/FL(%)	PBX	4 26%	94	0 00%	44		0 10305	0 4129	YES
A.2 12.4.1.2	P-9 PBX/<10 circuits/Non-Dispatch/FL(%) P-9 PBX/>=10 circuits/Dispatch/FL(%)	PBX PBX	4 02%	224	0 00%	26		0 04069	0 9875	YES
A.2 12.4.2.1 A.2.12 4.2.2	P-9 PBX/>=10 circuits/Dispatch/FL(%) P-9 PBX/>=10 circuits/Non-Dispatch/FL(%)	PBX	14.29% 3.70%	7 27	0 00%	1		0 37409	0 3819	YES
7.2.14 4.2.2	II - II - D. 42 - 10 GIUGIGATAOTI-DIAPAGONI L.(19)	FBA	3.70%	۷.	11 11%	9	16:11	0 07269	-1 0190	YES

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A2 (13.1)   2			• • • • • • • • • • • • • • • • • • • •	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
A 2 (2.5.1) 2 9 Commercial control broadchaff (1/5) 0 Commercial Contr	A 2 12 5 1 1	P-9	Centrey/<10 circuits/Disnatch/FL(%)	Centrey	1.08%	730	0.00%	1		0.10366	0.1045	vee 1
A.2.15.2.2   F.								18				
A212.62.22   E.   Contractive Contractiv							0.0078	10		0.02480	0.4400	150
A 2 14 1		P-9					0.00%	3		0.00000		YES
A 2   24.12   P.4     SDN-10 ground-broad-part   P.1     SDN-10 ground-broad-part   P.1   SDN-10 ground-broad-part   P.1   SDN-10 ground-broad-part   P.1		P-9									0.5449	
A2 / 12   P.   SINN-10 consult/Purple/File   SINN-10 consult/Pur	A.2.12.5.1.2	P-9	ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN								
Activity   Completion Notes Interval - Mechanism   Activity   Completion Notes Interval - Mechanism   Activity   Completion Notes Interval - Mechanism   Activity   Completion Notes Interval - Activity   Completion   Completi	A.2.12.6.2.1		ISDN/>=10 circuits/Dispatch/FL(%)	ISDN							2,1,1	
P.S.   Residence(*10 crossins/paper/Fil floors)   Res   3.88   4.45   576,777   676   71,525   71,422   0.25096   8220   YES   42,441.11   P.S.   Residence(*10)   Considerate(*10)	A.2.12.6.2.2	P-9	ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN	0.00%	52						
A.2.14.1.12   P.5.   Residency (10 Crastaly Non-Capacita (17 (Lours))   Res   1.45   76.777   0.75   17.303   7.44   0.02585   74.459   VES   A.2.14.1.21   P.5.   Residency (10 Crastaly Non-Capacita (17 (Lours))   Res   3.69				<u></u>								
A.2.4.1.2.1							0.99			0.29064	9.9230	
A2.14.12   P.S.   Residence** to ceruple Non-Depatch* (Fluorus)   Res												
PS					1.04	101	0.02	6	6.683	2.80819	0.3624	YE\$
A2.44.2.1.2   P.S.   Busineset**10 circustPost Post Post Post Post Post Post Pos							<u> </u>					
## A2.14.2.2.1   P.S.   Businespi-10 circulatification   P.S.   P.S.   Businespi-10 circulatification   P.S.   P.S.   Businespi-10 circulatification   P.S.   P.S.   P.S.   Businespi-10 circulatification   P.S.   P.												
A 2.14.3.1.1 A 2.14.3.1.2 A 2.1												
P.S.   Design (Specials)-10 crount/Depath/Fil.(nours)   Design   127.21   1.305   0.02   2   385.331   272.6780   0.4654   YES   P.S.   Design (Specials)-17-0 crount/Depath/Fil.(nours)   Design   197.51   5   0.02   3   107.072   0.34555   0.02												
P.S.   Design (Specials)+10 crousthen-Design/Filthours)   Design   154.40   51   11.105   12.105   1												
P-5   Design (Specials)>=10 (crusts/Despatch/Fil.(nours)   Design   197.51   5   128.657   Nat. 24.14.31.2   P-5   Design (Specials)>=10 (crusts/Nan-Despatch/Fil.(nours)   PRX   1.12   45   0.02   3   107.022   83.81955   0.8007   VES   PRX   1.12   1.14   Nat. 24.14.41.2   P-5   PRX   PRX   PRX   PRX   PRX   PRX   1.12   1.14   Nat. 24.14.41.2   P-5   PRX							0.02			2/2.0/000	0.4004	TES
A2.14.3.2							<del> </del>					
PSA   PEXAMEN   PSA   PEXAMEN   PSA   PEXAMEN   PSA   PEXAMEN   PSA   PEXAMEN   PSA   PS		P-5			107.51				120.007			
A2.14.4.12					51.12	45	0.02	3	107 022	63.81565	0.8007	YES
A2.14.4.2.1   P-5   PBXP=10 circularNo-pagetorPL(hours)   PBX   3.62   4   2.472		P-5						<u> </u>		40.01000	0.0001	
A2.14.4.22		P-5										
A2.14.5.12	A.2.14.4.2.2	P-5	PBX/>=10 circuits/Non-Dispatch/FL(hours)	PBX		37						
A2.14.5.2.1 P.5 Centrexi—Tot circulat/Dispatch/FL(hours) Centrex A2.14.5.2.1 P.5 ISDN:10 circulat/Dispatch/FL(hours) ISDN 195.87 S29 S58.45.5  A2.14.6.2.1 P.5 ISDN:10 circulat/Dispatch/FL(hours) ISDN 10.15 SSDN 10.15 SSD	A.2.14.5.1.1		Centrex/<10 circuits/Dispatch/FL(hours)	Centrex	10.77	506			40.154			
A2.14.5.22   P.5   Centracy—10 circulation—Depath/FL(hours)   SIDN   195.87   5.29   5.584.55   SIDN   195.87   5.29   5.584.55   SIDN   195.87   5.29   5.584.55   SIDN   195.87   5.29   5.584.55   SIDN   10.15   6.93   SIDN   SIDN   10.15   6.93   SIDN   SIDN   10.15   6.93   SIDN   SIDN   10.15   6.93   SIDN   SIDN   SIDN   10.15   6.93   SIDN   SIDN   SIDN   SIDN   SIDN   6.91   4.3   SIDN   SIDN   SIDN   SIDN   SIDN   SIDN   SIDN   SIDN   6.91   4.3   SIDN				Centrex	3.64	860			17.685			
A2.14.6.1.1   P-5   ISDN×10 circuistNo-DepatchFL(hours)   ISDN   196.87   5.99   5.58.455							1		18.221			
A2.14.6.12   P.5   ISDNX=10 circuits/Non-Dispatch/FL(hours)   ISDN   10.15   693   S0.156												
A2.14.6.2.1 P.5 ISDN/>=10 circuits/Dispatch/FL(hours)  A2.14.6.2.2 P.5 ISDN/>=10 circuits/Dispatch/FL(hours)  A2.14.6.2.2 P.5 Residence/<0 circuits/Non-Dispatch/FL(hours)  A2.15.1.1.1 P.5 Residence/<0 circuits/Non-Dispatch/FL(hours)  Diagnostic  A2.15.1.2 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  A2.15.1.2.1 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  A2.15.1.2.1 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  A2.15.1.2.1 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  Diagnostic  Diagnostic  A2.15.2.1.1 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  Diagnostic  Diagnostic  Diagnostic  A2.15.2.1.1 P.5 Residence/<0 circuits/Dispatch/FL(hours)  Diagnostic  Diagnostic  Diagnostic  Diagnostic  Diagnostic  Diagnostic  A2.15.2.1.2 P.5 Residence/<0 circuits/Non-Dispatch/FL(hours)  Diagnostic  Diagnostic  Diagnostic  Diagnostic  A2.15.2.2 P.5 Residence/<0 circuits/Non-Dispatch/FL(hours)  Diagnostic  A2.15.2.1.2 P.5 Residence/<0 circuits/Non-Dispatch/FL(hours)  Diagnostic  Diagnosti												
A.2.14.6.2.2   P.5					10.15	693		· · · · · · · · · · · · · · · · · · ·	50.156			
Average Completion Notice Interval - Non-Mechanized					204				40.030			
A.2.15.1.1.1   P.5   Residencak-10 circuist/Nepath/FL(hours)   Diagnostic   25.08   228   491   Diagnostic   25.10   25.08	~2.14.0.2.2		<u> </u>		0.91	43			40.019			
A2.15.1.2 P-5 Residence/> P-5 Residence/> P-10 circuits/Non-Dispatch/FL(hours)  A2.15.1.2.1 P-5 Residence/> P-5 Residence/> P-7 Residence/> P-7 Residence/> P-8 Residence/> Residence/> P-8 Residence/> Residence/> P-9 Residence/> Residence/> P-10 circuits/Non-Dispatch/FL(hours)  Diagnostic  Dia	4045444			<b>-</b>								
A 2.15.1.2.1 P.5 Residence/>=10 circuits/Dispatch/FL(hours) Diagnostic Diagn												
A2.15.2.1.2 P-5 Residence/r=10 circuits/Non-Dispatch/FL(hours)  A2.15.2.1.1 P-5 Business/<10 circuits/Dispatch/FL(hours)  A2.15.2.2.1 P-5 Business/<10 circuits/Dispatch/FL(hours)  A2.15.2.2.1 P-5 Business/<10 circuits/Dispatch/FL(hours)  A2.15.2.2.1 P-5 Business/<10 circuits/Dispatch/FL(hours)  P-5 Business/> Business/> P-10 circuits/Dispatch/FL(hours)  P-5 Business/> Business/> P-10 circuits/Dispatch/FL(hours)  Diagnostic  Diagnostic  A2.15.3.1.1 P-5 Design (Specials) - Design (Specials) - Design (Specials) - P-5 Design (Specials) - Design (Specials) - Design (Specials) - P-5 Design (Specials) - Design (Specials) </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>25.28</td> <td>491</td> <td></td> <td></td> <td></td> <td></td>							25.28	491				
A2.15.2.1.1 P-5 Business/<10 circuits/Dispatch/FL(hours) Diagnostic Diagnost												
A2.15.2.1.2 P-5 Business/≈10 circuits/Non-Dispatch/FL(hours) A2.15.2.2.1 P-5 Business/≈10 circuits/Non-Dispatch/FL(hours) Diagnostic Diagnosti							20.40	424				
A2.15.2.2.1 P-5 Business>=10 circuits/Dispatch/FL(hours)  A2.15.2.2.2 P-5 Business>=10 circuits/Non-Dispatch/FL(hours)  A2.15.3.1.1 P-5 Design (Specials) A2.15.3.1.2 P-5 Design (Specials) A2.15.3.1.2 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.2 P-5 Design (Specials) A2.15.3.2.2 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.2 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.2 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.1 P-5 Design (Specials) A2.15.3.2.2 P-5 Design (Specials) A2.15.3.2.1 P-5 PBX/<10 circuits/Non-Dispatch/FL(hours)  Diagnostic												
A2.15.2.2 P.5 Business>=10 circuits/Non-Dispatch/FL(hours)  A2.15.3.1.1 P-5 Design (Specials)         Diagnostic Diagnosti												
A2.15.3.1.1 P-5 Design (Specials)<10 circuits/Dispatch/FL(hours) Diagnostic							10.00					
A2.15.3.1.2 P-5 Design (Specials)/<10 circuits/Non-Dispatch/FL(hours)  A2.15.3.2.1 P-5 Design (Specials)/⇒10 circuits/Non-Dispatch/FL(hours)  A2.15.3.2.2 P-5 Design (Specials)/⇒10 circuits/Non-Dispatch/FL(hours)  A2.15.4.1.1 P-5 PBX/<10 circuits/Dispatch/FL(hours)  A2.15.4.1.2 P-5 PBX/>=10 circuits/Non-Dispatch/FL(hours)  A2.15.4.2.1 P-5 PBX/>=10 circuits/Non-Dispatch/FL(hours)  A2.15.4.2.2 P-5 PBX/>=10 circuits/Non-Dispatch/FL(hours)  A2.15.5.1.1 P-5 Centrex/<10 circuits/Non-Dispatch/FL(hours)  A2.15.5.1.2 P-5 Centrex/<10 circuits/Non-Dispatch/FL(hours)  Diagnostic							57.17					
A2.15.3.2.1         P-5         Design (Specials)/>=10 circuits/Dispatch/FL(hours)         Diagnostic           A2.15.3.2.2         P-5         Design (Specials)/>=10 circuits/Dispatch/FL(hours)         Diagnostic           A2.15.4.1.1         P-5         PBX/<10 circuits/Dispatch/FL(hours)												
A2.15.3.2.2       P-5       Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)       Diagnostic         A2.15.4.1.1       P-5       PBX/<10 circuits/Dispatch/FL(hours)		P-5										
A2.15.4.1.1 P-5 PBX/<10 circuits/Dispatch/FL(hours) Diagnostic Di	A.2.15.3.2.2		Design (Specials)/>=10 circuits/Non-Dispatch/FL(hours)	Diagnostic								
A2.15.4.1.2       P-5       PBX/<10 circuits/Non-Dispatch/FL(hours)	A.2.15.4.1.1	P-5	PBX/<10 circuits/Dispatch/FL(hours)	Diagnostic			26.21	6				
A2.15.4.2.1 P-5 PBX/>=10 circuits/Dispatch/FL(hours) Diagnostic D	A.2.15.4.1.2	P-5	PBX/<10 circuits/Non-Dispatch/FL(hours)	Diagnostic			35.04					
A2.15.5.1.1       P-5       Centrex/<10 circuits/Dispatch/FL(hours)				Diagnostic			13.37	1				
A.2.15.5.1.1       P-5       Centrex/<10 circuits/Dispatch/FL(nours)							17.93	9				
A2.15.5.1.2       P-5 Centrex/<10 circuits/Non-Dispatch/FL(hours)												
A.2.15.5.2.2       P-5       Centrex/>=10 circuits/Non-Dispatch/FL(hours)       Diagnostic       14.00       1       Diagnostic         A.2.15.6.1.1       P-5       ISDN/<10 circuits/Dispatch/FL(hours)							16.91	19				
A.2.15.6.1.1       P-5       ISDN/<10 circuits/Dispatch/FL(hours)												Diagnostic
A.2.15.6.1.2       P-5       ISDN/<10 circuits/Non-Dispatch/FL(hours)												
A.2.15.6.2.1 P-5 ISDN/>=10 circuits/Dispatch/FL(hours) Diagnostic Diagnostic												
Diagnosic							22.26	19				
Diagnostic Diagnostic So.00 2 Diagnostic							00.00					
	M.4. 10.0.2.2	1F-0	Tions is - to circuits unit-rasbattra tritonia)	Liagnostic			80.00	2				Diagnostic

Benchmark /

BST

BST

CLEC

CLEC

Standard Standard

	Florida, November 2001	Benchmark / Analog	BST Measure	BŞT Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
		-								
	Total Service Order Cycle Time - Mechanized	5			4 02	2,074				Diagnostic
A 2 17 1 1 1	P-10 Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			0 69	32,225				Diagnostic
A.2.17 1 1 2	P-10 Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2 00	2				Diagnostic
A 2 17 1 2.1	P-10 Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic			200			7.00		Diagnostic
A.2.17 1.2.2	P-10 Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	100		3 29	204	-			Diagnostic
A 2 17.2.1 1	P-10 Business/<10 circuits/Dispatch/FL(days)	Diagnostic			1 22	1.019	-1480			Diagnostic
A 2 17 2 1 2	P-10 Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			3.67	3	-			Diagnostic
A 2 17 2 2 1	P-10 Business/>=10 circuits/Dispatch/FL(days)	Diagnostic			3.07		- 1			Diagnostic
A 2 17.2.2.2	P-10 Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					7.000			Diagnostic
A.2 17 3.1.1	P-10 Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
A.2 17 3 1 2	P-10 Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					- P. S. M. J.			Diagnostic
A 2 17 3 2 1	P-10 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic					8.833			Diagnostic
A.2 17.3.2.2	P-10 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 17 4 1 1	P-10 PBX/<10 circuits/Dispatch/FL(days)	Diagnostic					3.35			Diagnostic
A.2 17.4.1.2	P-10 PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					-			Diagnostic
A 2 17 4 2.1	P-10 PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic			1					Diagnostic
A.2 17 4.2.2	P-10 PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.17.5.1.1	P-10 Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.17 5.1 2	P-10 Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.17.5.2.1	P-10 Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic					- Carrier 1997			Diagnostic
A.2.17.5.2.2	P-10 Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					4000			Diagnostic
A 2 17 6 1 1	P-10 ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic					-			Diagnostic
A 2 17 6.1.2	P-10 ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.17.6 2.1	P-10 ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic								Diagnostic
A 2 17.6.2.2	P-10   iSDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnosac								
	Total Service Order Cycle Time - Partially Mechanized									
A.2 18.1.1.1	P-10 Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			2 97	387	anni.			Diagnostic
A.2 18 1 1 2	P-10 Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1.36	9,521				Diagnostic
A.2 18 1 2.1	P-10 Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 18.1.2 2	P-10 Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.18.2.1.1	P-10 Business/<10 circuits/Dispatch/FL(days)	Diagnostic	***		3 75	74		Ave Si		Diagnostic
A.2.18.2 1 2	P-10 Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			1 68	671		and the second		Diagnostic
A.2.18.2.2 1	P-10 Business/>=10 circuits/Dispatch/FL(days)	Diagnostic			7.00	2	The species			Diagnostic
A.2 18 2 2 2	P-10 Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					. with			Diagnostic
A.2.18 3 1 1	P-10 Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic	Committee of		5 00	1				Diagnostic
A.2.18.3 1 2	P-10 Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.18.3.2.1	P-10 Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic					_			Diagnostic
A.2.18.3.2.2	P-10 Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic:					_			Diagnostic
A.2.18.4.1 1	P-10 PBX/<10 circuits/Dispatch/FL(days)	Diagnostic			8 50	2	- 1			Diagnostic
A.2.18.4.1.2	P-10 PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic					_			Diagnostic
A.2 18.4 2.1	P-10 PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 18.4.2.2	P-10 PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 18 5 1.1	P-10 Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic				_	488			Diagnostic
A.2 18.5 1.2	P-10 Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.18 5.2 1	P-10 Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.18.5.2.2	P-10 Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					3460			Diagnostic
A.2.18.6.1.1	P-10 ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic					2000000 1970			Diagnostic
A 2.18 6 1.2	P-10 ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	1				6.2.			Diagnostic
A.2.18 6.2 1	P-10  SDN/>=10 circuits/Dispatch/FL(days)	Diagnostic					100	2000 A		Diagnostic
A.2.18 6.2 2	P-10 ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	0000							Diagnostic
	Total Service Order Cycle Time - Non-Mechanized									
404044	P-10 Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			4 72	100			1,198	Diagnostic
A.2.19 1 1.1		Diagnostic			3.20	225	and the state of			Diagnostic
A.2.19.1.1.2		Diagnostic			-/		1 4 mg			Diagnostic
A.2.19.1 2.1		Diagnostic					355 N 1588		Sec. 25 7	Diagnostic
A.2 19.1.2 2	P-10 Residence/>=10 circuits/Non-Dispatch/FL(days) P-10 Business/<10 circuits/Dispatch/FL(days)	Diagnostic			7 10	49			A second	Diagnostic
A 2 19.2.1.1		Diagnostic			2 92	213				Diagnostic
A.2.19.2.1.2	P-10 Business/<10 circuits/Non-Dispatch/FL(days) P-10 Business/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2 19.2.2.1	P-10 Business/>=10 circuits/Dispatch/FL(days)	Dagnosco								

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A.2 19.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)
A 2 19 3 1 1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
A.2 19 3 1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
A.2.19.3 2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
A.2 19.3.2.2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
A 2 19 4.1.1	P-10	PBX/<10 circuits/Dispatch/FL(days)
A.2 19.4 1 2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
A.2.19.4.2.1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
A.2.19.4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
A.2.19 5 1 1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A.2 19.5 1.2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A.2 19 5 2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
A 2 19 5 2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A.2 19 6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2 19 6 1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
12 19 6 2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
A.2.19 6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
	Total S	ervice Order Cycle Time (offered) - Mechanized
A.2.21.1 1.1	P-10	Residence/<10 circuits/Dispatch/FL(days)
A.2.21.1.1.1 A.2.21.1.1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)
A.2 21.1 2.1	P-10	Residence/>=10 circuits/Dispatch/FL(days)
2.21.1.2.2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)
2.21 2.1.1	P-10	Business/<10 circuits/Dispatch/FL(days)
2.21.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)
.2.21 2.2.1	P-10	Business/>=10 circuits/Dispatch/FL(days)
.2.21.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)
2.21 3.1.1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
2.21.3.1.2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
2.21.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)
221322	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
.2.21 4.1 1	P-10	PBX/<10 circuits/Dispatch/FL(days)
4.2 21,4.1 2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)
A.2.21.4.2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)
4.2.21.4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)
A.2 21.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)
A.2.21 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)
A.2 21.5 2 1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)
.2.21 5.2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)
A.2 21 6.1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)
A.2 21.6 1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)
A.2 21 6 2 1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)
1.2 21.6 2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)
	Total 5	Service Order Cycle Time (offered) - Partially Mechanized
A.2.22 1.1.1	P-10	Residence/<10 circuits/Dispatch/FL(days)
A.2 22.1 1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)
A.2 22.1 2.1	P-10	Residence/>=10 circuits/Dispatch/FL(days)
A.2 22.1 2.2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)
A.2 22.2.1 1	P-10	Business/<10 circuits/Dispatch/FL(days)
A.2.22.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)
A.2.22 2.2 1	P-10	Business/>=10 circuits/Dispatch/FL(days)
A.2.22.2 2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)
	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)
A.2 22.3 1 1 <sup>-</sup>		
	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)
A.2.22.3.1.2		Design (SpecialsV>=10 circuits/Dispatch/FL(days)
A.2.22.3.1.2 A.2 22.3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)   Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)
A.2.22.3.1.2 A.2 22.3.2.1 A.2.22 3.2 2	P-10 P-10 P-10 P-10	Design (Specials)>=10 circuits/Dispatch/FL(days)   Design (Specials)>=10 circuits/Non-Dispatch/FL(days)   PBX/<10 circuits/Dispatch/FL(days)
A2 22.3 1 1/ A2.22.3.1.2 A2 22.3.2.1 A2.22 3.2 2 A2 22 4.1.1 A2.22 4.1.2	P-10 P-10 P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)   Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)

Benchmark / Analog	BŞT Measure	B\$T Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Diagnostic								Diagnostic.
Diagnostic			8 67	3				Diagnostic
Diagnostic			7.50	2				Diagnostic
Diagnostic						100		Diagnostic
Diagnostic								Diagnostic
Diagnostic	1.0		8.50	2				Diagnostic
Diagnostic			6.00	7	a record of the			Diagnostic
Diagnostic			2.00	1				Diagnostic
Diagnostic			6.00	3				Diagnostic
Diagnostic								Diagnostic
Diagnostic			6 67	6				Diagnostic
Diagnostic					1.4 6 6 6 1.0	200		Diagnostic
Diagnostic					1. A. 1.N.			Diagnostic
Diagnostic			13 00	8	_			Diagnostic
Diagnostic			6 13	8				Diagnostic
Diagnostic			<u> </u>					Diagnostic
Diagnostic	400				<b>-</b>			Diagnostic
Diagnosiic								
			2.07	4.005				Diagnostic
Diagnostic			3 97	1,925				Diagnostic
Diagnostic			0 77	22,974	_			Diagnostic
Diagnostic			3 00	1	1000			
Diagnostic								Diagnostic
Diagnostic			3 31	201		allow an		Diagnostic
Diagnostic	1.0		1 25	961	- 2330	. 9		Diagnostic
Diagnostic			3 67	3	1000			Diagnostic
Diagnostic								Diagnostic
Diagnostic					_			Diagnostic
Diagnostic								Diagnostic
Diagnostic					10.00	2.00		Diagnostic
Diagnostic							1 1 Sec. 18	Diagnostic
Diagnostic					J. 3000 5 11		in a second	Diagnostic
Diagnostic					1000			Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic							E 2000	Diagnostic .
Diagnostic					4.00			Diagnostic
Diagnostic					7.93			Diagnostic
Diagnostic								Diagnostic
Diagnostic				·				Diagnostic
Diagnostic								Diagnostic
Diagnostic					19.75			Diagnostic
Diagnostic				<del>                                     </del>				Diagnostic
g								
B			2 95	371				Diagnostic
Diagnostic					_			Diagnostic
Diagnostic			1 36	7,827	_			Diagnostic
Diagnostic								
Diagnostic								Diagnostic
Diagnostic			3 89	66	_			Diagnostic
Diagnostic			1 73	545				Diagnostic
Diagnostic			7.00	22	298,34		reading the contract of	Diagnostic
Diagnostic								Diagnostic
Diagnostic						f ::::::: *** *** **		Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic			8 50	2	3,000			Diagnostic
Diagnostic								Diagnostic
Diagnostic				1	A TORRES			Diagnostic
Singinous								

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#### **BellSouth Monthly State Summary** Florida, November 2001

	Bell	South Monthly State Summary									
	Flori	da, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		<del>,</del>	Analog	Measure	Volume	Measure	Volume	Deviation		70	Ete.
			Aidiog	MCASUIG	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
A.2 22 4 2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			<b>I</b> 1					Diagnostic
A 2.22.5.1.1	P-10	Centrex/<10 circuits/Dispatch/FŁ(days)	Diagnostic								Diagnostic
A.2 22.5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.22.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A 2.22 5.2 2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					898.8			Diagnostic
A.2 22.6 1 1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic					- 33333			Diagnostic
A.2.22.6.1 2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic				******				Diagnostic
A.2 22.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/FL(days)	Diagnostic				***************************************				Diagnostic
A.2.22 6.2 2	P-10	ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
	Total S	ervice Order Cycle Time (offered) - Non-Mechanized									*X
A.2.23.1 1 1	P-10	Residence/<10 circuits/Dispatch/FL(days)	Diagnostic			4 81	96				
A.2.23 1.1.2	P-10	Residence/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2 89	177				Diagnostic
A.2.23.1.2.1	P-10	Residence/>=10 circuits/Dispatch/FL(days)	Diagnostic			2 03	111	1 1 1 1 1			Diagnostic
A.2.23.1.2.2	P-10	Residence/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic							2.866	Diagnostic
A 2.23 2 1 1	P-10	Business/<10 circuits/Dispatch/FL(days)	Diagnostic			7 28	46				Diagnostic Diagnostic
A.2.23.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			2.90	173	the second			Diagnostic
A.2.23.2.2.1	P-10	Business/>=10 circuits/Dispatch/FL(days)	Diagnostic			- 2.55		< 0.000			Diagnostic
A.2 23.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	13.194			P-1				Diagnostic
A.2.23.3.1.1	P-10	Design (Specials)/<10 circuits/Dispatch/FL(days)	Diagnostic			8 67	3				Diagnostic
A.2 23 3.1 2	P-10	Design (Specials)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			7 50	2				Diagnostic
A.2 23 3.2.1	P-10	Design (Specials)/>=10 circuits/Dispatch/FL(days)	Diagnostic	2.32							Diagnostic
A.2.23.3.2.2	P-10	Design (Specials)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23.4.1.1	P-10	PBX/<10 circuits/Dispatch/FL(days)	Diagnostic			8.50	2				Diagnostic
A.2.23.4.1.2	P-10	PBX/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			6.80	5				Diagnostic
A.2 23.4 2 1	P-10	PBX/>=10 circuits/Dispatch/FL(days)	Diagnostic	· ·		2 00	1				Diagnostic
A.2.23 4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic			8 00	2	1,000			Diagnostic
A.2.23 5 1.1	P-10	Centrex/<10 circuits/Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23 5 1 2	P-10	Centrex/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			6 50	2	rriero A			Diagnostic
A.2.23.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/FL(days)	Diagnostic	200							Diagnostic
A.2.23.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic					100			Diagnostic
A.2.23.6.1.1	P-10	ISDN/<10 circuits/Dispatch/FL(days)	Diagnostic			13 00	8				Diagnostic
A.2.23 6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/FL(days)	Diagnostic			6.67	6				Dragnostic
A.2.23.6.2 1 A.2.23 6.2.2	P-10 P-10	ISDN/>=10 crcuits/Dispatch/FL(days) ISDN/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic								Diagnostic
A.2.23 G.2.2	P-10	ISDN/2=10 Circuits/Non-Dispatch/FL(days)	Diagnostic			L				er, 461	Diagnostic
	% Com	pletions w/o Notice or < 24 hours									
A.2.24 1 1	P-6	Residence/Dispatch/FL(%)	Diagnostic			46.24%	3,030				Diagnostic
A.2.24.1.2	P-6	Residence/Non-Dispatch/FL(%)	Diagnostic			89.12%	44,436				Diagnostic
A.2.24.2.1	P-6	Business/Dispatch/FL(%)	Diagnostic			53.98%	452				Diagnostic
A.2.24 2 2	P-6	Business/Non-Dispatch/FL(%)	Diagnostic			77 53%	2,359				Diagnostic
A.2.24 3 1	P-6	Design (Specials)/Dispatch/FL(%)	Diagnostic			0 00%	4				Diagnostic
A.2.24.3.2	P-6	Design (Specials)/Non-Dispatch/FL(%)	Diagnostic		200	50 00%	2				Diagnostic
A.2.24.4.1	P-6	PBX/Dispatch/FL(%)	Diagnostic			66 67%	6				Diagnostic
A.2.24.4.2	P-6	PBX/Non-Dispatch/FL(%)	Diagnostic			55.56%	18			-0.00	Diagnostic
A 2 24.5.1	P-6	Centrex/Dispatch/FL(%)	Diagnostic			100 00%	6				Diagnostic
A.2.24.5 2	P-6	Centrex/Non-Dispatch/FL(%)	Diagnostic	· · · · · · · · · · · · · · · · · · ·		78 57%	14				Diagnostic
A.2.24.6.1	P-6	ISDN/Dispatch/FL(%)	Diagnostic			57 89%	19				Diagnostic
A.2.24.6.2	P-6	ISDN/Non-Dispatch/FL(%)	Diagnostic			63 64%	11			ngo sagasa in sa	Diagnostic
	Service	Order Accuracy									
A.2.25 1 1.1	P-11	Residence/<10 circuits/Dispatch/FL(%)	>= 95%			96 92%	65	11			YES
A.2.25 1 1 2	P-11	Residence/<10 circuits/Non-Dispatch/FL(%)	>= 95%			96.43%	140				YES
A.2.25 1 2 1	P-11	Residence/>=10 circuits/Dispatch/FL(%)	>= 95%			100.00%	16			,	YES
A.2.25.1 2 2	P-11	Residence/>=10 circuits/Non-Dispatch/FL(%)	>= 95%								
A 2 25 2 1 1	P-11	Business/<10 circuits/Dispatch/FL(%)	>= 95%			98 57%	70	20.00			YES
A.2.25.2.1 2	P-11	Business/<10 circuits/Non-Dispatch/FL(%)	>= 95%			99 26%	135				YES
A.2.25.2.2 1	P-11	Business/>=10 circuits/Dispatch/FL(%)	>= 95%			91 30%	23		S 100 00 00	₩. Ç. Z.	NO
A 2.25.2.2.2	P-11	Business/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			93 55%	31			777	NO
A.2.25.3.1.1	P-11	Design (Specials)/<10 circuits/Dispatch/FL(%)	>= 95%			90 00%	50			W gas	NO
A.2.25.3.1.2	P-11	Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95%			94 55%	55	y produced Na		J. 18 19	NO

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
A.2.25 3 2 1 A.2 25 3 2 2	P-11   Design (Specials)>=10 circuits/Dispatch/FL(%) P-11   Design (Specials)>=10 circuits/Non-Dispatch/FL(%)	>= 95% >= 95%			100 00% 96 00%	3 25				YES YES
A.2 25 3 2 2	P-11   Design (Specials)/>= 10 dicults/Non-Dispatch/PL(%)	>- 95%			90 00%	25				169
	Resale - Maintenance and Repair									
	Missed Repair Appointments									
A.3 1.1 1	M&R-1 Residence/Dispatch/FL(%)	Res	10.91%	82,972	6 87%	3,641		0 00528	7 6674	YES
A 3.1.1 2	M&R-1 Residence/Non-Dispatch/FL(%)	Res	1 47%	47,295	1 53%	2,424		0 00251	-0 2099	YES
A.3.1.2 1	M&R-1 Business/Dispatch/FL(%)	Bus	11.21%	16,027	7.62%	774		0 01161	3 0867	YES
A 3.1.2 2	M&R-1 Business/Non-Dispatch/FL(%)  M&R-1 Design (Specials)/Dispatch/FL(%)	Bus Design	2.16% 4.12%	9,353 1,312	2.75%	510 37		0 00661 0 03312	-0 8856 1 2428	YES YES
A.3.131 A 3 1 3 2	M&R-1 Design (Specials)/Non-Dispatch/FL(%)	Design	1.38%	1,662	18 18%	22	_	0 03312	-6 7002	NO NO
A.3 1.4 1	M&R-1 PBX/Dispatch/FL(%)	PBX	14 41%	340	5 26%	19		0.08279	1.1050	YES
A.3.1.4.2	M&R-1 PBX/Non-Dispatch/FL(%)	PBX	1 64%	183	0 00%	5		0 05756	0 2848	YES
A.3 1.5.1	M&R-1 Centrex/Dispatch/FL(%)	Centrex	19.47%	1,315	0 00%	6		0 16201	1 2016	YES
A.3 1.5.2	M&R-1   Centrex/Non-Dispatch/FL(%)	Centrex	5.12%	898	0.00%	2		0 15606	0 3282	YES
A.3 1.6.1	M&R-1   ISDN/Dispatch/FL(%)	ISDN	2 83%	283	10 00%	10	4.65	0 05333	-1 3451	YES
A3162	M&R-1   ISDN/Non-Dispatch/FL(%)	IŞDN	1 02%	391	0 00%	6		0 04139	0 2471	YES
	Customer Trouble Report Rate	•								
A.3211	M&R-2 Residence/Dispatch/FL(%)	Res	1 86%	4,456,966	1 92%	190,127		0 00032	-1 6716	NO
A.3 2 1 2	M&R-2 Residence/Non-Dispatch/FL(%)	Res Bus	1.31%	4,456,966 1,220,007	1.27% 9.30%	190,127 8,325		0 00024 0 00126	-8 8624 -63 3388	NO NO
A.3.2.2.1 A.3.2 2 2	M&R-2   Business/Dispatch/FL(%) M&R-2   Business/Non-Dispatch/FL(%)	Bus	0.77%	1,220,007	6 13%	8,325		0 00096	-55 6602	NO NO
A.3.231	M&R-2 Design (Specials/Dispatch/FL(%)	Design	0.59%	220,834	0 62%	5,951	1.0	0.00101	-0 2729	YES
A.3.2 3.2	M&R-2 Design (Specials)/Non-Dispatch/FL(%)	Design	0 75%	220,834	0 37%	5,951		0 00114	3 3600	YES
A.3.2.4.1	M&R-2 PBX/Dispatch/FL(%)	PBX	0 20%	166,410	0.30%	6,438	1.4	0.00057	-1 5816	YES
A.3.2.4 2	M&R-2 PBX/Non-Dispatch/FL(%)	PBX	0.11%	166,410	0 08%	6,438	110.000	0.00042	0 7670	YES
A.3.2.5.1	M&R-2   Centrex/Dispatch/FL(%)	Centrex	0.56%	234,973	0 29%	2,081		0 00165	1 6472	YES
A.3 2.5 2	M&R-2   Centrex/Non-Dispatch/FL(%) M&R-2   ISDN/Dispatch/FL(%)	Centrex ISDN	0 38% 0 08%	234,973 361,438	0 10% 0 16%	2,081 6,138		0.00136 0.00036	2 1016 -2 3494	YES NO
A.3 2.6.1 A.3.2.6.2	M&R-2 ISDN/Non-Dispatch/FL(%)	ISDN	0 11%	361,438	0 10%	6,138		0.00036	0 2463	YES
,	Maintenance Average Duration									
A.3.3.1.1	M&R-3 Residence/Dispatch/FL(hours)	Res	21 41	82,972	18 13	3,641	27 320	0 46260	7 0876	YES
A.3.3.1 2	M&R-3 Residence/Non-Dispatch/FL(hours)	Res	6 77	47,295	4 61	2,424	14 040	0 29238	7 4124	YES
A3321	M&R-3 Business/Dispatch/FL(hours)	Bus	16 76	16,027	15 74	774 510	22.953 9 692	0 84473	1 2013	YES
A3322	M&R-3 Business/Non-Dispatch/FL(hours)	Bus Design	4 23 18 41	9,353 1,312	3 18 4.90	37	126 943	0 44072 21 16149	2 3837 0 6382	YES YES
A.3 3.3 1 A.3 3.3.2	M&R-3   Design (Specials)/Dispatch/FL(hours)  M&R-3   Design (Specials)/Non-Dispatch/FL(hours)	Design	6 29	1,662	9.03	22	108 016	23.18109	-0 1184	YES
A.3 3 4.1	M&R-3 PBX/Dispatch/FL(hours)	PBX	17 48	340	4 47	19	31 171	7 34824	1 7709	YES
A.3 3.4.2	M&R-3 PBX/Non-Dispatch/FL(hours)	PBX	9 67	183	4 16	5	21 224	9 62064	0 5726	YES
A3351	M&R-3   Centrex/Dispatch/FL(hours)	Centrex	18 78	1,315	13.12	6	26 747	10 94426	0 5179	YES
A 3 3 5.2	M&R-3   Centrex/Non-Dispatch/FL(hours)	Centrex	5 14	898	0 74	2	15 295	10 82752	0 4059	YES
A.3.3.6 1	M&R-3  SDN/Dispatch/FL(hours)	ISDN ISDN	7 17 2 41	283 391	6.74 5.51	10 6	15 785 4 537	5 07902 1 86617	0 0845 -1.6612	YES NO
A3362	M&R-3   ISDN/Non-Dispatch/FL(hours)  % Repeat Troubles within 30 Days	ISDIA	241	391	0.51		4 537	1 000 17	-1.0012	NO
A.3.4 1 1	M&R-4 Residence/Dispatch/FL(%)	Res	20.56%	82,972	18 40%	3,641		0.00684	3 1576	YES
A.3.4 1.2	M&R-4   Residence/Non-Dispatch/FL(%)	Res	18.07%	47,295	18 28%	2,424		0 00801	-0 2571	YEŞ
A.3421	M&R-4 Business/Dispatch/FL(%)	Bus	17 18%	16,027	18 22%	774		0 01388	-0.7445	YES
A.3422	M&R-4 Business/Non-Dispatch/FL(%)	Bus	15 50%	9,353	16 86%	510	اسي	0.01646	-0 8262	YES
A.3.4 3.1	M&R-4 Design (Specials)/Dispatch/FL(%)	Design	36 89%	1,312	29 73%	37	8-8-1	0 08043	0 8902	YES
A 3.4 3.2	M&R-4 Design (Specials)/Non-Dispatch/FL(%)	Design	37 30%	1,662	22 73%	22		0 10379	1 4045	YES
A.3.4 4.1	M&R-4 PBX/Dispatch/FL(%)	PBX PBX	22 06% 14 21%	340 183	10 53% 0 00%	19 5		0 09775 0 15825	1 1798 0 8978	YES YES
A.3 4.4.2	M&R-4 PBX/Non-Dispatch/FL(%) M&R-4 Centrex/Dispatch/FL(%)	Centrex	15.21%	1,315	0 00%		التنب	0 15825	1 0351	YES
A.3.4.5.1 A.3 4.5.2	M&R-4   Centrex/Dispatch/FL(%)	Centrex	22.61%	898	50.00%	- 2	100	0 29610	-0 9252	YES
A3461	M&R-4 ISDN/Dispatch/FL(%)	ISDN	30 39%	283	20 00%	10		0 14799	0 7020	YES
A.3 4 6.2	M&R-4 ISDN/Non-Dispatch/FL(%)	ISDN	26 09%	391	33 33%	6	and the first	0.18064	-0 4012	YES
		•								

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	BellSouth Monthly State Summary									
	Florida, November 2001	Benchmark /	BST	BŞT	CLEC	CLEC	Standard	Standard		
	. 101144, 11010111201	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
	Out of Service > 24 hours									
40511	M&R-5 [Residence/Dispatch/FL(%)	Res	20 34%	52,827	16 75%	2,621		0 00806	4 4575	YES
A3511	M&R-5   Residence/Non-Dispatch/FL(%)	Res	6 78%	12,413	1 56%	768		0 00935	5 5835	YES
A3512	M&R-5   Residence Non-Dispatch / E(%)	Bus	15 13%	9,725	12.73%	487		0 01664	1 4395	YES
A 3.5.2 1	M&R-5   Business/Non-Dispatch/FL(%)	Bus	3.24%	3,698	2 16%	278	20076	0.01102	0 9862	YES
A 3 5.2.2	M&R-5 Design (Specials)/Dispatch/FL(%)	Design	4.12%	1,312	0.00%	37	900000 pm	0 03312	1 2428	YES
A.3.5.3.1	M&R-5   Design (Specials)/Non-Dispatch/FL(%)	Design	1 38%	1,662	18.18%	22	1.0	0 02507	-6 7002	NO
A.3 5.3 2	M&R-5   PBX/Dispatch/FL(%)	PBX	12.80%	250	0 00%	16		0.08615	1 4857	YES
A.3.5.4 1	M&R-5   PBX/Non-Dispatch/FL(%)	T PBX	13.89%	108	0 00%	3	- 1 / 1 / 2 / 2 / 2 / 2 / 2 / 2 / 2 / 2 /	0 20242	0 6861	YES
A.3.5.4.2	M&R-5   Centrex/Dispatch/FL(%)	Centrex	21.61%	930	0 00%	4		0 20624	1 0479	YES
A.3.5.5.1	M&R-5   Centrex/Non-Dispatch/FL(%)	Centrex	3 09%	388	0.00%	2		0 12273	0 2520	YES
A.3.5 5.2	M&R-5   ISDN/Dispatch/FL(%)	ISDN	3 19%	282	10.00%	10	- W.W.	0 05656	-1 2037	YES
A 3 5 6 1 A 3.5.6 2	M&R-5   ISDN/Non-Dispatch/FL(%)	ISDN	1 02%	391	0.00%	6		0 04139	0 2471	YES
A.3.5.0 2	Mark-5   ISDITERIOR-DISPARATO E(10)	<b>.</b>								
	Resale - Billing									
	Invoice Accuracy	DCT Chate	00 200/ I	\$504,739,660	OT 99.96% T	\$12,818,798		0 00004	-450 1708	YES
A.4.1	B-1 [FL(%)	BST - State	30 32 %	\$304,738,000	33.50%	Ψ12,010,730		0 00004	-100 1700	
	Mean Time to Deliver Involces - CRIS				· · · · · · · · ·	4 858				YES
A42	B-2 Region(business days)	BST - Region	4 13		3 46	1,859				123

F	Floria	la, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
Ū	Jnbund	led Network Elements - Ordering									
,	% Rejec	ted Service Requests - Mechanized									
11 <b>[</b> 2	D-7	Switch Ports/FL(%)	Diagnostic								Diagnostic
	D-7	Local Interoffice Transport/FL(%)	Diagnostic								Diagnostic
13 🖸	0-7	Loop + Port Combinations/FL(%)	Diagnostic			23.39%	9,618	9.8			Diagnostic
1.4	)-7	Combo Other/FL(%)	Diagnostic								Diagnostic
1.5	)-7	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			28 82%	288	- A			Diagnostic
	)-7	ISDN Loop (UDN, UDC)/FL(%)	Diagnostic			50 00%	6				Diagnostic
	)-7	Line Shanng/FL(%)	Diagnostic			19 72%	71	- 200000			Diagnosti
	<b>)-7</b>	2W Analog Loop Design/FL(%)	Diagnostic			14 05%	961				Diagnosti
	)-7	2W Analog Loop Non-Design/FL(%)	Diagnostic			10 98%	492	_			Diagnosti
	D-7	2W Analog Loop w/INP Design/FL(%)	Diagnostic								Diagnosti
	0-7	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			00.540/		_			Diagnosti
	D-13	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			29 51% 62 03%	61 266	_			Diagnosti
	0-13	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			36 69%	169	- 2000			Diagnosti
	)-7	Other Design/FL(%)	Diagnostic Diagnostic	i i		54 43%	7,770	_			Diagnosti
	D-7 D-7	Other Non-Design/FL(%) INP Standalone/FL(%)	Diagnostic			34 43 76	7,770	-			Diagnosti Diagnosti
		LNP Standalone/FL(%)	Diagnostic			12 03%	2,686				Diagnosti
_			Diagocao			12 00 /0	2,000				Diagnost
		ted Service Requests - Partially Mechanized	1								
	D-7	Switch Ports/FL(%)	Diagnostic					-			Diagnosti
	)-7	Local Interoffice Transport/FL(%)	Diagnostic			20.000/	0.040				Diagnosti
	)-7	Loop + Port Combinations/FL(%)	Diagnostic Diagnostic			29 86%	6,242	_			Diagnosti
	)-7	Combo Other/FL(%)	Diagnostic			0.00%	33				Diagnosti Diagnosti
	)-7	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			100 00%	1	_			Diagnosti
	D-7 D-7	ISDN Loop (UON, UDC)/FL(%) Line Sharing/FL(%)	Diagnostic			16.00%	50				Diagnosti
	)-7 )-7	2W Analog Loop Design/FL(%)	Diagnostic			28 76%	372				Diagnosti
	)-7 )-7	2W Analog Loop Non-Design/FL(%)	Diagnostic			20 50%	839	T &			Diagnosti
	)-7 )-7	2W Analog Loop W/INP Design/FL(%)	Diagnostic			20 30 %	033				Diagnosti
	)-7 )-7	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic					-			Diagnosti
	D-13	2W Anatog Loop w/LNP Design/FL(%)	Diagnostic			43 31%	658	- 4.0			Diagnosti
	D-13	2W Anatog Loop w/LNP Non-Design/FL(%)	Dragnostic			25 01%	2,083				Diagnosti
	D-7	Other Design/FL(%)	Diagnostic			45 53%	123				Diagnosti
	)-7 )-7	Other Non-Design/FL(%)	Diagnostic			86 08%	2,745	T			Diagnosti
	<del>5.7</del>	INP Standalone/FL(%)	Diagnostic			0.00%	1				Diagnostic
		LNP Standalone/FL(%)	Diagnostic			43 21%	1,222				Diagnosti
-	% Relec	ted Service Requests - Non-Mechanized	-								
	D-7	Switch Ports/FL(%)	Diagnostic			33.33%	3				Diagnostic
	<del></del>	Local Interoffice Transport/FL(%)	Diagnostic			61 73%	81				Diagnostic
	D-7	Loop + Port Combinations/FL(%)	Diagnostic			45 84%	866	The second secon			Diagnostic
	5-7	Combo Other/FL(%)	Diagnostic	100						*	Diagnostic
. <b>.</b>	<b>D-7</b>	xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			29.92%	391				Diagnostic
	D-7	ISDN Loop (UDN, UDC)/FL(%)	Diagnostic	1 1		17 98%	595			75 V.	Diagnostic
.7 [7	D-7	Line Sharing/FL(%)	Diagnostic			19 17%	120				Diagnostic
3 [7	<b>D-7</b>	2W Analog Loop Design/FL(%)	Diagnostic			41.67%	228			2 350 L	Diagnostic
	<b>D-7</b>	2W Analog Loop Non-Design/FL(%)	Diagnostic			25.54%	1,343	1,000		100	Diagnosti
	<b>D-7</b>	2W Analog Loop w/INP Design/FL(%)	Diagnostic			33.33%	3	10000			Diagnostic
	<b>D-7</b>	2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			38.46%	13				Diagnosti
	D-13	2W Analog Loop w/LNP Design/FL(%)	Diagnostic			40 91%	66				Diagnostic
	D-13	2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			56 67%	150		118 70	100000 200000   1000000	Diagnostic
	D-7	Other Design/FL(%)	Diagnostic			28 69%	603	1 10 10 10 10 10 10 10 10 10 10 10 10 10			Diagnostic
	)-7	Other Non-Design/FL(%)	Diagnostic			35 40%	1,551	W.W.			Diagnostic
	0-7	INP Standalone/FL(%)	Diagnostic			44 44%	63 951			A STATE OF	Diagnostic
17 C	<b>∑</b> -13	LNP Standalone/FL(%)	Diagnostic			30 49%	951			and the second	Diagnostic

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Équity
	Reject Interval - Mechanized									
B141	O-8   Switch Ports/FL(%)	>= 97% w in 1 hr								
B 1.4.2	O-8 Local Interoffice Transport/FL(%)	>= 97% win 1 hr								
B 1.43	O-8 Loop + Port Combinations/FL(%)	>= 97% win 1 hr			94.76%	2.254				NO
B144	O-8 Combo Other/FL(%)	>= 97% w in 1 hr			<u> </u>	2,201				
B145	O-8 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 97% w ın 1 hr			98 81%	84	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			YES
B.1 4.6	O-8 ISDN Loop (UDN, UDC)/FL(%)	>= 97% w/in 1 hr			33.33%	3				NO
B.1.4 7	O-8 Line Shanng/FL(%)	>= 97% w in 1 hr			78 57%	14	Laster 1			NO
B.1 4 8 B 1.4.9	O-8 2W Analog Loop Design/FL(%) O-8 2W Analog Loop Non-Design/FL(%)	>= 97% win 1 hr			77 62%	143			300	NO
B 1.4.9 B 1.4 10	O-8 2W Analog Loop Non-Design/FL(%) O-8 2W Analog Loop w/INP Design/FL(%)	>= 97% w in 1 hr >= 97% w in 1 hr			61.02%	59	- 11.03			NO
B 1.4 11	O-8 2W Analog Loop w/NP Non-Design/FL(%)	>= 97% win 1 hr					_			
B 1 4 12	O-14 2W Analog Loop w/LNP Design/FL(%)	>= 97% win 1 hr			83 33%	18	3.23			NO
B.1.4 13	O-14 2W Analog Loop w/LNP Non-Design/FL(%)	>= 97% w in 1 hr			73 94%	165	1,000			NO
B.1 4 14	O-8 Other Design/FL(%)	>= 97% w in 1 hr			81 54%	65				NO
B 1 4 15	O-8 Other Non-Design/FL(%)	>= 97% w in 1 hr			68 69%	4,350				NO
B.1.4.16	O-8 INP Standalone/FL(%)	>= 97% win 1 hr	1							
B.1.4.17	O-14 LNP Standalone/FL(%)	>= 97% w m 1 hr			82 97%	323				NO
	Reject Interval - Partially Mechanized - 10 hours									
B.1 7 1	O-8   Switch Ports/FL(%)	>= 85% w in 10 hrs				-				
B.1 7.2	O-8 Local Interoffice Transport/FL(%)	>= 85% w in 10 hrs								
B.1.7 3	O-8 Loop + Port Combinations/Fi.(%)	>= 85% w in 10 hrs			97 89%	1,946				YES
B.1.7.4	O-8 Combo Other/FL(%) O-8 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 10 hrs					6-67 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			
8175 8176	O-8   xDSL (ADSL, HDSL and UCL)/FL(%)   O-8   ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 10 hrs			400 0001		- T		V	
B.1.7.7	O-8 Line Sharing/FL(%)	>= 85% w in 10 hrs >= 85% w in 10 hrs			100.00% 50.00%	1 8	-			YES
B.1 7.8	O-8 2W Analog Loop Design/FL(%)	>= 85% win 10 hrs			86.24%	109	- 1 W - 1			NO YES
B179	O-8 2W Analog Loop Non-Design/FL(%)	>= 85% w in 10 hrs			80.11%	176	~			NO NO
B.1.7 10	O-8 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 10 hrs			- 5511115				9000 BBC	
B 1.7 11	O-8 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% w in 10 hrs								
B 1.7.12	O-14 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 10 hrs			86 76%	287			Y	YES
B.1.7.13	O-14 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 10 hrs			78.79%	547				NO
B.1.7.14	O-8 Other Design/FL(%)	>= 85% w in 10 hrs			91 07%	56				YES
B 1 7 15 B.1.7 16	O-8 Other Non-Design/FL(%) O-8 INP Standalone/FL(%)	>= 85% win 10 hrs >= 85% win 10 hrs			97 29%	2,396				YES
B.1.7 10 B 1 7 17	O-14 LNP Standalone/FL(%)	>= 65% win 10 nrs >= 85% win 10 hrs			90 29%	546				YES
_ ,,,,	Reject Interval - Non-Mechanized				00 20 /0	0-70				ILS
B 1 8.1	O-8   Swtch Ports/FL(%)	>= 85% w in 24 hrs			100.00%	1	<b>T</b>			YES
B.1.8.2	O-8 Local Interoffice Transport/FL(%)	>= 85% w in 24 hrs			100 00%	54				YES
B.1.8.3	O-8 Loop + Port Combinations/FL(%)	>= 85% w in 24 hrs			97 79%	407				YES
B.1.8.4	O-8 Combo Other/FL(%)	>= 85% w in 24 hrs					X			
B.1.8 5	O-8 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 24 hrs			100 00%	119				YES
B 1.8.6	O-8 ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 24 hrs			98 18%	110				YES
B187	O-8 Line Sharing/FL(%)	>= 85% w in 24 hrs			100.00%	23				YES
B 1 8.8 B 1.8.9	O-8 2W Analog Loop Design/FL(%) O-8 2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs			100 00%	96	2000			YES
B 1.8.9 B 1.8 10	O-8 2W Analog Loop Non-Design/FL(%)	>= 85% w in 24 hrs >= 85% w in 24 hrs			99.71% 100 00%	347 1			14.4	YES
B.1.8.11	O-8 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% win 24 hrs			100 00%	5			4601	YES YES
B.1.8 12	O-14 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 24 hrs			96.43%	28				YES
B.1.8.13	Q-14 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 24 hrs			100 00%	87				YES
B 1.8.14	O-8 Other Design/FL(%)	>= 85% w in 24 hrs			98 86%	175	1 1			YES
B 1 8 15	O-8 Other Non-Design/FL(%)	>= 85% w in 24 hrs			99 29%	564	0.807		3,332	YES
B 1 8.16	O-8 INP Standalone/FL(%)	>= 85% win 24 hrs	5 J		100 00%	28				YES
B 1 8 17	O-14 LNP Standalone/FL(%)	>= 85% w in 24 hrs		77.	99.66%	293				YES
	FOC Timeliness - Mechanized									
B191	O-9 Switch Ports/FL(%)	>= 95% w in 3 hrs								
B192	O-9 Local Interoffice Transport/FL(%)	>= 95% w in 3 hrs							Art Sec	

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Ctandard		
		Analog	Measure	Volume	Measure	Volume	Standard Deviation	Standard Error	ZScore	Equity
						- Volume	Deviation	£1101	200016	Equity
B193	O-9 Loop + Port Combinations/FL(%)	>= 95% w in 3 hrs			99 23%	7,248				YES
B.1 9.4 B.1 9.5	O-9   Combo Other/FL(%) O-9   xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% w in 3 hrs								
B.1.9.6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 95% w in 3 hrs >≈ 95% w in 3 hrs			95 21%	146				YES
B.197	O-9 Line Shanng/FL(%)	>= 95% win 3 hrs			100 00% 98 21%	5 56	3,00			YES
B 1 9.8	O-9 2W Analog Loop Design/FL(%)	>= 95% w in 3 hrs			99 50%	804	- 1,000			YES YES
B.1.9 9	O-9 2W Analog Loop Non-Design/FL(%)	>= 95% w in 3 hrs	100		98 59%	427	- 1			YES
8.1.9.10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 95% w in 3 hrs					200			123
B 1 9 11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 95% w in 3 hrs								
B.1.9.12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 95% w in 3 hrs			94.74%	38				NO
B 1.9.13 B.1.9.14	O-15 2W Analog Loop w/LNP Non-Design/FL(%) O-9 Other Design/FL(%)	>= 95% w in 3 hrs			100.00%	58				YES
B.1 9.15	O-9 Other Non-Design/FL(%)	>= 95% w in 3 hrs >= 95% w in 3 hrs			100.00%	103				YES
B.1 9 16	O-9 INP Standalone/FL(%)	>= 95% win 3 hrs			99 30%	4,398	_			YES
B 1 9.17	O-15 LNP Standalone/FL(%)	>= 95% win 3 hrs			87.51%	2,313				NO NO
	FOC Timeliness - Partially Mechanized - 10 hours									140
B 1.12.1	O-9   Switch Ports/FL(%)	>= 85% w in 10 hrs								
B.1 12.2	O-9 Local Interoffice Transport/FL(%)	>= 85% win 10 hrs					-			
B.1 12.3	O-9 Loop + Port Combinations/FL(%)	>= 85% w in 10 hrs			95 15%	4,821				YES
B.1 12.4	O-9 Combo Other/FL(%)	>= 85% w in 10 hrs								
B 1 12 5	O-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w in 10 hrs			100.00%	16			, mark	YES
B.1.12.6	O-9 ISDN Loop (UDN, UDC)/FL(%) O-9 Line Shanng/FL(%)	>= 85% w in 10 hrs			100 00%	1	100			YES
B 1.12.7 B.1 12.8	O-9 Line Sharmg/FL(%) O-9 2W Analog Loop Design/FL(%)	>= 85% w in 10 hrs			97.92%	48	7.00			YES
B.1.12.9	O-9 2W Analog Loop Design/FL(%)	>= 85% w in 10 hrs >= 85% w in 10 hrs			86 62% 95.90%	299	25-55			YES
B.1.12.10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 10 hrs			95.90%	732				YES
B 1.12.11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% win 10 hrs								
B.1.12.12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 10 hrs			76.16%	411				NO
B 1.12 13	O-15 2W Analog Loop w/LNP Non-Design/FL(%)	>= 85% w in 10 hrs			91.96%	1.667			20000	YES
B 1 12 14	O-9 Other Design/FL(%)	>= 85% w in 10 hrs			79 76%	84				NO
B 1.12 15	O-9 Other Non-Design/FL(%)	>= 85% w in 10 hrs			95 05%	424				YES
B 1.12 16 B 1 12 17	O-9 INP Standalone/FL(%) O-15 INP Standalone/FL(%)	>= 85% w in 10 hrs								
6 1 12 17	· · · · · · · · · · · · · · · · · · ·	>= 85% w in 10 hrs			91.03%	691				YES
	FOC Timeliness - Non-Mechanized									
B 1 13 1 B 1 13 2	O-9 Switch Ports/FL(%)	>= 85% w in 36 hrs			100 00%	3				YES
B 1.13 3	O-9 Local Interoffice Transport/FL(%) O-9 Loop + Port Combinations/FL(%)	>= 85% w in 36 hrs >= 85% w in 36 hrs			93.75%	32				YES
B 1 13 4	O-9 Combo Other/FL(%)	>= 85% w in 36 hrs			97.61%	419				YES
B.1.13.5	0-9 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 85% w m 36 hrs			100 00%	279	100			
B.1.13.6	O-9 ISDN Loop (UDN, UDC)/FL(%)	>= 85% w in 36 hrs			99 38%	483				YES YES
B 1.13.7	O-9 Line Sharing/FL(%)	>= 85% w in 36 hrs			100 00%	90				YES
B 1.13 8	O-9 2W Analog Loop Design/FL(%)	>= 85% w in 36 hrs			99.17%	121				YES
B 1 13.9	O-9 2W Analog Loop Non-Design/FL(%)	>= <b>85%</b> win 36 hrs			99.16%	948				YES
B.1.13.10	O-9 2W Analog Loop w/INP Design/FL(%)	>= 85% w in 36 hrs			100 00%	2			10.00	YES
B 1 13 11	O-9 2W Analog Loop w/INP Non-Design/FL(%)	>= 85% win 36 hrs	1000		100.00%	6	Q		A. C. C.	YES
B 1.13.12	O-15 2W Analog Loop w/LNP Design/FL(%)	>= 85% w in 36 hrs	- 30,000		97 62%	42	2000	3000	and the state of	YES
B 1.13.13	O-15 2W Analog Loop w/LNP Non-Design/FL(%) O-9 Other Design/FL(%)	>= 85% w in 36 hrs			100 00%	64				YE\$
B.1.13.14 B.1.13.15	O-9 Other Design/FL(%)	>= 85% w in 36 hrs >= 85% w in 36 hrs			99.26%	404	100			YES
B 1.13.16	O-9 INP Standalone/FL(%)	>= 85% win 36 hrs			99 46% 100.00%	926 31				YES
B.1.13.17	O-15 LNP Standalone/FL(%)	>= 85% w in 36 hrs			99 54%	647			- Carlon - C	YES
	FOC & Reject Response Completeness - Mechanized				300.70	<u> </u>			44	163
B 1.14 1 1	O-11 Switch Ports/EDVFL(%)	>= 95%								
B 1.14 1 2	O-11 Switch Ports/TAG/FL(%)	>= 95%							. v.Z.	
B 1 14 2 1	O-11 Local Interoffice Transport/EDVFL(%)	>= 95%							100000	
B.1.14 2.2	O-11 Local Interoffice Transport/TAG/FL(%)	>= 95%							. 600000	
B 1 14 3.1	O-11 Loop + Port Combinations/EDI/FL(%)	>= 95%		0	99 49%	791			M. M.F.	YES
B 1 14.3.2	O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			98 18%	8,827			- 5/2	YES
										-

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CLEC

CLEC

Standard Standard

# BellSouth Monthly State Summary Florida, November 2001

	riorida, November 2007	Analog	Measure	Volume	Measure	Volume	Deviation	Error	<b>ZS</b> core	Equity
		_								· ·
B.1.14.4.1	O-11 Combo Other/EDVFL(%)	>= 95%			<u> </u>					
B.1.14.4.2	O-11 Combo Other/TAG/FL(%)	>= 95%			00 740/	20				<del></del>
B 1 14.5 1	O-11 xDSL (ADSL, HDSL and UCL)/EDVFL(%)	>= 95% >= 95%			89 74% 77.91%	39 249	-			NO NO
B 1.14 5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%) O-11 ISDN Loop (UDN, UDC)/ED/FL(%)	>= 95% >= 95%			77.91%	249	-		. g 16 mm	NO
B 1.14.6.1	O-11 ISDN Loop (UDN, UDC)/EDVFL(%) O-11 ISDN Loop (UDN, UDC)/TAG/FL(%)	>= 95% >= 95%			100 00%	6	20,100	535.3680		YES
B 1 14.6 2 B 1 14 7 1	O-11   Line Sharing/EDI/FL(%)	>= 95% >= 95%			100 00 %			444.0		153
B 1 14.7.2	O-11 Line Sharing/EDIFE(%)	>= 95%			94 37%	71		1 1000		NO
B 1.14 8.1	O-11 2W Analog Loop Design/EDI/FL(%)	>= 95%			92 72%	316	00000			NO NO
B 1 14.8.2	O-11 2W Analog Loop Design/TAG/FL(%)	>= 95%			97 52%	645				YES
B 1.14.9.1	O-11 2W Analog Loop Non-Design/EDI/FL(%)	>= 95%			51 5 <u>2 7</u>	0.10				120
B.1.14 9 2	O-11 2W Analog Loop Non-Design/TAG/FL(%)	>= 95%			94 72%	492				NO
B.1.14.10.1	O-11 2W Analog Loop w/INP Design/EDVFL(%)	>≂ 95%								
B.1 14.10.2	O-11 2W Analog Loop w/INP Design/TAG/FL(%)	>= 95%								
B.1.14.11.1	Q-11 2W Analog Loop w/INP Non-Design/EDI/FL(%)	>= 95%								
B.1.14.11.2	O-11 2W Analog Loop w/INP Non-Design/TAG/FL(%)	>= 95%	69.606							
B.1.14.12.1	O-11 2W Analog Loop w/LNP Design/EDVFL(%)	> <b>=</b> 95%	\$1.00 m		94.29%	35	200			NO
B.1.14.12.2	O-11 2W Analog Loop w/LNP Design/TAG/FL(%)	>= 95%			88 46%	26	1,3474			NO
B.1.14.13.1	O-11 2W Analog Loop w/LNP Non-Design/EDI/FL(%)	>= 95%	4		97.06%	34	20000000			YES
B.1.14.13.2	O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%)	>≂ 95%			81.90%	232				NO
B.1.14.14.1	O-11 Other Design/EDVFL(%)	>= 95%			100.00%	29				YES
B.1 14.14.2	O-11 Other Design/TAG/FL(%)	>= 95%			90.71%	140				NO
B.1.14.15.1	O-11 Other Non-Design/EDI/FL(%)	>= 95%			99.86%	7,169				YES
B 1.14.15 2	O-11 Other Non-Design/TAG/FL(%)	>= 95%			98 17%	601				YES
B.1.14.16.1	O-11 INP Standalone/EDI/FL(%)	>= 95%					A STATE			
B.1.14.16.2	O-11 INP Standalone/TAG/FL(%)	>= 95%								
B.1.14.17.1	O-11 LNP Standalone/EDI/FL(%)	>= 95% >= 95%			98 69% 94 21%	2,375 311				YES NO
B.1.14.17 2	O-11 LNP Standalone/TAG/FL(%)	>= 95%			94 21%	311		<u> </u>	1000	L NO
	FOC & Reject Response Completeness - Partially Mechanized									
B.1.15.1.1	O-11 Switch Ports/EDI/FL(%)	>= 95%								
B 1.15.1.2	O-11   Switch Ports/TAG/FL(%)	>= 95%					5.1			
B.1.15.2.1	O-11 Local Interoffice Transport/EDVFL(%)	>= 95%								
B.1.15.2.2	O-11 Local Interoffice Transport/TAG/FL(%)	>= 95%								
B.1.15.3.1	O-11 Loop + Port Combinations/EDI/FL(%)	>= 95%			100 00%	342				YES
B 1 15.3,2	O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			99 86%	5,900	- 1.5°	- 000 <b>8</b> 0000		YES
B 1.15 4.1	O-11 Combo Other/EDVFL(%)	>= 95%								
B.1 15.4.2	O-11 Combo Other/TAG/FL(%)	>= 95%			75.000		10.00			— <u></u>
B.1 15 5 1	O-11 xDSL (ADSL, HDSL and UCL)/EDI/FL(%)	>= 95%			75.00% 48.28%	4 29	william.			NO
B.1.15.5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%)	>= 95%			48.28%		- 255			NO
B.1.15.6.1	O-11 ISDN Loop (UDN, UDC)/EDVFL(%)	>= 95% >= 95%	**		100 00%	1	- Autom			YES
B.1.15.8.2	O-11 ISDN Loop (UDN, UDC)/TAG/FL(%) O-11 Line Shanng/ED/FL(%)	>= 95% >= 95%	*:		100 00%	<del>'</del>				1E9
B.1.15.7.1	O-11 Line Sharing/TAG/FL(%)	>= 95% >= 95%			100.00%	50	-			YES
B 1 15.7.2	O-11 2W Analog Loop Design/EDI/FL(%)	>= 95%			100.00%	225				YES
B 1.15 8.1	O-11 2W Analog Loop Design/TAG/FL(%)	>= 95%			98.64%	147	- 1988			YES
B 1.15.8.2 B.1.15.9.1	O-11 2W Analog Loop Design/TAG/T-L(%)	>= 95%			30.04 /0					
B.1.15.9.2	O-11 2W Analog Loop Non-Design/TAG/FL(%)	>= 95%			99 52%	839				YES
B.1.15.10.1	O-11 2W Analog Loop w/INP Design/ED/FL(%)	>= 95%	. 540.57				1 2 2 7 7			
B 1 15.10.1	O-11 2W Analog Loop w/INP Design/TAG/FL(%)	>= 95%					100			
B.1.15 11.1	O-11 2W Analog Loop w/INP Non-Design/EDVFL(%)	>= 95%								
B.1.15.11.2	O-11 2W Analog Loop w/INP Non-Design/TAG/FL(%)	>= 95%								
B.1.15.12.1	O-11 2W Analog Loop w/LNP Design/EDI/FL(%)	>= 95%			99.75%	404	31.00			YES
B.1.15.12.2	O-11 2W Analog Loop w/LNP Design/TAG/FL(%)	>= 95%	7.64.		99.61%	254				YES
B.1.15.13.1	O-11 2W Analog Loop w/LNP Non-Design/EDI/FL(%)	>= 95%			99 81%	534	6-50,405			YES
B.1 15.13.2	O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%)	>= 95%			100.00%	1,549	A Same			YES
B 1 15.14.1	O-11 Other Design/EDI/FL(%)	>= 95%			100 00%	12			A Project	YES
B 1.15.14.2	O-11 Other Design/TAG/FL(%)	>= 95%	4		100.00%	111			way yan a	YES
B.1.15.15.1	O-11 Other Non-Design/EDVFL(%)	>= 95%			99 92%	2,463	7,000			YES
	O-11 Other Non-Design/TAG/FL(%)	>= 95%			99.65%	282	15.00	7400	SS 20 118	YES
B.1 15 15 2	O-11 Other Non-Design/TAG/FL(%)	J- 80%			99.0076	2.02.	والمستحديد المستحديد			

Benchmark /

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Votume	Deviation	Error	ZScore	Equity
5445464	Out INDO. II FOUT IN									
B.1 15 16 1 B.1 15 16 2	O-11 INP Standalone/EDI/FL(%) O-11 INP Standalone/TAG/FL(%)	>= 95%			100.00%	1				YES
B.1 15.17.1	O-11 LNP Standalone/EDVFL(%)	>= 95% >= 95%	N		00.000/	007				
B 1.15 17 2	O-11 LNP Standalone/TAG/FL(%)	>= 95%	•		99 68% 99 30%	937 285				YES YES
		3070			33 30 /6	203	· · · · · · · · · · · · · · · · · · ·			155
B.1.16.1	FOC & Reject Response Completeness - Non-Mechanized  O-11   Switch Ports/FL(%)									
B 1 16.2	O-11 Local Interoffice Transport/FL(%)	>= 95% >= 95%			100 00%	3				YES
B.1 16 3	O-11 Loop + Port Combinations/FL(%)	>= 95% >= 95%			92 59% 92 61%	81 866				NO
B.1.16.4	O-11 Combo Other/FL(%)	>= 95%			92 0176	000				NO
B 1 16 5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%			98 47%	391			30388	YES
B 1 16 6	O-11 ISDN Loop (UDN, UDC)/FL(%)	>= 95%			93 28%	595				NO NO
B 1 16.7	O-11 Line Sharing/FL(%)	>= 95%			93.33%	120	147			NO
B.1.16.8	O-11 2W Analog Loop Design/FL(%)	>= 95%	5, 5,5		89 47%	228				NO
B.1.16 9	O-11 2W Analog Loop Non-Design/FL(%)	>= 95%			92.61%	1,340	7 790111			NO
B.1 16 10 B.1 16 11	O-11 2W Analog Loop w/INP Design/FL(%) O-11 2W Analog Loop w/INP Non-Design/FL(%)	>= 95%			100.00%	3				YES
B.1 16 12	O-11   2W Analog Loop with P Non-Design/FL(%)     2W Analog Loop w/LNP Design/FL(%)	>= 95% >= 95%			84 62%	13				NO
B.1 16 13	O-11 2W Analog Loop w/LNP Non-Design/FL(%)	>= 95%			96 97% 95.33%	66 150				YES
B 1 16 14	O-11 Other Design/FL(%)	>= 95%			91.87%	603			ar in the	YES NO
B 1.16 15	O-11 Other Non-Design/FL(%)	>= 95%			91.87%	1,549	- 300		-5	NO NO
B.1.16.16	O-11 INP Standalone/FL(%)	>= 95%	35.7634		92.06%	63				NO
B 1.16.17	O-11 LNP Standalone/FL(%)	>= 95%			95 48%	951		F. 100		YES
	FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
B 1 17.1 1	O-11   Switch Ports/EDI/FL(%)	>= 95%			F I				V 5	<del></del>
B.1.17.1.2	O-11 Switch Ports/TAG/FL(%)	>= 95%								
8.1 17.2.1	O-11 Local Interoffice Transport/EDVFL(%)	>= 95%								
B.1.17.2 2	O-11 Local Interoffice Transport/TAG/FL(%)	>= 95%								
B.1.17 3.1	O-11 Loop + Port Combinations/EDVFL(%)	>= 95%			77.51%	787				NO
B.1.17.3.2 B.1 17 4 1	O-11 Loop + Port Combinations/TAG/FL(%) O-11 Combo Other/EDI/FL(%)	>= 95%			93.39%	8,666	100			NO
B.1.1741 B.1.1742	O-11 Combo Other/EDVFL(%) O-11 Combo Other/TAG/FL(%)	>= 95% >= 95%					80.80.00			
B 1.17 5 1	O-11 xDSL (ADSL, HDSL and UCL)/EDI/FL(%)	>= 95%			100 00%	35				
B.1.17.5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%)	>= 95%			100.00%	194				YES YES
B 1.17.6.1	O-11 ISDN Loop (UDN, UDC)/EDVFL(%)	>= 95%			100.00 /0				4	ieo .
B.1.17.6.2	O-11 ISDN Loop (UDN, UDC)/TAG/FL(%)	>= 95%			50.00%	6				NO
B.1.17 7.1	O-11 Line Sharing/EDVFL(%)	>= 95%								
B.1 17 7.2	O-11 Line Sharing/TAG/FL(%)	>= 95%			95 52%	67				YES
B 1.17.8.1	O-11 2W Analog Loop Design/EDVFL(%) O-11 2W Analog Loop Design/TAG/FL(%)	>= 95%			64 51%	293				NO
B.1 17.8.2 B.1.17.9.1	O-11 2W Analog Loop Design/TAG/FL(%) O-11 2W Analog Loop Non-Design/ED/FL(%)	>= 95% >= 95%			95.55%	629				YES
B.1.17.9.2	O-11 2W Analog Loop Non-Design/EDVFL(%)	>= 95% >= 95%	7.7		94 64%	466				
B.1 17.10 1	O-11 2W Analog Loop w/INP Design/EDI/FL(%)	>= 95%			94 04%	400				NO
B 1.17 10 2	O-11 2W Analog Loop w/INP Design/TAG/FL(%)	>= 95%							55555	
B 1.17 11 1	O-11 2W Analog Loop w/INP Non-Design/EDI/FL(%)	>= 95%			1					
B.1.17 11.2	O-11 2W Analog Loop w/INP Non-Design/TAG/FL(%)	>= 95%	481					4.5		
B 1.17 12 1	O-11 2W Analog Loop w/LNP Design/EDI/FL(%)	>= 95%			100 00%	33				YES
B 1.17 12 2	O-11 2W Analog Loop w/LNP Design/TAG/FL(%)	>= 95%			100.00%	23				YES
B.1.17.13 1	O-11 2W Analog Loop w/LNP Non-Design/EDVFL(%)	>= 95%			100.00%	33				YES
B 1 17 13.2 B 1.17 14 1	O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%) O-11 Other Design/EDI/FL(%)	>= 95%			100.00%	190				YES
B.1.17 14 1 B.1.17 14 2	O-11 Other Design/EDVFL(%)	>= 95% >= 95%			68 97% 70.08%	29 127		2.33	- 1 A - 2 A	NO
B.1.17.15 1	O-11 Other Non-Design/EDI/FL(%)	>= 95% >= 95%	effect (		43 15%	7,159			150	NO
B 1.17 15.2	O-11 Other Non-Design/TAG/FL(%)	>= 95%			84.75%	590			- X	NO NO
B 1.17 16.1	O-11 INP Standalone/EDVFL(%)	>= 95%			54.7575		. 1170171	7. 7. i	100	140
B 1.17 16.2	O-11 INP Standalone/TAG/FL(%)	>= 95%				-			and a second	
B 1 17 17.1	O-11 LNP Standalone/EDI/FL(%)	>= 95%			100.00%	2,344		2 000000 2 0 000000	300000	YES
B.1.17.17.2	O-11 LNP Standalone/TAG/FL(%)	>= 95%			100.00%	293			1.0	YES

FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized

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	Florida, November 2001	Benchmark /	BST	вѕт	CLEC	CLEC	Standard	Standard		_
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B.1.18 1 1	O-11 Switch Ports/EDI/FL(%)	>= 95%								
B 1 18 1.2	O-11 Switch Ports/TAG/FL(%)	>= 95%								
B.1.18 2.1	O-11 Local Interoffice Transport/EDVFL(%)	>= 95%								
B.1.18 2.2	O-11 Local Interoffice Transport/TAG/FL(%)	>= 95%								
B.1.18.3.1	O-11 Loop + Port Combinations/EDVFL(%) O-11 Loop + Port Combinations/TAG/FL(%)	>= 95%			92.69%	342				NO
B.1 18.3.2 B 1 18.4 1	O-11 Combo Other/EDI/FL(%)	>= 95% >= 95%			91.04%	5,892				NO
B 1.18 4.2	O-11 Combo Other/TAG/FL(%)	>= 95%								
B.1.18.5.1	O-11 xDSL (ADSL, HDSL and UCL)/EDVFL(%)	>= 95%			100.00%	3				YES
B.1.18.5.2	O-11 xDSL (ADSL, HDSL and UCL)/TAG/FL(%)	>= 95%			100.00%	14				YES
B 1 18.6 1	O-11 ISDN Loop (UDN, UDC)/EDI/FL(%)	>= 95%			100.0070					123
B 1.18 6 2	O-11 ISDN Loop (UDN, UDC)/TAG/FL(%)	>= 95%	U.S.		0 00%	1	<del>-</del> "			NO
B 1 18 7 1	O-11 Line Sharing/EDI/FL(%)	>= 95%					2.55			
B.1.18 7.2	O-11 Line Shanng/TAG/FL(%)	>= 95%			88.00%	50				NO
B 1.18 8 1	O-11 2W Analog Loop Design/EDI/FL(%)	>= 95%	The second		94 67%	225				NO
B 1 18.8.2	O-11 2W Analog Loop Design/TAG/FL(%)	>= 95%			87 59%	145				NO
B.1.18 9.1	O-11 2W Analog Loop Non-Design/EDI/FL(%)	>= 95%						and the second		
B.1.18.9.2	O-11 2W Analog Loop Non-Design/TAG/FL(%)	>= 95%			91.86%	835				NO
B 1 18 10.1 B.1.18 10 2	O-11 2W Analog Loop w/INP Design/EDVFL(%) O-11 2W Analog Loop w/INP Design/TAG/FL(%)	>≂ 95% > <del>=</del> 95%	3000		i		1100000000			<b></b>
B.1.18 10 2 B.1 18 11 1	O-11 2W Analog Loop w/INP Non-Design/ED/FL(%)	>= 95%								<b></b>
B 1 18 11 2	O-11 2W Analog Loop w/INP Non-Design/TAG/FL(%)	>= 95%	15.00							<b>├</b> ───┤
B.1.18 12 1	O-11 2W Analog Loop w/LNP Design/EDI/FL(%)	>= 95%			98.01%	403	50 54			YES
B.1 18.12.2	O-11 2W Analog Loop w/LNP Design/TAG/FL(%)	>= 95%			90.91%	253	100			NO
B.1.18.13.1	O-11 2W Analog Loop w/LNP Non-Design/EDVFL(%)	>= 95%			97.37%	533				YES
B.1.18 13 2	O-11 2W Analog Loop w/LNP Non-Design/TAG/FL(%)	>= 95%			95 61%	1,549				YES
B 1.18.14.1	O-11 Other Design/EDI/FL(%)	>= 95%			91 67%	12				NO
B 1.18 14.2	O-11 Other Design/TAG/FL(%)	>= 95%			86.49%	111	3,000			NO
B.1 18.15.1	O-11 Other Non-Design/EDI/FL(%)	>= 95%			91.91%	2,461				NO
B.1.18.15 2	O-11 Other Non-Design/TAG/FL(%)	>= 95%			96 80%	281				YES
B 1.18 16 1	O-11 INP Standalone/EDVFL(%)	>= 95%			100.00%	. 1				YES
B 1.18.16.2 B 1.18.17.1	O-11 INP Standalone/TAG/FŁ(%) O-11 LNP Standalone/EDI/FL(%)	>= 95% >= 95%			98.82%	934	100001			
B 1.18 17.2	O-11 LNP Standalone/TAG/FL(%)	>= 95%			97.17%	283	au.			YES YES
<b>.</b>					07.1770					
B 1 19 1	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized  [O-11   Switch Ports/FL(%)	>= 95%			66.67%	3				NO
B 1.19.2	O-11 Local Interoffice Transport/FL(%)	>= 95%			86.67%	75				NO
B.1 19 3	O-11 Loop + Port Combinations/FL(%)	>= 95%	ALC: 1		92.27%	802				NO
B.1.19.4	O-11 Combo Other/FL(%)	>= 95%			02.2.70					<del>- NO</del>
B 1 19.5	O-11 xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95%			95 58%	385				YES
B 1 19 6	O-11 ISDN Loop (UDN, UDC)/FL(%)	>= 95%			93 33%	555				NO
B.1.19 7	O-11 Line Shanng/FL(%)	>= 95%			98 21%	112				YES
B.1.19 8	O-11 2W Analog Loop Design/FL(%)	>= 95%			94.12%	204				NO
8.1.19.9	O-11 2W Analog Loop Non-Design/FL(%)	>= 95%			93 88%	1,241				NO
B.1.19.10	O-11 2W Analog Loop w/INP Design/FL(%)	>= 95%			100 00%	3				YES
B 1.19 11	O-11 2W Analog Loop w/INP Non-Design/FL(%)	>= 95%			100.00%	11				YES
B 1.19.12	O-11 2W Analog Loop w/LNP Design/FL(%)	>= 95%			90 63%	64		aala kii in kaal		NO
B 1.19.13	O-11   2W Analog Loop w/LNP Non-Design/FL(%) O-11   Other Design/FL(%)	>= 95%			90 21%	143 554				NO
B 1 19.14 B 1.19.15	O-11 Other Non-Design/FL(%)	>= 95% >= 95%			94 95% 94 87%	1,423				NO NO
B 1 19.16	O-11 INP Standalone/FL(%)	>= 95%			96 55%	1,423 58				NO.
B.1.19.17	O-11 LNP Standalone/FL(%)	>= 95%			94 16%	908				YES NO
201110111	[				<del></del>					
	Unbundled Network Elements - Provisioning									
	Order Completion Interval									
B 2.1 1 1 1	P-4 Switch Ports/<10 circuits/Dispatch/FL(days)	R&B (POTS)	3 85	85,237			5 840	T		
B.2.1.1 1 2	P-4 Switch Ports/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)		654,116			2.000			
B 2 1.1 2.1	P-4 Switch Ports/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9.18	418			11 498			

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B.2 1 1 2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B 2 1 2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B 2.1 2 1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B 2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B21222	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B21311	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.1 3 1 2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B 2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(days)
B.2.1 3.1 4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL(days)
B 2 1.3 2 1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.1323	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(days)
B.2.1.3.2 4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(days)
B.2.1 4.1 1	P-4	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.1.4 1.4	P-4	Combo Other/<10 circuits/Dispatch In/FL(days)
B.2.1.4 2 1	P-4	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2.1.4.2 4	P-4	Combo Other/>=10 circuits/Dispatch In/FL(days)
B.2.1.5.3.1	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/FL(days)
8.2.1.5.3.2	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/FL(days)
B.2.1.5 4.1	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/FL(days)
B.2.1.5.4.2	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/FL(days)
B.2.1.5.5.1	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/FL(days)
B 2 1 5.5 2	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/FL(days)
B.2 1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL(days)
B.2 1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL(days)
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL(days)
B.21642	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL(days)
B.2 1 6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL(days)
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL(days)
B.2.1.7 3.1	P-4 P-4	Line Sharing/<6 circuits/Dispatch/FL(days) Line Sharing/<6 circuits/Non-Dispatch/FL(days)
B 2.1 7.3.2	P4	Line Sharing/6-13 circuits/Dispatch/FL(days)
B2174.1	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL(days)
B2174.2	P-4	Line Sharing/>=14 circuits/Dispatch/FL(days)
B21751	P-4	Line Shanng/>=14 circuits/Non-Dispatch/FL(days)
B 2 1.7.5.2	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2.1.8.1.2	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2 1.8.2.1 B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.1.9 1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(days)
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(days)
B.2.1.9.2.4 B.2.1.10 1.1	P-4	2W Analog Loop w/tNP Design/<10 circuits/Dispatch/FL(days)
B.2.1.10 1.1 B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B 2.1.10.1.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2 1.10.2 2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.1.11.1 1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(days)
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2 1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(days)
B.2.1.12.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B.2 1.12 1 2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.1.12 1.2 B.2.1.12 2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B.2.1 12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.1 13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(days)
B.2.1.13.1.4 B 2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(days)
D.Z. 1. 13.Z.7		Territorial rook and in 12012 parishing to another reference of the last of th

Benchmark /	BST	в\$т	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
R&B (POTS)	2.69	16			2 878			
DS1/DS3	16 85	2,028	18 11	9	15 836	5 29040	-0 2391	YES
DS1/DS3					T			
DS1/DS3								
DS1/DS3			- 1					
R&B	3 87	85,794	3 28	424	5.857	0 28512	2 0682	YES
R&B	0 91	655,604	0.67	7,738	2 002	0 02289	10 6387	YES
R&B	0 33	400,543	0.36	5,626	0 164	0 00221	-11 0322	NO
R&B	1 83	255,061	151	2,112	2.982	0 06516	4 9259	YES
R&B	9 16	454	4 27	5	11 188	5 03086	0 9727	YES
R&B	1 71	88	6 00	1	1 702	1 71205	-2 5029	NO
R&B	0 39	28			0 316			
R&B	2 33	60	6 00	1	1 734	1 74809	-2 0975	NO
R&B&D - Disp	4 49	88,359	12 35	26	8 338	1 63542	-4 8040	NO
R&B&O - Disp	4 49	88,359			8.338			
R&B&D - Disp	9 49	461			11 518			
R&B&D - Disp	9 49	461			11 518			
ADSL to Retail	4 42	8,606	5.31	117	3 722	0 34640	-2 5693	NO
ADSL to Retail	3 74	5,483			1 377	-		
ADSL to Retail	5 61	38			3.398			
ADSL to Retail								
ADSL to Retail								
ADSL to Retail								
ISDN - BRI	14 83	369	10.64	200	13 437	1 17983	3 5513	YES
ISDN - BRI	3 69	517			7.869			
ISDN - BRI	18.00	1			0 000			
ISDN - BRI								-
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	4.42	8,606			3 722			
ADSL to Retail	3 74	5,483	5 00	5	1 377	0.61623	-2 0447	NO
ADSL to Retail	5 61	38			3 398			
ADSL to Retail								
ADSL to Retail								
ADSL to Retail								
R&B - Disp	3.87	85,794	5.24	230	5 857	0 38669	-3 5450	МО
R&B - Disp	3.87	85,794			5.857			
R&B - Disp	9 16	454	21 00	1	11 188	11 20021	-1 0572	YES
R&B - Disp	9 16	454			11.188			
R&B (POTS) excl SB Or	3 85	85,237	4 56	415	5 840	0 28735	-2 4624	NO
R&B (POTS) excl SB Or	1 82	254,155	4 00	9	2 981	0 99365	-2 1890	NO
R&B (POTS) excl SB Or	9 18	418	6.13	8	11 498	4 10402	0 7433	YES
R&B (POTS) excl SB Or	4 10	10			2 807			
R&B - Disp	3.87	85,794			5 857			
R&B - Disp	3 87	85,794			5 857			
R&B - Disp	9 16	454	7 00	1	11 188	11 20021	0 1928	YES
R&B - Disp	9 16	454			11 188			
R&B (POTS) excl SB Or	3 85	85,237			5 840			
R&B (POTS) excl SB Or	1 82	254,155			2.981			
R&B (POTS) excl SB Or	9 18	418			11 498			
R&B (POTS) excl SB Or	4 10	10			2 807			
R&B - Disp	3 87	85,794	5 54	176	5 857	0 44191	-3 7780	NO
R&B - Disp	387	85,794			5 857			
R&B - Disp	9 16	454	7 00	11	11 188	11 20021	0 1928	YES
R&B - Disp	9.16	454			11 188			
R&B (POTS) excl SB Or	3 85	85,237	5 44	204	5 840	0 40934	-3 8638	NO
R&B (POTS) excl SB Or	1.82	254,155			2 981			
R&B (POTS) exc! SB Or	9 18	418	7 00	7	11 498	4 38223	0 4964	YES
R&B (POTS) excl SB Or	4 10	10			2 807			

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	Florida. November 2001	Danah mada I	BST	DOT	01.50	01.50				
	Fiorida, November 2001	Benchmark i	- + -	BST	CLEC	CLEC	Standard	Standard		-
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B211411	P-4 Other Design/<10 circuits/Dispatch/FL(days)	Design	25.27	2,565	5 98	28	28 337	5 38428	3 5838	YES
B.2.1.14.1 2	P-4 Other Design/<10 circuits/Non-Dispatch/FL(days)	Design	9 15	484	0 30	20	13.391	3 30420	3 3030	163
B 2 1 14 2.1	P-4 Other Design/>=10 circuits/Dispatch/FL(days)	Design	30 71	7			13 684			
B 2.1 14 2 2	P-4 Other Design/>=10 circuits/Non-Dispatch/FL(days)	Design	3.24	49			3.462			
B.2.1.15 1 1	P-4 Other Non-Design/<10 circuits/Dispatch/FL(days)	R&B	3.87	85,794	2 22	6	5.857	2 39103	0 6887	YES
B 2.1.15 1.2	P-4 Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	R&B	0.91	655,604	0 73	5	2 002	0.89537	0 2034	YES
B.2.1.15 2.1	P-4 Other Non-Design/>=10 circuits/Dispatch/FL(days)	R&B	9.16	454			11 188			
B 2.1.15 2.2	P-4 Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	R&B	171	88			1 702			
B.2.1 16.1.1	P-4 INP (Standalone)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	3.85	85,237			5 840			
B.2.1.16 1.2	P-4 INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	0.91	654,116			2 000			
B 2.1.16 2 1	P-4 INP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS)	9 18	418			11 498			
B 2 1.16 2 2	P-4 INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	2 69	16			2 878			
B 2.1.17 1.1	P-4 LNP (Standalone)/<10 circuits/Dispatch/FL(days)	R&B (POTS)	3 85 0 91	85,237	0.33	3	5 840	3 37155	1 0454	YES
B.2.1.17.1.2	P-4   LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days) P-4   LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	R&B (POTS) R&B (POTS)	9 18	654,116 418	0 76	2,722	2 000 11 498	0 03841	3 8839	YES
B.2.1.17.2 1 B.2.1.17.2 2	P-4 LNP (Standalone)/>=10 circuits/Dispatch/FL(days) P-4 LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	R&B (POTS)	2 69	16	0 43	7	2 878	1 30398	1 7336	YES
B.2.1.18 1.1	P-4 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)	Digital Loop < DS1	5 36	9,471	8 74	307	5 862	0 33992	-9 9578	NO NO
B.2.1 18.1.2	P-4 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1	4.19	6,458	0.14	307	5.059	0 33992	-9 9310	
B.2.1.18 2.1	P-4 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)	Digital Loop < DS1	5 61	38	-		3 398		-	
B.2.1 18 2.2	P-4 Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop < DS1					0 555			
B.2.1.19.1.1	P-4   Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Digital Loop >= DS1	41.31	498	7 32	130	44 741	4 40655	7 7137	YES
B.2.1 19 1 2	P-4 Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	Digital Loop >= DS1	7 59	317			14 225			
B211921	P-4 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	Digital Loop >= D\$1	12.00	1			0 000			- I
B.2 1 19 2.2	P-4   Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Digital Loop >= DS1	3.24	49			3 462			
	Order Completion Interval within X days	-								
B 2.2.1	P-4 xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL(days)	14 days			21 00	1				NO
						<u>-</u>				
B.2.2.2	P-4 xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL(days)	7 days			5.18	116				YES
B.2.2.2		7 days			5.18	116				YES
	Held Orders		8.26	618	5.18	116	10.412			YES
B.2.3 1.1 1	Heid Orders P-1   Switch Ports/<10 circuits/Facility/FL(days)	R&B (POTS)	8.26	618	5.18	116	10 412			YES
B.2.3 1.1 1 B.2 3.1.1.2	Held Orders  P-1   Switch Ports/<10 circuits/Facility/FL(days)  P-1   Switch Ports/<10 circuits/Equipment/FL(days)	R&B (POTS) R&B (POTS)	0.00	0	5.18	116				YES
B.2.3 1.1 1 B.2 3.1.1.2 B 2.3.1.1.3	Heid Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days)  P-1 Switch Ports/<10 circuits/Equipment/FL(days)  P-1 Switch Ports/<10 circuits/Other/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS)	0.00 5.86		5.18	116	13 482			YES
B.2.3 1.1 1 B.2 3.1.1.2 B.2.3.1.1.3 B.2.3 1.2.1	Heid Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/=10 circuits/Facility/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.00	0 94	5.18	118				YES
B.2.3 1.1 1 B.2 3.1.1.2 B 2.3.1.1.3	Heid Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/=10 circuits/Facility/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS)	0.00 5.86 15.00	0 94 1	5.18	116	13 482			YES
B.2.3 1.1 1 B.2.3.1.1.2 B.2.3.1.1.3 B.2.3 1.2.1 B.2.3.1.2.2	Held Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.00 5.86 15.00 0.00	0 94 1 0	5.18	116	13 482			YES
B.2.3 1.1 1 B.2 3.1.1.2 B 2.3.1.1.3 B.2.3 1.2.1 B.2.3.1.2.2 B.2.3.1.2.3	Heid Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Equipment/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice DS1/ DS3 - Interoffice	0.00 5.86 15.00 0.00 0.00 0.00 0.00	0 94 1 0 0 0			13 482			
B231.11 B231.12 B231.13 B231.21 B231.22 B231.23 B23.211 B23.212 B23.211	Held Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Local interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local interoffice Transport/<10 circuits/Equipment/FL(days) P-1 Local interoffice Transport/<10 circuits/Equipment/FL(days) P-1 Local interoffice Transport/<10 circuits/Other/FL(days)	R&B (POTS) D\$1/D\$3 - Interoffice D\$1/D\$3 - Interoffice	0.00 5.86 15.00 0.00 0.00	0 94 1 0 0	0 00	0	13 482			YES
B231.11 B231.12 B231.13 B231.21 B231.22 B231.23 B232.11 B23.212 B23.213 B23.213 B23.22.13	Held Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Capipment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Other/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) DS1/ DS3 - Interoffice DS1/ DS3 - Interoffice DS1/ DS3 - Interoffice DS1/ DS3 - Interoffice	0.00 5.86 15.00 0.00 0.00 0.00 0.00	0 94 1 0 0 0	0 00	0 0	13 482 0 000			YES YES
B231.11 B231.12 B231.13 B2312.1 B231.22 B231.23 B232.21 B232.21 B232.21 B232.21 B232.21 B232.21	Heid Orders  P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Equipment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Other/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days)	R&B (POTS) D31/D53 - Interoffice D51/D53 - Interoffice D51/D53 - Interoffice D51/D53 - Interoffice D51/D53 - Interoffice	0.00 5.86 15.00 0.00 0.00 0.00 0.00	0 94 1 0 0 0	0 00	0 0	13 482 0 000			YES YES
B231.11 B23.1.12 B23.1.13 B23.1.21 B23.1.22 B23.1.23 B23.2.11 B23.2.12 B23.2.13 B23.2.13 B23.2.21 B23.2.21 B23.2.21	Held Orders	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) DS1/ DS3 - Interoffice	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60	0 94 1 0 0 0 0 0 5	0 00 0.00 0 00	0 0 0	13 482 0 000 5 771			YES YES YES
B231.11 B231.12 B231.13 B231.21 B231.21 B231.22 B23.221 B23.221 B23.221 B23.221 B23.221 B23.222 B23.223 B23.231	Held Orders	R&B (POTS) D\$1/ D\$3 - Interoffice R\$1/ D\$3 - Interoffice R\$1/ D\$3 - Interoffice R\$1/ D\$3 - Interoffice	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60	0 94 1 0 0 0 0 0 5	0 00 0.00 0 00 9 83	0 0 0	13 482 0 000	4 27054	-0 3554	YES YES YES
B231.11 B231.12 B231.13 B231.21 B231.22 B23.123 B23.211 B232.21 B232.21 B232.23 B23.223 B23.223 B23.33.11 B23.3.12	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Cher/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Facility/FL(days)	R&B (POTS) DS1/DS3 - Interoffice R&B R&B	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60	0 94 1 0 0 0 0 0 5 5	0 00 0.00 0 00 0 00 9 83 0 00	0 0 0	13 482 0 000 5 771	4 27054	-0 3554	YES YES YES YES
B231.11 B23.1.12 B23.1.13 B23.12.1 B23.12.2 B23.12.3 B23.2.11 B23.2.12 B23.2.13 B23.2.2.1 B23.2.2.1 B23.2.2.2 B23.2.3 B23.3.1.2 B23.3.1.2 B23.3.1.3	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Other/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Cher/FL(days)	R&B (POTS) DS1/DS3 - Interoffice R&B R&B R&B R&B	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60	0 94 1 0 0 0 0 0 5 5	0 00 0.00 0 00 0 00 9 83 0 00 0.00	0 0 0 0	13 482 0 000 5 771 10 411 13 482	4 27054	-0.3554	YES YES YES YES YES YES YES YES
B231.11 B23.1.12 B23.1.13 B23.1.21 B23.1.22 B23.1.23 B23.2.11 B23.2.12 B23.2.13 B23.2.21 B23.2.21 B23.2.23 B23.3.11 B23.3.1.2 B23.3.1.2 B23.3.1.3 B23.3.2.1	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/>=10 circuits/PL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Equipment/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Equipment/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Cother/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Cother/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Equipment/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Equipment/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS) DS1/ DS3 - Interoffice DS1/ BS3 - RAB R&B R&B R&B R&B	0.00 5.86 15.00 0.00 0.00 0.00 10.60 8.32 0.00 8.32 0.00 5.86 12.00	0 94 1 0 0 0 0 0 5 5	9 83 0 00 0 00 0 00	0 0 0 0	13 482 0 000 5 771	4 27054	-O 3554	YES
B231.11 B231.12 B231.13 B231.21 B231.22 B23.123 B23.211 B23.212 B23.213 B23.221 B23.223 B23.223 B23.323 B23.321 B23.321 B23.321 B23.3.12 B23.3.13 B23.3.21	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Equipment/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Equipment/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Other/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 10.60 8 32 0.00 5 86 12.00 0.00	0 94 1 0 0 0 0 5 5	9 83 0 00 0.00 0 00	0 0 0 0	13 482 0 000 5 771 10 411 13 482	4 27054	-0 3554	YES YES YES YES YES YES YES YES
B231.11 B23.1.12 B23.1.13 B23.12.1 B23.12.2 B23.1.23 B23.2.21 B23.2.12 B23.2.13 B23.2.2.1 B23.2.2.1 B23.2.2.2 B23.3.3.1 B23.3.1.2 B23.3.1.2 B23.3.1.2 B23.3.2.2 B23.3.2.2	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Chter/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Equipment/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Chter/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Other/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cother/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8.32 0.00 5.86 12.00 0.00 0.00	0 94 1 0 0 0 0 0 5 5 624 0 94 3 0	9 83 0 00 0 00 0 00 0 00 0 00 0 00 0 00 0	0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083	4 27054	-0 3554	YES
B231.11 B23.1.12 B23.1.13 B23.1.21 B23.1.22 B23.1.23 B23.2.11 B23.2.21 B23.2.13 B23.2.21 B23.2.23 B23.3.21 B23.3.21 B23.3.11 B23.3.21 B23.3.11 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Deprent/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Pacility/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Cher/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Pacility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Cher/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Cher/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Dther/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Dther/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Dther/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cher/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cher/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cher/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Other/FL(days)     P-1   Loop + Port Combinations/=10 circuits/Other/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.0	0 94 1 0 0 0 0 5 5 624 0 94 3 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	6 0 0 0 0	13 482 0 000 5 771 10 411 13 482	4 27054	-0 3554	YES
B231.11 B231.12 B231.13 B231.21 B231.22 B231.23 B23.211 B23.212 B23.213 B23.221 B23.221 B23.222 B23.323 B23.311 B23.3.12 B23.3.13 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Cother/FL(days) P-1 Switch Ports/<10 circuits/Cother/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local interoffice Transport/<10 circuits/Dter/FL(days) P-1 Local interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/>=10 circuits/Equipment/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Other/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8.32 0.00 5.86 12.00 0.00 0.00	0 94 1 0 0 0 0 0 5 5 624 0 94 3 0	9 83 0 00 0 00 0 00 0 00 0 00 0 00 0 00 0	0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083	4 27054	-0 3554	YES
B231.11 B23.1.12 B23.1.13 B23.1.21 B23.1.22 B23.1.23 B23.2.11 B23.2.21 B23.2.13 B23.2.21 B23.2.23 B23.3.21 B23.3.21 B23.3.11 B23.3.21 B23.3.11 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Equipment/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Equipment/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Local interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local interoffice Transport/<10 circuits/Cuipment/FL(days) P-1 Local interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local interoffice Transport/>=10 circuits/Equipment/FL(days) P-1 Local interoffice Transport/>=10 circuits/Equipment/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Other/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Other/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Chepriment/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8.32 0.00 5.86 12.00 0.0	0 94 1 0 0 0 0 5 5 5 624 0 94 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	6 0 0 0 0 0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083	4 27054	-0 3554	YES
B231.11 B231.12 B231.13 B231.21 B23.121 B23.122 B23.123 B23.211 B23.212 B23.213 B23.221 B23.221 B23.223 B23.321 B23.3.12 B23.3.12 B23.3.12 B23.3.12 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.21	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Equipment/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Equipment/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Other/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Facility/FL(days)     P-1   Local Port Combinations/<10 circuits/Equipment/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cuts/Facility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Cuts/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Facility/FL(days)     P-1   Combo Other/<10 circuits/Other/FL(days)	R&B (POTS) DS1/DS3 - Interoffice R&B	0.00 5.86 15.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.0	0 94 1 0 0 0 0 0 5 5 624 0 94 3 0 0 0 94 3 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	6 0 0 0 0 0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083 10.422 13 275	4 27054	-0 3554	YES
B231.11 B231.12 B23.1.13 B23.12.1 B23.12.2 B23.12.3 B23.2.12 B23.2.13 B23.2.12 B23.2.2.1 B23.2.2.2 B23.2.3 B23.3.1.3 B23.3.1.2 B23.3.1.3 B23.3.2.1 B23.4.1.1 B23.4.1.2 B23.4.1.3 B23.4.2.1	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/<10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Facility/FL(days) P-1 Switch Ports/>=10 circuits/Gacility/FL(days) P-1 Switch Ports/>=10 circuits/Gacility/FL(days) P-1 Switch Ports/>=10 circuits/Gacility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/>=10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	8 32 0.00 5.86 15.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.0	0 94 1 0 0 0 0 0 5 5 624 0 94 3 0 0 624 0 0 0 94 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	6 0 0 0 0 0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083 10.422 13 275 6 083	4 27054	-0 3554	YES
B231.11 B23.1.12 B23.1.13 B23.1.21 B23.1.22 B23.1.23 B23.2.11 B23.2.21 B23.2.13 B23.2.21 B23.2.21 B23.2.21 B23.2.21 B23.3.21 B23.3.11 B23.3.12 B23.3.13 B23.3.21	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Chapment/FL(days) P-1 Switch Ports/>10 circuits/Chapment/FL(days) P-1 Switch Ports/>=10 circuits/Cher/FL(days) P-1 Switch Ports/>=10 circuits/Cher/FL(days) P-1 Switch Ports/>=10 circuits/Cher/FL(days) P-1 Switch Ports/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/<10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/<10 circuits/Equipment/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/>=10 circuits/Facility/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	8 32 0.00 5.86 15.00 0.00 0.00 0.00 10.60 8 32 0.00 5 86 12 00 0 00 8 35 0 00 8 35 0 00 3 33 3 4	0 94 1 0 0 0 0 0 5 5 624 0 0 624 0 0 624 0 0 3 0 0 3 0 9 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	6 0 0 0 0 0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083 10.422 13 275	4 27054	-0.3554	YES
B231.11 B231.12 B231.13 B231.21 B231.22 B23.123 B23.211 B23.212 B23.213 B23.221 B23.223 B23.233 B23.221 B23.223 B23.323 B23.323 B23.321 B23.3.12 B23.3.13 B23.3.21 B23.3.22 B23.3.13 B23.3.21 B23.3.21 B23.3.21 B23.3.21 B23.3.22 B23.3.23 B23.3.21 B23.3.22 B23.3.23 B23.3.21 B23.3.22 B23.3.23 B23.3.21	P-1   Switch Ports/<10 circuits/Facility/FL(days)     P-1   Switch Ports/<10 circuits/Equipment/FL(days)     P-1   Switch Ports/<10 circuits/Other/FL(days)     P-1   Switch Ports/>=10 circuits/Facility/FL(days)     P-1   Switch Ports/>=10 circuits/Gacility/FL(days)     P-1   Switch Ports/>=10 circuits/Gacility/FL(days)     P-1   Switch Ports/>=10 circuits/Gacility/FL(days)     P-1   Switch Ports/>=10 circuits/Gacility/FL(days)     P-1   Local Interoffice Transport/<10 circuits/Facility/FL(days)     P-1   Local Interoffice Transport/>=10 circuits/Gacility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Gacility/FL(days)     P-1   Loop + Port Combinations/<10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Facility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Gacility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Gacility/FL(days)     P-1   Loop + Port Combinations/>=10 circuits/Gacility/FL(days)     P-1   Combo Other/<10 circuits/Facility/FL(days)     P-1   Combo Other/>=10 circuits/Cacility/FL(days)     P-1   Combo Other/>=10	R&B (POTS) DS1/ DS3 - Interoffice R&B	8 32 0.00	0 94 1 0 0 0 0 5 5 624 0 94 3 0 0 0 624 0 0 0 3 0 0 7 7 7 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	0 0 0 0 0 0 0 0 0 0 0 0	13 482 0 0000 5 771 10 411 13 482 6.083 10.422 13 275 6 083	4 27054	-0 3554	YES
B231.11 B231.12 B23.1.13 B23.121 B23.122 B23.123 B23.211 B23.212 B23.213 B23.221 B23.222 B23.233 B23.311 B23.3.12 B23.3.13 B23.3.21 B23.3.22 B23.3.13 B23.3.21 B23.3.21 B23.3.22 B23.3.23 B23.4.11 B23.4.12 B23.4.21 B23.4.21 B23.4.21 B23.4.23 B23.4.21 B23.4.23	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Cother/FL(days) P-1 Switch Ports/>10 circuits/Other/FL(days) P-1 Switch Ports/>10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/>=10 circuits/Cher/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	8 32 0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.00 0.00 8 35 12.00 0.00 0.00 3.34 0.00 0.	0 94 1 1 0 0 0 0 0 0 5 5 5 624 0 94 3 0 0 0 94 3 0 0 0 94 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	0 0 0 0 0 0 0 0 0 0 0	13 482 0 000 5 771 10 411 13 482 6.083 10.422 13 275 6 083	4 27054	-0 3554	YES
B231.11 B231.12 B231.13 B231.21 B23.122 B23.123 B23.211 B23.212 B23.213 B23.221 B23.222 B23.223 B23.321 B23.321 B23.322 B23.33.12 B23.3.12 B23.3.22 B23.3.13 B23.3.21 B23.3.22 B23.3.23 B23.3.11 B23.3.22 B23.3.23 B23.4.11 B23.4.21 B23.4.21 B23.4.21 B23.4.21 B23.4.23 B23.4.21 B23.4.23 B23.5.11 B23.5.12 B23.5.13 B23.5.11 B23.5.13 B23.5.21	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Coupment/FL(days) P-1 Switch Ports/>10 circuits/Coupment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Equipment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Coupment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Other/FL(days) P-1 Local Interoffice Transport/>10 circuits/Other/FL(days) P-1 Local Interoffice Transport/>10 circuits/Coupment/FL(days) P-1 Local Interoffice Transport/>10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/>10 circuits/Facility/FL(days) P-1 Local Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Cother/FL(days) P-1 Loop + Port Combinations/>10 circuits/Cother/FL(days) P-1 Loop + Port Combinations/>10 circuits/Cother/FL(days) P-1 Loop + Port Combinations/>10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/>10 circuits/Facility/FL(days) P-1 Combo Other/>10 circuits/Facility/FL(days) P-1 Combo Other/>10 circuits/Other/FL(days) P-1 C	R&B (POTS)	8 32 0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.00 0.00 0.00 3.34 0.00 0.0	0 94 1 0 0 0 0 0 5 5 624 0 0 624 0 0 0 624 0 0 0 3 0 9 7 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	0 0 0 0 0 0 0 0 0 0 0 0	13 482 0 0000 5 771 10 411 13 482 6.083 10.422 13 275 6 083	4 27054	-0 3554	YES
B231.11 B231.12 B231.13 B231.21 B231.22 B231.23 B23.211 B23.212 B23.213 B23.221 B23.223 B23.223 B23.323 B23.321 B23.3.12 B23.3.12 B23.3.13 B23.3.22 B23.3.13 B23.3.22 B23.3.13 B23.3.22 B23.3.23 B23.3.21 B23.3.22 B23.3.23 B23.3.21 B23.4.21 B23.4.21 B23.4.21 B23.4.23 B23.4.21 B23.4.23 B23.5.11 B23.5.12 B23.5.13	P-1 Switch Ports/<10 circuits/Facility/FL(days) P-1 Switch Ports/<10 circuits/Cother/FL(days) P-1 Switch Ports/>10 circuits/Other/FL(days) P-1 Switch Ports/>10 circuits/Other/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Switch Ports/>=10 circuits/Gupment/FL(days) P-1 Local Interoffice Transport/<10 circuits/Facility/FL(days) P-1 Local Interoffice Transport/<10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Cher/FL(days) P-1 Local Interoffice Transport/>=10 circuits/Facility/FL(days) P-1 Local Port Combinations/<10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/<10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Facility/FL(days) P-1 Loop + Port Combinations/>=10 circuits/Cher/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/<10 circuits/Facility/FL(days) P-1 Combo Other/>=10 circuits/Cher/FL(days)	R&B (POTS) DS1/ DS3 - Interoffice R&B	8 32 0.00 5.86 15.00 0.00 0.00 0.00 0.00 10.60 8 32 0.00 5.86 12.00 0.00 0.00 8 35 12.00 0.00 0.00 3.34 0.00 0.	0 94 1 1 0 0 0 0 0 0 5 5 5 624 0 94 3 0 0 0 94 3 0 0 0 94 3 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	9 83 0 00 0.00 0 00 0 00 0 00 0 00 0 00 0 0	0 0 0 0 0 0 0 0 0 0 0 0	13 482 0 0000 5 771 10 411 13 482 6.083 10.422 13 275 6 083	4 27054	-0.3554	YES

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YES

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LINE ISDN/<10 circuits/Facility/FL(days) B23611 B23612 UNE ISDN/<10 circuits/Equipment/FL(days) B23613 P-1 UNE ISDN/<10 circuits/Other/FL(days) B.2.3.6.2.1 UNE ISON/>=10 circuits/Facility/FL(days) B23622 UNE ISDN/>=10 circuits/Equipment/FL(days) B23623 UNE ISDN/>=10 circuits/Other/FL(days) B23711 Line Sharing/<10 circuits/Facility/FL(days B23712 Line Sharing/<10 circuits/Equipment/FL(days) B 2 3 7 1.3 Line Shanng/<10 circuits/Other/FL(days) B.2.3721 Line Sharing/>=10 circuits/Facility/FL(days) B.2.3.7 2 2 Line Sharing/>=10 circuits/Equipment/FL(days) B23723 P-1 Line Sharing/>=10 circuits/Other/FL(days) B23811 2W Analog Loop Design/<10 circuits/Facility/FL(days) B 2 3 8.1 2 2W Analog Loop Design/<10 circuits/Equipment/FL(days) B.2.3.8 1 3 2W Analog Loop Design/<10 circuits/Other/FL(days) B.2.3.8 2 1 2W Analog Loop Design/>=10 circuits/Facility/FL/days B.2.3 8.2.2 2W Analog Loop Design/>=10 circuits/Equipment/FL(days) B.2.3.8.2.3 2W Analog Loop Design/>=10 circuits/Other/FL(days) B.2.3.9.1.1 2W Analog Loop Non-Design/<10 circuits/Facility/FL(days) B.2.3.9.1.2 2W Analog Loop Non-Design/<10 circuits/Equipment/FL(days) B23913 2W Analog Loop Non-Design/<10 circuits/Other/FL(days) B.2.3.9.2 1 2W Analog Loop Non-Design/>=10 circuits/Facility/FL(days) B.2.3.9 2.2 2W Analog Loop Non-Design/>=10 circuits/Equipment/FL(days) B.2.3.9.2.3 2W Analog Loop Non-Design/>=10 circuits/Other/FL(days) B.2.3.10.1.1 2W Analog Loop w/INP Design/<10 circuits/Facility/FL(days) B 2 3.10.1 2 2W Analog Loop w/INP Design/<10 circuits/Equipment/FL(days) 2W Analog Loop w/INP Design/<10 circuits/Other/FL(days) B.2.3.10.1.3 2W Analog Loop w/INP Design/>=10 circuits/Facility/FL(days B.2.3 10.2 1 B.2.3.10.2.2 2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL(days) B.2.3.10.2.3 2W Analog Loop w/INP Design/>=10 circuits/Other/FL(days) B 2.3.11.1.1 2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL(days) B.2.3.11.12 W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL(days) B 2.3 11.1.3 2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL(days) B231121 2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL(days) B.2.3 11 2 2 2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL(days) B.2 3.11.2 3 2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL(days) B.2.3.12 1.1 2W Analog Loop w/LNP Design/<10 circuits/Facility/FL(days) B.2 3 12.1 2 2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL(days 2W Analog Loop w/LNP Design/<10 circuits/Other/FL(days) B.2.3.12.1.3 B.2.3.12.2.1 2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL(days B.2.3.12 2.2 2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL(days B.2 3.12.2.3 2W Analog Loop w/LNP Design/>=10 circuits/Other/FL(days) B.2.3.13.11 2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL(days B 2.3 13 1 2 2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL(days) B.2.3.13.1.3 2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL(days) B.2.3.13.2 1 2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL(days) B.2.3.13.2 2 2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL(days) B.2.3.13.23 2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL(days) B.2.3.14.1.1 Other Design/<10 circuits/Facility/FL(days) B.2.3.14.1 2 Other Design/<10 circuits/Equipment/FL(days B.2.3.14.1.3 Other Design/<10 circuits/Other/FL(days) B.2.3.14.2.1 Other Design/>=10 circuits/Facility/FL(days) B 2.3.14 2 2 Other Design/>=10 circuits/Equipment/FL(days) B 2 3 14.2 3 Other Design/>=10 circuits/Other/FL(days) B.2.3.15.1.1 Other Non-Design/<10 circuits/Facility/FL(days B.2 3 15.1 2 Other Non-Design/<10 circuits/Equipment/FL(days) B.2 3 15 1.3 Other Non-Design/<10 circuits/Other/FL(days) B 2 3 15.2 1 Other Non-Design/>=10 circuits/Facility/FL(days B.2.3.15 2.2 Other Non-Design/>=10 circuits/Equipment/FL(days)

Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
ISDN - BRI	0.00	0	13 60	5	T	· · · · · · · · · · · · · · · · · · ·		NO
ISDN - BRI	0 00	. 0	0 00	0				YES
ISDN - BRI	0.00	0	5 00	2				NO
ISDN - BRI								
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	31 34	348	0 00	0	47 466			YES
ADSL to Retail	0.00	0	0 00	0				YES
ADSL to Retail	2 26	43	0 00	0	2 361			YES
ADSL to Retail	0.00	0	T					
ADSL to Retail	0.00	0						
ADSL to Retail	0 00	0						
R&B - Disp	8 32	624	2 50	2	10 416	7 37702	0 7884	YES
R&B - Disp	0.00	0	0.00	0				YES
R&B - Disp	5 86	94	0.00	0	13 545			YES
R&B - Disp	12.00	3	0 00	0	6 083			YES
R&B - Disp	0.00	Ö	0.00	0	<del> </del>			YES
R&B - Disp	0.00	0	0 00	0	1			YES
R&B (POTS) excl SB Or	8.26	618	8 00	5	10 412	4 67514	0 0557	YES
R&B (POTS) excl SB Or	0.00	0	0.00	0	1		- ¥ ¥ 0 0 ·	YES
R&B (POTS) excl SB Or	5 86	94	0.00	0	13 482			YES
R&B (POTS) excl SB Or	15.00	1	0.00	0	0 000			YES
R&B (POTS) excl SB Or	0 00	Ö	0 00	ŏ	1 000			YES
R&B (POTS) excl SB Or	0 00	0	0 00	Ö				YES
R&B - Disp	8.32	624	0.00	Ö	10 416			YES
R&B - Disp	0.00	0	0.00	0	104.0		~~	YES
R&B - Disp	5.86	94	0.00	ŏ	13 545			YES
R&B - Disp	12.00	3	0 00	0	6 083			YES
R&B - Disp	0 00	0	0 00	0	1 000			YES
R&B - Disp	0 00	Ö	0.00	- v	<del> </del>			YES
R&B (POTS) excl SB Or	8.26	618	0.00	0	10 412			YES
R&B (POTS) excl SB Or	0.00	0	0 00	ő	10.112			YES
R&B (POTS) excl SB Or	5.86	94	0.00	ŏ	13 482			YES
R&B (POTS) excl SB Or	15.00	1	<del>   </del>	<u>-</u>	0 000			-,20
R&B (POTS) excl SB Or	0 00	ö	!		0 000			
R&B (POTS) excl SB Or	0.00	ő	<del> </del>					
R&B - Disp	8.32	624	15 50	2	10 416	7 37702	-0 9739	YES
R&B - Disp	0.00	0	0 00	0	10 110	701702	-0 3103	YES
R&B - Disp	5.86	94	0 00	0	13 545			YES
R&B - Disp	12 00	3	0 00	0	6 083			YES
R&B - Disp	0.00	0	0 00	0	0 000	·		YES
R&B - Disp	0 00	Ö	0 00	0				YES
R&B (POTS) excl SB Or	8 26	618	10 00	1	10.412	10 42032	-0 1669	YES
R&B (POTS) excl SB Or	0 00	0	0 00	Ö	10.712	10 42002	1005	YES
R&B (POTS) excl SB Or	5 86	94	0 00	0	13 482			YES
R&B (POTS) excl SB Or	15 00	1	0 00	0	0 000			YES
R&B (POTS) excl SB Or	0.00	0	0 00	0	0 000			YES
R&B (POTS) excl SB Or	0.00	0	0.00	Ö				YES
Design	22 00	1	0.00	0	0 000			YES
Design	0 00	0	0 00	0	0 000			YES
Design	500	4	0 00	0	3,464		· · · · · · · · · · · · · · · · · · ·	
Design	0 00	0	000		3.404			YES
Design Design	0.00	0	<del>                                     </del>		<del> </del>			
Design Design	0.00	0	<b>  </b>		<del>  </del>			
			000		10 444			
R&B R&B	8 32	624	0 00	0	10 411			YES
R&B	0.00	0	0 00	0	12 402			YES
R&B	5 86	94	0 00	0	13 482			YES
NØD DØD	12 00	3	0 00	0	6 083			YES

0 00

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R&B

0.00

Autor   Control   Contro		Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
Baz   Port   Post   P		•	Analog	Measure	Volume	Measure	Volume	Deviation	Error	<b>ZScore</b>	Equity
Baz   Port   Post   P	B B B 45 B B						······	1			
B2.216.12   P.1   WP (Guntariory) (Total) (T						0.00	0	40.440			YES
Base   Ports								10412			
B2.3   Fig.   The   Standardon's 18 principle (Compt.)   Standards   Standar								12 492			
B2.21   P.2.1   BPF   Semestrone) = 10 aroute Segment (Fig. 14th)											
B2.31   F.1   RMP (Samedonial) = 10 contact (Chemff Lidger)   R84 (POTS)   A.50   618   0.00   0   10.412   VES   R84 (POTS)   A.50   618   0.00   0   10.412   VES   R84 (POTS)   A.50   618   0.00   0   10.412   VES   R84 (POTS)   A.50								0 000			
B2.317.11   F.1   LPF (Standburg*)   December (1997)   December						-					
B23 F17   2						0.00	0	10 412			VEC
B23 F1   S   F1   NP (Standarden)**10 grouts/Denniff-(day)								10 4 12			
Page								12 492			
Pack											
## RAS (POTS)   Do 0   D   D   D   D   D   D   D   D   D								0.000			
B 23 8 1 1 1 1 1 1 1											
Part   Digital Loop								47 207	21 20757	0.0264	
P.1   Digital Loop C DS1+TO excutation Perf (Light)								4/ 20/	2129131	0 0204	
B 2.2.18 2.2 P-1 Opital Loop < DS17+10 Control Equator Facility* (Loop > DS1 - DS)								0.640	6.06605	0.1079	
9.2.3.18.2.2   P.1   Digital Loop < DS16+10 GroutsEquipment(Fl(days)						<del>- 300  </del>		9 040	0 90003	-0 1970	!53
P.1   Digital Loog > DSI +						<del></del>			_		
Part								<del> </del>			
Part   Part   Part   Depted   Loop >= DS1/1-10 creatishSequement/E(days)   Depted   Loop >= DS1/1-10 creatishSequement/E(days)   Depted   Depted						5.00	1	<del> </del>			NO.
B2.3.19.1.3   P.1   Digital Loop >= DSIA*10 cross/bCPma*FL(days)   DSIA*10 cr								·			
P.1   Digital Loop >= DS10=10 corcusts=GalleyTL(days)								<del> </del>			
B2.5.1   B2.5   B7.1   Digital Loop >= B51   Digital Loop >= D51   Digital Loop >= D51						0.00		<u> </u>			- 123
Biglial Loop == DSI   Digital Loop == DSI								<del> </del>			
### R& (POTS)   D.S.4   P.2   Switch PortsFL(%)   Switch PortsFL(%)   D.S.4   Si0,769											
RAB (POTS)   P.2.   Switch PortsFf(%)   P.2.   Loop in Port CombinationsFf(%)   DSF)   DSF)	D.2.G. 15.2.G		Digital Edop 1 Do 1			<u> </u>					
P.2   Local Interoffice TransportFi(%)   P.2   Combo Other Fi(%)   P.2   Local Interoffice TransportFi(%)   Diagnostic   P.2   Local Interoffice TransportFi(%)   Diagnostic   Di			DAG (DOTO)	0.540/	040 700					<del></del>	
B2.5.3         P.2         Loop + Port Combinations/FL(%)         RAB         0.54%         813,091         0.13%         7,674         0.00644         4903         YES           B2.5.5         P.2         ADSI (ADSI, HDSL and UCL/FL(%)         ADSI (ADSI, HDSL and UCL/FL(%)         10.00664         15.79%         20.838         2.91%         10.3         0.03601         35743         YES           B2.5.6         P.2         UNE SIDMFL(%)         BSINL PRIB         15.79%         20.838         2.91%         10.0         0.06641         35743         YES           B2.5.5         P.2         UNE SharingFL(%)         BSINL PRIB         15.79%         20.836         0.00%         5         0.14887         10003         YES           B2.5.9         P.2         ZW Analog Loop WhIP DesnynFL(%)         R8B-Bosp         9.5%         813,091         10.43%         23.3         0.00485         20.9330         NO           B2.5.11         P.2         ZW Analog Loop WhIP DesnynFL(%)         R8B-Bosp         9.5%         813,091         10.43%         23.3         0.00475         20.9330         NO           B2.5.12         P.2         ZW Analog Loop WhIP Non-Design/FL(%)         R8B-Dosp         9.5%         813,091         6.96         0.004 <td></td>											
P.   Combo Other/EL(%)						0.120	7.674		0.00084	4.0063	VEC
B2.5.5   P.2   INDS. (ADSL, HDSL and UCL)FL(%)   ADSL to Retail   15.79%   20.836   2.91%   103   0.03801   3.5743   YES     B2.5.6   P.2   Unite SharringFL(%)   ADSL to Retail   15.79%   20.836   0.00%   25   0.05747   YES     B2.5.7   P.2   Unite SharringFL(%)   ADSL to Retail   15.79%   20.836   0.00%   6   0.14887   1.0603   YES     B2.5.8   P.2   ZW Analog Loop DesignFL(%)   R8B - Dep   0.54%   813.091   1.043%   2230   0.00485   20.3830   NO     B2.5.9   P.2   ZW Analog Loop NumP DesignFL(%)   R8B - Dep   0.54%   813.091   1.043%   2230   0.00485   20.3830   NO     B2.5.11   P.2   ZW Analog Loop will PostingFL(%)   R8B - Dep   0.54%   813.091   1.03%   410.041   3.39%   1.77   0.00770   3.0283   NO     B2.5.11   P.2   ZW Analog Loop will PostingFL(%)   R8B - Dep   0.54%   813.091   1.05%   410.041   3.39%   1.77   0.00770   3.0283   NO     B2.5.12   P.2   ZW Analog Loop will PostingFL(%)   R8B - Dep   0.54%   813.091   5.04%   476   0.00337   13.3326   NO     B2.5.13   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.54%   813.091   5.04%   476   0.00337   13.3326   NO     B2.5.14   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.54%   813.091   5.04%   476   0.00337   13.3326   NO     B2.5.15   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.54%   813.091   5.04%   476   0.00337   13.3326   NO     B2.5.16   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.54%   813.091   5.04%   476   0.00337   13.3326   NO     B2.5.15   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.004   4.365   NO     B2.5.16   P.2   Dept Loop - DesignFL(%)   R8B - Desp   0.004   4.365   NO     B2.5.17   P.2   ZW Analog Loop will PostingFL(%)   R8B - Desp   0.004   2.780   0.00139   3.632   YES     B2.5.19   P.2   Deptal Loop > DEPTE											
B2.5.6   P.2 UNE SCNNFU[%]   SON - BRI   8.8 % 996   0.00%   25   0.05747   1.5374   YES											
ADSL to Retail   R2.5.5   P.2   Une SharmyFL(%)   ADSL to Retail   R.8.6 - Day   D											
RAB - Dep   P.2   2W Analog Loop Design/FL(%)   RAB - Dep   P.2   2W Analog Loop Non-Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   RAB - Dep   P.2   2W Analog Loop NiNP Design/FL(%)   P.2   2W Analog Loop NiNP Design/FL(%)   P.2   2W Analog Loop NiNP Design/FL(%)   P.2   2W Analog Loop NiNP NiNP Design/FL(%)   P.2   2W Analog Loop NiNP NiNP Design/FL(%)   P.2   2W Analog Loop NiNP NiNP NiNP NiNP NiNP NiNP NiNP NiN											
B 2 5.9         P.2         2W Analog Loop Non-Desgn/FL(%)         R8B (POTS) excl SB Or R8B . Disp         410,041         3.39%         177         0.00770         3.0283         NO           B 2.5.11         P.2         2W Analog Loop wiNP Design/FL(%)         R8B - Disp         0.54%         813,091         .											
P.2   ZW Analog Loop wilnP DesignFL(%)   R&B - Disp   R											
Rab (POTS) excl SB 0r						3 38 %			0 00770	-3 0203	
R&B - Disp   R&											
R.2.   13   P.2   2W Analog Loop wtNP Non-Design/FL(%)   R8B (POTS) excl SB Or Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 06%   410,041   11 111%   396   0 00515   19 5308   NO Design   1 00516   NO Design						5.04%	476		0.00337	12 2226	NO
B.2.5.14   P.2   Other Design/FL(%)   P.2   Other Non-Design/FL(%)   P.2   Other Non-Design/FL(%)   R&B   O.54%   B13,091   6.25%   32   O.01301   4.3865   NO     B.2.5.16   P.2   INP (Standalone)/FL(%)   R&B (POTS)   O.54%   B10,769   O.00%   2.780   O.03270   3.9632   YES     B.2.5.18   P.2   Digital Loop > DS1/FL(%)   Diagnostic   Diagnostic								100000			
R&B   P.2   Other Non-Design/FL(%)   R&B   O.54%   813,091   6.25%   32   0.01301   4.3865   NO     B.2.5.16   P.2   INP (Standalone)/FL(%)   R&B (POTS)   0.54%   810,769   0.00%   2,780   0.00139   3.632   YES     B.2.5.18   P.2   Digital Loop > DS1/FL(%)   Digital Loop < DS1/FL(%)   Digital Loop > Dig											
R&B (POTS)   R&B											
R&B (POTS)   P-2   LNP (Standalone)/FL(%)   R&B (POTS)   Digital Loop < DS1/FL(%)   Digital Loop < DS1/FL(%)   Digital Loop >= DS1   15,33%   22,943   2,48%   122   0,03270   3,9354   YES						0.2370	<u></u>	10.000.01	0.01301	- 3000	
B 2.5.18   P-2   Digital Loop > DS1/FL(%)   Digital Loop > DS1/FL(%)   Digital Loop > DS1   Digital Loop \ Digi						0.00%	2 780	200000	0.00130	3.8632	VES
B2.5 19   P-2   Digital Loop >= DS1   FL(%)   Digital Loop >= DS1   6 34%   1.562   59 17%   120   0.02308   -22 8891   NO											
X Jeopardles - Non-Mechanized           B 2.6.1         P-2         Switch Ports/FL(%)         Diagnostic         Diagnostic           B 2.6.2         P-2         Local Interoffice Transport/FL(%)         Diagnostic         0.00%         11         Diagnostic           B 2.6.3         P-2         Loop + Port Combinations/FL(%)         Diagnostic         0.32%         313         Diagnostic           B 2.6.4         P-2         Combo Other/FL(%)         Diagnostic         28 26%         46         Diagnostic           B 2.6.5         P-2         xDSL (ADSL, HDSL and UCL)/FL(%)         Diagnostic         11 40%         114         Diagnostic           B 2.6.6         P-2         UINE ISDN/FL(%)         Diagnostic         13 03%         284         Diagnostic           B 2.6.7         P-2         Line Sharing/FL(%)         Diagnostic         Diagnostic         Diagnostic           B 2.6.8         P-2         ZW Analog Loop Design/FL(%)         Diagnostic         0 00%         38         Diagnostic           B 2.6.9         P-2         ZW Analog Loop Non-Design/FL(%)         Diagnostic         5 88%         102         Diagnostic           B 2.6.10         P-2         ZW Analog Loop wiNP Design/FL(%)         Diagnostic         33 33%									0.02308		
B 2.6.1   P-2   Switch Ports/FL(%)   Diagnostic   Diagn	D.Z.3 19		Digital Coop = 501						5.52.555		
B 2.6.2         P-2         Local Interoffice Transport/FL(%)         Diagnostic           B 2.6.3         P-2         Loop + Port Combinations/FL(%)         Diagnostic           B 2.6.4         P-2         Combo Other/FL(%)         Diagnostic           B 2.6.5         P-2         LOSL (ADSL, HDSL and UCL)/FL(%)         Diagnostic           B 2.6.6         P-2         UNE ISDN/FL(%)         Diagnostic           B 2.6.7         P-2         Line Shanng/FL(%)         Diagnostic           B 2.6.8         P-2         ZW Analog Loop Design/FL(%)         Diagnostic           B 2.6.9         P-2         ZW Analog Loop Design/FL(%)         Diagnostic           B 2.6.10         P-2         ZW Analog Loop Non-Design/FL(%)         Diagnostic           B 2.6.11         P-2         ZW Analog Loop w/lNP Non-Design/FL(%)         Diagnostic           B 2.6.11         P-2         ZW Analog Loop w/lNP Non-Design/FL(%)         Diagnostic											
B.2 6.3   P.2   Loop + Port Combinations/FL(%)   Diagnostic   Diagno						0.000/					
B.2 6 4         P-2         Combo Other/FL(%)         Diagnostic         28 26%         46         Diagnostic           B.2.6.5         P-2         xDSL (ADSL, HDSL and UCL)/FL(%)         Diagnostic         11 40%         114         Diagnostic           B.2.6.6         P-2         LVNE ISDN/FL(%)         Diagnostic         13 03%         284         Diagnostic           B.2.6.7         P-2         Line Shanng/FL(%)         Diagnostic         28 26%         10 00%         38         Diagnostic           B.2.6.8         P-2         2W Analog Loop Design/FL(%)         Diagnostic         0.00%         38         Diagnostic           B.2.6.9         P-2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5.88%         102         Diagnostic           B.2.6.10         P-2         2W Analog Loop w/lNP Design/FL(%)         Diagnostic         33 33%         3         Diagnostic           B.2.6.11         P-2         2W Analog Loop w/lNP Non-Design/FL(%)         Diagnostic         0.00%         1         Diagnostic											
B.2.6.5         P-2         xDSL (ADSL, HDSL and UCL)/FL(%)         Diagnostic         11 40%         114         Diagnostic           B.2.6.6         P-2         UNE ISDN/FL(%)         Diagnostic         13 03%         284         Diagnostic           B.2.6.7         P-2         Line Sharing/FL(%)         Diagnostic         Diagnostic           B.2.6.8         P-2         2W Analog Loop Design/FL(%)         Diagnostic         0.00%         38         Diagnostic           B.2.6.9         P-2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5.88%         102         Diagnostic           B.2.6.10         P-2         2W Analog Loop w/INP Design/FL(%)         Diagnostic         33 33%         3         Diagnostic           B.2.6.11         P-2         2W Analog Loop w/INP Non-Design/FL(%)         Diagnostic         0.00%         1         Diagnostic											
B.2.6.6         P-2         UNE ISDN/FL(%)         Diagnostic         13 03%         284         Diagnostic           B.2.6.7         P-2         Line Sharing/FL(%)         Diagnostic         Diagnostic           B.2.6.8         P-2         2W Analog Loop Design/FL(%)         Diagnostic         0.00%         38         Diagnostic           B.2.6.9         P-2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5.88%         102         Diagnostic           B.2.6.10         P-2         2W Analog Loop w/NP Design/FL(%)         Diagnostic         33 33%         3         Diagnostic           B.2.6.11         P-2         2W Analog Loop w/NP Non-Design/FL(%)         Diagnostic         0.00%         1         Diagnostic											
B.2.6.7         P.2         Line Shanng/FL(%)         Diagnostic         Diagnostic           B.2.6.8         P.2         2W Analog Loop Design/FL(%)         Diagnostic         0.00%         38         Diagnostic           B.2.6.9         P.2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5.88%         102         Diagnostic           B.2.6.10         P.2         2W Analog Loop w/INP Design/FL(%)         Diagnostic         33.33%         3         Diagnostic           B.2.6.11         P.2         2W Analog Loop w/INP Non-Design/FL(%)         Diagnostic         0.00%         1         Diagnostic											
B 2.6 8         P-2         2W Analog Loop Design/FL(%)         Diagnostic         0.00%         38         Diagnostic           B 2.6 9         P-2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5.88%         102         Diagnostic           B 2.6 10         P-2         2W Analog Loop w/INP Design/FL(%)         Diagnostic         33.33%         3         Diagnostic           B 2.6 11         P-2         2W Analog Loop w/INP Non-Design/FL(%)         Diagnostic         0.00%         1         Diagnostic						13 03%	284				
B 2 6 9         P-2         2W Analog Loop Non-Design/FL(%)         Diagnostic         5 88%         102         Diagnostic           B 2.6 10         P-2         2W Analog Loop w/NP Design/FL(%)         Diagnostic         33 33%         3         Diagnostic           B.2.6 11         P-2         2W Analog Loop w/NP Non-Design/FL(%)         Diagnostic         0 00%         1         Diagnostic				4.00		0.000/	20	200			
B 2.6 10         P-2         2 W Analog Loop w/NP Design/FL(%)         Diagnostic         33 33%         3         Diagnostic           B.2.6 11         P-2         2 W Analog Loop w/NP Non-Design/FL(%)         Diagnostic         0 00%         1         Diagnostic				30 C							
B.2.6 11 P-2 2W Analog Loop w/INP Non-Design/FL(%) Diagnostic 0 000% 1 Diagnostic										19.30	
				11.5.				in the second			
B.2.6 12   P-2										100000000000000000000000000000000000000	
	B.2.6 12	P-2 [2W Analog Loop w/LNP Design/FL(%)	Diagnostic			5 19%		888			Diagnostic

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
	10.104, 110.104, 1204,	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2 6 13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			6 82%	44				Diagnostic
B.2 6 14	P-2 Other Design/FL(%)	Diagnostic			0 00%	1				Diagnostic
B.2.6 15	P-2 Other Non-Design/FL(%)	Diagnostic				<del></del>	_			Diagnostic
B.2 6 16	P-2 INP (Standalone)/FL(%)	Diagnostic					_	9.6		Diagnostic
B.2 6 17	P-2 LNP (Standalone)/FL(%)	Diagnostic			0 00%	11				Diagnostic
B.2.6.18	P-2 Digital Loop < DS1/FL(%)	Diagnostic	i		12 85%	389	1000		Description	Diagnostic
B.2.6.19	P-2 Digital Loop >= DS1/FL(%)	Diagnostic		. :	26 83%	164				Diagnostic
	Average Jeopardy Notice Interval - Mechanized									
B.2.8.1	P-2 Switch Ports/FL(hours)	>= 48 hrs								
B282	P-2 Local Interoffice Transport/FL(hours)	>= 48 hrs								
B283	P-2 Loop + Port Combinations/FL(hours)	>= 48 hrs			124 80	10	- 1			YES
B.2 8 4	P-2 Combo Other/FL(hours)	>= 48 hrs			400.00					YES
B285	P-2 xDSL (ADSL, HDSL and UCL)/FL(hours) P-2 UNE ISDN/FL(hours)	>= 48 hrs			192.00	3	- 89			YES
B.2 8.6	P-2 UNE ISDN/FL(hours) P-2 Line Sharing/FL(hours)	>= 48 hrs >= 48 hrs								
B.2.8 7 B.2.8.8	P-2 2W Analog Loop Design/FL(hours)	>= 48 hrs			256.00	24				YES
B.2.8.9	P-2 2W Analog Coop Design/FL(hours)	>= 48 hrs	- 6556		176.00	6				YES
B.2.8.10	P-2 2W Analog Loop w/INP Design/FL(hours)	>= 48 hrs			110.00	<u>_</u>				
B.2.8.11	P-2 2W Analog Loop w/INP Non-Design/FL(hours)	>= 48 hrs					70			
B.2.8.12	P-2   2W Analog Loop w/LNP Design/FL(hours)	>= 48 hrs			220 00	24				YES
B.2.8.13	P-2 2W Analog Loop w/LNP Non-Design/FL(hours)	>= 48 hrs	***		138.55	44			7	YES
B.2.8 14	P-2 Other Design/FL(hours)	>= 48 hrs			180.00	2				YES
B.2.8.15	P-2 Other Non-Design/FL(hours)	>= <b>48</b> hrs			144.00	2				YES
B.2.8.16	P-2 INP (Standalone)/FL(hours)	>= 48 hrs								
B.2.8.17	P-2 LNP (Standalone)/FL(hours)	>= 48 hrs					_			
B.2.8 18	P-2 Digital Loop < DS1/FL(hours)	>= 48 hrs			192 00	3	2000		-8-7 Boses	YES
B.2.8.19	P-2   Digital Loop >= DS1/FL(hours)	>= 48 hrs			492 85	71	A 83.55	10.00	7 (7 (3 (4 ))	YES
	Average Jeopardy Notice Interval - Non-Mechanized									
B.2.9.1	P-2 Switch Ports/FL(hours)	Diagnostic								Diagnostic
B.2.9.2	P-2 Local Interoffice Transport/FL(hours)	Diagnostic			120.00	1				Diagnostic Diagnostic
B 2 9.3	P-2 Loop + Port Combinations/FL(hours) P-2 Combo Other/FL(hours)	Diagnostic Diagnostic	1000		363 69	13				Diagnostic
B.2.9.4 B.2.9.5	P-2 Combo Other/FL(hours) P-2 xDSL (ADSL, HDSL and UCL)/FL(hours)	Diagnostic			247 38	13	- 200			Diagnostic
B 2.9 6	P-2 UNE ISDN/FL(hours)	Diagnostic			288.00	37	-			Diagnostic
B.2.9.7	P-2 Line Sharing/FL(hours)	Diagnostic			230.00		1.000.424			Diagnostic
B.2.9.8	P-2 2W Analog Loop Design/FL(hours)	Diagnostic			1		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			Diagnostic
B.2 9.9	P-2 2W Analog Loop Non-Design/FL(hours)	Diagnostic	- A		104.00	6				Diagnostic
B 2.9 10	P-2 2W Analog Loop w/INP Design/FL(hours)	Diagnostic			192 00	1		i ilaa		Diagnostic
B 2.9.11	P-2 2W Analog Loop w/INP Non-Design/FL(hours)	Diagnostic								Diagnostic
B.2.9.12	P-2 2W Analog Loop w/LNP Design/FL(hours)	Diagnostic			276 00	4				Diagnostic
B.2.9.13	P-2 2W Analog Loop w/LNP Non-Design/FL(hours)	Diagnostic			200 00	3				Diagnostic
B 2 9 14	P-2 Other Design/FL(hours)	Diagnostic			L					Diagnostic
B.2.9.15	P-2 Other Non-Design/FL(hours)	Diagnostic					12.200			Diagnostic
B 2 9 16	P-2 INP (Standalone)/FL(hours)	Diagnostic	1 Wali							Diagnostic
B.2.9.17	P-2 LNP (Standalone)/FL(hours)	Diagnostic			277.44	50	- J			Diagnostic Diagnostic
B 2 9 18	P-2 Digital Loop < DS1/FL(hours)	Diagnostic Diagnostic			182.18	44	200000			Diagnostic
B 2.9.19	P-2   Digital Loop >= DS1/FL(hours)	Diagnostic			102.10	44		- 400 c 1		Diagnostic
	% Jeopardy Notice >= 48 hours - Mechanized	AE0/ - 40 h -			,				سعيس	
B 2.10.1	P-2 Switch Ports/FL(%)	95% >= 48 hrs								<b></b>
B 2 10.2	P-2 Local interoffice Transport/FL(%)	95% >= 48 hrs 95% >= 48 hrs			100 00%	10	3,38471.57			YES
B 2.10 3	P-2   Loop + Port Combinations/FL(%)	95% >= 48 nrs 95% >≈ 48 hrs			100 00%	70				150
B.2.10 4	P-2 Combo Other/FL(%) P-2 xDSL (ADSL, HDSL and UCL)/FL(%)	95% >= 46 hrs			100 00%	3	. 2000		an de la	YES
B.2.10 5 B.2.10.6	P-2 (XDSL (ADSL, HDSL and OCL)/FL(%) P-2 (UNE ISDN/FL(%)	95% >= 48 hrs		***************************************	100 00 70		1 3 3 4			
B.2.10.6 B 2 10 7	P-2 Line Sharing/FL(%)	95% >= 48 hrs				-				
B 2 10.8	P-2 2W Analog Loop Design/FL(%)	95% >= 48 hrs			100 00%	24	1 x 200 ( ) \$ 1.	100	1000	YES
B 2 10.9	P-2 2W Analog Loop Non-Design/FL(%)	95% >= 48 hrs	3.0		100.00%	6				YES
B.2.10.10	P-2 2W Analog Loop w/INP Design/FL(%)	95% >= 48 hrs	33.53				3000		1000	
		•								

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
	DO JOVA I I I INDA D. JELON	050/ - 40 h								
B 2 10.11	P-2 2W Analog Loop w/INP Non-Design/FL(%) P-2 2W Analog Loop w/LNP Design/FL(%)	95% >= 48 hrs 95% >= 48 hrs			100 00%	24	-			\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
B 2.10 12 B.2.10.13	P-2 2W Analog Loop w/LNP Design/FL(%) P-2 2W Analog Loop w/LNP Non-Design/FL(%)	95% >= 48 hrs			100 00%	44				YES YES
B.2.10.13 B.2.10 14	P-2 Other Design/FL(%)	95% >= 48 hrs			100 00%	2	3.00			YES
B 2.10.15	P-2 Other Non-Design/FL(%)	95% >= 48 hrs			100 00%	2			ş.,	YES
B 2.10.16	P-2 INP (Standalone)/FL(%)	95% >= 48 hrs			100 00 /6	<del></del>				
B 2 10.17	P-2 LNP (Standalone)/FL(%)	95% >= 48 hrs					-			
B 2.10.18	P-2 Digital Loop < DS1/FL(%)	95% >= 48 hrs			100 00%	3				YES
8 2.10.19	P-2 Digital Loop >= DS1/FL(%)	95% >= 48 hrs			100 00%	71				YES
	% Jeopardy Notice >= 48 hours - Non-Mechanized						*****		····	
B 2.11 1	P-2   Switch Ports/FL(%)	Diagnostic				······································				Diagnostic
B 2.11 2	P-2 Local Interoffice Transport/FL(%)	Diagnostic					- 3			Diagnostic
B 2 11 3	P-2 Loop + Port Combinations/FL(%)	Diagnostic			100.00%	1	20020759		Y	Diagnostic
B 2 11.4	P-2 Combo Other/FL(%)	Diagnostic			100.00%	13	- 300, 500		3.37	Diagnostic
B 2.11.5	P-2 xDSL (ADSL, HDSL and UCL)/FL(%)	Diagnostic			100 00%	13				Diagnostic
B.2.11.6	P-2 UNE ISDN/FL(%)	Diagnostic			100 00%	37	- 1 Salay			Diagnostic
B.2.11.7	P-2 Line Sharing/FL(%)	Diagnostic			100.0070		76			Diagnostic
B 2.11.8	P-2 2W Analog Loop Design/FL(%)	Diagnostic	5.500							Diagnostic
B.2.11.9	P-2 2W Analog Loop Non-Design/FL(%)	Diagnostic			100 00%	6	- 20.0000			Diagnostic
B.2.11.10	P-2 2W Analog Loop w/INP Design/FL(%)	Diagnostic	1.		100.00%	1	_			Diagnostic
B.2.11.11	P-2 2W Analog Loop w/INP Non-Design/FL(%)	Diagnostic			100.00%		57	production.		Diagnostic
B.2.11 12	P-2 2W Analog Loop w/LNP Design/FL(%)	Diagnostic			100 00%	4				Diagnostic
B.2.11.13	P-2 2W Analog Loop w/LNP Non-Design/FL(%)	Diagnostic			100 00%	3			, V	Diagnostic
B.2.11 14	P-2 Other Design/FL(%)	Diagnostic			100 0070		-			Diagnostic
B 2.11.15	P-2 Other Non-Design/FL(%)	Diagnostic	:							Diagnostic
B.2.11 16	P-2 INP (Standalone)/FL(%)	Diagnostic								Diagnostic
B.2.11.17	P-2 LNP (Standalone)/FL(%)	Diagnostic	4		·					Diagnostic
B.2.11 18	P-2 Digital Loop < DS1/FL(%)	Diagnostic			100 00%	50	20,000			Diagnostic
8.2.11.19	P-2 Digital Loop >= DS1/FL(%)	Diagnostic			100.00%	44	7 10 20 20 41	3886		Diagnostic
	Coordinated Customers Conversions									
B.2.12.1	P-7 Loops with INP/FL(%)	>= 95% w in 15 min								
B 2 12.2	P-7 Loops with LNP/FL(%)	>= 95% w in 15 min			99 90%	7,769	1 (149.7			YES
	% Hot Cuts > 15 minutes Early	FOV			0.440/	0.074				
B 2.13.1	P-7A Time-Specific SL1/FL(%)	<= 5%			0 44%	2,274				YES
B.2.13.2	P-7A Time-Specific SL2/FL(%)	<= 5% <= 5%			0 00%	282 39				YES YES
B 2.13 3	P-7A Non-Time Specific SL1/FL(%) P-7A Non-Time Specific SL2/FL(%)	<= 5% <= 5%			0.00%	290				YES
B 2.13.4	P-/A   Non-time Specific SLZ/FL(%)	<= 5%			0.00%	290				TES
	Hot Cut Timeliness									
B.2.14.1	P-7A Time-Specific SL1/FL(%)	>= 95% w in 15 min			99.08%	2,274				YES
B 2.14 2	P-7A Time-Specific SL2/FL(%)	>= 95% w in 15 min			99 65%	282				YES
B.2.14.3	P-7A Non-Time Specific SL1/FL(%)	>= 95% w in 15 min			100.00%	39		3002		YES
B 2.14.4	P-7A   Non-Time Specific SL2/FL(%)	>= 95% w in 15 min			100 00%	290	6,58	3000000000		YES
	% Hot Cuts > 15 minutes Late									
B.2.15.1	P-7A Time-Specific St.1/FL(%)	<= 5%			0 48%	2,274				YES
B.2 15.2	P-7A Time-Specific SL2/FL(%)	<= 5%			0 35%	282				YES
B 2 15.3	P-7A Non-Time Specific St.1/FL(%)	<= 5%			0 00%	39			130.150 (1)	YES YES
B.2.15.4	P-7A Non-Time Specific SL2/FL(%)	<= 5%			0 00%	290				YEŞ
	Average Recovery Time - CCC									
B.2.16.1	P-78 Loops with INP/FL(minutes)	Diagnostic			1					Diagnostic
B.2.16.1 B.2.16.2	P-78 Loops with LNP/FL(minutes)	Diagnostic			341 08	9	- Special Co			Diagnostic
D.2. 10 £						<del></del>				
	% Provisioning Troubles within 7 Days - Hot Cuts	. 50/			0.000/	0.007				\#C
B.2.17 1 1	P-7C UNE Loop Design/Dispatch/FL(%)	<= 5%			2 00%	3,307				YES
B.2 17.1 2	P-7C UNE Loop Design/Non-Dispatch/FL(%)	<= 5%			0 00%	1 2.500		3000		YES
B 2.17.2 1	P-7C UNE Loop Non-Design/Dispatch/FL(%)	<= 5% <= 5%			1 50% 0.68%	2,592 2.811	The second			YES YEŞ
B 2 17.2 2	P-7C UNE Loop Non-Design/Non-Dispatch/FL(%)	<b>\-</b> 3%			0.06%	2,011	- 48 July 2 2000 40	, , , , , , , , , , , , , , , , , , ,		I ILS

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	, ,0,,,	iu, november koot	Delicillia k /	001	001	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
	% Miss	ed Installation Appointments									
B 2.18 1 1 1	P-3	Switch Ports/<10 circuits/Dispatch/FL(%)	R&B (POTS)	3 74%	95,197			<u> </u>			
B.2.18 1 1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0 04%	710,343						
B.2 18 1 2.1	P-3	Switch Ports/>=10 circuits/Dispatch/Fi.(%)	R&B (POTS)	7.16%	489						
B 2 18 1 2 2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	0.00%	18						i
B.2.18 2 1 1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)	DS1/DS3	3.01%	2,028	9 09%	11	9.5	0 05164	-1 1780	YES
B.2.18 2 1 2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)	DS1/DS3						,		
B.2.18 2 2 1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)	DS1/DS3					-			
B.2.18.2 2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)	DS1/DS3								
B 2.18 3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)	R&B	3.75%	95,818	3 64%	851		0 00654	0 1589	YES
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)	R&B	0.04%	711,866	0 11%	10,916		0 00020	-3 3612	NO NO
B.2.18 3 1 3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)	R&B	0.00%	400,878	0 00%	5,663	-	0 00002	0 1180	YES
B.2.18 3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)	R&B	0 10%	310,988	0 23%	5.253	- 30476 7	0 00043	-3 0057	NO
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)	R&B	7 18%	529	15 38%	13		0 07249	-1 1314	YES
B.2 18.3.2 2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)	R&B	0 00%	96	0 00%	5	- 10	0 00000	-1 1314	YES
B.2.18 3 2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)	R&B	0 00%	28	0 00 /6		- 1000c a 100g	0 00000		153
B.2 18.3 2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)	R&B	0 00%	68	0 00%	5	-, 14000000000000000000000000000000000000	0 00000		YES
B.2.18.4 1 1	P-3	Combo Other/<10 circuits/Dispatch/FL(%)	R&B&D - Disp	3.75%	98,491	0.00%	49	4 A. R.	0 00000	1 3817	YES
B 2 18.4 1.4	P-3	Combo Other/<10 circuits/Dispatch In/FL(%)	R&B&D - Disp	3.75%	98,491	00078	49	***	0 02713	1 3017	723
B 2.18.4 2.1	P-3	Combo Other/>=10 circuits/Dispatch/FL(%)	R&B&D - Disp	7.08%	537	<del> </del>					
B.2.18.4 2.4	P-3	Combo Other/>=10 circuits/Dispatch In/FL(%)	R&B&D - Disp	7.08%	537	<del> </del>					
B.2.18.5 1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)	ADSL to Retail	6 86%	14,013	3.13%	192		0 01836	2 0327	YES
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 04%	6,737	3.13 /6	192	-	0.01630	2 0321	150
B.2.18 5 2.1	P-3	xDSL (ADSL, HDSL and UCLV>=10 circuits/Dispatch/FL(%)	ADSL to Retail	8 96%	67	1					
B.2.18.5 2.2	P.3	xDSL (ADSL, HDSL and UCLV>=10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0 30 %		<del></del>		- 1000			
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/FL(%)	ISDN - BRI	6 41%	390	3.53%	283		0 01913	1 5040	YES
B 2 18.6 1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)	ISDN - BRI	2.31%	520	3.53%	263		0.01913	1 5040	TES
B.2 18.6.2.1	P-3	UNE ISDN/>=10 circuits/Noi1-Dispatch/FL(%)	ISDN - BRI	2.31%	520						
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)	ISDN - BRI								
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	6 86%	14.013	-		A 4 2 2 2 3			
B.2.18.7.1.1 B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL(%)	ADSL to Retail	0.04%	6,737	0.00%	6	60.60	0 00862	0.0517	
B.2.18.7.2.1	P-3		ADSL to Retail		67	0 00%	0		0 00862	0 0517	YES
	P-3	Line Sharing/>=10 circuits/Dispatch/FL(%)	ADSL to Retail	8 96%	0/	<del> </del>		- 1.			
B 2.18 7 2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	3.75%	95,818	1.53%	327	- 2	- 204050	- 24004	<u>\</u>
B 2 18.8 1.1		2W Analog Loop Design/<10 circuits/Dispatch/FL(%)				1.53%	321	-	0 01052	2 1081	YES
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	3.75%	95,818	0.000/					
B.2.18 8 2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	7.18%	529	0 00%	3		0 14950	0 4805	YES
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	7.18%	529	0.540/	747		0.00740	4.7000	
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	3.74%	95,197	2 51%	717		0 00712	1 7332	YES
B.2 18 9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 10%	310,048	0.00%	10		0 00980	0 0981	YES
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	7 16%	489	0 00%	17		0 06360	1.1254	YES
B.2 18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0 00%	12			a card			
B.2.18 10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	3 75%	95,818	0 00%	2		0 13428	0.2790	YES
B.2 18.10 1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	3 75%	95,818			2.35			
B 2 18.10 2.1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	7.18%	529	0 00%	1	2000	0 25846	0 2779	YES
B.2.18 10.2 2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	7.18%	529						
B.2 18.11 1.1	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	3.74%	95,197	100 00%	1		0 18983	-5 0705	NO
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.10%	310,048			3000			
B.2.18 11.2.1	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	7 16%	489			· · · · · · · · · · · · · · · · · · ·			
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	12						
B.2.18.12.1.1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	3.75%	95,818	1 24%	565	Alex Fin	0 00801	3 1297	YES
B.2.18.12.1.2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	3.75%	95,818			3 2000			
B 2 18.12.2.1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	7 18%	529	0 00%	6		0 10601	0 6776	YES
B.2 18 12.2 2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	7.18%	529						
B.2.18.13.1.1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	3.74%	95,197	1 07%	748		0 00697	3 8379	YES
B.2.18.13.1.4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.10%	310,048				1		
B 2 18 13.2.1	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	7 16%	489	5 00%	20	×	0 05881	0 3669	YES
B 2.18.13 2.4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	0.00%	12			10000			
B.2 18.14 1.1	P-3	Other Design/<10 circuits/Dispatch/FL(%)	Design	3 93%	2,673	0 00%	191		0 01455	2 6998	YES
B.2.18.14.1.2		Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	2 32%	518			remige de		···	
			-						······································		

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B.2.18.14 2.1	P-3	Other Design/>=10 circuits/Dispatch/FL(%)
B.2.18.14.2 2	P-3	Other Design/>=10 circuits/Non-Dispatch/FL(%)
B.2 18 15.1 1	P-3	Other Non-Design/<10 circuits/Dispatch/FL(%)
B.2.18.15 1 2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/Dispatch/FL(%)
B.2.18 15.2.2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)
B.2.18.16.1.1	P-3	INP (Standalone)/<10 circuits/Dispatch/FL(%)
B.2 18 16 1 2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B.2.18.16.2.1	P-3	INP (Standalone)/>=10 circuits/Dispatch/FL(%)
B 2.18.16 2 2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B.2 18 17 1 1	P-12	LNP (Standalone)/<10 circuits/Dispatch/FL(%)
B.2.18.17 1 2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
	P-12	
B.2.18.17.2.2		LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)
B 2.18 18 1 1	P-3 P-3	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)
B.2.18 18.1.2		Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)
B.2.18.18.2 1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)
B.2.18.18.2.2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)
B.2.18.19.1.2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)
B.2 18.19.2.1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)
B.2 18.19.2.2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)
	% Prov	isioning Troubles within 30 Days
B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/FL(%)
B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/FL(%)
B 2 19 1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/FL(%)
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/FL(%)
B.2.19 2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/FL(%)
B.2.19.2.1 2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(%)
B 2 19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/FL(%)
B.2 19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(%)
B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/FL(%)
B.2.19.3 1 2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(%)
B.2.19.3.1 3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(%)
B.2.19.3.1 4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/FL(%)
B.2.19.3.2 1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/FL(%)
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(%)
B.2 19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(%)
B.2.19.3.2 4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(%)
B.2.19.4.1 1	P-9	Combo Other/<10 circuits/Dtspatch/FL(%)
B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/FL(%)
B.2.19 4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/FL(%)
B.2.19.4 2.4	P-9	Combo Other/>=10 circuits/Dispatch In/FL(%)
B.2.19.5 1 1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(%)
B.2.19.5.1.2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(%)
B.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(%)
B 2 19.5 2 2	P-9	xDSt. (ADSt., HDSt and UCL)/>=10 circuits/Non-Dispatch/FL(%)
B.2.19.6.1 1	P-9	UNE ISDN/<10 circuits/Dispatch/FL(%)
B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/FL(%)
B.2 19 6 2 1	P-9	UNE ISDN/>=10 circuits/Dispatch/FL(%)
B 2.19 6 2.2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 7 1 1	P-9	Line Sharing/<10 circuits/Dispatch/FL(%)
B.2 19.7.1 2	P-9	Line Sharing/<10 circuits/Non-Dispatch/FL(%)
B.2.19.7.1.2 B 2.19.7.2 1	P-9	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)
	P-9	Line Sharing/>=10 circuits/Non-Dispatch/FL(%)
B 2 19 7.2 2	P-9	2W Analog Loop Design/<10 circuits/Dispatch/FL(%)
B.2 19.8.1.1		
B 2 19 8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(%)
B.2 19 8 2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/FL(%)
B 2 19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(%)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Design	0.00%	8	т					
Design	0.00%	50	<del>!</del>		_			
R&B	3 75%	95,818	0 00%	25		0 03799	0 9863	YES
R&B	0 04%	711,866	0.00%	7	7	0 00782	0 0548	YES
R&B	7.18%	529	50 00%	2		0 18293	-2 3406	NO
R&B	0.00%	96	00 00 70			0 10230	20400	
R&B (POTS)	3.74%	95.197	<del>                                     </del>					
R&B (POTS)	0.04%	710,343	<del>                                     </del>					
R&B (POTS)	7 16%	489	1					
R&B (POTS)	0.00%	18	<u> </u>					
R&B (POTS)	3 74%	95,197	0.00%	3		0 10960	0 3416	YES
R&B (POTS)	0.04%	710.343	0.04%	2,755		0 00039	0 1480	YES
R&B (POTS)	7 16%	489	0.00%	1	- 000 ( ) 100 o	0 25805	0 2774	YES
R&B (POTS)	0 00%	18	0.00%	23	~	0 00000		YES
Digital Loop < DS1	6 72%	14,941	3.48%	460	74.	0 01185	2 7351	YES
Digital Loop < DS1	0 19%	7,755						
Digital Loop < DS1	8.96%	67	1			•		
Digital Loop < DS1								
Digital Loop >= DS1	2.89%	519	3 46%	289		0 01230	-0 4636	YES
Digital Loop >= DS1	0.29%	339	1		1 - 1 - 1 - 2000000 - 1 - 1 - 1 - 2000000 - 1			
Digital Loop >= DS1	0 00%	1						
Digital Loop >= DS1	0 00%	50						

R&B (POT R&B (POT R&B (POT R&B (POT DS1/DS3 DS1/DS3 DS1/DS3 DS1/DS3 R&B R&B R&B R&B R&B R&B R&B R&B	S) S) S) S) S
R&B&D - D	ISP
ADSL to Re	
ADSL to Re	tail
ADSL to Re	tail
ADSL to Re	
ISDN - BF	15
ISDN - BF	
ISDN - BF	
ISDN - BF	
ADSL to Re	
R&B - Dis	
R&B - Dış	
R&B - Dis	
R&B - Dis	р

3.88%   794,943   10.55%   438   11.11%   9   9   9   9   9   9   9   9   9	5 75%	102,712	T T					
10.55% 438 11.11% 9 5.07% 2,445 0.00% 14 0.05881 0.8624 YES  5.72% 103,470 5.73% 593 0.00957 -0.0106 YES 3.87% 796,618 3.21% 10,375 0.00191 3.4672 YES 3.99% 453,156 3.12% 5,763 0.00259 3.3326 YES 3.72% 343,462 3.32% 4,612 0.00280 14204 YES 10.22% 489 0.00% 13 0.08514 1.2010 YES 2.06% 97 0.00% 2 0.10151 0.2031 YES 2.06% 97 0.00% 1 0.00000 YES 2.58% 78 0.00% 1 0.00000 YES 2.58% 78 0.00% 1 0.05907 0.1612 YES 5.63% 106,706 16.22% 37 0.03790 -2.7929 NO 10.12% 494 10.12% 494 11.127% 12,451 3.88% 309 0.01821 4.0553 YES 8.57% 6,216 15.79% 19  2.74% 438 4.18% 311 0.01210 -1.1899 YES 1.79% 504 19 0.01210 -1.1899 YES 1.79% 504 19 0.011955 -2.6424 NO 8.57% 6,216 28.57% 21 0.06120 -3.2674 NO 1.579% 19 19 15.79% 10.3470 12.94% 85 0.02521 -2.8636 NO			<del> </del>				ŀ	
11.11% 9 5.07% 2,445 0 00% 14 0 05881 0 8624 YES  5.72% 103,470 5 73% 593 0 00957 -0 0106 YES 3.87% 796,616 3 21% 10,375 0 00191 3 4672 YES 3.99% 453,156 3.12% 5,763 0 00259 3 3326 YES 3.72% 343,462 3.32% 4,612 0,00280 1 4204 YES 10.22% 489 0 00% 13 0,08514 1 2010 YES 2.06% 97 0,00% 2 0 10151 0 2031 YES 0.00% 19 0 00% 1 0 00000 YES 2.56% 78 0 000% 1 0 05907 0 1612 YES 5.63% 106,706 16 22% 37 0 03790 -2 7929 NO 5.63% 106,706 10 12% 494 11.27% 12,451 3.88% 309 0 01821 4 0553 YES 8.57% 6,216 15.79% 19  2.74% 438 4 18% 311 0 01210 -1 1899 YES 1.79% 504  11.27% 12,451 42.86% 7 0 11955 -2 6424 NO 8.57% 6,216 15.79% 19  5.72% 103,470 12 94% 85 0 02521 -2 8636 NO 5.72% 103,470 12 94% 85			<del> </del>					
5.07%         2,445         0.00%         14         0.05881         0.8624         YES           5.72%         103,470         5.73%         593         0.00957         -0.0106         YES           3.87%         796,618         3.21%         10,375         0.00191         3.4672         YES           3.99%         453,156         3.12%         5,763         0.00259         3.326         YES           3.72%         343,462         3.32%         4,612         0.00280         1.4204         YES           10.22%         489         0.00%         13         0.08514         1.2010         YES           0.00%         97         0.00%         2         0.10151         0.2031         YES           0.00%         19         0.00%         1         0.00000         YES         2.66%         78         0.00%         1         0.15907         0.1612         YES         5.63%         106,706         16 22%         37         0.03790         -2.7929         NO           5.63%         106,706         16 22%         37         0.03790         -2.7929         NO           10.12%         494         1         1.2451         3.88%         309<			! !				<del></del>	
5 72%         103,470         5 73%         593         0 00957         -0 0106         YES           3.87%         796,616         3 21%         10,375         0 00191         3 4672         YES           3.99%         453,156         3.12%         5,763         0 00259         3 3326         YES           3.72%         343,462         3.32%         4,612         0 00280         1 4204         YES           10.22%         499         0 00%         13         0.08514         1 2010         YES           2 06%         97         0.00%         2         0 10151         0 2031         YES           0.00%         19         0 00%         1         0 00000         YES         256%         78         0 00%         1         0 15907         0 1612         YES         5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           10 12%         494         1         1,2451         3 88%         309         0 01821         4 0553         YES           8.57%         6,216         15,79%         19         1         0 01210         -1 1899         YES           11 27%         12,451         42.86%			0.009/			0.05004	00004	VCC
3.87%         796,618         3 21%         10,375         0 00191         3 4672         YES           3.99%         453,156         3.12%         5,763         0 00259         33326         YES           3.72%         434,462         3.32%         4,612         0 00280         1 4204         YES           10.22%         489         0 00%         13         0.08514         1 2010         YES           2 08%         97         0.00%         2         0 10151         0 2031         YES           2 08%         78         0 00%         1         0 00000         YES         1 0 15907         0 1612         YES           5 63%         106,706         16 22%         37         0 03790         -2 7929         NO           5 5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           10 12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES           8.57%         6,216         28.57%         21         0 01210         -1 1899         YES           11 27%         12,451         42.86%         7         0 11955         -2 6424	3.07%	2,443	0 00%	14		0.02001	0.8024	TES
3.87%         796,618         3 21%         10,375         0 00191         3 4672         YES           3.99%         453,156         3.12%         5,763         0 00259         33326         YES           3.72%         434,462         3.32%         4,612         0 00280         1 4204         YES           10.22%         489         0 00%         13         0.08514         1 2010         YES           2 08%         97         0.00%         2         0 10151         0 2031         YES           2 08%         78         0 00%         1         0 00000         YES         1 0 15907         0 1612         YES           5 63%         106,706         16 22%         37         0 03790         -2 7929         NO           5 5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           10 12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES           8.57%         6,216         28.57%         21         0 01210         -1 1899         YES           11 27%         12,451         42.86%         7         0 11955         -2 6424			<del>                                     </del>		_			
3.87%         796,618         3 21%         10,375         0 00191         3 4672         YES           3.99%         453,156         3.12%         5,763         0 00259         33326         YES           3.72%         434,462         3.32%         4,612         0 00280         1 4204         YES           10.22%         489         0 00%         13         0.08514         1 2010         YES           2 08%         97         0.00%         2         0 10151         0 2031         YES           2 08%         78         0 00%         1         0 00000         YES         1 0 15907         0 1612         YES           5 63%         106,706         16 22%         37         0 03790         -2 7929         NO           5 5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           10 12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES           8.57%         6,216         28.57%         21         0 01210         -1 1899         YES           11 27%         12,451         42.86%         7         0 11955         -2 6424	<u> </u>		<del> </del>		- 000000		<del></del>	
3.87%         796,618         3 21%         10,375         0 00191         3 4672         YES           3.99%         453,156         3.12%         5,763         0 00259         33326         YES           3.72%         434,462         3.32%         4,612         0 00280         1 4204         YES           10.22%         489         0 00%         13         0.08514         1 2010         YES           2 08%         97         0.00%         2         0 10151         0 2031         YES           2 08%         78         0 00%         1         0 00000         YES         1 0 15907         0 1612         YES           5 63%         106,706         16 22%         37         0 03790         -2 7929         NO           5 5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           10 12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES           8.57%         6,216         28.57%         21         0 01210         -1 1899         YES           11 27%         12,451         42.86%         7         0 11955         -2 6424	5 720/	102 170	E 709/	502	- X	0.00057	0.0406	\#C
3.99% 453,156 3.12% 5,763 0 00259 3 3326 YES 3.72% 343,462 3.32% 4,612 0 00280 1 4204 YES 10.22% 489 0 00% 13 0.08514 1 2010 YES 0.06% 97 0.00% 2 0 10151 0 2031 YES 0.00% 19 0 00% 1 0 00000 YES 2 56% 78 0 000% 1 0 15907 0 1612 YES 5.63% 106,706 16 22% 37 0 03790 -2 7929 NO 5.63% 106,706 10 12% 494 11.27% 12,451 3 88% 309 0 01821 4 0553 YES 8.57% 6,216 15.79% 19 19 12451 4 2,86% 7 0 01955 -2 6424 NO 8 57% 6,216 28.57% 21 0 006120 -3 2674 NO 15 79% 19 19 10 127% 19 10 10 10 10 10 10 10 10 10 10 10 10 10								
3.72% 343,462 3.32% 4,612 0.00280 1.4204 YES 10.22% 489 0.00% 13 0.08514 1.2010 YES 2.06% 97 0.00% 2 0.10151 0.2031 YES 0.00% 19 0.00% 1 0.00000 YES 2.56% 78 0.00% 1 0.15907 0.1612 YES 5.63% 106,706 16.22% 37 0.03790 -2.7929 NO 10.12% 494 10.12% 494 11.12% 12.451 3.88% 309 0.01821 4.0553 YES 8.57% 6,216 15.79% 19 2.74% 438 4.18% 311 0.01210 -1.1899 YES 1.79% 504 11.27% 12,451 42.86% 7 0.11955 -2.6424 NO 8.57% 6,216 28.57% 21 0.06120 -3.2674 NO 15.79% 19 5.72% 103,470 12.94% 85 0.02521 -2.8636 NO								
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2 06%         97         0.00%         2         0 10151         0 2031         YES           0.00%         19         0 00%         1         0 00000         YES         2 58%         78         0 00%         1         0 15907         0 1612         YES         5.63%         106,706         16 22%         37         0 03790         -2 7929         NO         NO         5.63%         106,706         10 12%         494         10.12%         494         494         494         404         404         40553         YES         8.57%         6,216         15.79%         19         40553         YES         15.79%         19         YES         17.79%         504         11.27%         438         4 18%         311         0 01210         -1 1899         YES         17.79%         504         11.27%         12.451         42.86%         7         0 11955         -2 6424         NO         NO         8.57%         6.216         28.57%         21         0 06120         -3 2674         NO         15.79%         19         5.72%         103,470         12 94%         85         0 02521         -2 8636         NO         5.72%         103,470         10.22%         489         489         10.22								
0.00%         19         0.00%         1         0.00000         YES           2.56%         78         0.00%         1         0.15907         0.1612         YES           5.63%         106,706         16.22%         37         0.03790         -2.7929         NO           5.63%         106,706         16.22%         37         0.03790         -2.7929         NO           10.12%         494         4953         YES         8.57%         6,216         9.00         9.01821         4.0553         YES         17.72         9.00         9.00         9.01821         4.0553         YES         17.72         9.00         9.00         9.00         9.00         9.00         9.00         9.00         9.00 <t< td=""><td></td><td></td><td></td><td></td><td>_</td><td></td><td></td><td></td></t<>					_			
2 56%         78         0 00%         1         0 15907         0 1612         YES           5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           5.63%         106,706         10 12%         494         10.12%         494         10.12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES         8.57%         6,216         15.79%         19         19         19         19         YES         1 79%         504         11 27%         12,451         42.86%         7         0 11955         -2 6424         NO         865%         6,216         28.57%         21         0 06120         -3 2674         NO         15 79%         19         10 3,470         19 3,470         10 3,470					2 13		0 2031	
5.63%         106,706         16 22%         37         0 03790         -2 7929         NO           5.63%         106,706         10 12%         494         10.12%         494         11.27%         12,451         3 88%         309         0 01821         4 0553         YES         8.57%         6,216         15.79%         19         19         19         19         YES         179%         504         11 27%         12,451         42.86%         7         0 01210         -1 1899         YES         179%         504         NO         15 79%         6,216         28.57%         21         0 06120         -3 2674         NO         15 79%         19         10 3,470         12 94%         85         0 02521         -2 8636         NO         572%         103,470         12 94%         85         0 02521         -2 8636         NO         572%         103,470         12 94%         85         0 02521         -2 8636         NO         10.22%         489         489         10 00000000000000000000000000000000000							<b></b>	
5.63%         106,706           10 12%         494           10.12%         494           11.27%         12,451         3.88%         309         0.01821         4.0553         YES           8.57%         6,216         15.79%         19         19         19         19         YES         1.79%         504         11.27%         12,451         42.86%         7         0.11955         -2.6424         NO         NO         8.57%         6.216         28.57%         21         0.06120         -3.2674         NO         15.79%         19         10.3470         12.94%         85         0.02521         -2.8636         NO         5.72%         103,470         10.22%         489         489         10.22%         489         10.22%         489         10.22% </td <td></td> <td></td> <td></td> <td></td> <td>_</td> <td></td> <td></td> <td></td>					_			
10 12% 494 10 12% 494 11 127% 12,451 3 88% 309 0 0 1821 4 0553 YES 8.57% 6,216 15.79% 19  2 74% 438 4 18% 311 0 0 1210 -1 1899 YES 1 79% 504  11 27% 12,451 42.86% 7 0 11955 -2 6424 NO 8 57% 6,216 28.57% 21 0 0 6120 -3 2674 NO 15 79% 19  5 72% 103,470 12 94% 85 0 0 02521 -2 8636 NO 5 72% 103,470 12 94% 85			16 22%	37		0 03790	-2 7929	NO
10.12% 494 11.27% 12,451 3.88% 309 0.01821 4.0553 YES 8.57% 6,216 15.79% 19  2.74% 438 4.18% 311 0.01210 -1.1899 YES 1.79% 504  11.27% 12,451 42.86% 7 8.57% 6,216 28.57% 21 0.06120 -3.2674 NO 15.79% 19  5.72% 103,470 12.94% 85 0.02521 -2.8636 NO 5.72% 103,470 12.94% 85 0.02521 -2.8636 NO 10.22% 489					7			
11.27%     12,451     3.88%     309     0.01821     4.0553     YES       8.57%     6,216       15.79%     19       2.74%     438     4.18%     311     0.01210     -1.1899     YES       1.79%     504       11.27%     12,451     42.86%     7     0.11955     -2.6424     NO       8.57%     6,216     28.57%     21     0.06120     -3.2674     NO       15.79%     19       5.72%     103,470     12.94%     85     0.02521     -2.8636     NO       5.72%     103,470     12.94%     85     0.02521     -2.8636     NO       10.22%     489								
8.57% 6,216 15.79% 19  2.74% 438 418% 311 0.01210 -1.1899 YES 1.79% 504  11.27% 12,451 42.86% 7 8.57% 6,216 28.57% 21 0.06120 -3.2674 NO 15.79% 19  5.72% 103,470 12.94% 85 0.02521 -2.8636 NO 5.72% 103,470 12.94% 85 0.02521 -2.8636 NO 10.22% 489					and the			
15.79% 19  2 74% 438 4 18% 311 0 0 01210 -1 1899 YES  1 79% 504  11 27% 12,451 42.86% 7 0 11955 -2 6424 NO 15 79% 19  5 72% 103,470 12 94% 85 0 0 02521 -2 8636 NO 15 72% 103,470 10 22% 489		12,451	3 88%	309		0 01821	4 0553	YES
2 74% 438 4 18% 311 0 0 1210 -1 1899 YES 1 79% 504	8.57%	6,216			, 1.5%			
1 79% 504 0 11 27% 12,451 42.86% 7 0 11955 -2 6424 NO 8 57% 6,216 28.57% 21 0 06120 -3 2674 NO 15 79% 19 5 72% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	15.79%	19						
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11 27% 12,451 42.86% 7 0 11955 -2 6424 NO 8 57% 6,216 28.57% 21 0 06120 -3 2674 NO 15 79% 19 572% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	2 74%	438	4 18%	311		0 01210	-1 1899	YES
8 57% 6,216 28.57% 21 0 06120 -3 2674 NO 15 79% 19 572% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	1 79%	504						
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8 57% 6,216 28.57% 21 0 06120 -3 2674 NO 15 79% 19 572% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489			1	•				
8 57% 6,216 28.57% 21 0 06120 -3 2674 NO 15 79% 19 572% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	11 27%	12.451	42.86%	7		0 11955	-2 6424	NO
15 79% 19 5 72% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	8 57%			21				
5 72% 103,470 12 94% 85 0 02521 -2 8636 NO 5 72% 103,470 10.22% 489	15 79%		1					
5 72% 103,470 10.22% 489			1		- 1000 (h		·	
5 72% 103,470 10.22% 489	5 72%	103,470	12 94%	85		0.02521	-2 8636	NO
10.22% 489			1 7					
			1					
10 22% 489			<del>  </del>					

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	1 1011	da, November 2007	Denchmark /	651	851	CLEC	CLEC	Standard	Standard		
			Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 2 19 9 1 1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5 75%	102,710	4 02%	224		0 01558	1 1141	YES
B219914	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3 72%	342,621	0 00%	2		0 13384	0 2780	YEŞ
B.2.19.9.2 1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) exci SB Or	10 55%	436	16 67%	6	8.34	0 12627	-0 4844	YES
B 2 19 9.2 4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	12 50%	8	10 01 76	•		0 12021	-0 4044	IES
B.2.19.10 1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	5.72%	103,470	0.00%	10		0 07346	0 7791	YES
B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 72%	103,470	0.0078	10	-	0.07340	07191	153
B.2 19 10 2 1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	10.22%	489			100			
B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	10 22%	489			3.486			
B 2.19.11 1.1	P 9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5.75%	102,710	0.00%	3	- ( ) · · · · · · · · · · · · · · · · · ·	0 13444	0 4279	YES
B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3.72%	342,621	0 00 /8		-	0 /3444	04279	ILS
B.2.19.11.2 1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	10 55%	436	100 00%	1		0 30755	-2 9084	NO
B 2.19.11 2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	12 50%	8	100 00 %	<del></del>	- 20	0 30733	-2 5004	
B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(%)	R&B - Disp	5 72%	103,470	6 50%	1.077	200	0 00711	-1 0909	YES
B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(%)	R&B - Disp	5 72%	103,470	0 30 /6	1,077	to 188	0 00711	-1 0303	123
B.2.19 12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(%)	R&B - Disp	10 22%	489	17 65%	17		0 07475	-0 9929	YES
B 2 19 12 2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(%)	R&B - Disp	10 22%	489	17 03 76		1.00	007473	-0.5525	153
B.2 19.13 1 1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	5 75%	102,710	6.39%	845		0 00804	-0 7925	YES
B.2 19 13 1 4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	3 72%	342,621	0.0079	0-10		0 00004	-0 1323	
B.2.19.13 2 1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(%)	R&B (POTS) excl SB Or	10 55%	436	12.00%	25	100	0 06318	-0 2294	YES
B.2.19 13 2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(%)	R&B (POTS) excl SB Or	12.50%	8	12.0070			- 0000.0	- V Z Z Z V T	
B 2.19 14.1 1	P-9	Other Design/<10 circuits/Dispatch/FL(%)	Design	2.66%	3,238	7 20%	375		0 00877	-5 1808	NO
B.2 19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/FL(%)	Design	0.69%	1,301					- 0 .000	
B.2.19.14.2 1	P-9	Other Design/>=10 circuits/Dispatch/FL(%)	Design	0.00%	5	0 00%	3		0.00000		YES
B.2.19.14 2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/FL(%)	Design	0.00%	52			7.5	3.00000		
B 2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/FL(%)	R&B	5.72%	103,470	591%	728		0 00864	-0 2120	YE\$
B.2 19.15.1 2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/FL(%)	R&B	3.87%	796,616	11.11%	18	100	0.04547	-1 5926	YES
B.2 19 15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/FL(%)	R&B	10.22%	489	0.00%	10	NW.	0 09678	1 0565	YES
B 2.19 15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/FL(%)	R&B	2.06%	97				***************************************		
B.2.19.16.1 1	P-9	INP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	5.75%	102,712			*			
B.2 19.16 1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3 88%	794,943						
B.2.19 16.2 1	P-9	INP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	10 55%	436						
B.2 19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	11 11%	9			5.000			
B.2 19.17.1 1	P-9	LNP (Standalone)/<10 circuits/Dispatch/FL(%)	R&B (POTS)	5 75%	102,712	,		355.5			
B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	3 88%	794,943						
B.2.19 17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/FL(%)	R&B (POTS)	10 55%	436						
B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(%)	R&B (POTS)	11 11%	9			8.8			
8.2.19.18.1 1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/FL(%)	Digital Loop < DS1	10 37%	13,697	4.34%	622	1	0 01250	4 8222	YES
B.2 19 18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	7 20%	7,533			40000			]
B 2 19.18 2 1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop < DS1	15.79%	19						
B.2 19 18.2 2	P.9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop < DS1	0.00%	1						
B.2 19 19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	0 88%	680	6 38%	282	100	0 00662	-8 3044	NO
B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	0 18%	1,121						
B.2.19 19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(%)	Digital Loop >= DS1	0 00%	4			35 N. A. C.			
B.2.19.19.2 2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(%)	Digital Loop >= DS1	0 00%	52	i					
		e Completion Notice Interval - Mechanized						·····			
B.2.21 1.1.1	P-5	Swtch Ports/<10 circuits/Dispatch/FL(hours)	R&B (POTS)	3.89	53,613			19 522			
B.2 21 1 1 2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	1 48	612,700			7 495			
B 2.21.1.2 1	P-5	Switch Ports/>=10 circuits/Dispatch/FL(hours)	R&B (POTS)	4 07	341			18 976			
B.2.21.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	5 43	15			8 744			
B.2.21.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/FL(hours)	DS1/ DS3 - Interoffice	69 77	1,728			243 619			
B 2 21 2.1 2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(hours)	DS1/DS3 - Interoffice	<b>—</b>				<b> </b>			
B.2.21 2 2 1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)	DS1/ DS3 - Interoffice	<del>                                     </del>				<del></del>		<del></del> +	
B.2.21 2 2 2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)	DS1/ DS3 - Interoffice	L 200	51.400		000	10.744	- 000000	- 5 0700	
B.2.21.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)	R&B	3.93	54,106	0 54	992	19 744	0 63259	5 3730	YES
B 2 21.3 1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)	R&B	1 48	614,078	0 92	13,702	7 527	0 06502	8 6789	YES
B 2 21.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)	R&B R&B	1 94	330,439	0.81	6,758	8 786	0 10797	10 4644 -1 0716	YES
B.2.21.3.1 4	P-5 P-5	Loop + Port Combinations/<10 circuits/Dispatch In/FL(hours)  Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)	R&B	0 95 4 16	283,639 370	1 02 1 80	6,944 16	5 675 18 803	0 06893 4 80124	0 4923	YES YES
B 2.21.3 2 1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)	R&B	2 15	90	100	10	4 845	4 00 124	0 4923	153
B 2 21 3 2 2	P-5	Ecop + Port Comomations/>=10 dicults/non-Dispatch/PE(nours)	Rab	Z 13	อน			4 645		L	

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Standard Standard

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#### **BellSouth Monthly State Summary** Florida, November 2001

	Florida, November 2001		Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		,	Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B.2.21 3 2 3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)	R&B	2.36	28	<del></del>		4 828	<del></del>		<del></del>
	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)	R&B	2.06	62	· · ·		4 889	-		
B221411	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)	R&B&D - Disp	8 68	55,998	0.02	2	87 034	61 54315	0 1407	YES
B 2.21 4 1 4	P-5	Combo Other/<10 circuits/Dispatch In/FL(hours)	R&B&D - Disp	0.00	30,330		<del></del>	07 034	0104010	0 1407	
B.2 21 4.2 1	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)	R&B&D - Disp	6 60	375	<del>                                     </del>		31 133			
B.2 21.4.2 4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)	R&B&D - Disp	7.00		<del>                                     </del>					
B 2 21 5.1 1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)	ADSL to Retail	10 10	12,601			29 005			
B 2 21 5 1 2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	1 18	6,179		_	9 768			
B.2 21.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)	ADSL to Retail	5 35	48	<del> </del>		14 766			
B 2.21 5 2 2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail			<b>†</b>					<del></del>
B 2.21.6 1.1	P-5	UNE ISDN/<10 circuits/Dispatch/FL(hours)	ISDN - BRI	48 82	301	0.07	6	85 479	35 24287	1 3832	YES
B.2 21.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)	ISDN - BRI	9 13	464			38 432			i
B.2.21.6.2 1	P-5	UNE ISDN/>=10 circuits/Dispatch/FL(hours)	ISDN - BRI								
B.2.21 6 2 2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours)	ISDN - BRI								
B 2.21 7 1 1	P-5	Line Sharing/<10 circuits/Dispatch/FL(hours)	ADSL to Retail	10 10	12,601			29 005			
B 2 21.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	1 18	6,179	0.46	4	9 768	4 88558	0 1477	YES
B 2.21 7 2 1	P-5	Line Sharing/>=10 circuits/Dispatch/FL(hours)	ADSL to Retail	5 35	48			14 766			L
B 2 21.7 2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/FL(hours)	ADSL to Retail	1							L
B 2 21 8 1 1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	3 93	54,106	15 00	458	19 744	0 92646	-11 9413	NO
B221812	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	3 93	54,106			19 744			
B221821	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	4 16	370	0 13	3	18 803	10 89966	0 3700	YES
B 2.21 8 2 2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4 16	370			18 803			
B 2.21 9.1 1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	3 89	53,613	0 59	977	19 522	0 63024	5 2460	YES
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0 94	282,752	0 02	2	5 618	3 97240	0 2319	YES
B 2.21 9 2 1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4 07	341	0 66	23	18 976	4 08799	0 8358	YES
B.2 21.9 2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	5 27	9	<b></b>		9 754			
B 2.21.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp R&B - Disp	3 93	54,106		<del></del>	19 744 19.744			
B.2.21.10.1.2 B 2 21.10.2.1	P-5 P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(hours)  2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	3 93 4 16	54,106 370			18.803			
B.2.21.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4.16	370			18 803			
B.2.21.11 1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	3.89	53,613	<del>                                     </del>		19 522			
B.2 21.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0.94	282,752	+		5.618			
B.2 21.11.2 1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4 07	341	<del>                                     </del>		18 976			
B 2 21 11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	5 27	9			9 754			
B.2 21 12 1 1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)	R&B - Disp	3 93	54,106	24 22	575	19 744	0.82774	-24 5109	NO
B.2 21 12.1 2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	3 93	54,106			19 744			
B 2 21 12 2 1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)	R&B - Disp	4 16	370	44 62	4	18 803	9 45203	-4 2800	NO
B.2 21.12.2 2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B - Disp	4.16	370			18.803			
B 2 21.13.1 1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	3.89	53,613	1 10	1,026	19 522	0 61528	4 5400	YES
B.2.21 13.1 4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	0 94	282,752			5 618			
B.2.21.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B (POTS) excl SB Or	4 07	341	1 28	19	18 976	4 47298	0 6253	YES
B 2 21 13 2 4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)	R&B (POTS) excl SB Or	5 27	9			9 754			
B.2.21.14.1.1	P-5	Other Design/<10 circuits/Dispatch/FL(hours)	Design	144 46	1,892	27.57	2	440 528	311 66479	0 3751	YES
B.2.21 14.1 2	P-5	Other Design/<10 circuits/Non-Dispatch/FL(hours)	Design	47.25	379			199 038			
B.2.21.14.2 1	P-5	Other Design/>=10 circuits/Dispatch/FL(hours)	Design	187.51	5	$\vdash$		128 657			
B.2.21.14 2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/FL(hours)	Design	6 91	43	II		40 019			
B 2 21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/FL(hours)	R&B	3 93	54,106			19 744			
B.2 21 15 1 2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)	R&B	1.48	614,078	ļ		7 527			
B 2.21 15.2 1	P-5	Other Non-Design/>=10 circuits/Dispatch/FL(hours)	R&B	4 16	370			18 803			
B 2.21.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)	R&B R&B (POTS)	2 15 3 89	90 53,613	<del>                                     </del>		4 845 19.522			
B.2 21.16.1 1	P-5	INP (Standalone)/<10 circuits/Dispatch/FL(hours)				<del>                                     </del>		7 495			J
B 2 21 16 1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)	R&B (POTS) R&B (POTS)	1 48 4 07	612,700 341	<del>  </del>		18 976	<u> </u>		
	P-5 P-5	INP (Standalone)/>=10 circuits/Dispatch/FL(hours)	R&B (POTS)	5.43	15	+		8 744			
B 2 21 16.2 2	P-5 P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)  LNP (Standalone)/<10 circuits/Dispatch/FL(hours)	R&B (POTS)	3.43	53,613	<del>                                     </del>		19 522	<del> </del>		
B.2 21 17 1 1	P-5 P-5	LNP (Standalone)/<10 circuits/Dispatch/FL(hours)	R&B (POTS)	148	612,700	0.76	212	7 495	0 51485	1 3909	YES
B 2.21 17 1 2 B.2 21 17.2 1	P-5	LNP (Standalone)/>=10 circuits/Nori-Dispatch/FL(hours)	R&B (POTS)	4 07	341	<del>  ~~.'~</del>		18 976	5 5 1 4 5 5	1 3303	
B 2 21 17.2 1	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)	R&B (POTS)	5 43	15	<del> </del>		8 744		<del></del> †	
	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)	Digital Loop < DS1	12.09	13,267	0.07	6		15 24372	0 7882	YES
D4411011	r-9	Toriginal Ecopy < Do tr < to encourastraspation in Ethopia)		16.03	10,201	, ,,,,,	<u> </u>	0,001	10 = 4012	0,000	

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2.21.18 1 2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)	
2 21 18 2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)	
2.21 18 2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)	_
.21.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)	
.21.19.12	P-5	Digital Loop >= DS1/≤10 circuits/Non-Dispatch/FL(hours)	
21 19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)	
21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)	
		e Canyleti Iod merva - No	
22 1 1 1	P-5	Switch Ports/c10 circuits/Dispatch/FI (hours)	_

Benchmark / Analog
Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop >= DS1

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
1 78	6,660			14 463			<u> </u>
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40.05	239	1 20 00		203,434	1100101	17101	
6 91	43			40 019			L

B 2 21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(hours)
		e C xinj leti I loi I mervi - No
B 2.22 1 1 1	P-5	Switch Ports/<10 circuits/Dispatch/FL(hours)
B.2.22.1 1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/FL(hours)
B.2.22.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/FL(hours)
B 2.22.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/FL(hours)
B 2.22.2 1 1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/FL(hours)
B 2.22 2 1 2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(hours)
B 2.22.2 2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/FL(hours)
B.2.22.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.3.1 1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/FL(hours)
B 2 22.3 1 2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(hours)
B.2.22.3.13	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL(hours)
B.2.22.3 1.4	P-5	Logo + Port Combinations/<10 circuits/Dispatch In/FL(hours)
B 2 22.3 2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/FL(hours)
B 2 22.3 2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL(hours)
B.2.22 3.2 4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/FL(hours)
B.2.22.4.1 1	P-5	Combo Other/<10 circuits/Dispatch/FL(hours)
B.2.22.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/FL(hours)
B.2.22 4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/FL(hours)
B.2.22.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/FL(hours)
B.2.22.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(hours)
B.2.22.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(hours)
B 2 22.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(hours)
B.2.22.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(hours)
B 2 22.6 1.1	P-5	UNE ISDN/<10 circuits/Dispatch/FL(hours)
B.2.22.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/FL(hours)
B 2 22 6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/FL(hours)
B.2 22 6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/FL(hours)
B 2.22 7.1.1	P-5	Line Shanng/<10 circuits/Dispatch/FL(hours)
B.2 22.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/FL(hours)
B.2 22.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/FL(hours)
B.2.22.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/FL(hours)
B 2.22.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/FL(hours)
B.2.22.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(hours)
B.2.22.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/FL(hours)
B 2.22.8.2 2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.9 1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(hours)
B.2.22.9 1 4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL(hours)
B.2.22.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(hours)
B 2 22 9.2 4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL(hours)
B.2.22 10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(hours)
B 2.22.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(hours)
B.2 22.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(hours)
B 2.22.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(hours)
B 2.22.11 1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(hours)
B.2.22.11 1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL(hours)
B.2.22.11.2 1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(hours)
B.2.22.11.2 4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL(hours)
B 2 22.12 1 1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(hours)
B 2.22.12.1 2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(hours)
B 2 22.12 2 1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(hours)

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B.2 22 12.2 2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(hours)
B.2,22,13,1,1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(hours)
B.2 22 13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL(hours)
B.2 22 13.2 1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(hours)
B.2 22 13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL(hours)
B.2.22.14.1.1	P-5	Other Design/<10 circuits/Dispatch/FL(hours)
B.2 22.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/FL(hours)
B.2.22.14.2 1	P-5	Other Design/>=10 circuits/Dispatch/FL(hours)
B 2.22.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.15 1 1	P-5	Other Non-Design/<10 circuits/Dispatch/FL(hours)
B 2.22.15.1 2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/FL(hours)
B.2.22 15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/FL(hours)
B.2.22.15.2 2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/FL(hours)
B.2.22.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)
B.2.22 16 2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/FL(hours)
B 2 22 16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22 17.1 1	P-5	LNP (Standalone)/<10 circuits/Dispatch/FL(hours)
B.2.22 17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(hours)
B.2 22.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/FL(hours)
B.2.22.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/FL(hours)
B.2.22.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(hours)
B.2.22.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/FL(hours)
B.2.22.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(hours)
B.2.22.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/FL(hours)
B.2.22.19.1 2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(hours)
B 2.22.19 2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(hours)
B.2.22.19.2.2	P-5	Digital Loop >= D\$1/>=10 circuits/Non-Dispatch/FL(hours)
	Total S	Service Order Cycle Time - Mechanized
B.2.24 1 1.1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B.2.24.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B 2.24.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B 2 24.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B 2.24.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B.2 24.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B 2.24.2 2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.24.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.24.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.24.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.24.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.4 1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2 24 4.1 2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.24.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)

P-10 Combo Other/>=10 circuits/Dispatch/FL(days) Combo Other/>=10 circuits/Non-Dispatch/FL(days)

P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)

P-10 UNE ISDN/<10 circuits/Non-Dispatch/FL(days) UNE ISDN/>=10 circuits/Dispatch/FL(days)

P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)

P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)

P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)

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Line Sharing/>=10 circuits/Dispatch/FL(days)

P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)

P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)

P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)

B.2.24.4.2.2

B.2.24.5.1.1 B.2 24 5 1 2 B.2.24 5.2.1

B.2.24.5.2.2

B.2.24 6.1.1 B.2.24 6.1.2

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B 2 24 8 1 2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2 24.8 2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2.24.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 24.9.1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B 2 24 9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B 2 24.9 2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B 2 24 10 1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.10 2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2 24.10.2 2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.11 1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.24.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2 24.11.2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2 24 11.2 2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B 2 24 12 1 2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 24.12 2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B 2 24 12 2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B 2.24.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B 2.24.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.13.2 1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.24.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 24.14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.24.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
B.2.24.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
B.2.24.14.2 2 B 2 24.15.1 1	P-10 P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B.2,24,15,1,2	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
B.2.24.15.1.2 B.2.24 15 2 1	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)  Other Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.24.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 24 16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.24 16 1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.24.16.2 1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.24.16 2 2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.17 1 1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2 24 17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.24.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B 2.24 17 2 2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B 2 24 18 1 2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B 2 24 18.2 1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B 2.24.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.24.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2 24.19 1.2	P-10 P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B 2.24.19.2 1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
B.2.24.19.2.2	7-10	Digital Loop >= D\$1/>=10 circuits/Non-Dispatch/FL(days)
	Total \$	ervice Order Cycle Time - Partially Mechanized
B 2 25.1 1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B 2 25.1 1 2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.25.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B.2.25.1 2 2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B 2 25.2 1 1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B 2.25.2 1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2 25.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.25.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2 25.3 1 2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2.25 3 2 1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)

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	=	Description (days)
	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B 2.25 4.1.1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2 25.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.25.4 2 1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2 25 4 2 2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
B 2 25.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/FL(days)
B 2.25.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B 2 25.5 2 2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
B.2.25 6 1 2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
B 2.25 6.2 1	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
B 2.25 6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
B.2.25.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
B 2 25.7 2.1	P-10	Line Sharing/>=10 circuits/Dispatch/FL(days)
B 2 25.7 2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
8 2.25.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
8.2 25.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2 25.8.2 1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2 25.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.9.1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.25 9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B.2.25.10.1 2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2.25.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.11 1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25 11.1 2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2 25.11 2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.25 11 2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days) 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25 12.1.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B.2 25 12 2 1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 25.12.2.2	P-14	2W Analog Loop w/LNP besign/> to circuits/Not/Puspatich/EL(days)
B.2.25 13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.13.1.2	P-14 P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.25.13.2.1	P-14	2W Analog Loop wiLNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.13.2.2 B.2.25 14.1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.25.14.1.1 B.2.25.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
8 2 25.14.2.1	P-10	Other Design/>=10 circuits/toir Dispatch/FL(days)
B 2 25.14.2.1 B 2 25 14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B 2.25.15 1 1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
B.2.25.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.25.15.1.2 B.2.25 15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.25 15.2.1	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.25 16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2 25 16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B 2 25.16 2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.25.17.1.1	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.25.17.1.2 B.2.25.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.25.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.25.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B.2.25 18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)

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Diagnostic   Dia		Measure	Volume	Measure	Volume	Devlation	Error	ZScore	Equity
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B 2,25,18 2,1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2.25.18.2 2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2 25.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2.25 19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B 2 25.19 2 1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
B.2.25.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)
	Total S	ervice Order Cycle Time - Non-Mechanized
B.2.26.1.1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B.2.26.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.26.1 2 1	P-10	Switch Ports/>=10 crcuits/Dispatch/FL(days)
B 2.26.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B.2.26 2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
B 2.26.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B 2.26.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.26 2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.26.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B 2.26.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2.26.3.2 2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.4 1 1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B 2.26.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.26.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
B.2.26.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
B 2.26.5 1 2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2.26 5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B.2.26.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/FL(days)
B.2.26.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
B.2.26.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
B.2.26 6 2.1	P-10 P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days) UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.6.2 2	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
B.2.26.7.1.1	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
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B.2.26.7.2.1	P-10	Line Shanng/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.8.1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
B.2.26.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2 26.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2.26.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.9 1 1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B 2.26 9 1 2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.26.9 2 1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.26.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 26.10 1 1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B.2 26.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 26.10 2.1	P-10	2W Analog Loop w/INP Design/>=10 crcuits/Dispatch/FL(days)
B.2.26.10.2.2	P-10	12W Analog Loop w/INP Design/>=10 crcuits/Non-Dispatch/FL(days)
B 2 26.11.1 1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B 2 26 11.1 2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.26.11 2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.26.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.12 1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B.2.26.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B.2.26 12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B.2 26.12 2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B.2 26 13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.26.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days) 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.26 13 2 2	P-14	Izva Analog Loop within Non-Design/2=10 dictilis/Non-Dispatch/Ft(days)

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8 2 26, 14 2.1  P-10 Other Design/> P-10 Circusts/Dispatch/FL(days)  8 2 26, 15 1.1  P-10 Other Design/> P-10 Other Design/> P-10 Other Design/> P-10 Other Non-Design/           8 2 2 2 1 5 1.1  P-10 Other Non-Design/         P-10 Other Non-Design/           8 2 2 2 1 5 1.1  P-10 Other Non-Design/         P-10 Other Non-Design/           8 2 2 2 1 6 1.1  P-10 Other Non-Design/> P-10 Circusts/Non-Dispatch/FL(days)  B-2 2 2 2 1 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1	B 2 26 14 1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
8 2 26.1 14.2 2 P-10 Other Design/> 8 2 26.1 15.1 2 P-10 Other Non-Design/     P-10 Other Non-Design/       8 2 2 2 2 15 15.1 2 P-10 Other Non-Design/     P-10 Other Non-Design-       8 2 2 2 2 2 15 2 2.1 P-10 Other Non-Design/     P-10 Other Non-Design-       8 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2			
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B 2.26 15.2.1 P-10 Other Non-Design/≃10 circuits/Non-Dispatch/FL(days) B 2.26 15.2.1 P-10 Other Non-Design/≃10 circuits/Dispatch/FL(days) B 2.26 16.2.1 P-10 INP (Standalone)/<10 circuits/Dispatch/FL(days) B 2.26 16.2.1 P-10 INP (Standalone)/=10 circuits/Dispatch/FL(days) B 2.26 17.1 P-14 LNP (Standalone)/=10 circuits/Dispatch/FL(days) B 2.26 17.2.1 P-14 LNP (Standalone)/=10 circuits/Dispatch/FL(days) B 2.26 17.2.1 P-14 LNP (Standalone)/=10 circuits/Dispatch/FL(days) B 2.26 17.2.1 P-14 LNP (Standalone)/=10 circuits/Dispatch/FL(days) B 2.26 18.2.1 P-10 Ligital Loop < DS1/F10 circuits/Dispatch/FL(days) B 2.26 18.2.1 P-10 Digital Loop < DS1/F10 circuits/Dispatch/FL(days) B 2.26 18.2.2 P-10 Digital Loop < DS1/F10 circuits/Dispatch/FL(days) B 2.26 19.1.2 B 2.26 19.	B 2 26.14.2.2		
B 2 26. 15 2.1  P-10 Other Non-Design/> B 2 26. 16 2.2  P-10 Other Non-Design/> B 2 26. 16 2.2  P-10 INP (Standalone) - INP (Standalone) - INP (Standalone) - B 2 26. 16 2.1  P-10 INP (Standalone) - INP (Standalone) - INP (Standalone) - B 2 26. 16 2.1  P-10 INP (Standalone) - INP (Standalone) - INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - B 2 26. 16 2.2  P-10 INP (Standalone) - B 2 26. 16 2.2  P-14 LINP (Standalone) - B 2 26. 16 2.2  P-14 LINP (Standalone) - B 2 26. 16 2.2  P-14 LINP (Standalone) - B 2 26. 18 2.1  P-14 LINP (Standalone) - B 2 26. 18 2.1  P-15 Digital Loop < DS1/ <b 10="" 18="" 2="" 2.1="" 26.="" <="" <b="" b="" circuits="" digital="" dispatch="" ds1="" fl(days)="" loop="" non-dispatch="" p-10=""> DS1/<b 10="" 18="" 2="" 2.1="" 26.="" b="" circuits="" digital="" dispatch="" fl(days)="" loop="" p-10=""> DS1/<b 10="" 18="" 2="" 2.1="" 26.="" b="" circuits="" digital="" dispatch="" fl(days)="" loop="" p-10=""> DS1/<b 10="" 19="" 2="" 2.1="" 26.="" b="" circuits="" digital="" dispatch="" fl(days)="" loop="" p-10=""> DS1/<b 10="" 19="" 2="" 2.1="" 26.="" b="" circuits="" digital="" dispatch="" fl(days)="" loop="" p-10=""> DS1/<b 10="" 19="" 2="" 2.2="" 28.="" b="" circuits="" digital="" dispatch="" fl(days)="" loop="" p-10=""> DS1/<b 10="" 19="" 2="" 2.2="" 28.="" b="" circuits="" digital="" fl(days)="" loop="" non-dispatch="" p-10=""> DS1/<b +="" 1="" 10="" 2="" 2.1="" 2.2="" 28.="" <b="" b="" circuits="" combinations="" dispatch="" fl(days)="" interoffice="" local="" loop="" non-dispatch="" p-10="" p-1<="" port="" ports="" switch="" td="" transport="" ⇒10=""><td>B 2 26 15 1.1</td><td></td><td>Other Non-Design/&lt;10 circuits/Dispatch/FL(days)</td></b></b></b></b></b></b></b></b>	B 2 26 15 1.1		Other Non-Design/<10 circuits/Dispatch/FL(days)
B 226.16.1.2 P-10 Other Non-Designt>=10 crounts/Non-Dispatch/FL(days) B 226.16.1.1 P-10 INP (Standalone)<10 circuits/Dispatch/FL(days) B 226.16.2.1 P-14 INP (Standalone)<10 circuits/Non-Dispatch/FL(days) B 226.17.2.1 P-14 INP (Standalone)<10 circuits/Non-Dispatch/FL(days) B 226.17.2.1 P-14 INP (Standalone)<10 circuits/Non-Dispatch/FL(days) B 226.17.2.1 P-14 INP (Standalone)<10 circuits/Non-Dispatch/FL(days) B 226.18.1.1 P-10 Digital Loop of St1/<0 circuits/Dispatch/FL(days) B 226.18.1.2 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 226.18.1.2 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 226.19.1.1 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 226.19.1.2 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 226.19.1.2 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 226.19.2.2 P-10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 227.10 Digital Loop of St1/<0 circuits/Non-Dispatch/FL(days) B 228.11 B 228.11 B 228.11 B 228.11 B 228.11 B 228.12 B 228.12 B 228.11 B 228.12 B 228.	B 2.26 15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2 26 16 1.1 P-10 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 16 2.1 P-10 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 16 2.1 P-10 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 17 1.1 P-14 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 17 1.1 P-14 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 17 1.2 P-14 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 18 1.7 P-14 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 18 1.7 P-14 INP (Standalone)\s/-10 circuits/Dispatch/FL(days) B 2 26 18 1.7 P-10 Digital Loop < DST/<10 circuits/Dispatch/FL(days) B 2 26 18 1.2 P-10 Digital Loop < DST/<10 circuits/Dispatch/FL(days) B 2 26 18 1.2 P-10 Digital Loop < DST/<10 circuits/Dispatch/FL(days) B 2 26 18 1.2 P-10 Digital Loop < DST/<10 circuits/Dispatch/FL(days) B 2 26 18 1.2 P-10 Digital Loop > DST/<10 circuits/Dispatch/FL(days) B 2 26 18 1.1 P-10 Digital Loop > DST/<10 circuits/Dispatch/FL(days) B 2 26 19 2.1 B 2 26 19 2.1 P-10 Digital Loop > DST/<10 circuits/Dispatch/FL(days) B 2 26 19 2.1 B 2 26 2.2 B 2 2	B.2 26.15 2.1		Other Non-Design/>=10 circuits/Dispatch/FL(days)
8.2.26.16.1.2 P-10 INP (Standalone)V=10 circuits/Non-Dispatch/FL(days) 8.2.26.16.2.1 P-10 INP (Standalone)V=10 circuits/Dispatch/FL(days) 8.2.26.17.1 P-14 INP (Standalone)V=10 circuits/Dispatch/FL(days) 8.2.26.17.1 P-14 INP (Standalone)V=10 circuits/Dispatch/FL(days) 8.2.26.17.2.1 P-14 INP (Standalone)V=10 circuits/Dispatch/FL(days) 8.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) 8.2.26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) 8.2.26.18.2.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) 8.2.26.19.1.2 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) 8.2.26.19.1.2 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) 8.2.26.19.1.2 P-10 Digital Loop > DS1/<10 circuits/Dispatch/FL(days) 8.2.26.19.1.2 P-10 Digital Loop > DS1/<10 circuits/Dispatch/FL(days) 8.2.26.19.1.1 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8.2.26.19.1.2 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8.2.26.19.1.1 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8.2.28.1.1 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8.2.28.1.2 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) 8.2.28.2.2 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) 8.2.28.2.2 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) 9.2.28.2.2 P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days) 9.2.28.2.3 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9.2.28.3.1 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9.2.28.1 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9.2.28.1 P-10 Local In	B 2.26.15 2.2		
8 2 2 6 16 2 1 P-10 INP (Standalone)/> P-10 crouts/Dispatch/FL(days) 8 2 2 8 1 7 2 1 P-10 INP (Standalone)/> P-10 crouts/Dispatch/FL(days) 8 2 2 8 1 7 1 1 P-14 LNP (Standalone)/> P-10 crouts/Non-Dispatch/FL(days) 8 2 2 8 1 7 1 2 P-14 LNP (Standalone)/> P-10 crouts/Non-Dispatch/FL(days) 8 2 2 8 1 7 2 1 P-14 LNP (Standalone)/> P-10 crouts/Non-Dispatch/FL(days) 8 2 2 8 1 8 1 2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 8 1 2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 8 1 2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 8 2 2 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 8 2 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 9 1 1 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 9 1 1 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 9 1 1 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 9 1 1 P-10 Digital Loop > DS1/ 8 2 2 8 1 9 1 1 P-10 Digital Loop > DS1/ 8 2 8 2 8 1 1 1 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 1 1 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 1 2 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 1 2 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 8 2 2 8 1 1 2 P-10 Switch Ports/<10 circuits/Non-Dispatch/FL(days) 9 2 2 8 2 2 1 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9 2 2 8 2 2 1 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9 2 2 8 2 2 1 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) 9 2 2 8 2 2 9 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	B 2 26 16 1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2 2 6.1 6.2 2 P-10 INF (Standalone)/> = 10 crouts/Non-Dispatch/FL(days) B.2 2 8.1 7.1 1 P-14 LNP (Standalone)/> = 10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 7.2 1 P-14 LNP (Standalone)/> = 10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 1.2 P-14 LNP (Standalone)/> = 10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 1.1 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 8.1 1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) B.2 2 8.1 8.1 1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days) B.2 2 8.1 8.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 8.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 8.2 P-10 Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 9.1 P-10 Digital Loop > DS1/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.1 9.1 P-10 Digital Loop > DS1/ B.2 2 8.1 9.1 P-10 Digital Loop > DS1/ B.2 2 8.1 9.1 P-10 Digital Loop > DS1/ B.2 2 8.1 9.1 P-10 Digital Loop > DS1/ B.2 2 8.1 9.1 P-10 Digital Loop > DS1/ B.2 1 9.1 Digital Loop > DS1/ B.2 2 8.1 9.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) B.2 2 8.1 9.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) B.2 2 8.1 9.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) B.2 2 8.1 2.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days) B.2 2 8.2 1.2 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 2.2 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 2.2 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 3.1 P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 3.2 P-10 Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 3.1 P-10 Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 4.2 P-10 Loop + Dort Combinations/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 4.2 P-10 Loop + Dort Combinations/<10 circuits/Non-Dispatch/FL(days) B.2 2 8.2 8.1 P-10 Combo Other/> B.2 8.2 8.1 P-10 Combo Other/<10 circuits/Non-Dispatch/FL(da	B.2.26.16.1.2		
B. 2.8.17.1 1 P-14 INP (Standalone)/ <a>     Inv (Standalone) (Standal</a>	B 2 26 16 2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B. 2.8.17.1.2   P-14	B 2 26.16 2 2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.28.17.2.1   P-14	B.2 26.17 1 1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
8.2.8.17.2.2   P-14	B.2.26.17.1 2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B 2 26.18.1.1 P-10 Digital Loop < DS1/<10 circuits/Dispatch/FL(days)  B 2.26.18.2.1 P-10 Digital Loop < DS1/> P-10 Digital Loop > DS1/> P-10 Switch Ports/  **Total Sarvice Order Crucle Time Inference*  **Total Sarvice Order Cr	B.2.26.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
8.2.26.18.2.2   P-10   Digital Loop < DS1/>=10   circuits/Dispatch/FL(days)	B.2.26.17 2 2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.26.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)  B.2.26.18.2.1 P-10 Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)  B.2.26.19.1.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)  B.2.26.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)  B.2.26.19.2.1 P-10 Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)  B.2.26.19.2.1 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)  B.2.26.19.2.2 P-10 Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)  Total Service Order Cvcle Time (offered) - Mechanized  B.2.28.19.2.2 P-10 Switch Ports/<10 circuits/Dispatch/FL(days)  B.2.28.1.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days)  B.2.28.1.2.1 P-10 Switch Ports/<10 circuits/Dispatch/FL(days)  B.2.28.1.1 P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)  B.2.28.2.2 P-10 Switch Ports/>=10 circuits/Dispatch/FL(days)  B.2.28.2.1 P-10 Local Interoffice Transport/<10 circuits/Dispatch/FL(days)  B.2.28.2.1 P-10 Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)  B.2.28.3.1 P-10 Loop + Port Combinations/<10 circuits/Dispatch/FL(days)  B.2.28.3.1 P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)  B.2.28.3.1 P-10 Compo Other/<10 circuits/Dispatch/FL(days)  B.2.28.4 1 P-10 Combo Other/<10 circuits/Dispatch/FL(days)  B.2.28.5.1 P-10 Combo Other/<10 circuits/Dispatch/FL(days)  B.2.28.5.1 P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)  B.2.28.5.1 P-10 Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)  B.2.28.5.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)  B.2.28.5.1 P-10 Combo Other/<10 circuits/Dispatch/FL(days)  B.2.28.6.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)  B.2.28.6.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)  B.2.28.6.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days)  B.2.28.7 P-10 Une Shanng/>=10	B 2 26.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
3.2.26.18.2.2   P-10   Digital Loop > DS1/>=10   Circuits/Non-Dispatch/FL(days)	B.2 26.18 1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
P-10   Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	8.2.26 18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
P-10   Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)	8 2.26.18.2.2		
P-10   Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)	B.2.26 19.1.1		
P-10   Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	B.2.26.19.1.2		
Total Sarvice Order Cvcle Time (offered) - Mechanized	B.2.26.19.2.1		
P-10   Switch Ports/<10 circuits/Dispatch/FL(days)	3 2.26.19.2.2	P-10	Digital Loop >= D\$1/>=10 circuits/Non-Dispatch/FL(days)
P-10   Switch Ports/<10 circuits/Non-Dispatch/Fi_(days)		Total S	ervice Order Cycle Time (offered) - Mechanized
P-10   Switch Ports/<10 circuits/Non-Dispatch/FL(days)	B 2.28 1 1.1		
P-10   Switch Ports/>=10 crcuts/Dispatch/FL(days)		P-10	
B 2.28 2.1.1  P-10			
B 2.28 2.1.1 P-10	B 2 28.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
P-10	B 2.28 2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)
R.2.2.8.2.2.2	B.2.28.2.1 2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
P-10	B.2.28 2 2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B 2.28 3.1 2 P-10	B.2.28.2.2 2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
P-10	B.2.28.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B.2.28.3.2.2 P-10 Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days) B.2.28.4.1.1 P-10 Combo Other/<-10 circuits/Dispatch/FL(days) B.2.28.4.2.1 P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days) B.2.28.4.2.2 P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days) B.2.28.5.1.1 P-10 xDSL (ADSL, HDSL and UCL)/<-10 circuits/Dispatch/FL(days) B.2.28.5.1.2 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) B.2.28.5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) B.2.28.5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) B.2.28.5.1 P-10 UNE ISDN/<-10 circuits/Dispatch/FL(days) B.2.28.6.1 P-10 UNE ISDN/<-10 circuits/Dispatch/FL(days) B.2.28.6.2.1 P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days) B.2.28.6.2.1 P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days) B.2.28.7.1 P-10 Line Sharing/<-10 circuits/Non-Dispatch/FL(days) B.2.28.7.1 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days) B.2.28.7.1 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days) B.2.28.7.2 P-10 W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	B 2.28 3 1 2	P-10	
B 2 28 4 1 1  P-10	B 2 28.3.2.1		
3.2.28.4.1.2 P-10 Combo Other/<10 circuits/Non-Dispatch/FL(days) 3.2.28.4.2.1 P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days) 3.2.28.4.2.2 P-10 Combo Other/>=10 circuits/Dispatch/FL(days) 3.2.28.5.1.1 P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) 3.2.28.5.1.2 P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) 3.2.28.5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) 3.2.28.5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) 3.2.28.6.1.1 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days) 3.2.28.6.1.2 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) 3.2.28.6.2.1 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) 3.2.28.7.1 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) 3.2.28.7.2 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) 3.2.28.7.2 P-10 Une Sharing/<10 circuits/Non-Dispatch/FL(days) 3.2.28.7.2.2 P-10 Une Sharing/>=10 circuits/Non-Dispatch/FL(days) 3.2.28.7.2 P-10 Une Sharing/>=10 circuits/Non-Dispatch/FL(days)	B.2.28 3 2.2		
B 2.28 4 2.1 P-10	B 2 28.4 1 1		
B. 2.28 4.2.2 P-10 Combo Other/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 5.1.1 P-10 xDSL (ADSL, HDSL and UCL)/>10 circuits/Dispatch/FL(days) B. 2.28 5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) B. 2.28 5.2.2 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 6.1.1 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days) B. 2.28 6.1.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days) B. 2.28 6.2.1 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B. 2.28 6.2.1 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B. 2.28 7.1 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B. 2.28 7.1 P-10 Line Sharing/<10 circuits/Dispatch/FL(days) B. 2.28 7.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 7.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.1 P-10 ZW Analog Loop Design/<10 circuits/Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 8.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 9.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 9.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B. 2.28 9.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)	B.2.28.4 1 2	P-10	
P-10   xDSL (ADSL, HDSL and UCL)/<10 crcuts/Dispatch/FL(days)		P-10	
B.2.28 5.1.2 P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days) B.2.28 5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) B.2.28 5.2.2 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days) B.2.28 6.1.2 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days) B.2.28 6.1.2 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B.2.28 6.2.2 P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days) B.2.28 6.2.2 P-10 UNE ISDN/>=10 circuits/Non-Dispatch/FL(days) B.2.28 7.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28 7.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28 7.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28 7.2 P-10 Line Sharing/=10 circuits/Non-Dispatch/FL(days) B.2.28 8.1 P-10 ZW Analog Loop Design/<10 circuits/Dispatch/FL(days) B.2.28 8.2 P-10 ZW Analog Loop Design/<10 circuits/Dispatch/FL(days) B.2.28 8.2.1 P-10 ZW Analog Loop Design/<10 circuits/Dispatch/FL(days) B.2.28 8.2.1 P-10 ZW Analog Loop Design/>=10 circuits/Dispatch/FL(days) B.2.28 9.1.1 P-10 ZW Analog Loop Design/>=10 circuits/Dispatch/FL(days)			
B.2.28.5.2.1   P-10   xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)			
B.2.28.5.2.2 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days) B.2.28.6.1.1 P-10 UNE ISDN/<10 circuits/Dispatch/FL(days) B.2.28.6.2.2 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B.2.28.6.2.2 P-10 UNE ISDN/>=10 circuits/Dispatch/FL(days) B.2.28.7.1 P-10 Line Sharing/<10 circuits/Dispatch/FL(days) B.2.28.7.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28.7.2 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28.7.2.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days) B.2.28.7.2.2 P-10 Line Sharing/=10 circuits/Non-Dispatch/FL(days) B.2.28.7.2.1 P-10 Line Sharing/=10 circuits/Dispatch/FL(days) B.2.28.7.2.1 P-10 ZW Analog Loop Design/<10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 ZW Analog Loop Design/<10 circuits/Non-Dispatch/FL(days) B.2.28.2.2 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.2.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.1.1 P-10 ZW Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)			
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B 2 28 7.1 1 P-10 Line Sharing/<10 circuits/Dispatch/FL(days)  B.2.28 7.2 P-10 Line Sharing/<10 circuits/Non-Dispatch/FL(days)  B 2.28 7.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)  B 2.28 7.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/FL(days)  B 2.28 8.1 P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)  B 2.28 8.2 P-10 2W Analog Loop Design/<10 circuits/Dispatch/FL(days)  B 2.28 8.2.1 P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)  B 2.28 8.2.2 P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)  B 2.28 9.1.1 P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)			
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B.2.28 8.1.2 P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days) B.2.28 8.2.1 P-10 2W Analog Loop Design/>=10 circuits/Dispatch/FL(days) B.2.28.9.2.2 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days) B.2.28.9.1.1 P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)			
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B 2.28.9.1.1 P-10 2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)			
B.2.28.9.12 IP-10 I2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)			
	B.2.28.9.1 2	P-10	IZW Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)

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28 9.2 1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
2.28.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
28.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
2 28 10 1 2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
28 10 2 1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
.28.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
28.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
28.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
8.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
3 11.2 2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
12 1 1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
2 1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
.2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
3.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
3.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
1.1	P-10	Other Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
511	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)	Diagnostic	
1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
5.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)	Diagnostic	
.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic	
.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic	3
.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic	
1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)		
2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)	Diagnostic Diagnostic	
.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
11	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)  Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
21	P-10	Digital Loop < DS1/>=10 circuits/bispatch/FL(days)	Diagnostic	
2	P-10 P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic	
1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)	Diagnostic	
1.2	P-10	Digital Loop >= DS1/>=10 circuits/NoiPDispatch/FL(days)	Diagnostic	
2.1 2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	, 848C
4.2		Service Order Cycle Time (offered) - Partially Mechanized		
11	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)	Diagnostic	
.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)	Diagnostic	
2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
1 1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/FL(days)	Diagnostic	
2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)	Diagnostic	
22	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	in the
1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)	Diagnostic	
.12	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)	Diagnostic	,
2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)	Diagnostic	
1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)	Diagnostic	
	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)	Diagnostic	
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1.2 2 1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)	Diagnostic	

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B.2 29 5 1 1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
B.2 29 5 1 2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2 29 5 2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B.2.29.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.6 1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)
B 2.29 6 1 2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
B 2.29 6 2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
B 2 29 6 2 2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
B 2.29.7 1 1	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
B 2.29 7 1.2	P-10	Line Shanng/<10 circuits/Non-Dispatch/FL(days)
B.2 29.7 2.1	P-10	Line Shanng/>=10 circuits/Dispatch/FL(days)
B.2.29.7 2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
B.2.29 8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
B.2.29.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B.2.29.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B 2.29.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B.2.29.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.29.9.2.1	P-10	2W Analog Loop Non-Design/>≈10 circuits/Dispatch/FL(days)
B.2.29.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2 29 10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B.2.29.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B.2 29.10 2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2.29.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B 2.29 11 1 2	P-10 P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)  2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.29.11.2 1 B.2.29.11.2 2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.11.2.2 B.2.29.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B.2.29.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B 2.29.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B 2.29 12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2 29 13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)
B 2.29 13.1 2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2 29.13 2 1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL(days)
B.2 29 13 2 2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.29 14 1 1	P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.29.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
B.2.29.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/FL(days)
B.2.29 14.2 2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
B.2.29.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.29.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.29.15.2 2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2 29.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.29.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.29 16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.29.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.29 17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.29 17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.29 17.2 1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B 2.29.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2 29.18.1 1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B.2.29.18.1 2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.29.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2.29.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B.2.29.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B.2.29 19 1 2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B 2.29 19 2 1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)

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B.2 29 19 2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)
	Total Se	ervice Order Cycle Time (offered) - Non-Mechanized
B.2.30 1.1 1	P-10	Switch Ports/<10 circuits/Dispatch/FL(days)
B 2.30 1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/FL(days)
B.2.30.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/FL(days)
B 2.30 1.2 2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/FL(days)
B.2.30 2.1.1	P-10	Local Interoffice Transport/<10 circults/Dispatch/FL(days)
B 2.30.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL(days)
B.2.30.2 2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/FL(days)
B.2.30 2 2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.3.1 1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/FL(days)
B 2 30.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL(days)
B.2 30.3 2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/FL(days)
B.2 30 3 2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL(days)
B.2.30 4 1 1	P-10	Combo Other/<10 circuits/Dispatch/FL(days)
B.2.30 4 1 2	P-10	Combo Other/<10 circuits/Non-Dispatch/FL(days)
B.2.30 4.2 1	P-10	Combo Other/>=10 circuits/Dispatch/FL(days)
B 2 30.4 2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL(days)
B.2 30.5 1 2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL(days)
B.2.30 5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL(days)
B.2 30.5.2 2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/FL(days)  UNE ISDN/<10 circuits/Non-Dispatch/FL(days)
B.2.30.6.1.2	P-10 P-10	UNE ISDN/>=10 circuits/Dispatch/FL(days)
B.2 30.6.2.1 B.2.30.6.2 2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.6.2 2 B.2.30.7 1.1	P-10	Line Sharing/<10 circuits/Dispatch/FL(days)
B.2.30.7 1.1 B 2 30 7.1 2	P-10	Line Sharing/<10 circuits/Non-Dispatch/FL(days)
B.2.30.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/FL(days)
B.2.30.7.2.1	P-10	Line Sharing/>=10 circuits/Non-Dispatch/FL(days)
B 2.30 8 1 1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/FL(days)
B 2 30.8 1 2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30.8 2 1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/FL(days)
B.2.30.8.2 2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL(days)
B 2.30.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL(days)
B.2.30.9 1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL(days)
B.2.30.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.10 1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL(days)
B.2.30 10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL(days)
B 2 30.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL(days)
B.2.30.10 2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL(days)
B 2.30.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL(days)
B.2.30 11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30 11.2 1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.30 11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.30 12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL(days)
B 2 30.12 1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30 12.2 1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL(days)
B 2.30.12.2 2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.30 13.1.1	P-14	ZW Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL(days)  ZW Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30.13.1.2	P-14 P-14	2W Analog Loop w/LNP Non-Design/ <a> 10 circuits/Non-Dispatch/FL(days)</a>
B 2 30 13 2.1	P-14 P-14	2W Analog Loop w/LNP Non-Design/>= 10 circuits/Non-Dispatch/FL(days)
B.2.30 13 2 2	P-14 P-10	Other Design/<10 circuits/Dispatch/FL(days)
B.2.30.14 1 1	P-10	Other Design/<10 circuits/Non-Dispatch/FL(days)
B 2.30.14.1.2 B.2 30.14.2.1	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.14.2.1 B 2.30.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/FL(days)
B 2 30.15 1.1	P-10	Other Non-Design/<10 circuits/Dispatch/FL(days)
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B 2.30 15 1 2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/FL(days)
B.2.30.15 2 1	P-10	Other Non-Design/>=10 circuits/Dispatch/FL(days)
B 2.30 15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/FL(days)
B 2.30 16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/FL(days)
B 2.30 16 1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B.2.30.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispetch/FL(days)
B.2.30 16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/FL(days)
B.2.30.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/FL(days)
B 2.30.17 2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/FL(days)
B.2.30.17 2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL(days)
B.2.30.18.1 1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/FL(days)
B 2 30 18 1 2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL(days)
B.2.30 18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/FL(days)
B.2 30.18.2 2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL(days)
B 2 30 19.1 1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/FL(days)
B 2 30.19.1 2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL(days)
B 2 30.19 2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/FL(days)
B.2.30.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL(days)
	Diecor	nnect Timeliness
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Benchmark / Analog
Diagnostic

>= 95% w in 15 min

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
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	Disconnect Timelines
B 2 31	P-13   LNP/FL(%)

B 2.32.15 2

B 2 32.16 1 B.2.32.16 2

B 2.32 17.1 B.2 32.17 2

B 2 32 18 1

B.2.32.18 2

P-6

P-6

P-6

B.2.32.1 1	P-8	Switch Ports/Dispatch/FL(%)
B.2 32 1.2	P-6	Switch Ports/Non-Dispatch/FL(%)
B.2.32.2.1	P-6	Local Interoffice Transport/Dispatch/FL(%)
B.2.32.2.2	P-6	Local Interoffice Transport/Non-Dispatch/FL(%)
B 2.32 3.1	P-6	Loop + Port Combinations/Dispatch/FL(%)
B.2.32.3.2	P-6	Loop + Port Combinations/Non-Dispatch/FL(%)
B.2.32.4.1	P-6	Combo Other/Dispatch/FL(%)
B.2.32 4.2	P-6	Combo Other/Non-Dispatch/FL(%)
B 2.32.5.1	P-6	xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)
B.2 32 5 2	P-6	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)
B.2 32.6.1	P-6	UNE ISDN/Dispatch/FL(%)
B.2 32.6.2	P-6	UNE ISDN/Non-Dispatch/FL(%)
B.2.32 7.1	P-6	Line Shanng/Dispatch/FL(%)
B 2 32.7.2	P-6	Line Sharing/Non-Dispatch/FL(%)
B.2.32.8.1	P-6	2W Analog Loop Design/Dispatch/FL(%)
B.2.32.8.2	P-6	2W Analog Loop Design/Non-Dispatch/FL(%)
B.2.32.9.1	P-6	2W Analog Loop Non-Design/Dispatch/FL(%)
B.2 32 9.2	P-6	2W Analog Loop Non-Design/Non-Dispatch/FL(%)
B.2.32.10.1	P-6	2W Analog Loop w/INP Design/Dispatch/FL(%)
B.2 32.10.2	P-6	2W Analog Loop w/INP Design/Non-Dispatch/FL(%)
B.2.32.11.1	P-6	2W Analog Loop w/INP Non-Design/Dispatch/FL(%)
B.2 32 11 2	P-6	2W Analog Loop w/INP Non-Design/Non-Dispatch/FL(%)
B.2.32.12.1	P-6	2W Analog Loop w/LNP Design/Dispatch/FL(%)
B.2.32.12.2	P-6	2W Analog Loop w/LNP Design/Non-Dispatch/FL(%)
B 2 32 13.1	P-6	2W Analog Loop w/LNP Non-Design/Dispatch/FL(%)
B.2.32.13.2	P-6	2W Analog Loop w/LNP Non-Design/Non-Dispatch/FL(%)
B.2.32.14 1	P-6	Other Design/Dispatch/FL(%)
B.2.32.14 2	P-6	Other Design/Non-Dispatch/FL(%)
B 2.32.15.1	P-6	Other Non-Design/Dispatch/FL(%)
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Other Non-Design/Non-Dispatch/FL(%)
INP (Standalone)/Dispatch/FL(%)
INP (Standalone)/Non-Dispatch/FL(%)

LNP (Standalone)/Non-Dispatch/FL(%) Digital Loop < DS1/Dispatch/FL(%)
Digital Loop < DS1/Non-Dispatch/FL(%)

LNP (Standalone)/Dispatch/FL(%)

% Completions w/o Notice or < 24 hours

Diagnostic
Diagnostic

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				Diagnostic
	35.14%	535		Diagnostic
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	belisouth Monthly State Summary									
	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
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B 2.32 19 1	P-8 Digital Loop >= DS1/Dispatch/FL(%)	Diagnostic			44 03%	134				Diagnostic -
B 2.32 19.2	P-6  Digital Loop >= DS1/Non-Dispatch/FL(%)	Diagnostic							dia.	Diagnostic
	% Cooperative Test Attempts for xDSL									
B.2 33.1	P-8  xDSL (ADSL, HDSL and UCL)/FL(%)	>= 95% of requests			100 00%	181				YES
B.2.33 2	P-8 xDSL Other/FL(%)	>= 95% of requests			100 00 70	101				123
	Service Order Accuracy									<u> </u>
B.2.34.1.1.1		<b>7</b>			00 5001					
B.2.34.1.1.1 B.2.34.1.1.2	P-11 Design (Specials)/<10 circuits/Dispatch/FL(%) P-11 Design (Specials)/<10 circuits/Non-Dispatch/FL(%)	>= 95% >= 95%			98 50%	200				YES
B.2.34.1.2.1	P-11 Design (Specials)/>=10 circuits/Dispatch/FL(%)	>= 95% >= 95%			100.00% 100.00%	30 35				YES
B.2 34.1.2.2	P-11 Design (Specials)/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			100 00%	35	-			YES
B.2.34.2.1.1	P-11 Loops Non-Design/<10 circuits/Dispatch/FL(%)	>= 95%			97.14%	35	-			YES
B.2.34.2.1.2	P-11 Loops Non-Design/<10 circuits/Non-Dispatch/FL(%)	>= 95%	:		94.67%	300	-			NO
B.2.34.2.2.1	P-11 Loops Non-Design/>=10 circuits/Dispatch/FL(%)	>= 95%			100 00%	70	- 1			YES
B.2.34.2.2.2	P-11 Loops Non-Design/>=10 circuits/Non-Dispatch/FL(%)	>= 95%			84 48%	58				NO
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	Unbundled Network Elements - Maintenance and Repair									
										j
	Missed Repair Appointments	<u> </u>								
B.3 1 1 1	M&R-1 Switch Ports/Dispatch/FL(%)	R&B (POTS)	10 96%	98,999						
B.3.1.1.2	M&R-1 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	1.59%	56,648					ļ	
B.3.1.2.1	M&R-1 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	0.00%	913	0 00%	2		0 00000		YES
B.3.1 2.2 B.3 1 3.1	M&R-1 Local Interoffice Transport/Non-Dispatch/FL(%)  M&R-1 Loop + Port Combinations/Dispatch/FL(%)	DS1/DS3 R&B	0.29%	685	0.00%	17		0 01325	0 2204	YES
B.3.1.3.2	M&R-1   Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	11 09% 1 64%	100,610 57,711	9 99% 3.01%	1,462 697		0.00827 0.00484	1 3289	YES
B 3.1 4 1	M&R-1 Combo Other/Dispatch/FL(%)	R&B&D - Disp	10.96%	101,957	0.00%	21	1.1	0 06816	-2 8340 1 6072	NO YES
B.3.1.4.2	M&R-1 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	10.96%	101,957	6 25%	16	- (60)	0 07809	0 6026	YES
B.3.1.5.1	M&R-1  xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	52.94%	2,055	0.00%	49		0 07215	7 3381	YES
B.3 1 5.2	M&R-1 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	5.99%	4,327	0 00%	22		0 05070	1 1805	YES
B.3.1 6.1	M&R-1 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	3 45%	232	0 00%	91	100000	0.02257	1 5279	YES
B 3.1 6.2	M&R-1 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1 56%	257	1.92%	52		0.01882	-0 1948	YES
B.3 1 7.1	M&R-1   Line Sharing/Dispatch/FL(%)	ADSL to Retail	52.94%	2,055	28.57%	14		0 13385	1 8209	YES
B.3.1 7 2	M&R-1 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	5.99%	4,327	13 89%	36		0 03970	-1 9907	NO
B.3.1.8.1	M&R-1   2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	11 09%	100,610	2 99%	869		0 01070	7 5668	YES
B.3.1.8 2	M&R-1 2W Analog Loop Design/Non-Dispatch/FL(%) M&R-1 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B - Disp	11 09%	100,610	0 00%	254		0.01972	5 6203	YES
B 3 1.9.1 B.3.1.9.2	M&R-1   2W Analog Loop Non-Design/Dispatch/FL(%)  M&R-1   2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT R&B (POTS) excl SB FT	10.94%	98,707 46,843	10 42% 13 33%	777 30		0.01124	0 4623	YES NO
B.3.1.10.1	M&R-1 Other Design/Dispatch/FL(%)	Design	2.64%	2,724	5 56%	18		0.02211 0.03793	-5 3571 -0 7677	YES
B.3.1.10.2	M&R-1 Other Design/Non-Dispatch/FL(%)	Design	0.95%	3,276	0 00%	9		0.03793	0 2928	YES
B.3.1.11.1	M&R-1 Other Non-Design/Dispatch/FL(%)	R&B	11.09%	100,610	5.88%	68	Contract Contract	0.03232	1 3662	YES
B.3 1 11 2	M&R-1 Other Non-Design/Non-Dispatch/FL(%)	R&B	1.64%	57,711	0.00%	53		0 01746	0 9399	YES
B 3.1 12 1	M&R-1 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	10 96%	98,999						[ · · · · · · · · · · · · · · · · · · ·
B.3.1.12.2	M&R-1   LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	1 59%	56,648						
	Customer Trouble Report Rate									
B.3.2.1.1	M&R-2   Switch Ports/Dispatch/FL(%)	R&B (POTS)	1.74%	5,676,973			ı i			
B.3.2.1.2	M&R-2 Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	1 00%	5,676,973	<del>                                     </del>					
B.3.2.2.1	M&R-2   Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	1.80%	50,783	0 16%	1,228		0 00387	4 2223	YES
B.3.2.2.2	M&R-2   Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	1.35%	50,783	1 38%	1,228		0 00335	-0 1058	YES
B3231	M&R-2   Loop + Port Combinations/Dispatch/FL(%)	R&B	1 67%	6,037,272	1.14%	128,212	20 20	0 00036	14 4423	YES
B.3 2.3.2	M&R-2 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	0 96%	6,037,272	0 54%	128,212		0 00028	14 9412	YES
B3241	M&R-2 Combo Other/Dispatch/FL(%)	R&B&D - Disp	1 53%	6,660,628	1.48%	1,416	0.000	0 00329	0 1450	YES
B3242	M&R-2 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	1 53%	6,660,628	1 13%	1,416		0 00329	1 2189	YES
B 3 2.5 1	M&R-2 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	0 70%	293,230	0 89%	5,528		0 00114	-1 6329	YES
B.3 2.5 2	M&R-2 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	1 48%	293,230	0 40%	5,528		0 00165	6 5346	YES
B.3.2 6.1	M&R-2 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	0.86%	27,117	1 47%	6,171		0 00130	-4 7455	NO
B3262	M&R-2 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	0 95%	27,117	0 84%	6,171	- 40	0 00137	0 7654	YES
B327.1	M&R-2 Line Sharing/Dispatch/FL(%)	ADSL to Retail	0 70%	293,230	1 24%	1,132	- 4 9-80 S	0 00249	-2 1498	NO
B.3.2.7.2	M&R-2 Line Shanng/Non-Dispatch/FL(%)	ADSL to Retail	1 48%	293,230	3 18%	1,132		0 00362	4 7121	NO

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
		_								
B.3 2.8.1	M&R-2 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	1 67%	6,037,272	1 11%	78,426	_	0 00046	12 0364	YES
B 3.2.8 2 B 3 2.9.1	M&R-2 2W Analog Loop Design/Non-Dispatch/FL(%)  M&R-2 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B - Disp	1.67%	6,037,272	0 32%	78,426		0 00046	28 9385	YES
B.3.2.9.2	M&R-2   2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	1 74%	5,676,973	1 44%	53,811		0 00057	5 1615	YES
B 3 2.10 1	M&R-2 Other Design/Dispatch/FL(%)	R&B (POTS) excl SB FT Design	0 83% 0 31%	5,676,973 884,052	0 06%	53,811		0 00039	19 5555	YES
B 3.2.10.2	M&R-2 Other Design/Non-Dispatch/FL(%)	Design	0.37%	884,052	1 39% 0.69%	1,299 1,299	- 43	0 00154	-6 9913	NO
B.3.2.11.1	M&R-2 Other Non-Design/Dispatch/FL(%)	R&B	1.67%	6,037,272	10.37%	656	-	0 00169 0 00504	-1 9067 -17 2590	NO NO
B.3.2.11.2	M&R-2 Other Non-Design/Non-Dispatch/FL(%)	1 R&B	0.96%	6,037,272	8 08%	656		0 00304	-17 2590 -18 6597	NO NO
B.3.2.12.1	M&R-2 LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	1 74%	5,676,973	0.00%	030		0.00362	-10 0091	NO
B.3.2.12.2	M&R-2 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	100%	5,676,973	<del>                                     </del>					<del></del>
	Maintenance Average Duration	•			<u> </u>				4	
B.3.3.1.1	M&R-3   Switch Ports/Dispatch/FL(hours)	R&B (POTS)	20.65	98.999	1		26 717		<del></del>	
B.3.3.1 2	M&R-3 Switch Ports/Non-Dispatch/FL(hours)	R&B (POTS)	6 35	56,648			13 453			
B.3.3.2 1	M&R-3   Local Interoffice Transport/Dispatch/FL(hours)	DS1/DS3	3.79	913	10.98	2	3 097	2 19259	-3 2764	NO
B.3.3.2 2	M&R-3   Local Interoffice Transport/Non-Dispatch/FL(hours)	DS1/DS3	2 24	685	1 85	17	15 156	3 72118	0 1046	YES
B.3.3.3.1	M&R-3 Loop + Port Combinations/Dispatch/FL(hours)	R&B	20 62	100,610	15 30	1,462	26 734	0 70425	7.5556	YES
B 3 3.3.2	M&R-3 Loop + Port Combinations/Non-Dispatch/FL(hours)	R&B	6 34	57,711	5 40	697	13 518	0 51510	1 8355	YES
B334.1	M&R-3 Combo Other/Dispatch/FL(hours)	R&B&D - Disp	20 55	101,957	4 43	21	32 823	7 16330	2 2506	YES
B.3.3 4 2	M&R-3 Combo Other/Non-Dispatch/FL(hours)	R&B&D - Disp	20.55	101,957	3 97	16	32.823	8 20639	2 0205	YES
B 3.3.5 1	M&R-3 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(hours)	ADSL to Retail	90.25	2,055	4 93	49	226 987	32 81099	2 6004	YES
B.3 3.5.2	M&R-3 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(hours)	ADSL to Retail	9.87	4,327	2.53	22	62 835	13 43037	0 5467	YES
B.3.3.6.1 B.3.3.6.2	M&R-3 UNE ISDN/Dispatch/FL/hours)	ISDN - BRI	7.91	232	4.73	91	17 722	2 19202	1 4515	YES
B 3 3.7 1	M&R-3 UNE ISDN/Non-Dispatch/FL(hours)  M&R-3 Line Shanng/Dispatch/FL(hours)	ISDN - BRI	2 60	257	4 23	52	11.759	1 78800	-0 9090	YES
B.3.3.7.2	M&R-3 Line Shanng/Non-Dispatch/FL(hours)	ADSL to Retail ADSL to Retail	90.25 9.87	2,055 4,327	21 74 11.39	14 36	226 987 62 835	60 87104	1 1254	YES
B.3.3.8.1	M&R-3 2W Analog Loop Design/Dispatch/FL(hours)	R&B - Disp	20 62	100,610	5.70	869	26 734	10 51589 0 91080	-0 1444 16 3867	YES
B 3.3.8 2	M&R-3   2W Analog Loop Design/Non-Dispatch/FL(hours)	R&B - Disp	20 62	100,610	2 62	254	26,734	1 67956	10 7198	YES YES
B.3.3.9.1	M&R-3   2W Analog Loop Non-Design/Dispatch/FL(hours)	R&B (POTS) excl SB FT	20.65	98.707	17.19	777	26.728	0 96264	3 5963	YES
B.3.3.9.2	M&R-3 2W Analog Loop Non-Design/Non-Dispatch/FL(hours)	R&B (POTS) excl SB FT	6 62	46,843	9 86	30	13 538	2 47254	-1 3082	YES
B.3.3.10.1	M&R-3 Other Design/Dispatch/FL(hours)	Design	13 19	2.724	5 48	18	152 753	36 12287	0 2133	YES
B.3.3.10.2	M&R-3 Other Design/Non-Dispatch/FL(hours)	Design	5.52	3,276	0.81	9	123 697	41 28897	0 1139	YES
B.3.3 11 1	M&R-3 Other Non-Design/Dispatch/FL(hours)	R&B	20 62	100,610	13 69	68	26 734	3 24308	2 1371	YES
B.3.3.11 2	M&R-3 Other Non-Design/Non-Dispatch/FL(hours)	R&B	6 34	57,711	4.63	53	13 518	1 85764	0 9215	YES
B 3 3 12.1	M&R-3 LNP (Standalone)/Dispatch/FL(hours)	R&B (POTS)	20 65	98,999			26 717			
B.3.3.12.2	M&R-3   LNP (Standalone)/Non-Dispatch/FL(hours)	R&B (POTS)	6 35	56,648			13 453			
	% Repeat Troubles within 30 Days	_								
B3411	M&R-4   Switch Ports/Dispatch/FL(%)	R&B (POTS)	20 02%	98,999						
B.3.4.1 2	M&R-4   Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	17 65%	56,648						
B.3.4 2.1	M&R-4 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	31 65%	913	50 00%	2		0.32925	-0 5572	YES
B.3.4.2.2	M&R-4 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	29 20%	685	0.00%	17		0 11163	2 6 1 5 4	YES
B.3 4.3.1 B.3.4.3.2	M&R-4   Loop + Port Combinations/Dispatch/FL(%)	R&B	19.96%	100,610	16.14%	1,462		0 01053	3 6217	YES
B.3.4.3.2 B.3.4 4.1	M&R-4   Loop + Port Combinations/Non-Dispatch/FL(%) M&R-4   Combo Other/Dispatch/FL(%)	R&B	17 71% 20 16%	57,711	12 20%	697		0.01455	3 7915	YES
B.3.4.4.2	M&R-4   Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp R&B&D - Disp	20 16%	101,957 101,957	33 33% 25 00%	21 16		0 08756	-1 5043	YES
B.3.4.5.1	M&R-4   xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	48.13%	2,055	18 37%	49	00000	0 10031	-0 4824 4 1204	YES YES
B3452	M&R-4   xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	41.90%	4,327	13 64%	22		0 10546	2 6800	YES
B.3.4.6.1	M&R-4 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	28.45%	232	27.47%	91		0 05581	0 1748	YES
B.3.4.6.2	M&R-4 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	28 40%	257	23 08%	52		0 06857	0 7770	YES
B.3.4.7.1	M&R-4 Line Sharing/Dispatch/FL(%)	ADSL to Retail	48 13%	2,055	28 57%	14	3000303	0 13399	1 4594	YES
B.3.4.7 2	M&R-4 Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	41 90%	4,327	30 56%	36	40.000000000000000000000000000000000000	0 08257	1 3738	YES
B 3 4.8.1	M&R-4 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	19 96%	100,610	20 37%	869	5000 50000	0 01362	-0 3033	YES
B.3.4 8 2	M&R-4 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	19 96%	100,610	15.35%	254		0 02511	1 8324	YES
B 3.4 9 1	M&R-4 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	19.97%	98,707	15 06%	777		0 01440	3 4111	YES
B.3.4 9 2	M&R-4 2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	17.60%	46,843	23 33%	30		0 06955	-0 8245	YES
B34101	M&R-4 Other Design/Dispatch/FL(%)	Design	35.46%	2,724	16 67%	18	( to 18 %)	0 11313	1 6614	YES
B34102	M&R-4 Other Design/Non-Dispatch/FL(%)	Design	33 76%	3,276	33 33%	9	At August	0 15785	0 0271	YES
B.3.4.11.1	M&R-4 Other Non-Design/Dispatch/FL(%)	R&B	19 96%	100,610	14.71%	68	190000	0 04848	1 0827	YES
B.3.4.11.2	M&R-4 Other Non-Design/Non-Dispatch/FL(%)	R&B	17 71%	57,711	5 66%	53		0 05246	2 2969	YEŞ

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	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
B 3 4 12.1	M&R-4   LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	20 02%	98,999	1					
B 3 4.12.2	M&R-4 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	17 65%	56,648						
	Out of Service > 24 hours	-					•			L
B 3 5 1.1	M&R-5   Switch Ports/Dispatch/FL(%)	R&B (POTS)	19 53%	62,552	T					3
B.3.5 1.2	M&R-5   Switch Ports/Non-Dispatch/FL(%)	R&B (POTS)	5.97%	16,111	<del></del>		-			
B3521	M&R-5 Local Interoffice Transport/Dispatch/FL(%)	DS1/DS3	0.00%	913	0.00%	2		0 00000		YES
B 3 5.2.2	M&R-5 Local Interoffice Transport/Non-Dispatch/FL(%)	DS1/DS3	0.29%	685	0.00%	17		0 01325	0 2204	YES
B 3.5 3.1	M&R-5 Loop + Port Combinations/Dispatch/FL(%)	R&B	19.54%	63,687	13 54%	990	•	0 01323	4 7300	YES
B 3 5.3 2	M&R-5 Loop + Port Combinations/Non-Dispatch/FL(%)	R&B	5 96%	16,589	4 87%	390		0 01212	0 8941	YES
B.3.5.4 1	M&R-5 Combo Other/Dispatch/FL(%)	R&B&D - Disp	19 16%	65,293	0.00%	21		0 08589	2 2303	YES
B.3.5.4.2	M&R-5 Combo Other/Non-Dispatch/FL(%)	R&B&D - Disp	19 16%	65,293	6 25%	16	1	0 09839	1 3116	YES
B 3.5 5.1	M&R-5 xDSL (ADSL, HDSL and UCL)/Dispatch/FL(%)	ADSL to Retail	52 94%	2,055	0.00%	49		0 07215	7 3381	YES
B 3.5.5.2	M&R-5 xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL(%)	ADSL to Retail	5 99%	4,327	0.00%	22	1	0.05070	1 1805	YES
B 3 5.6.1	M&R-5 UNE ISDN/Dispatch/FL(%)	ISDN - BRI	3 90%	231	0.00%	91		0 02395	1 6268	YES
B 3 5 6.2	M&R-5 UNE ISDN/Non-Dispatch/FL(%)	ISDN - BRI	1 56%	257	1.92%	52		0 01882	-0 1948	YES
B 3 5.7.1	M&R-5   Line Sharing/Dispatch/FL(%)	ADSL to Retail	52 94%	2.055	0.00%	0		0 0 1002	70 1340	YES
B.3 5.7.2	M&R-5   Line Sharing/Non-Dispatch/FL(%)	ADSL to Retail	5.99%	4.327	0.00%	0	(3.34.)			YES
B 3 5 8.1	M&R-5 2W Analog Loop Design/Dispatch/FL(%)	R&B - Disp	19 54%	63.687	2 99%	869		0 01354	12 2210	YES
B3582	M&R-5 2W Analog Loop Design/Non-Dispatch/FL(%)	R&B - Disp	19 54%	63.687	0 00%	254		0 02493	7 8390	YES
B.3.5 9.1	M&R-5 2W Analog Loop Non-Design/Dispatch/FL(%)	R&B (POTS) excl SB FT	19 53%	62,519	21 05%	57	content	0.05253	-0 2895	YES
B.3.5 9 2	M&R-5   2W Analog Loop Non-Design/Non-Dispatch/FL(%)	R&B (POTS) excl SB FT	5.93%	16,049	0 00%	1		0.23623	0 2511	YES
B.3.5.10.1	M&R-5 Other Design/Dispatch/FL(%)	Design	2 64%	2,724	5 56%	18		0.03793	-0 7677	YES
B.3.5.10.2	M&R-5 Other Design/Non-Dispatch/FL(%)	Design	0.95%	3.276	0 00%	9		0.03232	0 2928	YES
B35111	M&R-5 Other Non-Design/Dispatch/FL(%)	R&B	19 54%	63.687	11.11%	45		0.05913	1 4259	YES
B.3.5.11 2	M&R-5 Other Non-Design/Non-Dispatch/FL(%)	R&B	5 96%	16,589	0 00%	24		0.04834	1 2320	YES
B.3.5.12.1	M&R-5  LNP (Standalone)/Dispatch/FL(%)	R&B (POTS)	19 53%	62,552	"			0.04004	12020	1123
B.3.5.12.2	M&R-5 LNP (Standalone)/Non-Dispatch/FL(%)	R&B (POTS)	5 97%	16,111	<del>                                     </del>			i		
		, , ,			<u> </u>					
	Hadron died Meteorie Chemente - Differe	· · · · · · · · · · · · · · · · · · ·			******					
	Unbundled Network Elements - Billing									
	Invoice Accuracy									
B.4.1	B-1 [FL(%)	BST - State	98.32%	504,739,660	99 65%	\$6,409,812		0 00005	-259 7934	YES
	Mean Time to Deliver Invoices - CRIS						-			
B.4 2	B-2 Region(business days)	BST - Region	4.13	1	3.49	1,395				YES

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	'Equity
	Local Interconnection Trunks - Ordering									
C11	% Rejected Service Requests  O-7   Local Interconnection Trunks/FL(%)	Diagnostic			62.41%	141				Diagnostic
C 1.2	Reject Interval  [O-8     Local Interconnection Trunks/FL(%)	>= 85% w in 4 days			89.77%	88				YES
C.1.3	FOC Timeliness  [O-9   Local Interconnection Trunks/FL(%)	>= 95% w in 10 days			91 03%	156				NO
C 1 4	FOC & Reject Response Completeness O-11   Local Interconnection Trunks/FL(%)	>= 95%			94 16%	120	3			NO
C.1.5	FOC & Reject Response Completeness (Multiple Responses)  O-11   Local Interconnection Trunks/FL(%)	>= 95%								
	Local Interconnection Trunks - Provisioning			•				·		
C 2.1	Order Completion Interval  P-4   Local Interconnection Trunks/FL(days)  Held Orders	Parity w Retail	19.83	48	23.20	15	8 554	2 53016	-1 3306	YES
C 2.2	P-1  Local Interconnection Trunks/FL(days)	Parity w Retail	0 00	0	0 00	0	<u></u>			YES
C.2.3	% Jeopardies  [P-2   Local Interconnection Trunks/FL(%)  Average Jeopardy Notice Interval	Panty w Retail	0.00%	54	0.00%	25		0 00000		YES
C 2 4	P-2 [Local Interconnection Trunks/FL(hours)  % Missed Installation Appointments	95% >= 48 hrs							• .	
C 2 5	P-3 [Local Interconnection Trunks/FL(%)  % Provisioning Troubles within 30 Days	Parity w Retail	0.00%	48	0 00%	15		0 00000		YES
C.2.6	P-9   Local Interconnection Trunks/FL(%)  Average Completion Notice Interval	Parity w Retail	0 07%	6,138	0.00%	1,169	,	0 00081	0 8002	YES
C.2.7	P-5   Local Interconnection Trunks/FL(hours)  Total Service Order Cycle Time	Parity w Retail	51.58	40	13 94	15	104.014	31 49165	1 1952	YES
C.2.8	P-10   Local Interconnection Trunks/FL(days)  Total Service Order Cycle Time (offered)	Diagnostic		::::::::::::::::::::::::::::::::::::::						
C 2 9	P-10      Local Interconnection	Diagnostic	M. C. T. Selfons		5 W " " " W	W. 7.X.				
C.2 10 1 C.2.10.2	P-6   Local Interconnection Trunks/Dispatch/FL(%) P-6   Local Interconnection Trunks/Non-Dispatch/FL(%)	Diagnostic Diagnostic			100 00%	15				Diagnostic Diagnostic
C.2 11.1.1 C 2 11.1.2 C.2.11.2.1 C 2.11.2.2	P-11 Local Interconnection Trunks/<10 circuits/Dispatch/FL(%) P-11 Local Interconnection Trunks/<10 circuits/Non-Dispatch/FL(%) P-11 Local Interconnection Trunks/>=10 circuits/Dispatch/FL(%) P-11 Local Interconnection Trunks/>=10 circuits/Non-Dispatch/FL(%)	>= 95% >= 95% >= 95% >= 95%			100 00% 92 31% 100 00% 100 00%	53 26 1 3				YES NO YES YES
	Local Interconnection Trunks - Maintenance and Repair							-		
C 3 1 1 C.3 1 2	Missed Repeir Appointments  M&R-1   Local Interconnection Trunks/Dispatch/FL(%)  M&R-1   Local Interconnection Trunks/Non-Dispatch/FL(%)  Customer Trouble Report Rate	Panty w Retail Panty w Retail	33 33% 0.99%	9 101	0 00%	3 22		0 31427 0 02329	1 0607 0 4250	YES YES

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	BellSouth Monthly State Summary									
	Florida, November 2001	Benchmark /	BST	BST	CLEC	CLEC	Standard	Standard		
		Analog	Measure	Volume	Measure	Volume	Deviation	Error	ZScore	Equity
C.3 2 1	M&R-2   Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	0 00%	408,183	0 00%	144,207		0 00001	0 0866	YES
C.3 2.2	M&R-2   Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	0 02%	408,183	0 02%	144,207	ي بين	0 00005	1 9690	YES
	Maintenance Average Duration	_								
C.3 3 1	M&R-3 Local Interconnection Trunks/Dispatch/FL(hours)	Parity w Retail	883 50	9	5 68	3	1339 979	893 31907	0 9827	YES
C.332	M&R-3 Local Interconnection Trunks/Non-Dispatch/FL(hours)	Parity w Retail	1.17	101	0 98	22	3 772	0 88754	0 2128	YES
	% Repeat Troubles within 30 Days	_								
C 3 4.1	M&R-4 Local Interconnection Trunks/Dispatch/FL(%)	Parity w Retail	33.33%	9	0 00%	3		0 31427	1 0607	YES
C 3 4.2	M&R-4 Local Interconnection Trunks/Non-Dispatch/FL(%)	Parity w Retail	9 90%	101	4 55%	22	<u> </u>	0 07027	0 7621	YES
	Out of Service > 24 hours	_								·
C.3.5.1	M&R-5 Local Interconnection Trunks/Dispatch/FL(%)	Panty w Retail Panty w Retail	33 33% 0 99%	101	0 00%	22		0 31427 0 02329	1 0607 0 4250	YES YES
C.3.5.2	M&R-5   Local Interconnection Trunks/Non-Dispatch/FL(%)	J Parity W Retail	0 99%	1 101	1 000%	- 22		0 02329	0 4250	TES
	Local Interconnection Trunks - Billing									
				*						
C41	Invoice Accuracy  B-1  FL(%)	BST - State	98 32%	\$504,739,660	97.71%	\$6,987,186		0 00005 T	123 1955	NO
04.										السستفنيسيا
C.4.2	Mean Time to Deliver invoices - CABS  [B-2   Region(calendar days)	BST - Region	5 06		4 61	4,082				YES
O.4.2	D Z Trogon(cuonad cayo)				1					
	LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING									
	Trunk Group Performance - Aggregate									
C.5.1	TGP-1 IFL	>0 5% dif 2 consec. Hrs			0			-		YES

	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Équity
		•							2000.0	=quity
	Operations Support Systems - Pre-Ordering			***************************************		••••				
	% Interface Availability - CLEC,									
D111	OSS-2 EDVRegion(%)	>= 99 5%			100 00%		• • • • • • • • • • • • • • • • • • • •			YES
D112	OSS-2  HAL/Region(%)	>= 99.5%			100 00%					YES
D.1.1.3	OSS-2 LENS/Region(%)	>= 99 5%			99.92%					YES
D.1 1 4	OSS-2 LEO MAINFRAME/Region(%)	>= 99.5%			100.00%					YES
D.1 1.5	OSS-2 (LEO UNIX/Region(%)	>= 99.5%								
D116 D117	OSS-2 LESOG/Region(%)	>= 99.5% >= 99.5%			100 00%					YEŞ
D.1.1.8	OSS-2 TAG/Region(%) OSS-2 PSIMS/Region(%)	= 99.5% >= 99.5%			99.98% 100.00%				in the	YES YES
0.1.1.0	1. 2	32 3 70			100.00%	· · · · · · · · · · · · · · · · · · ·	yd gwysia			152
0404	% Interface Availability - BST & CLEC  OSS-2  ATLAS/COFF/Region(%)	7								
D.1.21 D 1 2 2	OSS-2 BOCRIS/Region(%)	>= 99 5% >= 99.5%			99 99%					YES
D123	OSS-2 DSAP/Region(%)	>= 99.5%			99 99% 99 98%				og i	YES YES
D.1 2.4	OSS-2 RSAG/Region(%)	>= 99.5%			99 99%					YES
D 1.2.5	OSS-2 SOCS/Region(%)	>≈ 99.5%			99.99%					YES
D.1.2 6	OSS-2  SONGS/Region(%)	>= 99.5%			99.99%					YES
D 1.2.7	OSS-2 DOE/Region(%)	>= 99 5%			100.00%					YES
D.128	OSS-2 LNP Gateway/Region(%)	>= 99.5%			100.00%			8.8%		YES
D.1.2.9	OSS-2 COG/Region(%)	>= 99.5%			100.00%					YES
D 1.2.10 D.1 2 11	OSS-2 DOM/Region(%) OSS-2 SOG/Region(%)	>= 99.5% >= 99.5%			99.99% 100.00%				100	YES
0.1211					100 00%			Section 1		YES
04044	Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)	7 pus para t 71. s	T		· · · · · · ·					
D.13.11 D1312	OSS-1 RSAG, by TN/Region(seconds) OSS-1 RSAG, by TN/Region(seconds)	RNS - RSAG, by TN + 2 sec ROS - RSAG, by TN + 2 sec	3 06	2,788,670	1.41	340,259	-			YES
D.1.3 2 1	OSS-1 RSAG, by ADDR/Region(seconds)	RNS - RSAG, by ADDR + 2 sec	3.18	6,413 8,259,421	1.41	340,259 222,420	-		6866	YES YES
D 1 3.2 2	OSS-1 RSAG, by ADDR/Region(seconds)	ROS - RSAG, by ADDR + 2 sec	4 86	749,470	1.26	222,420				YES
D 1.3.3 1	OSS-1 ATLAS/Region(seconds)	RNS - ATLAS + 2 sec	3.16	821,942	1 13	85,216	_		ecce ecc	YES
D 1 3.3.2	OSS-1 ATLAS/Region(seconds)	ROS - ATLAS + 2 sec	2 74	243,059	1 13	85,216				YES
D.1.3.4.1	OSS-1 DSAP/Region(seconds)	RNS - DSAP + 2 sec	2 79	1,488,382	0.59	1,232	1		to an experience	YES
D.1.3 4.2	OSS-1 DSAP/Region(seconds)	ROS - DSAP + 2 sec	2 68	296,523	0.59	1,232			911	YES
D.1.3 5 1	OSS-1 HAL/CRIS/Region(seconds)	RNS - CRSACCTS + 2 sec	10 10	4,803,641	1.42	1,176,815	- Janaina -			YES
D 1 3.5.2 D.1 3.6.1	OSS-1 HAL/CRIS/Region(seconds) OSS-1 COFF//Region(seconds)	ROS - CRSOCSR + 2 sec RNS - OASISBIG + 2 sec	3 29 4.74	536,898 10,143,203	1 42 6 33	1,176,815 47,940	- Z 100			YES
D.1.3.6.2	OSS-1 COFF/Region(seconds)	ROS - OASISBIG + 2 sec	5.53	643,081	6 33	47,940	-			NO NO
D.1.3.7.1	OSS-1 PSIMS/ORB/Region(seconds)	RNS - OASISBIG + 2 sec	4 74	10,143,203	0 10	96,782	-			YES
D.1 3 7.2	OSS-1 PSIMS/ORB/Region(seconds)	ROS - OASISBIG + 2 sec	5 53	643,081	0 10	96,782				YES
	Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)	<del></del>								
D.1.4.1 1	OSS-1 RSAG, by TN/Region(seconds)	RNS - RSAG, by TN + 2 sec	3 06	2,788,670	1 68	193,263				YES
D.1.4 1.2	OSS-1 IRSAG, by TN/Region(seconds)	ROS - RSAG, by TN + 2 sec	3 31	6,413	1 68	193,263				YES
D1421	OSS-1 RSAG, by ADDR/Region(seconds)	RNS - RSAG, by ADDR + 2 sec	3 18	8,259,421	172	44,039				YES
D 1 4 2.2	OSS-1 RSAG, by ADDR/Region(seconds)	ROS - RSAG, by ADDR + 2 sec	4 86	749,470	1 72	44,039				YES
D.1.4.3 1	OSS-1 ATLAS - MLH/Region(seconds)	Diagnostic								Diagnostic
D.1.4 3.2	OSS-1 ATLAS - MLH/Region(seconds)	Diagnostic					-		A	Diagnostic
D 1 4.4.1 D 1 4 4 2	OSS-1 ATLAS - DID/Region(seconds)	Diagnostic					1,000,000,000		3.4.0.95	Diagnostic
D 1 4.5.1	OSS-1 ATLAS - DID/Region(seconds) OSS-1 ATLAS - TN/Region(seconds)	Diagnostic RNS - ATLAS - TN + 2 sec	3 16	821,942	1.96	6,798				Diagnostic YES
D.1.4 5.2	OSS-1 ATLAS - Thregion(seconds)	ROS - ATLAS - TN + 2 sec	274	243,059	1.96	6,798	-	y":		YES
D.1 4 6.1	OSS-1 IDSAP/Region(seconds)	RNS - DSAP + 2 sec	279	1,488,382	2 65	253,701				YES
D.1 4 6.2	OSS-1 DSAP/Region(seconds)	ROS - DSAP + 2 sec	2 68	296,523	265	253,701	V		7 Lajibba	YES
D 1 4 7.1	OSS-1 HAL/CRIS/Region(seconds)	RNS - CRSACCTS + 2 sec	10 10	4,803,641	2 16	167,141	With the			YES
D 1 4 7.2	OSS-1  HAL/CRIS/Region(seconds)	ROS - CRSOCSR + 2 sec	3 29	536,898	2 16	167,141	a tibble			YES
D 1 4 8.1	OSS-1 CRSEINT/Region(seconds)	RNS - CRSACCTS + 2 sec		Current P	The same		\$ 1:2001, and	35471		
D1482	OSS-1  CRSEINT/Region(seconds)	ROS - CRSOCSR + 2 sec			THE CONTROL		g 8-1-2007) and	27.4.7.3		1

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
D 1 4 9.1	OSS-1  CRSECSRL/Region(seconds)	RNS - CRSACCTS + 2 sec				endicité et	c 2-1-2001; eu	#1471 ·	<u>.</u>	
D.1 4 9 2	OSS-1 CRSECSRL/Region(seconds)	ROS - CRSOCSR + 2 sec			This that he	qualicable afte	¥ 3-1-2001) and	D1471	·	
	Operations Support Systems - Maintenance and Repair									
	% Interface Availability - BST									
D.2.1	OSS-3  TAFVRegion(%)	>= 99 5%	100.00%							YES
	% Interface Availability - CLEC									
D.2.2 1	OSS-3   CLEC TAF/Region(%)	>= 99 5%			100 00%					YES YES
D.2.2.2	OSS-3 ECTA/Region(%)	>= 99.5%			99.98%				4	TES
	% Interface Availability - BST & CLEC	00 59/			00.000/					100
D.2.3 1	OSS-3   CRIS/Region(%)	>= 99 5% >= <b>99 5%</b>			99 99% 100 00%					YES YES
D.2.3.2 D 2.3.3	OSS-3 LMOS HOST/Region(%) OSS-3 LNP/Region(%)	>= 99.5%			100 00%					YES
D.2.3.4	OSS-3 MARCH/Region(%)	>= 99.5%			100 00%					YES
D.2.3.5	OSS-3   OSPCM/Region(%)	>= 99.5%			100 00%				in August	YES
D.2.3.6	OSS-3 Predictor/Region(%)	>= 99 5%			100 00%					YES
D.2.3.7	OSS-3  SOCS/Region(%)	>= 99 5%			99 99%					YES
	Average Response Interval							····		
D.2 4 1 1	OSS-4   CRIS/Region(%) <= 4 Seconds	Parity w Retail	95 48%	1,438,663	94.86%	92,121		0 00071	8 8276	NO
D.2.4 1 2	OSS-4   CRIS/Region(%) <= 10 Seconds	Parity w Retail Parity w Retail	98.87% 1 13%	1,438,663 1,438,663	99.23% 0 77%	92,121 92,121		0 00036	-9 9247 9 9247	YES YES
D.2.4.1.3 D 2.4.2.1	OSS-4   CRIS/Region(%) > 10 Seconds OSS-4   DLETH/Region(%) <= 4 Seconds	Panty w Retail	9.96%	40,891	9 48%	770		0 01089	0 4385	YES
D 2.4.2.1 D 2.4.2.2	OSS-4 DLETH/Region(%) <= 10 Seconds	Panty w Retail	79.74%	40,891	85 71%	770		0 01462	-4 0884	YES
D.2.4.2.3	OSS-4 DLETH/Region(%) > 10 Seconds	Panty w Retail	20 26%	40,891	14.29%	770		0.01462	4 0884	YES
D.2.4 3.1	OSS-4 DLR/Region(%) <= 4 Seconds	Parity w Retail	5.98%	31,830	14 51%	38,390	1 4 1	0 00180	-47 4622	YES
D 2.4 3.2	OSS-4 DLR/Regron(%) <= 10 Seconds	Parity w Retail	84 33% 15.67%	31,830	92 41% 7 59%	38,390	0.00	0 00276 0 00276	-29 3310 29 3310	YES YES
D 2.4.3.3	OSS-4 DLR/Region(%) > 10 Seconds	Parity w Retail Parity w Retail	99.68%	31,830 1,438,646	97 90%	38,390 93,350		0 000276	92 8188	NO
D 2 4 4.1 D.2.4 4.2	OSS-4   LMOS/Region(%) <= 4 Seconds OSS-4   LMOS/Region(%) <= 10 Seconds	Panty w Retail	99.81%	1,438,646	99 38%	93,350		0 00015	29 1786	NO
D.2 4.4.3	OSS-4 LMOS/Region(%) > 10 Seconds	Parity w Retail	0 19%	1,438,646	0 62%	93,350	23.5	0 00015	-29 1786	NO
D.2.4.5.1	OSS-4 LMOSupd/Region(%) <= 4 Seconds	Parity w Retail	98 42%	1,044,460	93.57%	53,540		0 00055	87 7760	NO
D.2.4.5.2	OSS-4 LMOSupd/Region(%) <= 10 Seconds	Parity w Retail	99.86%	1,044,460	96 17%	53,540		0.00017	222 5064	NO
D.2.4.5.3	OSS-4 LMOSupd/Region(%) > 10 Seconds	Parity w Retail	0 14%	1,044,460	3.83% 98.88%	53,540		0.00017 0.00068	-222 5064 13 1916	NO NO
D.2.4.6.1	OSS-4 LNP/Region(%) <= 4 Seconds	Parity w Retail Parity w Retail	99.77% 99.91%	104,122 104,122	98 88%	5,180 5,180	100	0 00043	3 8011	NO
D.2.4.6.2 D 2 4 6 3	OSS-4 LNP/Region(%) <= 10 Seconds OSS-4 LNP/Region(%) > 10 Seconds	Panty w Retail	0.09%	104,122	0.25%	5,180		0 00043	-3 8011	NO
D 2 4.7.1	OSS-4 MARCH/Region(%) <= 4 Seconds	Parity w Retail	29.95%	6,534	24 78%	464		0 02201	2 3478	NO
D.2.4.7.2	OSS-4   MARCH/Region(%) <= 10 Seconds	Parity w Retail	29.95%	6,534	24.78%	464	initia es	0 02201	2 3478	NO
D.2.4 7.3	OSS-4 MARCH/Region(%) > 10 Seconds	Parity w Retail	70.05%	6,534	75.22%	464	- 0.850	0 02201	-2 3478	NO
D.2.4 8 1	OSS-4   OSPCM/Region(%) <= 4 Seconds	Parity w Retail	51 15% 96 53%	4,645 4,645	49.33% 100 00%	75 75	3 2000	0.05818 0.02129	0 3125 -1 6279	YES YES
D.2.4.8.2	OSS-4 OSPCM/Region(%) <= 10 Seconds	Parity w Retail Parity w Retail	3 47%	4,645	0.00%	75	3 5555	0.02129	1 6279	YES
D 2 4.8 3	OSS-4   OSPCM/Region(%) > 10 Seconds OSS-4   Predictor/Region(%) <= 4 Seconds	Panty w Retail	20.18%	69,102	31.36%	5,721		0 00552	-20 2443	YES
D 2 4.9 1 D.2 4 9 2	OSS-4 Predictor/Region(%) <= 4 Seconds	Panty w Retail	20 18%	69,102	31.36%	5,721		0 00552	-20 2443	YES
D 2 4.9.3	OSS-4 Predictor/Region(%) > 10 Seconds	Parity w Retail	79.82%	69,102	68 64%	5,721		0 00552	20 2443	YEŞ
D.2.4.10.1	OSS-4   SOCS/Region(%) <= 4 Seconds	Parity w Retail	99.82%	214,410	99 88%	14,791		0 00036	-1 5089	YES
D 2.4.10.2	OSS-4 SOCS/Region(%) <= 10 Seconds	Panty w Retail	99 98%	214,410	99 99%	14,791		0 00011	-0 2182	YES YES
D 2.4.10.3	OSS-4 SOCS/Region(%) > 10 Seconds	Parity w Retail Parity w Retail	0.02% 68 95%	214,410 57,955	0.01% 67 62%	14,791 3,447	4.60	0 00011	0 2182 1 6325	YES
D.2 4 11.1	OSS-4 NRW/Region(%) <= 4 Seconds	Parity w Retail	99 32%	57,955	99 16%	3,447	1.	0 00011	1 1477	YES
D.2.4.11.2 D 2 4 11 3	OSS-4 NIW/Region(%) <= 10 Seconds OSS-4 NIW/Region(%) > 10 Seconds	Parity w Retail	0 68%	57,955	0 84%	3,447	7.45.6 2.45.6	0 00144	-1 1477	YES
224110	AAA . Itania adami al. 10 december	-								

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
	Collocation - Collocation									
E.1.1 1 E.1.1 2 E 1 1 3	Average Response Time C-1 Virtual/FL (calendar days) C-1 Physical Caged/FL (calendar days) C-1 Physical Cageless/FL (calendar days)	<= 15 days <= 15 days <= 15 days			9 6 3	2 66 8				YES YES YES
E.1.2.1 E.1.2.2 E.1.2.3 E.1.2.4 E.1.2.5 E.1.2.6 E.1.2.7 E.1.2.8 E.1.2.9	Average Arrangement Time  C-2 Virtual-FL (calendar days)  C-2 Virtual-Augments/FL (calendar days)  C-2 Virtual-Augments/FL (calendar days)  C-2 Physical Caged-Ordinary/FL (calendar days)  C-2 Physical Caged-Augments/FL (calendar days)  C-2 Physical Caged-Augments/FL (calendar days)  C-2 Physical Caged-Augments/FL (calendar days)  C-2 Physical Caged-Sugments/FL (calendar days)  C-2 Physical Cageless-Ordinary/FL (calendar days)  C-2 Physical Cageless-Augments/FL (calendar days)  C-2 Physical Cageless-Augments/FL (calendar days)	<= 60 days <= 45 days <= 60 days <= 90 days <= 45 days <= 90 days <= 90 days <= 90 days	A A		3 27 9 41 42	1 2 68 6 7				YES YES YES YES YES YES
E.1.3.1 E.1.3.2	% Due Dates Missed C-3 (Virtual/FL (%) C-3 (Physical/FL (%)	< 10% missed < 10% missed			0 00%	1 83				YES YES

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	Florida, November 2001	Benchmark <i>i</i> Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Ştandard Devration	Ştandard Error	ZScore	Equity
	General - Flow Through	· · · · · · · · · · · · · · · · · · ·				•				7
	% Flow Through Service Requests									
F.1.1.1	O-3 Summary/Region(%)	Diagnostic			86 31%	272,689				Diagnostic
F.1.1 2 F.1.1.3	O-3 Aggregate/Region(%) O-3 Residence/Region(%)	Diagnostic >= 95%			86.31% 89.40%	272,689 189,172				Diagnostic NO
F 1.1 4 F 1.1 5	O-3 Business/Region(%) O-3 UNE/Region(%)	>= 90% >= 85%			75 18% 79 66%	6,402 77,115				NO NO
F 1.1 3	% Flow Through Service Requests - Achieved	7-05/0			73 00 70					
F.1 2.1	O-3 Summary/Region(%)	Diagnostic			76 67%	306,989				Diagnostic
F 1.2 2 F 1.2.3	O-3 Aggregate/Region(%) O-3 Residence/Region(%)	Diagnostic Diagnostic			76 67% 82.07%	306,989 206,049				Diagnostic Diagnostic
F.1.2.4	O-3 Business/Region(%)	Diagnostic			53 26%	9,037				Diagnostic
F.1 2.5		Diagnostic	197		66.84%	91,903	11.		4 8 6 6	Diagnostic
F131	% Flow Through Service Requests - LNP  O-3   Summary/Region(%)	>= 85%			91 24%	10,560				YES
F.1.3 2	C-3 Aggregate/Region(%)	>= 85%			91.24%	10,560				YES
F 1 3 3 F 1.3.4	C-3 Residence/Region(%) C-3 Business/Region(%)	Diagnostic Diagnostic								Diagnostic Diagnostic
	General - Pre-Ordering	,,								
F.2.1	Loop Makeup Inquiry (Manual) PO-1  Loops/FL(%)	>= 95% w in 3 bus days			100 00%	26				YES
	Loop Makeup Inquiry (Electronic)									
F22	PO-2  Loops/FL(%)	>= 95% w in 1 min			9794%	1,312			<u>-</u>	YES
	General - Ordering									
	Service Inquiry with Firm Order									
F.3.1.1 F.3.1.2	O-10   xDSL (ADSL, HDSL and UCL)/FL(%) O-10   Local Interoffice Transport/FL(%)	>= 95% w in 5 bus days >= 95% w in 5 bus days			94 87% 100 00%	78 7				NO YES
1.0.1.2		,								
	General - Ordering									
F41	Average Speed of Answer  O-12 Region(seconds)	Parity w Retail	174 38	6,225,045	27 24	69,274				YES
F * 1	O-12 Inegian(seconds)									
	General - Maintenance Center									
E 5 1	Average Answer Time M&R-6   Region(seconds)	Parity w Retail	37 42	1,903,933	24.23	77,177				YES
F.5.1	mar-o (region(seconos)	Tuniy w rouss	57.42	1,500,555	24.20	,				120
	General - Operator Services (Toll)					_				
	Average Speed to Answer	500								000
F.6.1	OS-1  FL(seconds)	PBD			5 11					PBD
F.6.2	% Answered in 30 seconds OS-2  FL(%)	PBD			96 70%					PBD
			7-114							
	General - Directory Assistance					<del> </del>				
F.7.1	Average Speed to Answer  [DA-1   FL(seconds)	PBD			5 64					PBD

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	Florida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	ÇLEÇ Volume	Standard Deviation	Standard Error	ZScore	Equity
F.7.2	% Answered in 20 seconds DA-2  FL(%)	PBD			94 40%					PBD
	General - E911 ,									
F.8 1	Mean Interval [E-3   FL(hours)	PBD			1.59	1,214				PBD
F 8.2	% Accuracy [E-2  FL(%)	PBD			93.02%	610,637				PBN
F 8.3	% Timeliness E-1  FL(%)	PBD			100 00%	1,214				PBD
	General - Billing	-								
F 9 1	Usage Data Delivery Accuracy  [B-3   Region(%)	Panty w Retail	99 41%	5,419	100 00%	18,003		0 00119	-4 9742	YES
F.9 2	Usage Data Delivery Timetiness  [B-5   Region(%)	Parity w Retail	98 89%	31,341	98.37%	270,743,237		0 00059	8 7900	NO
F 9.3	Usage Data Delivery Completeness  B-4   Region(%)  Mean Time to Deliver Usage	Parity w Retail	99 85%	31,341	99.54%	270,743,237		0 00022	14 5662	NO
F 9.4	B-6   Region(days)  Recurring Charge Completeness	Panty w Retail	3.38	31,341	2.74	270,743,237				YES
F.9.5.1 F 9 5 2 F.9.5.3	B-7   Resale/FL(%)     B-7   UNE/FL(%)     B-7   Interconnection/FL(%)	Panty w Retail >= 90% >= 90%	85 67%	\$21,723,295	96.40% 98.44% 92.56%	\$1,071,710 \$509,744 \$13,077		0 00092	-117 0968	YES YES YES
F 9.6.1 F.9.6.2 F 9.6 3	Non-Recurring Charge Completeness  B-8 Resale/FL(%)  B-8 UNE/FL(%)  B-8 Interconnection/FL(%)	Parity w Retail >= 90% >= 90%	88 16%	\$27,022,168	95.23% 96.26% 73.99%	\$1,011,714 \$1,869,967 \$535,910		0.00095	-74 3954	YES YES NO
	General - Change Management  % Software Release Notices Sent On Time									
F.10.1	CM-1  FL(%)	>= 98% w in 30 days			100 00%	1				YES
F.10 2	Average Software Release Notice Delay Days [CM-2   FL(average)	>= 25 days prior to release								
F.10 3	% Change Management Documentation Sent On Time [CM-3   FL(%)	>= 98% w in 30 days			0.00%	11				NO
F.10.5	Average Documentation Release Delay Days [CM-4   FL(average)	>= 25 days prior to release			-16	1				NÖ
F.10 6	% CLEC Interface Outages Sent within 15 Minutes  CM-5   FL(%)	>= 97% w in 15 min			100 00%	24				YES
	General - New Business Requests									
F.11.1	% New Business Requests Processed within 30 Business Days  BFR-1 [Region(%)	>= 90% w in 30 bus days								
F 11 2 1 F.11 2 2	% Quotes Provided within X Business Days BFR-2A   Region(%) BFR-2B   Region(%)	>= 90% w in 10 bus days >= 90% w in 30 bus days			****				1	

#### **BellSouth Monthly State Summary** Florida, November 2001 BST CLEC Benchmark / BST CLEC Standard Standard Analog Measure Volume Measure Volume Deviation Error ZScore Equity F 11 2.3 BFR-2CIRegion(%) >= 90% w in 60 bus days General - Ordering Acknowledgement Message Timeliness >= 95% w in 30 min 100 00% 85,178 291,001 YES YES EDI/Region(%) TAG/Region(%) F.12 1.1 99.99% F.12.1.2 >= 95% w in 30 min Acknowledgement Message Completeness EDI/Region(%) TAG/Region(%) 100% 100% 100.00% 100.00% 85,178 291,001 YES YES F.12.2.1 F.12.2.2 General - Database Updates Average Database Update Interval D-1 LIDB/FL(hours) PRO 3 62 0.08 3 62 PBD F.13.1.1 Directory Listings/FL(hours) Directory Assistance/FL(hours) PBD PBD 26 0.08 F.13 1.2 F.13 1.3 PBD 3 20 PBD % Update Accuracy YES LIDB/FL(%) >= 95% 100 00% F.13.2.1 Directory Listings/FL(%) Directory Assistance/FL(%) F.13 2 2 F.13.2.3 >= 95% 100 00% YES >= 95% 100 00% YES % NXXs / LRNs Loaded by LERG Effective Date 100 00% YES F.13.3 D-3 Region(%) 100% 55

Panty w Retail

F.14 1

General - Network Outage Notification

M&R-7 |Region(minutes)

Mean Time to Notify CLEC of Major Network Outages

## BellSouth Monthly State Summary Florida, November 2001

	riorida, November 2001	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Nov-01 Equity
	Collocation - Collocation									
	Average Response Time									
E.1 1.1	C-1 Virtual/FL (calendar days)	<= 20 days			6 1	- 3				160
E 1.1.2	C-1 Physical Caged/FL (calendar days)	<= 30 days			6	- 66				YES YES
E.1 1.3	C-1 Physical Cageless/FL (calendar days)	<= 30 days			3	8				YES
	Average Arrangement Time	_						<u> </u>		123
E121	C-2 Virtual-Ordinary/FL (calendar days)	<= 50 days			3					\#F\$
E 1.2.2	C-2 Virtual-Extraordinary/FŁ (calendar days)	<= 75 days				<del>'</del>	-			YES
E.1 2.3	C-2 Physical Caged/FL (calendar days)	<= 90 days	1877		10	70	-			YES
E.124	C-2 Physical Cageless/FL (calendar days)	<= 60 days			40	10	· · · · · · · · · · · · · · · · · · ·			YES
E125	C-2 Physical Cageless-Extraordinary/FL (calendar days)	<= 90 days			45	3	100			YES
	% Due Dates Missed	_	-							.20
Ë 1.3 1	C-3 Virtual/FL (%)	< 5% missed			0.00%					· · · · · · · · · · · · · · · · · · ·
E.1 3.2	C-3 Physical/FL (%)	< 5% missed			0.00%	83				YES YES
		•			0.0070					TEO

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CLEC AGGREGATE REGION ALL SERVICES  BST AGGREGATE REGION - RETAIL RESIDENCE - RETAIL BUSINESS*	76.67%  FLOW-THROUGH %  94.60%  TBD	86.31%
BST AGGREGATE REGION - RETAIL RESIDENCE	FLOW-THROUGH % 94.60%	86.31%
REGION - RETAIL RESIDENCE	94.60%	
REGION - RETAIL RESIDENCE	94.60%	
REGION - RETAIL RESIDENCE		
- RETAIL RESIDENCE		
- RETAIL BUSINESS	IBD	ł
*NOTE: BellSouth is reinstituting the reporting of business retain Public Service Commission. BellSouth currently has no way to Operating System (ROS) interface used by business retail. BellService requests submitted from all sources, including manually of an accurate report and will reflect this measure as soon as its	measure flow through f South retail reports cap y. BellSouth has initiate	for the Regional ture all business ed the developmen

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Company Info					LSR PR	ROCESSING								FLOWT	HROUGH
, ,					L	ESOG						-			
	Me	echanized	Interface l	Jsed	Manual	Rejects		Validated		Errors					ļ
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Fl Through
#1	0	100	0	100	12	22	1	65	16	7	9	49	72.06%	. 75 38%	87 50%
#2	15	0	0	15	4	2	0	9	3	2	1	6	50.00%	66.67%	75 00%
#3	596	0	0	596	69	96	6	425	94	73	21	331	69.98%	77 88%	81 93%
#4	1,605	0	0	1,605	231	29	7	1,338	239	216	23	1,099	71 09%	82 14%	83 57%
#5	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 009
#6	5	0	0	5	0	0	0	5	0	0	0	5	100 00%	100 00%	100.00%
#7	22	0	0	22	5	1	0	16	5	4	1	11	55.00%	68 75%	73 33%
#8	0	0	8	8	6	1	0	1	1	1	0	0	0.00%	0 00%	0 00%
#9	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0.00%
#10	1,130	0	0	1,130	49	166	5	910	666	593	73	244	27 54%	26.81%	29 15%
#11	292	0	0	292	199	16	4	73	13	11	2	60	22.22%	82.19%	84 51%
#12	69	0	0	69	26	8	1	34	10	9	1	24	40.68%	70 59%	72.73%
#13	492	0	0	492	19	26	0	447	40	34	6	407	88.48%	91.05%	92 29%
#14	0	736	0	736	180	113	5	438	179	144	35	259	44.43%	59 13%	64.27%
#15	0	0	114	114	0	33	0	81	11	11	0	70	86.42%	86 42%	86 42%
#16	888	0	0	888	214	64	6	604	185	139	46	419	54.27%	69 37%	75 09%
#17	0	81	0	81	4	23	0	54	27	18	9	27	55 10%	50 00%	60 00%
#18	0	0	4	4	0	0	0	4	0	0	0	4	100 00%	100 00%	100 00%
#19	369	0	0	369	53	43	5	268	50	36	14	218	71.01%	81.34%	85 83%
#20	84	0	0	84	3	19	0	62	20	14	6	42	71 19%	67 74%	75.00%
#21	0	0	63	63	9	10	0	44	19	13	6	25	53.19%	56.82%	65 79%
#22	17	0	0	17	0	4	0	13	2	2	0	11	84 62%	84 62%	84 62%
#23	0	91	0	91	34	6	2	49	16	8	8	33	44 00%	67 35%	80 49%
#24	4	0	0	4	0	0	0	4	2	0	2	2	100.00%	50 00%	100 00%
#25	754	0	0	754	92	64	3	595	35	28	7	560	82.35%	94 12%	95 24%
#26	0	0	3	3	00	2	0	1	0	0	0	1	100 00%	100 00%	100 00%
#27	0	0	1	1 1	00	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#28	0	0	7	7	0	4	0	3	1	0	1	2	100 00%	66.67%	100 00%
#29	0	0	26	26	0	10	0	16	6	6	0	10	62 50%	62 50%	62 50%
#30	35	0 _	0	35	6	2	0	27	13	7	6	14	51 85%	51 85%	66 67%
#31	764	0	0	764	23	47	0	694	30	21	9	664	93 79%	95 68%	96 93%
#32	442	0	0	442	40	21	0	381	17	14	3	364	87.08%	95 54%	96 30%
#33	0	1	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0 00%
#34	2,110	0	0	2,110	107	191	15	1,797	174	148	26	1,623	86 42%	90 32%	91 64%

Company Info					LSR PR	ROCESSING								FLOWT	HROUGH
	1					ESOG						+		12000	I I I
	Me	echanized	Interface	Jsed	Manual	Rejects		Validated		Errors				-	
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#35	0	764	0	764	43	92	5	624	163	129	34	461	72.83%	73 88%	78 14%
#36	1,474	0	0	1,474	172	122	8	1,172	282	215	67	890	69 69%	75.94%	80 54%
#37	135	0	0	135	14	22	3	96	29	19	10	67	67.00%	69 79%	77 91%
#38	112	0	0	112	13	13	1	85	11	11	0	74	75 51%	87.06%	87 06%
#39	17	0	0	17	1	2	0	14	0	0	0	14	93 33%	100 00%	100 00%
#40	46	0	0	46	11	6	0	29	2	0	2	27	71 05%	93.10%	100.00%
#41	440	0	0	440	12	44	0	384	10	8	2	374	94 92%	97.40%	97 91%
#42	69	0	0	69	3	2	0	64	4	2	2	60	92 31%	93 75%	96 77%
#43	227	0	0	227	56	56	1	114	23	18	5	91	55 15%	79.82%	83 49%
#44	165	0	0	165	9	7	0	149	8	8	0	141	89 24%	94.63%	94 63%
#45	349	0	0	349	55	30	0	264	27	17	10	237	76 70%	89 77%	93 31%
#46	462	0	0	462	35	31	0	396	25	23	2	371	86 48%	93 69%	94 16%
#47	129	0	0	129	18	14	1	96	15	9	6	81	75.00%	84.38%	90 00%
#48	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#49	27,355	0	0	27,355	2,125	1,714	43	23,473	1,617	1,402	215	21,856	86.10%	93 11%	93 97%
#50	36	0	0	36	4	4	0	28	12	9	3	16	55 17%	57.14%	64 00%
#51	0	0	2	2	0	1	0	1	1	1	0	0	0 00%	0 00%	0 00%
#52	54	0	0	54	6	5	0	43	10	8	2	33	70 21%	76.74%	80 49%
#53	0	0	2	2	0	0	0	2	1	1	0	1	50 00%	50.00%	50 00%
#54	21	0	0	21	5	0	0	16	2	2	0	14	66 67%	87.50%	87 50%
#55	0	0	8	8	0	2	0	6	2	2	0	4	66.67%	66 67%	66 67%
#56	29	O	0	29	1	1	0	27	26	15	11	1	5 88%	3 70%	6 25%
#57	5,796	0	0	5,796	497	481	6	4,812	342	258	84	4,470	85 55%	92.89%	94 54%
#58	474	0	0	474	28	17	1	428	33	31	2	395	87 00%	92 29%	92 72%
#59	36	0	0	36	7	12	0	17	7	3	4	10	50.00%	58 82%	76 92%
#60	27	0	0	27	4	1	0	22	1	1	0	21	80 77%	95 45%	95 45%
#61	0	78	0	78	66	5	0	7	3	2	1	4	5 56%	57 14%	66 67%
#62	93	0	0	93	5	8	2	78	26	19	7	52	68 42%	66.67%	73 24%
#63	96	0	0	96	9	10	0	77	6	5	1	71	83 53%	92.21%	93 42%
#64	426	0	0	426	50	58	2	316	144	113	31	172	51 34%	54 43%	60 35%
#65	425	0	0	425	54	74	3	294	106	86	20	188	57.32%	63.95%	68 61%
#66	26	0	0	26	3	5	0	18	6	3	3	12	66.67%	66 67%	80 00%
#67	0	398	0	398	281	94	<u></u>	22	13	9	4	9	3 01%	40 91%	50 00%
#68	5.213	0	0	5,213	683	467	43	4.020	758	610	148	3,262	71 61%	81 14%	84 25%

GGREGATE ORDER TYPES															
Company Info	·				LSR PF	ROCESSING								FLOWT	HROUGH
					L	ESOG								'	
	Me	echanized	Interface L	lsed	Manual	Rejects		Validated		Errors					
Name	LENS	, EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#69	0	2,683	0	2,683	35	440	2	2,206	722	668	54	1,484	67 86%	67 27%	68 96%
#70	233	0	0	233	1	20	0	212	43	33	10	169	83 25%	79 72%	83 66%
#71	0	0	109	109	7	6	0	96	9	4	5	87	88.78%	90 63%	95 60%
#72	14	0	0	14	0	1	0	13	1	1	0	12	92.31%	92 31%	92.31%
#73	0	0	74	74	34	4	2	34	5	2	3	29	44 62%	85 29%	93 55%
#74	23	0	0	23	1	1	1	20	3	2	1	17	85.00%	85 00%	89 47%
#75	347	0	0	347	24	13	1	309	15	13	2	294	88 82%	95.15%	95 77%
#76	0	2	0	2	1	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#77	0	0	7	7	0	3	0	4	4	4	0	0	0.00%	0 00%	0 00%
#78	59	0	0	59	24	11	0	24	13	10	3	11	24 44%	45 83%	52 38%
#79	21	0	0	21	2	5	0	14	0	0	0	14	87 50%	100 00%	100 00%
#80	555	0	0	555	93	22	5	435	44	38	6	391	74.90%	89 89%	91 14%
#81	0	5	0	5	1	2	0	2	0	0	0	2	66.67%	100 00%	100 00%
#82	2,485	0	0	2,485	503	752	18	1,212	461	367	94	751	46.33%	61 96%	67 17%
#83	226	0	0	226	13	15	1	197	22	16	6	175	85 78%	88 83%	91 62%
#84	0	0	163	163	1	31	0	131	1	1	0	130	98.48%	99 24%	99 24%
#85	8	0	0	8	0	1	0	7	0	0	0	7	100.00%	100 00%	100 00%
#86	0	0	172	172	69	4	0	99	18	17	1	81	48 50%	81 82%	82 65%
#87	23	0	0	23	0	4	0	19	1	1	0	18	94.74%	94 74%	94 74%
#88	0	181	0	181	151	12	2	16	7	6	1	9	5 42%	56.25%	60 00%
#89	4	0	0	4	1	0	0	3	1	0	1	2	66.67%	66 67%	100 00%
#90	47	0	0	47	15	14	0	18	8	5	3	10	33 33%	55 56%	66 67%
#91	844	0	0	844	66	53	2	723	68	56	12	655	84 30%	90 59%	92 12%
#92	2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0 00%	0.00%
#93	0	0	395	395	2	21	2	370	6	5	1	364	98 11%	98.38%	98 64%
#94	27	0	0	27	0	2	0	25	1	0	1	24	100 00%	96 00%	100 00%
#95	46	0	0	46	7	14	0	25	11	5	6	14	53.85%	56 00%	73 68%
#96	0	1,816	0	1,816	133	139	1	1,543	255	219	36	1,288	78.54%	83 47%	85 47%
#97	1,476	0	0	1,476	78	121	7	1,270	265	208	57	1,005	77.85%	79 13%	82 85%
#98	0	0	1,426	1,426	158	87	5	1,176	204	160	44	972	75.35%	82 65%	85 87%
#99	17	0	0	17	2	2	0	13		1	0	12	80.00%	92.31%	92 31%
#100	16	0	0	16		1	1	14	<u>:</u>	+	<u>-</u>	14	100 00%	100 00%	100 00%
#101	1,231	0	0	1,231	72	141	5	1,013	188	137	51	825	79.79%	81.44%	85 76%
#102	87	0	0	87	4	3	0	80	4	3	1	76	91.57%	95 00%	96 20%

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AGGREGATE ORDER TYPES														-	
Company Info					LSR PF	ROCESSING								FLOWT	HROUGH
					L	ESOG			·		·····				
	Me	echanized	Interface l	Jsed	Manual	Rejects		Validated	<del></del>	Errors				1	
Name	LENS	, EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#103	0	14	0	14	0	4	1	9	8	7	1	1	12 50%	11 11%	12 50%
#104	0	0	287	287	0	18	0	269	51	51	0	218	81 04%	81.04%	81 04%
#105	404	0	0	404	12	25	6	361	144	97	47	217	66 56%	60 11%	69 11%
#106	13	0	0	13	2	0	0	11	5	4	1	6	50 00%	54 55%	60 00%
#107	452	0	0	452	108	98	2	244	41	31	10	203	59 36%	83 20%	86 75%
#108	0	0	897	897	3	35	1	858	17	14	3	841	98.02%	98 02%	98 36%
#109	146	0	0	146	15	11	4	116	22	18	4	94	74 02%	81.03%	83 93%
#110	69	0	0	69	1	9	0	59	5	2	3	54	94 74%	91 53%	96 43%
#111	790	0	0	790	136	32	2	620	39	28	11	581	77 99%	93 71%	95 40%
#112	25	0	0	25	5	0	1	19	3	3	0	16	66.67%	84 21%	84 21%
#113	0	50	0	50	8	10	0	32	24	10	14	8	30.77%	25 00%	44 44%
#114	571	0	0	571	114	75	6	376	123	87	36	253	55.73%	67 29%	74.41%
#115	0	747	0	747	275	213	8	251	106	70	36	145	29.59%	57 77%	67 44%
#116	802	0	0	802	106	113	15	568	150	108	42	418	66 14%	73 59%	79 47%
#117	137	0	0	137	46	18	2	71	13	8	5	58	51 79%	81 69%	87 88%
#118	474	0	0	474	46	45	9	374	72	59	13	302	74 20%	80 75%	83 66%
#119	0	0	8,634	8,634	61	359	6	8,208	293	238	55	7,915	96 36%	96 43%	97 08%
#120	3,436	0	0	3,436	256	206	33	2,941	372	294	78	2,569	82.37%	87.35%	89 73%
#121	0	0	19	19	3	7	0	9	1	1	0	8	66.67%	88.89%	88.89%
#122	36	0	0	36	11	18	0	7	1	0	1	6	35 29%	85 71%	100 00%
#123	0	2,037	0	2,037	280	308	27	1,422	475	367	108	947	59 41%	66 60%	72.07%
#124	120	0	0	120	16	17	1	86	20	12	8	66	70 21%	76 74%	84 62%
#125	191	0	0	191	48	18	1	124	22	16	6	102	61 45%	82 26%	86 44%
#126	557	0	0	557	15	11	1	530	53	43	10	477	89 16%	90 00%	91 73%
#127	0	58	0	58	2	10	0	46	10	7	3	36	80.00%	78.26%	83 72%
#128	150	0	0	150	0	6	0	144	10	8	2	134	94 37%	93.06%	94 37%
#129	10,636	0	0	10,636	284	638	19	9,695	364	298	66	9,331	94.13%	96 25%	96 91%
#130	2,314	0	0	2.314	263	120	7	1.924	233	204	29	1,691	78.36%	87 89%	89 23%
#131	10	0	0	10	0	0	0	10	0	0	0	10	100.00%	100 00%	100 00%
#132	0	2	0	2	0	0	0	2	2	- 0	2	0	0 00%	0 00%	0 00%
#133	1.546	0	0	1,546	262	414	18	852	367	248	119	485	48 74%	56 92%	66 17%
#134	10	0	0	10	3	3	0	4	0	0	0	4	57 14%	100 00%	100 00%
#135	0	0	180	180	29	56	3	92	43	33	10	49	44.14%	53 26%	59 76%
#136	33	0	0	33	<u></u>	0	<u>-</u>	28	2	2	. 0	26	78 79%	92 86%	92 86%

GGREGATE ORDER TYPES															
Company Info						ROCESSING								FLOWT	HROUGH
					L	ESOG								'	
	M	echanized	Interface t	Jsed	Manual	Rejects		Validated		Errors					1
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manuai Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#137	695	0	0	695	14	38	4	639	65	48	17	574	90 25%	89 83%	92.28%
#138	20	0	0	20	9	1	0	10	6	3	3	4	25 00%	40.00%	57 14%
#139	0	0	50	50	27	6	0	17	10	6	4	7	17.50%	41 18%	53 85%
#140	0	0	94	94	16	16	1	61	23	12	11	38	57 58%	62 30%	76 00%
#141	0	0	112	112	28	45	Q	39	18	11	7	21	35.00%	53 85%	65 63%
#142	0	0	418	418	126	50	6	236	103	62	41	133	41.43%	56 36%	68 21%
#143	87	0	0	87	8	24	1	54	3	0	3	51	86 44%	94 44%	100 00%
#144	4	0	0	4	0	0	0	4	2	2	0	2	50.00%	50 00%	50 00%
#145	0	0	1	1	0	0	0	1	1	0	1	0	0.00%	0 00%	0 00%
#146	52	0	0	52	0	3	13	36	25	21	4	11	34 38%	30.56%	34 38%
#147	0	9,678	0	9,678	48	3,208	3	6,419	3,418	197	3,221	3,001	92.45%	46 75%	93 84%
#148	1	0	0	1	0	0	0	1	1	0	1	0	0 00%	0.00%	0 00%
#149	2	0	0	2	1	0	0	1	0	0	0	1	50 00%	100.00%	100 00%
#150	284	0	0	284	27	56	0	201	51	35	16	150	70 75%	74 63%	81 08%
#151	333	0	0	333	57	33	3	240	37	31	6	203	69 76%	84.58%	86 75%
#152	458	0	0	458	32	24	5	397	69	50	19	328	80 00%	82 62%	86 77%
#153	5	0	0	5	1	1	0	3	1	1	0	2	50 00%	66.67%	66 67%
#154	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0.00%	0.00%
#155	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0 00%
#156	0	0	331	331	125	47	6	153	35	16	19	118	45.56%	77 12%	88 06%
#157	193	0	0	193	32	22	2	137	40	29	11	97	61.39%	70 80%	76 98%
#158	0	0	151	151	34	9	8	100	52	40	12	48	39.34%	48 00%	54 55%
#159	0	6,704	0	6,704	121	1,296	2	5,285	1,920	1,739	181	3,365	64.40%	63.67%	65 93%
#160	497	0	0	497	14	52	2	429	38	22	16	391	91 57%	91 14%	94 67%
#161	36	0	0	36	2	19	2	13	7	4	3	6	50.00%	46 15%	60 00%
#162	0	4,440	0	4,440	67	1,093	1	3,279	986	875	111	2,293	70 88%	69.93%	72 38%
#163	307	0	0	307	9	21	1	276	15	11	4	261	92.88%	94 57%	95 96%
#164	0	11	0	11	3	3	0	5	2	1	1	3	42.86%	60 00%	75.00%
#165	0	0	3	3	3	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#166	86	0	0	86	14	17	1	54	8	4	4	46	71.88%	85 19%	92 00%
#167	8	0	0	8	4	3	0	1	0	0	0	1 1	20.00%	100.00%	100 00%
#168	55	0	0	55	15	2	2	36	18	7	11	18	45 00%	50 00%	72 00%
#169	0	477	0	477	228	51	9	189	85	74	11	104	25 62%	55 03%	58 43%
#170	51	0		51	4	9	0	38	9	4	. 5	29	78 38%	76 32%	87 88%

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REGATE ORDER TYPES												<u> </u>		****	
Company Info						OCESSING						1		FLOWT	HROUGH
					L	ESOG								<b>l</b> '	
	Me	chanized	Interface	Jsed	Manual	Rejects		Validated		Errors					i
Nam <del>e</del>	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#171	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
#172	0	0	6	6	4	0	0	2	0	0	0	2	33 33%	100 00%	100 00%
#173	29	0	0	29	2	10	0	17	1	1	0	16	84.21%	94 12%	94 12%
#174	0	80	0	80	35	18	1	26	18	5	13	8	16 67%	30.77%	61 54%
#175	3	0	0	3	1	0	0	2	0	0	0	2	66 67%	100.00%	100 00%
#176	0	38,143	0	38,143	2,279	5,675	63	30,126	5,484	4,071	1,413	24,642	79.51%	81 80%	85 82%
#177	65	0	0	65	4	9	0	52	3	2	1	49	89.09%	94 23%	96 08%
#178	0	0	1	1	1	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#179	134	0	0	134	19	38	0	77	9	8	1	68	71 58%	88 31%	89.47%
#180	0	1,552	0	1,552	6	167	0	1,379	264	32	232	1,115	96 70%	80 86%	97.21%
#181	0	0	90	90	18	34	0	38	9	8	1	29	52.73%	76 32%	78 38%
#182	13	0	0	13	0	4	0	9	3	2	1	6	75.00%	66 67%	i 75 00%
#183	0	0	3	3	0	0	0	3	2	2	0	1	33.33%	33.33%	33 33%
#184	33	0	0	33	1	6	0	26	10	4	6	16	76 19%	61.54%	80 00%
#185	0	1,053	0	1,053	199	177	8	669	258	104	154	411	57.56%	61 43%	79 81%
#186	0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0.00%	0 00%
#187	2,976	0	0	2,976	179	386	3	2,408	116	92	24	2,292	89 43%	95 18%	96 14%
#188	106	0	0	106	17	8	1	80	15	14	1	65	67 71%	81 25%	82 28%
#189	582	0	0	582	77	26	0	479	36	25	11	443	81.28%	92 48%	94 66%
#190	301	0	0	301	21	6	0	274	14	13	1	260	88 44%	94 89%	95 24%
#191	8	0	0	8	3	2	0	3	0	0	0	3	50 00%	100 00%	100 00%
#192	23	0	0	23	0	2	0	21	1	1	0	20	95.24%	95.24%	95 24%
#193	0	0	5	5	0	3	0	2	0	0	0	2	100 00%	100 00%	100 00%
#194	5	0	0	5	0	0	0	5	4	4	0	1 1	20.00%	20 00%	20 00%
#195	0	18	0	18	3	5	0	10	2	0	2	8	72.73%	80 00%	100 00%
#196	50	0	0	50	6	4	0	40	2	2	0	38	82 61%	95 00%	95 00%
#197	0	0	546	546	62	49	9	426	114	83	31	312	68.27%	73 24%	78 99%
#198	0	0	262	262	30	28	0	204	53	28	25	151	72 25%	74 02%	84 36%
#199	355	0	0	355	53	28	6	268	62	37	25	206	69 59%	76 87%	84 77%
#200	457	0	0	457	54	37	0	366	30	24	6	336	81 16%	91 80%	93 33%
#201	23	0	0 -	23	1	6	0	16	7	6	. <u></u>	9	56 25%	56 25%	60 00%
#202	54,190	0	0	54,190	5,033	10,912	471	37,774	10,615	7,608	3,007	27,159	68 24%	71 90%	78 12%
#202	24	0	0	24	8	3	2	11	3	2	1	8	44 44%	72 73%	80 00%
#204	472	0	0	472	50	31	1	390	43	31	12	347	81 07%	88 97%	91 80%

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AGGREGATE ORDER TYPES												i		i	ı
Company Info					LSR PF	ROCESSING								FLOWT	HROUGH
•		-			L	ESOG								,	
	Me	chanized	Interface l	Jsed	Manual	Rejects		Validated		Errors		1	-		
Name	LENS	, EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#205	0	0	353	353	1	63	0	289	5	5	0	284	97.93%	98 27%	98 27%
#206	37	0	0	37	0	4	0	33	5	2	3	28	93.33%	84.85%	93.33%
#207	0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0 00%	0 00%
#208	186	0	0	186	24	20	6	136	65	51	14	71	48.63%	52 21%	58 20%
#209	120	0	0	120	19	20	6	75	30	16	14	45	56.25%	60 00%	73 77%
#210	1,382	0	0	1,382	103	99	2	1,178	62	44	18	1,116	88.36%	94 74%	96 21%
#211	293	0	0	293	4	20	0	269	21	19	2	248	91.51%	92.19%	92 88%
#212	249	0	0	249	22	46	4	177	62	52	10	115	60 85%	64 97%	68 86%
#213	446	0	0	446	38	35	0	373	18	14	4	355	87.22%	95 17%	96 21%
#214	384	0	0	384	55	21	0	308	13	9	4	295	82 17%	95 78%	97 04%
#215	851	0	0	851	57	11	0	783	30	26	4	753	90.07%	96 17%	96 66%
#216	31	0	0	31	1	6	0	24	4	3	1	20	83 33%	83.33%	86 96%
#217	1,205	0	0	1,205	94	15	2	1,094	30	26	4	1,064	89.86%	97.26%	97 61%
#218	2	0	0	2	1	0	0	1	1	1	0	0	0 00%	0.00%	0 00%
#219	14	0	0	14	5	3	0	6	2	2	0	4	36.36%	66 67%	66 67%
#220	0	15	0	15	2	0	1	12	6	4	2	6	50 00%	50 00%	60 00%
#221	0	0	3,170	3,170	15	443	37	2,675	1,108	659	449	1,567	69.92%	58.58%	70 40%
#222	0	0	2,484	2,484	98	29	16	2,341	511	431	80	1,830	77 58%	78 17%	80 94%
#223	12,006	0	0	12,006	672	566	13	10,755	274	215	59	10,481	92.20%	97 45%	97 99%
#224	622	0	0	622	42	32	5	543	22	14	8	521	90 29%	95 95%	97 38%
#225	254	0	0	254	33	83	0	138	22	12	10	116	72.05%	84 06%	90 63%
#226	20	0	0	20	2	3	0	15	8	8	0	7	41 18%	46 67%	46 67%
#227	6	0	0	6	0	1	0	5	0	0	0	5	100.00%	100.00%	100 00%
#228	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#229	113	0	0	113	25	20	1	67	21	19	2	46	51.11%	68 66%	1 : 70 77%
#230	0	47	0	47	13	9	0	25	16	14	2	9	25 00%	36 00%	39 13%
#231	21	0	0	21	4	2	1	14	4	2	2	10	62.50%	71.43%	83 33%
#232	98	0	0	98	7	13	0	78	21	18	3	57	69.51%	73 08%	76 00%
#233	285	0	0	285	61	21	0	203	29	22	7	174	67 70%	85 71%	88 78%
#234	179	0	0	179	16	54	3	106	52	30	22	54	54 00%	50 94%	64.29%
#235	68	0	0	68	12	4	1	51	16	9	<del></del> . 7	35	62 50%	68 63%	79 55%
#236	0	23	0	23	1	4	0	18	6	5	1	12	66.67%	66 67%	70 59%
#237	31	0	0	31	12	-	0	18	<u>×</u> 8	6	2	10	35.71%	55 56%	62 50%
#238	55	0	0 -	55	14	4	0	37		7	. <del></del> 1	29	58 00%	78 38%	80 56%

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Company Info					LSR PR	OCESSING								FLOWT	HROUGH
					LI	ESOG					· · · · · ·			1	T
	Me	chanized	Interface l	sed	Manual	Rejects		Validated		Errors				Ì	
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#239	4,333	0	0	4,333	136	127	10	4,060	165	124	41	3,895	93 74%	95 94%	96 91%
#240	0	27	0	27	6	1	0	20	3	1	2	17	70 83%	85 00%	94 44%
#241	4	0	0	4	0	0	0	4	2	2	0	2	50 00%	50 00%	50 00%
#242	140	0	0	140	31	8	0	101	8	6	2	93	71 54%	92 08%	93 94%
#243	127	0	0	127	10	7	14	96	60	13	47	36	61 02%	37.50%	73 47%
#244	1,681	0	0	1,681	52	119	9	1,501	1,007	880	127	494	34 64%	32 91%	35 95%
#245	24	0	0	24	2	3	0	19	2	0	2	17	89.47%	89 47%	100 00%
#246	31	0	0	31	2	2	0	27	3	3	0	24	82 76%	88 89%	88 89%
#247	6	0	0	6	1	0	0	5	2	2	0	3	50.00%	60.00%	60 00%
#248	1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%
#249	364	0	0	364	37	23	0	304	22	21	1	282	82 94%	92 76%	93 07%
#250	0	0	3	3	3	0	0	0	0	0	0	0	0 00%	0 00%	0.00%
#251	62	0	0	62	8	7	0	47	6	5	1	41	75 93%	87.23%	89.13%
#252	2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100 00%
#253	18	0	0	18	2	3	2	11	4	1	3	7	70.00%	63.64%	87 50%
#254	11	0	0	11	0	1	0	10	2	2	0	8	80 00%	80.00%	80 00%
#255	15	0	0	15	0	9	0	6	2	2	0	4	66.67%	66.67%	66 67%
#256	116	0	0	116	17	7	0	92	13	11	2	79	73.83%	85 87%	87 78%
#257	0	0	84	84	5	23	0	56	21	14	7	35	64.81%	62.50%	71 43%
#258	19	0	0	19	14	2	0	3	2	2 -!	0	1	5 88%	33 33%	33 33%
#259	14	0	0	14	2	4	0	8	1	1 7	0	7	70 00%	87.50%	87 50%
#260	8	0	0	8	1	2	0	5	0	0	0	5	83 33%	100.00%	100 00%
#261	253	0	0	253	24	33	4	192	55	42	13	137	67 49%	71.35%	76 54%
#262	1,115	0	0	1,115	104	91	0	920	31	25	6	889	87.33%	96 63%	97 26%
#263	2,104	0	0	2,104	36	90	3	1,975	206	119	87	1,769	91.94%	89 57%	93 70%
#264	17	0	0	17	4	1	0	12	4	3	1	8	53 33%	66 67%	72 73%
#265	13	0	0	13	4	2	0	7	2	2	0	5	45.45%	71 43%	71 43%
#266	5	0	0	5	0	<u></u> 1	0	4	1	1	0	3	75 00%	75 00%	75 00%
#267	2.507	0	0	2.507	264	203	14	2,026	232	171	61	1,794	80 48%	88 55%	91 30%
#268	0	0	14,030	14,030	1,202	2,771	71	9,986	3,232	2,186	1.046	6,754	66 59%	67 63%	75 55%
#269	1,450	0	0	1,450	261	135	8	1,046	169	122	47	877	69 60%	83 84%	87 79%
#270	225	0	0	225	32	13	1	179	109	9	- <del>T</del> ( )	169	80 48%	94.41%	94 94%
#270	14	0	0	14	2	4	0 -	8	0	0	0	8	80 00%	100 00%	100 00%
#271	0	0	1	1	2	<del></del>	0	0		0	 0	- 0	0 00%	0 00%	0 00%

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AGGREGATE ORDER TYPES								[							:
Company Info					LSR PF	ROCESSING								FLOWT	HROUGH
	·				L	ESOG	·							İ	
	Me	echanized	Interface t	Jsed	Manual	Rejects		Validated		Errors				Ī	ŀ
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#273	135	0	0	135	17	3	0	115	10	9	1	105	80 15%	91.30%	92.11%
#274	9	0	0	9	0	1	0	8	2	2	0	6	75 00%	75.00%	75 00%
#275	28	0	0	28	9	3	1	15	5	5	0	10	41 67%	66 67%	66 67%
#276	0	0	169	169	0	9	0	160	0	0	0	160	100 00%	100 00%	100 00%
#277	13	0	0	13	0	0	1	12	1	1	0	11	91.67%	91 67%	91 67%
#278	1,222	0	0	1,222	85	89	4	1,044	40	31	9	1,004	89.64%	96.17%	97 00%
#279	501	0	0	501	31	56	3	411	34	30	4	377	86.07%	91 73%	92 63%
#280	0	0	1	1	1	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#281	7,588	0	0	7,588	377	594	8	6,609	697	530	167	5,912	86 70%	89.45%	91 77%
#282	120	0	0	120	15	6	0	99	10	8	2	89	79.46%	89 90%	91 75%
#283	2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50 00%	50 00%
#284	35	0	0	35	1	4	0	30	5	2	3	25	89.29%	83 33%	92 59%
#285	1,104	0	0	1,104	112	136	2	854	29	19	10	825	86.30%	96 60%	97 75%
#286	1,339	0	0	1,339	86	86	3	1,164	78	62	16	1,086	88.01%	93.30%	94 60%
#287	0	12	0	12	0	6	0	6	3	3	0	3	50.00%	50 00%	50 00%
#288	0	0	23	23	0	7	0	16	3	3	0	13	81.25%	81 25%	81 25%
#289	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#290	0	0	94	94	38	19	0	37	23	10	13	14	22.58%	37 84%	58 33%
#291	101	0	0	101	11	10	1	79	23	19	4	56	65.12%	70 89%	74 67%
#292	26	0	0	26	1	1	3	21	4	1	3	17	89.47%	80 95%	94 44%
#293	249	0	0	249	27	17	1	204	43	36	7	161	71.88%	78.92%	81 73%
#294	2,244	0	0	2,244	203	236	12	1,793	151	117	34	1,642	83.69%	91.58%	93 35%
#295	31	0	0	31	4	0	0	27	8	3	5	19	73.08%	70.37%	86 36%
#296	50	0	0	50	11	6	0	33	4	3	1	29	67.44%	87.88%	90 63%
#297	0	0	14	14	5	0	0	9	5	1 1	4	4	40.00%	44.44%	80 00%
#298	14	0	0	14	1	4	0	9	5	3	2	4	50.00%	44 44%	57.14%
#299	1	0	0	1	0	00	0	1	0	0	0	1 1	100 00%	100 00%	100 00%
#300	1,306	0	0	1,306	128	139	13	1,026	235	188	47	791	71.45%	77 10%	80 80%
#301	437	0	0	437	56	22	1	358	28	19	9	330	81 48%	92 18%	94 56%
#302	0	0	<b>7</b> 2	72	2	16	0	54	24	14	10	30	65 22%	55 56%	68 18%
#303	0	0	13	13	0	2	0	11	3	2	1	8	80 00%	72 73%	80 00%
#304	0	0	337	337	47	44	2	244	62	47	15	182	65 94%	74 59%	79 48%
#305	672	0	0	672	108	39	11	514	116	91	25	398	66 67%	77 43%	81 39%
#306	87	0	0	87	22	9	0	56	17	10	7	39	54 93%	69 64%	79 59%

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Exhibit November PM Data Attachment 2F

AGGREGATE ORDER TYPES						1							1	1	
Company Info					LSR PR	OCESSING								FLOWT	HROUGH
					L	ESOG								1	
	Me	echanized	Interface t	Jsed	Manual	Rejects		Validated		Errors			-	]	1
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo
#307	9	0	0	9	0	2	0	7	0	0	0	7	100.00%	100 00%	100 00%
#308	599	0	0	599	60	36	4	499	39	34	5	460	83.03%	92 18%	93 12%
#309	8	0	0	8	1	1	0	6	0	0	0	6	85 71%	100 00%	100 00%
#310	47	0	0	47	23	7	4	13	4	3	1	9	25 71%	69 23%	75 00%
#311	398	0	0	398	40	23	3	332	53	27	26	279	80.64%	84 04%	91 18%
#312	122	0	0	122	6	5	0	111	7	7	0	104	88 89%	93 69%	93 69%
#313	6	0	0	6	5	0	0	1	1	1	0	0	0.00%	0 00%	0.00%
#314	1,826	0	0	1,826	1,675	20	1	130	10	3	7	120	6.67%	92.31%	97 56%
#315	1,362	0	0	1,362	1,238	36	6	82	33	19	14	49	3 75%	59 76%	72 06%
#316	30	0	0	30	4	13	0	13	9	2	7	4	40 00%	30 77%	66.67%
#317	14	0	0	14	2	0	0	12	0	0	0	12	85 71%	100 00%	100 00%
#318	321	0	0	321	275	3	1	42	12	6	6	30	9.65%	71 43%	83 33%
#319	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#320	2,837	0	0	2,837	293	159	11	2,374	140	106	34	2,234	84 85%	94 10%	95 47%
#321	1,647	0	0	1,647	119	76	8	1,444	85	65	20	1,359	88.08%	94.11%	95 44%
#322	1,061	0	0	1,061	98	49	3	911	79	67	12	832	83.45%	91.33%	92 55%
#323	0	0	1,305	1,305	361	9	81	854	366	316	50	488	41 89%	57 14%	60 70%
#324	2,012	0	0	2,012	207	88	2	1,715	100	87	13	1,615	84 60%	94.17%	94 89%
#325	214	0	0	214	19	10	5	180	18	10	8	162	84 82%	90.00%	94 19%
#326	105	0	0	105	6	1	0	98	5	3	2	93	91.18%	94 90%	96 88%
#327	0	119	0	119	4	48	0	67	28	16	12	39	66.10%	58 21%	70 91%
#328	58	0	0	58	14	8	0	36	9	6	3	27	57 45%	75.00%	81 82%
#329	0	0	17	17	0	5	0	12	4	4	0	8	66 67%	66 67%	66 67%
#330	13	0	0	13	0	0	1	12	2	1	1	10	90.91%	83 33%	90 91%
#331	30	0	0	30	5	8	0	17	6	3	3	11	57.89%	64 71%	78 57%
#332	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#333	234	0	0	234	16	33	14	171	70	42	28	101	63.52%	59 06%	70 63%
#334	163	0	0	163	29	15	1	118	18	16	2	100	68 97%	84 75%	86 21%
#335	211	0	0	211	30	25	2	154	28	16	12	126	73 26%	81 82%	88 73%
#336	0	0	7	7	4	0	1	2	2	0	2	0	0.00%	0 00%	0 00%
#337	1	0	0	1	0	0	0	1	0	0	0	1 1	100 00%	100 00%	100 00%
#338	45	0	0	45	9	1	0	35	5	5	0	30	68 18%	85 71%	85 71%
#339	83	0	0	83	9	6	4	64	30	26	4	34	49 28%	53 13%	56 67%
#340	0	171	0	171	18	26	0	127	36	29	7	91	65 94%	71 65%	75 83%

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Company Info		!			LSR PR	ROCESSING								FLOWT	HROUGH
					L	ESOG								'	Ī
	Me	echanized	Interface	Jsed	Manual	Rejects	i	Validated		Errors	****		-	İ	l
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Throu
#341	i 0	0	1	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00
#342	75	0	0	75	17	7	1	50	12	11	1	38	57 58%	76 00%	77 55
#343	474	0	0	474	82	25	0	367	14	12	2	353	78 97%	96.19%	96.71
#344	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100.00
#345	202	0	0	202	19	12	0	171	23	16	7	148	80 87%	86 55%	90 24
#346	0	0	50	50	2	9	1	38	4	2	2	34	89 47%	89.47%	94.449
#347	99	0	0	99	19	5	0	75	12	7	5	63	70.79%	84 00%	90 009
#348	2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100 00%	100 00
#349	0	0	818	818	10	116	1	691	12	8	4	679	97.42%	98 26%	98 84%
#350	458	0	0	458	43	23	6	386	38	24	14	348	83 86%	90 16%	93 559
#351	0	0	2	2	2	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#352	86	0	0	86	10	8	1	67	15	8	7	52	74 29%	77 61%	86 679
#353	467	0	0	467	31	18	0	418	11	9	2	407	91.05%	97 37%	97 849
#354	349	0	0	349	59	8	1	281	16	12	4	265	78 87%	94 31%	95 67%
#355	37	0	0	37	0	4	3	30	17	7	10	13	65 00%	43 33%	65 009
#356	0	0	11	11	0	2	0	9	0	0	0	9	100 00%	100 00%	100 00
#357	8	0	0	8	0	2	0	6	1	1	0	5	83 33%	83 33%	83 339
#358	138	0	0	138	17	5	1	115	9	6	3	106	82 17%	92.17%	94 649
#359	174	0	0	174	20	16	2	136	13	10	3	123	80.39%	90.44%	92 48%
#360	0	0	462	462	3	33	0	426	15	14	1	411	96.03%	96 48%	96 719
#361	28	0	0	28	0	1	0	27	2	1	1	25	96.15%	92 59%	96 159
#362	497	0	0	497	53	13	1	430	25	19	6	405	84 91%	94 19%	95 52%
#363	0	29	0	29	22	2	1	4	3	2	1	1	4 00%	25 00%	33 33%
#364	82	0	0	82	7	2	0	73	3	2	1	70	88 61%	95.89%	97 22%
#365	878	0	0	878	109	63	1	705	98	74	24	607	76 84%	86.10%	89 13%
#366	0	0	51	51	0	9	0	42	8	8	0	34	80 95%	80.95%	80.95%
#367	32	0	0	32	2	3	1	26	4	3	1	22	81 48%	84 62%	88 00
#368	32	0	0	32	2	3	1	26	4	4	0	22	78 57%	84 62%	84 629
#369	167	0	0	167	3	12	1	151	14	9	5	137	91 95%	90.73%	93 849
#370	0	61	0	61	8	17	0	36	15	9	6	21	55 26%	58 33%	70 00%
#371	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00
#372	0	31	0	31	3	7	0	21	4	2	2	17	77.27%	80.95%	89 479
#373	5	0	0	5	0	0	0	5	2	1	<del>-</del> 1	3	75 00%	60 00%	75 00%
#374	115	0	0	115	15	2	0	98	9	8	1	89	79.46%	90 82%	91 75%

AGGREGATE ORDER TYPES  Company Info					I SR PR	OCESSING		<del> </del>		†				FLOWT	HROUGH
Company IIIIC				-		ESOG								1	III.
	Me	chanized	Interface	Used	Manual	Rejects		Validated		Errors		1	-		
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#375	113	0	0	113	23	11	1	78	21	14	7	57	60 64%	73.08%	80 28%
#376	0	0	193	193	24	19	0	150	60	52	8	90	54 22%	60.00%	63 38%
#377	223	0	0	223	25	15	2	181	53	36	17	128	67.72%	70.72%	78 05%
#378	113	0	0	113	21	7	1	84	19	15	4	65	64 36%	77 38%	81 25%
#379	0	0	27	27	7	7	1	12	5	0	5	7	50 00%	58.33%	100 00%
#380	15	0	0	15	0	1	1	13	1	1 1	0	12	92 31%	92 31%	92 31%
#381	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#382	10	0	0	10	4	1	0	5	2	2	0	3	33 33%	60 00%	60 00%
#383	287	0	0	287	28	24	1	234	29	27	2	205	78.85%	87.61%	88 36%
#384	23	0	0	23	3	1	0	19	0	0	0	19	86 36%	100 00%	100 00%
#385	6	0	0	6	2	1	0	3	1	0	1	2	50.00%	66 67%	100 00%
#386	74	0	0	74	39	7	0	28	6	4	2	22	33 85%	78.57%	84 62%
#387	12	0	0	12	2	2	0	8	1	0	1	7	77 78%	87 50%	100 00%
#388	0	0	89	89	17	4	0	68	22	19	3	46	56 10%	67 65%	70 77%
#389	109	0	0	109	6	8	0	95	12	8	4	83	85 57%	87 37%	91 21%
#390	1,269	0	0	1,269	231	84	2	952	77	63	14	875	74.85%	91.91%	93 28%
#391	100	0	0	100	10	9	5	76	11	5	6	65	81 25%	85 53%	92 86%
#392	0	0	11	11	9	0	0	2	0	0	0	2	18 18%	100.00%	100 00%
#393	11	0	0	11	1	0	0	10	4	4	0	6	54 55%	60 00%	60.00%
#394	168	0	0	168	24	13	2	129	28	25	3	101	6/ 33%	78.29%	80 16%
#395	73	0	0	73	18	5	0	50	8	8	0	42	61.76%	84 00%	84 00%
#396	36	0	0	36	3	1	0	32	8	6	2	24	72 73%	<sup>j</sup> 75.00%	80 00%
#397	0	54	0	54	0	14	0	40	13	13	0	27	67.50%	67 50%	67 50%
#398	0	0	7	7	0	1	0	6	2	2	0	4	66 67%	66 67%	66 67%
#399	51	0	0	51	6	13	0	32	3	1	2	29	80.56%	90 63%	96 67%
#400	0	0	26	26	9	6	0	11	1	1	0	10	50 00%	90 91%	90 91%
#401	16	0	0	16	0	2	0	14	5	2	3	9	81.82%	64 29%	81 82%
#402	0	69	0	69	5	12	0	52	15	4	11	37	80 43%	71.15%	90 24%
#403	3	0	0	3	0	1	0	2	1	0	1	1	100 00%	50 00%	100 00%
#404	5	0	0	5	0	0		5	2	2	0	3	60.00%	60 00%	60 00%
#405	36	0	0	36	5	6	0	25	9	3	6	16	66.67%	64 00%	84 21%
#406	5	0	0	5	0	0	1	4	4	1 1	3	0	0 00%	0 00%	0 00%
#407	0	0	19	19	0	5	0	14	2	2	0	12	85 71%	85 71%	85 71%
, #408	0	10	0	10	5	1	0	4	<u>2</u>	2	2	0	0.00%	0 00%	0 00%

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GGREGATE ORDER TYPES	1														
Company Info					LSR PR	ROCESSING								FLOWT	HROUGH
					L	ESOG									
	Me	echanized	Interface l	Jsed	Manual	Rejects		Validated		Errors			_		
Name	LENS	, EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo
#409	155	0	0	155	22	20	1	112	15	12	3	97	74 05%	86 61%	88 99%
#410	0	0	43	43	9	10	1	23	6	4	2	17	56 67%	73 91%	80 95%
#411	184	0	0	184	26	9	0	149	7	6	1	142	81 61%	95.30%	95 95%
#412	3,350	0	0	3,350	255	233	9	2,853	435	287	148	2,418	81.69%	84.75%	89 39%
#413	2,202	0	0	2,202	213	151	11	1,827	108	77	31	1,719	85 <b>56%</b>	94 09%	95 71%
#414	0	0	702	702	111	70	4	517	137	104	33	380	63.87%	73.50%	78 51%
#415	586	0	0	586	95	26	12	453	80	58	22	373	70 91%	82 34%	86 54%
#416	64	0	0	64	18	11	0	35	1	1	0	34	64 15%	97.14%	97 14%
#417	1,030	0	0	1,030	67	115	1	847	61	45	16	786	87 53%	92.80%	94 58%
#418	0	0	212	212	15	6	22	169	149	115	34	20	13 33%	11 83%	14 81%
#419	353	0	0	353	41	28	22	262	174	127	47	88	34.38%	33 59%	40 93%
#420	0	0	96	96	11	40	3	42	20	13	7	22	47.83%	52.38%	62 86%
#421	729	0	0	729	69	64	16	580	109	55	54	471	79.16%	81 21%	89 54%
#422	1,681	0	0	1,681	78	159	3	1,441	95	73	22	1,346	89 91%	93.41%	94 86%
#423	505	0	0	505	73	12	0	420	21	18	3	399	81.43%	95 00%	95 68%
#424	69	0	0	69	9	2	0	58	3	3	0	55	82 09%	94 83%	94 83%
#425	56	0	0	56	2	3	0	51	4	2	2	47	92 16%	92.16%	95 92%
#426	3,106	0	0	3,106	355	453	24	2,274	545	394	151	1,729	69 77%	76.03%	81 44%
#427	1,713	0	0	1,713	150	146	14	1,403	406	287	119	997	69.53%	71.06%	77 65%
#428	301	0	0	301	17	21	0	263	19	13	6	244	89.05%	92 78%	94 94%
#429	0	0	169	169	30	44	6	89	32	15	17	57	55 88%	64 04%	79 17%
#430	135	0	0	135	22	50	5	58	27	14	13	31	46.27%	53.45%	68 89%
#431	12	0	0	12	0	0	0	12	2	2	0	10	83 33%	83.33%	83 33%
#432	0	269	0	269	1	25	4	239	69	49	20	170	77 27%	71 13%	77 63%
#433	531	0	0	531	130	43	9	349	56	38	18	293	63.56%	83.95%	88 52%
#434	934	0	0	934	98	84	0	752	29	24	5	723	85 56%	96 14%	96 79%
#435	44	0	0	44	3	14	0	27	11	8	3	16	59 26%	59.26%	66 67%
#436	981	0	0	981	118	58	4	801	79	52	27	722	80 94%	90 14%	93 28%
#437	0	0	977	977	125	134	14	704	296	244	52	408	52 51%	57.95%	62 58%
#438	0	0	993	993	164	122	8	699	253	209	44	446	54 46%	63.81%	68 09%
#439	1	0	0	1	0	0	0	1	1	1	0	0	0 00%	0.00%	0 00%
#440	1	0	0	1	1	0	0	0	0	† · · · · · · †	0	0	0.00%	0.00%	0 00%
#441	7	0	0	7		3	0	4		2	2	0	0.00%	0.00%	0 00%
#442	821	0	0	821	108	89	4	620	82	51	31	538	77 19%	86 77%	91 34%

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Company Info					LSR PR	OCESSING	<u> </u>							EL OWT	HROUGH
						ESOG						+		FLOWI	HROUGH
	M	echanized	Interface l	lead	Manual	Rejects		Validated		Errors		<del>i                                     </del>		ł	
			Tittoriaco (	7500	Markai	Rejects		Valluateu		I I					
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Fig Through
#443	0	82	0	82	47	13	0	22	7	5	2	15	22.39%	68 18%	75 00%
#444	0	0	4	4	0	0	2	2	1	1	0	1	50 00%	50 00%	50 00%
#445	0	0	4	4	1	0	0	3	3	3	0	0	0.00%	0 00%	0 00%
#446	0	0	2,140	2,140	382	295	41	1,422	526	405	121	896	53.24%	63 01%	68 87%
#447	23	0	0	23	3	2	0	18	1	1	0	17	80.95%	94 44%	94 44%
#448	160	0	0	160	12	9	1	138	21	14	7	117	81.82%	84 78%	89 31%
#449	0	0	7	7	0	5	0	2	1	0	1	1 1	100.00%	50 00%	100 00%
#450	0	0	1,114	1,114	167	158	8	781	259	206	53	522	58.32%	66 84%	: 71.70%
#451	136	0	0	136	25	25	1	85	43	28	15	42	44.21%	49 41%	60 00%
#452	88	0	0	88	8	3	5	72	19	12	7	53	72.60%	73 61%	81 54%
#453	263	0	0	263	25	13	3	222	24	15	9	198	83.19%	89 19%	92 96%
#454	0	0	2,181	2,181	299	285	8	1,589	498	380	118	1,091	61.64%	68 66%	74 17%
#455	0	0	2	2	1	0	0	1	0	0	0	1	50 00%	100.00%	100 00%
#456	2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0 00%
#457	0	250	0	250	15	132	0	103	49	39	10	54	50 00%	52 43%	58 06%
#458	67	0	0	67	15	8	1	43	18	7	11	25	53 19%	58 14%	78 13%
#459	0	0	13	13	0	3	0	10	1	0	1	9	100.00%	90 00%	100 00%
#460	52	0	0	52	3	7	1	41	20	5	15	21	72.41%	51 22%	80 77%
#461	0	0	13	13	1	0	0	12	1	1	0	11	84.62%	91 67%	91 67%
#462	1,858	0	0	1,858	184	62	4	1,608	83	73	10	1,525	85.58%	94 84%	95 43%
#463	317	0	0	317	36	14	2	265	45	33	12	220	76 12%	83 02%	86 96%
#464	374	0	0	374	48	15	0	311	21	18	3	290	81.46%	93 25%	94 16%
#465	2,407	0	0	2,407	211	244	53	1,899	412	256	156	1,487	76 10%	78 30%	85 31%
#466	0	0	22	22	2	4	0	16	4	2	2	12	75.00%	75.00%	85 71%
#467	14	0	0	14	0	3	0	11	1	1	0	10	90 91%	90 91%	90 91%
#468	1,162	0	0	1,162	138	217	6	801	91	62	29	710	78 02%	88 64%	91 97%
#469	135	0	0	135	14	24	0	97	23	21	2	74	67 89%	76 29%	77 89%
#470	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0.00%
#471	856	0	0	856	124	35	3	694	50	43	7	644	79 41%	92.80%	93 74%
#472	0	40	0	40	4	13	0	23	4	4	0	19	70 37%	82 61%	82 61%
#473	18	0	0	18	6	1	0	11	0	0	0	11	64 71%	100.00%	100 00%
#474	0	9	0	9	5	0	0	4	3	3		1 1	11,11%	25 00%	25 00%
#475	0	0	16	16	0	3	0	13	2	2	0	11	84 62%	84 62%	84 62%
#476	7	0	0	7	0	1	0	6	<u>-</u>	1	1	4	80 00%	66 67%	80 00%

Exhibit November PM Data Attachment 2F

AGGREGATE ORDER TYPES												1		1	
Company Info					LSR PF	ROCESSING								FLOWT	HROUGH
					L	ESOG									
	Me	chanized	interface l	Jsed	Manual	Rejects		Validated		Errors					1
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#477	0	2,557	0	2,557	161	729	4	1,663	644	300	344	1,019	68.85%	61 27%	77 26%
#478	1,289	0	0	1,289	297	103	19	870	293	193	100	577	54 08%	66.32%	74 94%
#479	0	147	0 '	147	25	31	0	91	23	13	10	68	64 15%	74 73%	83 95%
#480	86	0	0	86	24	6	1	55	11	3	8	44	61.97%	80 00%	93 62%
LENS Subtotal	247,612	0	0	247,612	25,421	26,043	1,368	194,780	29,058	21,709	7,349	165,722	77.86%	85.08%	88 42%
EDI Subtotal	0	76,022	0	76,022	4,840	14,278	152	56,752	15,414	9,286	6,128	41,338	74.53%	72 84%	81 66%
TAG Subtotal	0	0	47,330	47,330	4,039	5,814	377	37,100	8,805	6,339	2,466	28,295	73.16%	76.27%	81 70%
TOTAL INTERFACES	247,612	76,022	47,330	370,964	34,300	46,135	1,897	288,632	53,277	37,334	15,943	235,355	76.67%	81.54%	86.31%

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GGREGATE ORDER TYPES				i i										1	
Company Info					LSR PR	OCESSING							F	LOWTHROUG	SH .
					L	ESOG								1	
	Me	echanized	Interface l	Jsed	Manual	Rejects	Valid	lated		Errors				1	<u> </u>
Name	LENS	EDI.	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#1	1,605	0	0	1,605	231	29	7	1,338	239	216	23	1,099	71 09%	82.14%	83 57%
#2	1	0	0	111	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#3	13	0	0	13	4	0	0	9	2	1	1	7	58 33%	77 78%	87 50%
#4	7	0	0	7	0	4	0	3	1	0	1	2	100.00%	66 67%	100 00%
#5	58	0	0	58	8	3	2	45	3	3	0	42	79 25%	93 33%	93 33%
#6	42	0	0	42	12	5	1	24	6	5	1	18	51.43%	75.00%	78 26%
#7	492	0	0	492	19	26	0	447	40	34	6	407	88.48%	91.05%	92 29%
#8	0	26	0	26	1	6	0	19	8	5	3	11	64 71%	57 89%	68 75%
#9	0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100 00%	100 00%
#10	156	0	0	156	8	13	0	135	8	5	3	127	90 71%	94 07%	96 21%
#11	53	0	0	53	3	13	0	37	10	7	3	27	72.97%	72.97%	79 41%
#12	754	0	0	754	92	64	3	595	35	28	7	560	82.35%	94 12%	95 24%
#13	758	0	0	758	23	47	0	688	29	20	9	659	93 87%	95 78%	97.05%
#14	442	0	0	442	40	21	0	381	17	14	3	364	87 08%	95.54%	96 30%
#15	2,089	0	0	2,089	106	186	14	1,783	172	146	26	1,611	86 47%	90 35%	91 69%
#16	4	0	0	4	0	4	0	0	0	0	0	0	0.00%	0 00%	0 00%
#17	36	0	0	36	0	6	1	29	7	5	2	22	81.48%	75 86%	81 48%
#18	111	0	0	111	13	13	1	84	11	11	0	73	75.26%	86 90%	86 90%
#19	17	0	0	17	1	2	0	14	0	0	0	14	93 33%	100 00%	100 00%
#20	46	0	0	46	11	6	0	29	2	0	2	27	71 05%	93.10%	100 00%
#21	440	0	0	440	12	44	0	384	10	8	2	374	94 92%	97 40%	97 91%
#22	69	0	0	69	3	2	0	64	4	2	2	60	92 31%	93.75%	96 77%
#23	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0 00%
#24	161	0	0	161	9	6	0	146	8	8	0	138	89.03%	94 52%	94 52%
#25	345	0	0	345	54	29	0	262	27	17	10	235	76.80%	89 69%	93 25%
#26	461	0	0	461	35	31	0	395	25	23	2	370	86.45%	93.67%	94 15%
#27	79	0	0	79	6	4	0	69	3	3	0	66	88.00%	95 65%	95 65%
#28	27,355	0	0	27,355	2,125	1,714	43	23,473	1,617	1,402	215	21,856	86.10%	93.11%	93 97%
#29	0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50 00%
#30	21	0	0	21	5	0	0	16	2	2	0	14	66.67%	87 50%	87 50%
#31	5,796	0	0	5,796	497	481	6	4,812	342	258	84	4,470	85 55%	92.89%	94 54%
#32	474	0	0	474	28	17	1	428	33	31	2	395	87.00%	92 29%	92 72%
#33	27	0	0	27	4	1	0	22	1	1	<del></del>	21	80 77%	95 45%	95 45%
#34	83	0	0	83	6	5	0	72	4	<del> </del>	0	68	87.18%	94.44%	94 44%
#35	26	0	0	26	3	<u>-</u>	0	18	6	3	3	12	66.67%	66 67%	80 00%

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Company Info						OCESSING							F	LOWTHROUG	H
		<u> </u>				ESOG		<u> </u>	!	<u> </u>			-		
	Me	echanized	Interrace (	Jsed	Manual Total	Rejects	Valid Pending	ated	Total	Errors	CLEC		Percent		
Name	LENS	EDI ·	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Supps (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	Percent Flow Through
#36	433	0	0	433	44	57	3	329	47	37	10	282	77.69%	85.71%	88 40%
#37	0	2,683	0	2,683	35	440	2	2,206	722	668	54	1,484	67 86%	67 27%	68 96%
#38	233	0	0	233	1	20	0	212	43	33	10	169	83 25%	79 72%	83 66%
#39	0	0	109	109	7	66_	0	96	9	4	5	87	88.78%	90 63%	95 60%
#40	14	0	0	14	0	1	0	13	1	1	0	12	92.31%	92 31%	92 31%
#41	0	0	11	11	4	0	0	7	2	1	1	5	50 00%	71 43%	83 33%
#42	347	0	0	347	24	13	1	309	15	13	2	294	88.82%	95 15%	95 77%
#43	21	0	0	21	2	5	0	14	0	0	0	14	87.50%	100 00%	100 00%
#44	555	0	0	555	93	22	5	435	44	38	6	391	74 90%	89 89%	91 14%
#45	209	0	0	209	12	15	1	181	18	12	6	163	87 17%	90 06%	93 14%
#46	0	0	163	163	1	31	0	131	1	1	0	130	98.48%	99.24%	99 24%
#47	8	0	0	8	0	1	0	7	0	0	0	7	100 00%	100 00%	100 00%
#48	0	0	157	157	66	2	0	89	11	11	0	78	50.32%	87 64%	87 64%
#49	843	0	0	843	66	53	2	722	68	56	12	654	84 28%	90.58%	92 11%
#50	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0 00%	0.00%
#51	0	0	395	395	2	21	2	370	6	5	1	364	98.11%	98 38%	98 64%
#52	27	0	0	27	0	2	0	25	1	0	1	24	100.00%	96.00%	100 00%
#53	13	0	0	13	0	4	0	9	4	1	3	5	83.33%	55 56%	83.33%
#54	0	1,816	0	1,816	133	139	1	1,543	255	219	36	1,288	78 54%	83.47%	85 47%
#55	1,476	0	0	1,476	78	121	7	1,270	265	208	57	1,005	77.85%	79 13%	82 85%
#56	0	0	1,426	1,426	158	87	5	1,176	204	160	44	972	75 35%	82 65%	85 87%
#57	17	0	0	17	2	2	0	13	1	1	0	12	80.00%	92 31%	92 31%
#58	11	0	0	11	0	0	0	11	0	0	0	11	100 00%	100 00%	100 00%
#59	1,229	0	0	1,229	71	141	5	1,012	188	137	51	824	79.84%	81.42%	85 74%
#60	87	0	0	87	4	3	0	80	4	3	1	76	91 57%	95.00%	96 20%
#61	5	0	0	5	0	0	0	5	0	0	0	5	100 00%	100.00%	100 00%
#62	149	0	0	149	11	11	1	126	23	16	7	103	79 23%	81 75%	86 55%
#63	0	0	897	897	3	35	1	858	17	14	3	841	98 02%	98 02%	98 36%
#64	146	0	0	146	15	11	4	116	22	18	4	94	74 02%	81.03%	83 93%
#65	69	0	0	69	1	9	0	59	5	2	3	54	94 74%	91 53%	96 43%
#66	789	0	0	789	136	32	2	619	39	28	11	580	77 96%	93 70%	95 39%
#67	18	0 -	0	18	5	:0	1	12	1	1	0	11	64.71%	91 67%	91 67%
#68	0	22	0	22	4	4	0	14	10	6	4	4	28 57%	28 57%	40 00%
#69	138	0	0	138	15	17	2	104	34	27	7	70	62 50%	67 31%	72 16%
#70	9	0	0	9	0	7		2	1		,	1	50 00%	50 00%	50 00%

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AGGREGATE ORDER TYPES			<u> </u>				ļ								
Company Info						OCESSING							F	LOWTHROUG	SH
						ESOG								'	ļ
	Me	chanized	Interface (	Jsed	Manual	Rejects	Valid	ated		Errors		<u> </u>		l	]
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#71	12	0	0	12	0	1	0	11	0	0	0	11	100 00%	100 00%	100 00%
#72	220	0	0	220	21	15	2	182	27	23	4	155	77 89%	85 16%	87 08%
#73	0	0	8,634	8,634	61	359	6	8,208	293	238	55	7,915	96.36%	96 43%	97 08%
#74	3,428	0	0	3,428	256	203	32	2,937	372	294	78	2,565	82.34%	87 33%	89 72%
#75	0	117	0	117	20	18	2	77	33	18	15	44	53.66%	57 14%	70 97%
#76	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100.00%	100 00%
#77	120	0	0	120	27	8	0	85	11	8	3	74	67.89%	87 06%	90 24%
#78	557	0	0	557	15	11	1	530	53	43	10	477	89 16%	90 00%	91 73%
#79	0	58	0	58	2	10	0	46	10	7 7	3	36	80 00%	78.26%	83.72%
#80	150	0	0	150	0	6	0	144	10	8	2	134	94.37%	93 06%	94 37%
#81	10,617	0	0	10,617	278	632	18	9,689	364	298	66	9,325	94 18%	96 24%	96 90%
#82	2,314	0	0	2,314	263	120	7	1,924	233	204	29	1,691	78 36%	87 89%	89 23%
#83	10	0	0	10	0	0	0	10	0	0	0	10	100.00%	100 00%	100 00%
#84	0	2	0	2	0	0	0	2	2	0	2	0	0.00%	0 00%	0 00%
#85	211	0	0	211	13	48	1	149	52	25	27	97	71 85%	65 10%	79 51%
#86	10	0	0	10	3	3	0	4	0	0	0	4	57 14%	100 00%	100.00%
#87	0	0	2	2	0	1	0	1	0	0	0	1	100 00%	100.00%	100 00%
#88	33	0	0	33	5	0	0	28	2	2	0	26	78.79%	92.86%	92 86%
#89	695	0	0	695	14	38	4	639	65	48	17	574	90 25%	89.83%	92 28%
#90	74	0	0	74	3	18	0	53	2	0	2	51	94 44%	96 23%	100 00%
#91	0	9,677	0	9,677	47	3,208	3	6,419	3,418	197	3,221	3,001	92 48%	46 75%	93 84%
#92	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#93	101	0	0	101	5	16	0	80	13	10	3	67	81 71%	83 75%	87 01%
#94	219	0	0	219	34	20	1	164	20	17	3	144	73.85%	87.80%	89 44%
#95	416	0	0	416	31	20	4	361	62	47	15	299	79.31%	82 83%	86 42%
#96	0	0	226	226	61	36	6	123	20	7	13	103	60 23%	83.74%	93 64%
#97	45	0	0	45	2	10	0	33	7	4	3	26	81.25%	78 79%	86 67%
#98	0	6,704	0	6,704	121	1,296	2	5,285	1,920	1,739	181	3,365	64 40%	63 67%	65 93%
#99	497	0	0	497	14	52	2	429	38	22	16	391	91 57%	91 14%	94 67%
#100	0	4,440	0	4,440	67	1,093	1	3,279	986	875	111	2,293	70 88%	69 93%	72 38%
#101	307	0	0	307	9	21	1	276	15	11	4	261	92 88%	94 57%	95 96%
#102	33	0	0	33	0	. 2	0	31	4	2		27	93.10%	87 10%	93 10%
#103	0	0	1	1	0	· <del>-</del>	0	1	0		0	1	100 00%	100 00%	100 00%
#103	1	0	0	1	0	0	0	<del>'</del> 1	0	0	0	1	100 00%	100 00%	100 00%
#105	0	437	0	437	<u></u>	16	0	395	3	2	- 1	392	93 33%	99.24%	99 49%

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AGGREGATE ORDER TYPES				1						!		i	I		!
Company Info					LSR PR	OCESSING				Ĭ.			F	LOWTHROUG	SH .
					L	ESOG									
	M	echanized	Interface l	Jsed	Manual	Rejects	Valid	lated		Errors			1		1
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flor Through
#106	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#107	0	0	1	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#108	14	0	0	14	0	0	0	14	0	0	0	14	100.00%	100.00%	100 00%
#109	0	1,552	0	1,552	6	167	0	1,379	264	32	232	1,115	96 70%	80 86%	97 21%
#110	0	491	0	491	2	96	0	393	133	35	98	260	87 54%	66.16%	88 14%
#111	2,975	0	0	2,975	179	386	3	2,407	115	91	24	2,292	89 46%	95.22%	96 18%
#112	62	0	0	62	10	4	0	48	9	8	1	39	68 42%	81.25%	82 98%
#113	580	0	0	580	76	26	0	478	36	25	11	442	81.40%	92.47%	94 65%
#114	301	0	0	301	21	6	0	274	14	13	1	260	88.44%	94.89%	95 24%
#115	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#116	23	0	0	23	0	2	0	21	1	1	0	20	95.24%	95.24%	95 24%
#117	1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0 00%	0 00%
#118	50	0	0	50	6	4	0	40	2	2	0	38	82.61%	95 00%	95 00%
#119	0	0	199	199	7	23	0	169	44	23	21	125	80.65%	73 96%	84 46%
#120	257	0	0	257	18	19	5	215	46	24	22	169	80.09%	78.60%	87 56%
#121	457	0	0	457	54	37	0	366	30	24	6	336	81.16%	91 80%	93 33%
#122	51,208	0	0	51,208	4,643	10,343	417	35,805	9,889	7,119	2,770	25,916	68.78%	72 38%	78 45%
#123	9	0	0	9	4	2	0	3	0	0	0	3	42.86%	100 00%	100 00%
#124	472	0	0	472	50	31	1	390	43	31	12	347	81.07%	88 97%	91 80%
#125	0	0	353	353	1	63	0	289	5	5	0	284	97.93%	98 27%	98 27%
#126	37	0	0	37	0	4	0	33	5	2	3	28	93 33%	84.85%	93 33%
#127	20	0	0	20	0	10	0	10	4	3	1	6	66 67%	60 00%	66 67%
#128	1,365	0	0	1,365	101	91	2	1,171	61	43	18	1,110	88 52%	94.79%	96.27%
#129	293	0	0	293	4	20	0	269	21	19	2	248	91.51%	92.19%	92 88%
#130	44	0	0	44	3	6	2	33	11	6	5	22	70.97%	66.67%	78 57%
#131	445	0	0	445	38	35	0	372	18	14	4	354	87 19%	95.16%	96 20%
#132	384	0	0	384	<b>5</b> 5	21	0	308	13	9	4	295	82 17%	95 78%	97 04%
#133	850	0	0	850	57	11	0	782	30	26	4	752	90 06%	96 16%	96 66%
#134	1,184	0	0	1,184	91	12	2	1,079	30	26	4	1,049	89 97%	97 22%	97 58%
#135	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0.00%	0.00%
#136	0	1	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#137	0	0	3,139	3,139	15	436	37	2,651	1,096	650	446	1,555	70 05%	58 66%	70 52%
#138	0	0	2,484	2,484	98	29	16	2,341	511	431	80	1,830	77.58%	78 17%	80 94%
#139	12,006	0	0	12,006	672	566	13	10,755	274	215	59	10,481	92 20%	97 45%	97 99%
#140	621	0	0	621	42	32	5	542	22	14	8	520	90 28%	95 94%	97 38%

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Company Info	1		i		LSR PR	OCESSING							F	LOWTHROUG	2H
						ESOG			<del></del>				<u>'</u>	LOWINKOOC	1
	Me	echanized	Interface t	Jsed	Manual	Rejects	Valid	ated		Errors					
Name	LENS	EDI .	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#141	235	0	0	235	23	78	0	134	22	12	10	112	76.19%	83 58%	90 32%
#142	18	0	0	18	2	2	0	14	7	7	0	7	43 75%	50 00%	50 00%
#143	6	0	0	6	0	11	0	5	. 0	0	0	5	100 00%	100 00%	100 00%
#144	1	0	0	1	0	11	00	0	0	0	0	0	0 00%	0 00%	0 00%
#145	2	0	0	2	1	0	0	11	0	0	0	1	50 00%	100 00%	100 00%
#146	0	23	0	23	77	2	0	14	7	7	0	7	33.33%	50 00%	50 00%
#147	6	0	0	6	2	0	1	3	1	0	1	2	50.00%	66 67%	100 00%
#148	285	0	0	285	61	21	0	203	29	22	7	174	67.70%	85 71%	88 78%
#149	171	0	0	171	13	50	3	105	51	29	22	54	56.25%	51 43%	65 06%
#150	10	0	0	10	5	0	0	5	1	1	0	4	40.00%	80 00%	80 00%
#151	50	0	0	50	10	21	2	17	11	0	11	6	37.50%	35 29%	100 00%
#152	0	1	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#153	140	0	0	140	31	8	0	101	8	6	2	93	71 54%	92.08%	93 94%
#154	7	0	0	7	0	0	0	7	0	0	0	7	100.00%	100 00%	100 00%
#155	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#156	24	0	0	24	2	3	0	19	2	0	2	17	89 47%	89 47%	100 00%
#157	28	0	0	28	2	0	0	26	3	3	0	23	82.14%	88.46%	88 46%
#158	364	0	0	364	37	23	0	304	22	21	1	282	82.94%	92 76%	93 07%
#159	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#160	114	0	0	114	16	7	0	91	13	11	2	78	74.29%	85 71%	87 64%
#161	4	0	0	4	0	2	0	2	2	2	0	0	0.00%	0 00%	0 00%
#162	14	0	0	14	2	4	0	8	1	1	0	7	70.00%	87 50%	87 50%
#163	8	0	0	8	1	2	0	5	0	0	0	5	83.33%	100 00%	100 00%
#164	12	0	0	12	1	3	2	6	0	0	0	6	85.71%	100 00%	100 00%
#165	1,106	0	0	1,106	95	91	0	920	31	25	6	889	88.11%	96 63%	97 26%
#166	2,104	0	0	2,104	36	90	3	1,975	206	119	87	1,769	91.94%	89 57%	93 70%
#167	5	0	0	5	0	1	0	4	1	1	0	3	75 00%	75 00%	75 00%
#168	2,492	0	0	2,492	264	201	14	2,013	229	171	58	1,784	80.40%	88 62%	91 25%
#169	0	0	16	16	1	13	1	1	0	0	0	1	50.00%	100 00%	100 00%
#170	7	0	0	7	0	4	0	3	1	1	0	2	66.67%	66 67%	66 67%
#171	225	0	0	225	32	13	1	179	10	9	1	169	80 48%	94.41%	94 94%
#172	14	0	0	14	2	4	0	8	0	0	0	8	80 00%	100 00%	100 00%
#173	135	0	0	135	17	3	0	115	10	9	1	105	80 15%	91 30%	92 11%
#174	9	0	0	9	0	1	0	8	2	2	_ 0	6	75 00%	75.00%	75 00%
#175	0	0	169	169	0	9		160	0	0 -	o O	160	100 00%	100 00%	100 00%

Company Info					LSR PR	OCESSING		!				<u> </u>	F	LOWTHROUG	SH .
					L.	ESOG								1	
	Me	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors				Ī	]
Name	LENS	EDI ·	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#176	13	0	0	13	0	0	1	12	1	1	0	11	91 67%	91 67%	91 67%
#177	1,222	0	0	1,222	85	89	4	1,044	40	31	9	1,004	89 64%	96 17%	97 00%
#178	492	0	0	492	28	54	3	407	34	30	4	373	86.54%	91 65%	92 56%
#179	0	0	1	1	1	0	0	0	0	0	0	0	0 00%	0.00%	0 00%
#180	7,588	0	0	7,588	377	594	8	6,609	697	530	167	5,912	86.70%	89 45%	91 77%
#181	120	0	0	120	15	6	0	99	10	8	2	89	79.46%	89 90%	91 75%
#182	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#183	24	0	0	24	1	3	0	20	1	1	0	19	90 48%	95.00%	95 00%
#184	1,083	0	0	1,083	111	129	2	841	26	17	9	815	86 43%	96 91%	97.96%
#185	1,335	0	0	1,335	85	86	2	1,162	78	62	16	1,084	88 06%	93 29%	94 59%
#186	0	0	2	2	0	0	0	2	1	0	1	1	100 00%	50 00%	100 00%
#187	34	0	0	34	4	1	1	28	7	5	2	21	70.00%	75.00%	80.77%
#188	2,244	0	0	2,244	203	236	12	1,793	151	117	34	1,642	83 69%	91 58%	93 35%
#189	50	0	0	50	11	6	0	33	4	3	1	29	67 44%	87 88%	90 63%
#190	11	0	0	11	1	3	0	7	4	2	2	3	50 00%	42 86%	60 00%
#191	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#192	5	0	0	5	0	0	0	5	1	1	0	4	80 00%	80.00%	80 00%
#193	435	0	0	435	56	21	1	357	28	19	9	329	81 44%	92 16%	94 54%
#194	9	0	0	9	0	2	0	7	0	0	0	7	100.00%	100 00%	100 00%
#195	599	0	0	599	60	36	4	499	39	34	5	460	83 03%	92 18%	93 12%
#196	3	0	0	3	1	1	0	1	0	0	0	1	50.00%	100.00%	100 00%
#197	396	0	0	396	38	23	3	332	53	27	26	279	81 10%	84 04%	91 18%
#198	122	0	0	122	6	5	0	111	7	7	0	104	88.89%	93 69%	93 69%
#199	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#200	14	0	0	14	2	0	0	12	0	0	0	12	85.71%	100 00%	100 00%
#201	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#202	2.836	0	0	2,836	293	159	<b>1</b> 1	2,373	140	106	34	2,233	84 84%	94 10%	95 47%
#203	1.646	0	0	1,646	119	76	8	1,443	85	65	20	1,358	88.07%	94 11%	95 43%
#204	1,049	0	0	1,049	95	49	3	902	79	67	12	823	83 55%	91 24%	92 47%
#205	2,012	0	0	2,012	207	88	2	1,715	100	87	13	1,615	84 60%	94 17%	94 89%
#206	214	0	0	214	19	10	5	180	18	10	8	162	84 82%	90 00%	94 19%
#207	104	† <del>-</del> 0	0	104	6	1	0	97	5	3	2	92	91.09%	94.85%	96 84%
#207	18	0	0	18	<u> </u>	3	0	11	4	2	- <del>-</del> 2	7	53 85%	63 64%	77 78%
#208	1	0	0	1	<del></del>	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#210	214	0	0	214	16	24	9	165	69	41	28	96	62 75%	58 18%	70 07%

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AGGREGATE ORDER TYPES													l		
Company Info					LSR PR	ROCESSING							F	LOWTHROUG	SH.
					L	ESOG								,	
	Me	echanized	Interface l	Jsed	Manual	Rejects	Valid	ated		Errors				1	
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#211	163	0	0	163	29	15	1	118	18	16	2	100	68 97%	84 75%	86 21%
#212	191	0	0	191	21	21	2	147	28	16	12	119	76 28%	80 95%	88 15%
#213	18	0	0	18	2	0	0	16	1	1	0	15	83.33%	93 75%	93 75%
#214	83	0	0	83	9	6	4	64	30	26	4	34	49.28%	53 13%	56 67%
#215	474	0	0	474	82	25	0	367	14	12	2	353	78 97%	96 19%	96 71%
#216	202	0	0	202	19	12	0	171	23	16	7	148	80 87%	86 55%	90 24%
#217	8	0	0	8	3	1	0	4	1	0	1	3	50.00%	75 00%	100 00%
#218	0	0	818	818	10	116	1	691	12	8	4	679	97 42%	98 26%	98 84%
#219	458	0	0	458	43	23	6	386	38	24	14	348	83.86%	90.16%	93 55%
#220	467	0	0	467	31	18	0	418	11	9	2	407	91 05%	97 37%	97 84%
#221	349	0	0	349	59	8	1	281	16	12	4	265	78 87%	94 31%	95 67%
#222	137	0	0	137	17	5	1	114	9	6	3	105	82.03%	92 11%	94 59%
#223	174	0	0	174	20	16	2	136	13	10	3	123	80 39%	90 44%	92.48%
#224	0	0	462	462	3	33	0	426	15	14	1	411	96.03%	96.48%	96 71%
#225	28	0	0	28	0	1	0	27	2	1	1	25	96 15%	92 59%	96 15%
#226	497	0	0	497	53	13	1	430	25	19	6	405	84 91%	94 19%	95 52%
#227	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100 00%	100 00%
#228	878	0	0	878	109	63	1	705	98	74	24	607	76.84%	86 10%	89 13%
#229	27	0	0	27	2	2	1	22	4	4	0	18	75 00%	81 82%	81 82%
#230	165	0	0	165	3	12	1	149	14	9	5	135	91.84%	90 60%	93 75%
#231	115	0	0	115	15	2	0	98	9	8	1	89	79 46%	90 82%	91 75%
#232	102	0	0	102	23	9	1	69	19	13	6	50	58 14%	72 46%	79 37%
#233	9	0	0	9	3	1	0	5	2	2	0	3	37.50%	60 00%	60 00%
#234	282	0	0	282	28	24	1	229	28	26	2	201	78 82%	87.77%	88 55%
#235	22	0	0	22	3	1	0	18	0	0	0	18	85.71%	100 00%	100 00%
#236	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#237	11	0	0	11	2	2	0	7	0	0	0	7	77 78%	100 00%	100 00%
#238	1,269	0	0	1,269	231	84	2	952	77	63	14	875	74 85%	91 91%	93 28%
#239	78	0	0	78	3	8	4	63	6	2	4	57	91 94%	90 48%	96 61%
#240	41	0	0	41	5	6	0	30	3	1	2	27	81 82%	90 00%	96 43%
#241	3	0	0	3	0	0	0	3	0	0	0	<sup>+</sup> 3	100 00%	100 00%	100 00%
#242	2	0	0	- 2	0	1	0	1	0	0	0	1 1	100 00%	100 00%	100 00%
#243	177	0	0	177	26	7	0	144	7	6	1	137	81 07%	95 14%	95 80%
#243	3,350	0	0	3,350	255	233	9	2,853	435	287	148	2,418	81 69%	84 75%	89 39%
#245	2,193	0	0	2,193	210	149	11	1,823	108	77	31	1,715	85 66%	94 08%	95 70%

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AGGREGATE ORDER TYPES															
Company Info					LSR PR	OCESSING							F	LOWTHROUG	GH .
					LI	ESOG									
	Me	echanized	Interface (	Used	Manual	Rejects	Valid	ated		Errors					
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#246	4	0	0	4	1	1	0	22	0	0	0	2	66.67%	100 00%	100 00%
#247	59	0	0	59	16	10	0	33	11	11	0	32	65 31%	96 97%	96 97%
#248	1,026	0	0	1,026	67	114	1	844	61	45	16	783	87.49%	92 77%	94 57%
#249	0	0	15	15	0	0	0	15	2	0	2	13	100 00%	86 67%	100.00%
#250	0	0	87	87	11	34	3	39	19	12	7	20	46.51%	51 28%	62 50%
#251	708	0	0	708	65	59	14	570	106	55	51	464	79 45%	81.40%	89 40%
#252	1,681	0	0	1,681	78	159	3	1,441	95	73	22	1,346	89 91%	93.41%	94 86%
#253	505	0	0	505	73	12	0	420	21	18	3	399	81.43%	95 00%	95 68%
#254	68	0	0	68	9	2	0	57	3	3	0	54	81.82%	94 74%	94 74%
#255	50	0	0	50	1	2	0	47	3	2	1	44	93.62%	93 62%	95 65%
#256	9	0	0	9	0	6	0	3	0	0	0	3	100 00%	100 00%	100 00%
#257	6	0	0	6	0	1	0	5	4	1	3	1	50.00%	20 00%	50 00%
#258	301	0	0	301	17	21	0	263	19	13	6	244	89.05%	92 78%	94 94%
#259	11	0	0	11	0	0	0	11	1	1	0	10	90 91%	90 91%	90 91%
#260	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#261	934	0	0	934	98	84	0	752	29	24	5	723	85.56%	96 14%	96 79%
#262	981	0	0	981	118	58	4	801	79	52	27	722	80 94%	90 14%	93 28%
#263	1	0	0	1	0	00	0	1	11	11	0	0	0.00%	0 00%	0 00%
#264	8	0	0	8	1	1	0	6	11	1 1	0	5	71 43%	83 33%	83 33%
#265	23	0	0	23	3	2	0	18	1	1 1	0	17	80 95%	94.44%	94 44%
#266	0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
#267	19	0	0	19	5	3	1	10	5	3	2	5	38.46%	50 00%	62 50%
#268	88	0	0	88	8	3	5	72	19	12	7	53	72.60%	73 61%	81 54%
#269	245	0	0	245	22	8	1	214	18	13	5	196	84.85%	91 59%	93 78%
#270	0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50 00%	50 00%
#271	2	0	0	2	0	2	0	0	0	0	0	0	0.00%	0 00%	0.00%
#272	23	0	0	23	2	7	0	14	11	4	7	3	33 33%	21 43%	42 86%
#273	1,855	0	0	1,855	184	62	4	1,605	83	73	10	1,522	85 55%	94.83%	95 42%
#274	316	0	0	316	36	14	2	264	44	33	11	220	76 12%	83.33%	86 96%
#275	363	0	0	363	41	14	0	308	20	17	3	288	83 24%	93 51%	94 43%
#276	30	0	0	30	2	11	0	17	0	0	0	17	89 47%	100.00%	100 00%
#277	2	0	0	2	0	: 2	0	0	0	0	0	0	0 00%	0 00%	0 00%
#278	294	0	0	294	38	67	2	187	21	13	8	166	76 50%	88 77%	92 74%
#279	135	0	0	135	14	24	0	97	23	21	2	74	67.89%	76 29%	77 89%
· #280	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%

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Exhibit November PM Data Attachment 2F

GGREGATE ORDER TYPES															
Company Info					LSR PR	COCESSING							F	LOWTHROUG	Н
					L	ESOG									
	Me	echanized	Interface (	Jsed	Manual	Rejects	Valid	ated		Errors			-	Î	1
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#281	856	0	0	856	124	35	3	694	50	43 i	7	644	79.41%	92 80%	93 74%
#282	1	0	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0.00%
#283	0	17	0	17	2	0	0	15	1	0	1	14	87.50%	93.33%	100 00%
#284	9	0	0	9	2	3	1	3	0	0	0	3	60 00%	100.00%	100 00%
#285	0	1	0	1	0	0	0	1	0	0	Ō	1	100 00%	100 00%	100 00%
LENS Subtotal	196,691	0	0	196,691	15,893	20,137	831	159,830	19,615	14,663	4,952	140,215	82 11%	87.73%	90 53%
EDI Subtotal	0	28,068	0	28,068	473	6,495	11	21,089	7,772	3,810	3,962	13,317	75.66%	63.15%	77.75%
TAG Subtotal	0	0	19,774	19,774	511	1,334	78	17,851	2,270	1,586	684	15,581	88 14%	87 28%	90 76%
TOTAL INTERFACES	196,691	28,068	19,774	244,533	16,877	27,966	920	198,770	29,657	20,059	9,598	169,113	82.07%	85.08%	89.40%

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AGGREGATE ORDER TYPES				1								ļ		·	
Company Info						ROCESSING							F	LOWTHROUG	SH .
					Ł	ESOG								l '	
	Me	echanized	Interface	Used	Manual	Rejects	Valid	lated		Errors					
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#1	5	0	00	5	0	0	0	5	0	0	0	5	100.00%	100.00%	100 00%
#2	9	0	0	9	1	11	0	7	3	3	0	4	50 00%	57 14%	57 14%
#3	0	0	8	8	6	1	0	1_	1	1 1	0	0_	0 00%	0 00%	0 00%
#4	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#5	3	0	0	3	0	0	0	3	0	0	0	3	100 00%	100 00%	100 00%
#6	234	0	0	234	191	13	2	28	10	8	2	18	8 29%	64 29%	69 23%
#7	25	0	0	25	14	2	0	9	3	3	0	6	26 09%	66 67%	66 67%
#8	50	0	0	50	8	9	0	33	6	1	5	27	75 00%	81 82%	96 43%
#9	0	54	0	54	3	16	0	35	19	13	6	16	50.00%	45 71%	55 17%
#10	0	0	2	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100 00%
#11	199	0	0	199	44	26	3	126	41	30	11	85	53 46%	67.46%	73 91%
#12	31	0	0	31	0	6	0	25	10	7	3	15	68 18%	60.00%	68 18%
#13	0	0	4	4	0	3	0	1	0	0	0	1	100.00%	100 00%	100 00%
#14	4	0	0	4	0	0	0	4	2	0	2	2	100.00%	50 00%	100 00%
#15	6	0	0	6	0	0	0	6	1	1	0	5	83 33%	83 33%	83 33%
#16	0	1	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0.00%
#17	21	0	0	21	1	5	1	14	2	2	0	12	80 00%	85 71%	85 71%
#18	44	0	0	44	1	6	0	37	11	6	5	26	78 79%	70 27%	81 25%
#19	97	0	0	97	14	15	2	66	21	13	8	45	62 50%	68.18%	77.59%
#20	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
#21	226	0	0	226	56	55	1	114	23	18	5	91	55.15%	79 82%	83 49%
#22	4	0	0	4	0	1	0	3	0	0	0	3	100 00%	100 00%	100 00%
#23	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100.00%
#24	48	0	0	48	10	10	1	27	12	6	6	15	48 39%	55 56%	71 43%
#25	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#26	36	0	0	36	4	4	0	28	12	9	3	16	55.17%	57.14%	64 00%
#27	54	0	0	54	6	5	0	43	10	8	2	33	70.21%	76.74%	80 49%
#28	36	0	0	36	7	12	0	17	7	3	4	10	50 00%	58 82%	76 92%
#29	92	0	0	92	4	8	2	78	26	19	7	52	69 33%	66 67%	73 24%
#30	13	0	0	13	3	5	0	5	2	1	1	3	42 86%	60 00%	75 00%
#31	2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100.00%	100 00%
#32	957	0	0	957	284	114	6	553	102	65	37	451	56.38%	81.56%	87 40%
#33	0	0	63	63	30	4	2	27	3	1 1	2	24	43 64%	88 89%	96 00%
#34	23	0	0	23		1	1	20	3	2	1	17	85 00%	85 00%	89 47%
#35	1	0	0	1	0	0	0	1	0	0	0	1 1	100 00%	100 00%	100 00%

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AGGREGATE ORDER TYPES													1	İ	
Company Info					LSR PR	ROCESSING			<del> </del>					LOWTHROUG	ЭH
					L	ESOG						i i		Ï .	
	Me	echanized	interface l	Jsed	Manual	Rejects	Valid	ated		Errors			<u> </u>		1
Name	LENS	EDI <sup>,</sup>	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#36	0	5	0	5	1	2	0	2	0	0	.0	2	66 67%	100 00%	100 00%
#37	23	0	0	23	3	7	0	13	4	2	2	9	64.29%	69.23%	81 82%
#38	17	0	0	17	1	0	0	16	4	4	0	12	70.59%	75 00%	75 00%
#39	0	0	15	15	3	2	0	10	7	6	1	3	25.00%	30 00%	33 33%
#40	4	0	0	4	1	0	0	3	1	0	1	2	66.67%	66 67%	100 00%
#41	47	0	0	47	15	14	0	18	8	5	3	10	33.33%	55.56%	66 67%
#42	1	0	0	1	0	0	0	1	0	0	0	1 1	100.00%	100.00%	100 00%
#43	33	0	0	33	7	10	0	16	7	4	3	9	45 00%	56 25%	69 23%
#44	5	0	0	5	0	1	1	3	0	0	0	3	100.00%	100.00%	100 00%
#45	2	0	0	2	1	0	0	1	0	0	0	1	50 00%	100 00%	100 00%
#46	8	0	0	8	2	0	0	6	5	4	1	1	14.29%	16 67%	20 00%
#47	299	0	0	299	96	87	0	116	18	15	3	98	46.89%	84.48%	86 73%
#48	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#49	7	0	0	7	0	0	0	7	2	2	0	5	71.43%	71.43%	71 43%
#50	0	28	0	28	4	6	0	18	14	4	10	4	33 33%	22 22%	50 00%
#51	428	0	0	428	97	56	4	271	89	60	29	182	53.69%	67.16%	75 21%
#52	0	2	0	2	2	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#53	34	0	0	34	4	15	0	15	6	4	2	9	52.94%	60 00%	69 23%
#54	125	0	0	125	46	17	2	60	13	8	5	47	46.53%	78 33%	85 45%
#55	248	0	0	248	24	30	4	190	45	36	9	145	70.73%	76.32%	80 11%
#56	1	0	0	1	0	0	0	1	0	0	0	1 1	100.00%	100 00%	100 00%
#57	0	0	19	19	3	7	0	9	1	1	0	8	66.67%	88 89%	88 89%
#58	30	0	0	30	10	13	0	7	1	0	1	6	37.50%	85 71%	100 00%
#59	0	276	0	276	29	50	3	194	82	59	23	112	56.00%	57 73%	65 50%
#60	65	0	0	65	4	5	0	56	14	8	6	42	77.78%	75 00%	84 00%
#61	71	0	0	71	21	10	1	39	11	8	3	28	49.12%	71.79%	77 78%
#62	2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100 00%	100 00%
#63	1,306	0	0	1,306	239	354	16	697	313	222	91	384	45 44%	55 09%	63 37%
#64	0	0	19	19	1	10	0	8	0	0	0	8	88.89%	100 00%	100 00%
#65	14	0	0	14	8	0	0	6	4	2	2	2	16.67%	33 33%	50 00%
#66	0	0	2	2	0	1	0	1	0	0	0	1	100.00%	100 00%	100 00%
#67	0	0	3	3	0	2	0	1	0	0	0	1	100 00%	100 00%	100 00%
#68	0	0	267	267	74	30	0	163	60	35	25	103	48.58%	63 19%	74 64%
#69	4	0	0	4	0	0	0	4	2	2	0	2	50.00%	50 00%	50 00%
#70	1	0	0	1	0	0	0	1	1	0	<del></del> - 1	0	0 00%	0.00%	0 00%

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AGGREGATE ORDER TYPES														· · · · · · · · · · · · · · · · · · ·	<del></del>
Company Info					LSR PR	OCESSING							F	LOWTHROUG	3H
					L	ESOG								1	I
	Me	echanized	Interface l	Jsed	Manual	Rejects	Valid	ated		Errors		1	1		Ī
Name	LENS	EÐI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flov Through
#71	1	0	0	1	0	0	0	1	0	0	0	1	· 100.00%	100 00%	100 00%
#72	135	0	0	135	18	33	0	84	29	20	9	55	59 14%	65 48%	73 33%
#73	114	0	0	114	23	13	2	76	17	14	3	59	61 46%	77 63%	80 82%
#74	42	0	0	42	1	4	1	36	7	3	4	29	87.88%	80 56%	90 63%
#75	5	0	0	5	1	1	0	3	1	1	0	2	50 00%	66.67%	66 67%
#76	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#77	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#78	0	0	90	90	59	3	0	28	14	9	5	14	17.07%	50.00%	60 87%
#79	119	0	0	119	14	3	2	100	31	24	7	69	64.49%	69 00%	74 19%
#80	0	0	2	2	0	0	0	2	2	1	1	0	0.00%	0 00%	0 00%
#81	36	0	0	36	2	19	2	13	7	4	3	6	50 00%	46 15%	60 00%
#82	0	11	0	11	3	3	0	5	2	1	1	3	42.86%	60 00%	75 00%
#83	8	0	0	8	4	3	0	1	0	0	0	1	20 00%	100 00%	100 00%
#84	0	15	0	15	9	0	1	5	2	1	1	3	23 08%	60 00%	75.00%
#85	51	0	0	51	4	9	0	38	9	4	5	29	78.38%	76 32%	87 88%
#86	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#87	0	0	5	5	4	0	0	1	0	0	0	1	20.00%	100.00%	100 00%
#88	26	0	0	26	2	8	0	16	1	1	0	15	83 33%	93.75%	93 75%
#89	0	33	0	33	21	2	1	9	6	1	5	3	12 00%	33.33%	75.00%
#90	3	0	0	3	1	0	0	2	0	0	0	2	66 67%	100 00%	100 00%
#91	118	0	0	118	18	38	0	62	9	8	1	53	67 09%	85 48%	86 89%
#92	0	0	64	64	6	27	0	31	4	4	0	27	72 97%	87.10%	87 10%
#93	12	0	0	12	0	3	0	9	3	2	1	6	75.00%	66 67%	75 00%
#94	0	74	0	74	33	11	0	30	9	5	4	21	35.59%	70.00%	80 77%
#95	1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0 00%	0 00%
#96	44	0	0	44	7	4	1	32	6	6	0	26	66 67%	81 25%	81 25%
#97	2	0	0	2	1	0	0	1	0	0	0	1 1	50.00%	100 00%	100 00%
#98	3	0	0	3	2	0	0	1	0	0	0	1	33 33%	100 00%	100 00%
#99	0	0	5	5	0	3	0	2	0	0	0	2	100.00%	100 00%	100 00%
#100	4	0	0	4	0	0	0	4	3	3	0	1	25.00%	25 00%	25 00%
#101	0	18	0	18	3	5	0	10	2	0	2	8	72 73%	80.00%	100 00%
#102	0	0	63	63	23	5	0	35	9	5	4	26	48 15%	74 29%	83 87%
#103	84	0	0	84	30	5	0	49	16	13	3	33	43 42%	67 35%	71 74%
#104	2,980	0	0	2,980	390	569	54	1,967	726	489	237	1,241	58 54%	63 09%	71 73%
#105	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0 00%

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AGGREGATE ORDER TYPES								<del> </del>							
Company Info					LSR PR	ROCESSING	!						F	LOWTHROUG	SH
					L	ESOG	ı							'	T
	M	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors					
Name	LENS	·EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#106	4	0	0	4	0	2	0	2	0	0	0	2	100 00%	100 00%	100 00%
#107	86	0	0	86	13	10	0	63	26	13	13	37	58 73%	58.73%	74 00%
#108	17	0	0	17	2	8	0	7	1	1	0	6	66.67%	85.71%	85 71%
#109	205	0	0	205	19	40	2	144	51	46	5	93	58 86%	64 58%	66 91%
#110	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#111	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#112	30	0	0	30	0	6	0	24	4	3	1	20	86.96%	83.33%	86 96%
#113	21	0	0	21	3	3	0	15	0	0	0	15	83 33%	100 00%	100 00%
#114	2	0	0	2	1	0	0	1	1	11	0	0	0 00%	0 00%	0 00%
#115	7	0	0	7	1	11	0	5	2	2	0	3	50 00%	60.00%	60 00%
#116	0	1	0	1	1	00	0	0	0	0	0	0	0 00%	0 00%	0 00%
#117	0	0	31	31	0	7	0	24	12	9	3	12	57 14%	50.00%	57 14%
#118	1	0	0	1	0	0	0	1	0	0	00	1 1	100 00%	100 00%	100 00%
#119	19	0	0	19	10	55	0	4	0	0	0	4	28.57%	100 00%	100 00%
#120	2	0	0	2	0	11	0	11	1	11	0	0	0 00%	0.00%	0.00%
#121	0	24	0	24	6	7	0	11	9	7	22	2	13.33%	18 18%	22 22%
#122	13	0	0	13	0	2	0	11	3	2	1	8	80 00%	72 73%	80 00%
#123	11	0	00	11	4	2	0	5	00	0	0	5	55 56%	100.00%	100 00%
#124	8	0	0	8	3	4	0	1	11	1	0	0	0.00%	0.00%	0 00%
#125	2	0	0	2	0	1	0	11	0	0	0	1	100 00%	100.00%	100 00%
#126	12	0	0	12	0	11	0	11	6	4	2	5	55 56%	45 45%	55 56%
#127	4	0	0	4	1	00	0	3	2	11	1	1	33 33%	33 33%	50 00%
#128	3	0	0	3	0	2	0	11	0	0	0	1	100 00%	100 00%	100 00%
#129	4	0	0	4	0	2	0	2	0	0	0	2	100 00%	100 00%	100 00%
#130	2	0	0	2	0	1	0	11	0	0	0	1	100 00%	100 00%	100 00%
#131	2	0	0	2	1	0	0	1	0	0	. 0	1	50 00%	100 00%	100.00%
#132	4	0	0	4	1	00	1	2	0	0	0	2	66.67%	100 00%	100 00%
#133	9	0	0	9	9	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#134	17	0	0	17	4	1	0	12	4	3	1	8	53 33%	66 67%	72 73%
#135	13	0	0	13	4	2	0	7	2	2	0	5	45 45%	71 43%	71 43%
#136	15	0	0	15	0	2	0	13	3	0	3	10	100 00%	76 92%	100 00%
#137	0	0	1	1	0		0 ]	0	0	0	0	0	0.00%	0 00%	0 00%
#138	12	0	0	12	2	1	0	9	1	1 1	0	8	72 73%	88 89%	88 89%
#139	28	0	0	28	9	3	1	15	5	5	0	10	41 67%	66 67%	66 67%
. #140	9	0	0	9	3	2	0	4	ō	0	0	4	57 14%	100 00%	100 00%

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AGGREGATE ORDER TYPES				Ţ										<del>                                     </del>	
Company Info					LSR PR	OCESSING						<del> </del>	F	LOWTHROUG	SH
					Li	ESOG								ı	<u> </u>
	Me	echanized	Interface	Used	Manual	Rejects	Valid	lated		Errors				İ	
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#141	1	0	0	1	00	0	0	1	1	1	0	0	0.00%	0 00%	0 00%
#142	11	0	0	11	. 0	1	0	10	4	1	3	6	85 71%	60 00%	85 71%
#143	21	0	0	21	1	7	0	13	3	2	1	10	76 92%	76 92%	83 33%
#144	4	0	0	4	1	0	1	2	0	0	0	2	66.67%	100.00%	100 00%
#145	0	0	16	16	1	1	0	14	10	2	8	4	57.14%	28.57%	66 67%
#146	67	0	0	67	7	9	0	51	16	14	2	35	62 50%	68 63%	71 43%
#147	24	0	0	24	1	0	3	20	4	1	3	16	88 89%	80 00%	94 12%
#148	18	0	0	18	1	2	0	15	2	2	0	13	81 25%	86 67%	86 67%
#149	31	0	0	31	4	0	0	27	8	3	5	19	73 08%	70.37%	86 36%
#150	0	0	7	7	2	0	0	5	2	1	1	3	50 00%	60.00%	75 00%
#151	3	0	0	3	0	1	0	2	1	1	0	1	50.00%	50.00%	50 00%
#152	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#153	2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100 00%	100 00%
#154	0	0	5	5	0	0	0	5	5	1	4	0	0.00%	0 00%	0 00%
#155	0	0	13	13	0	2	0	11	3	2	1	8	80 00%	72 73%	80 00%
#156	29	0	0	29	5	2	0	22	5	4	1	17	65.38%	77 27%	80 95%
#157	7	0	0	7	0	2	0	5	4	1	3	1	50 00%	20 00%	50 00%
#158	5	0	0	5	0	0	0	5	0	0	0	5	100 00%	100 00%	100 00%
#159	47	0	0	47	23	7	4	13	4	3	1	9	25 71%	69 23%	75 00%
#160	2	0	0	2	2	0	0	0	0	0	0	o '	0.00%	0 00%	0.00%
#161	4	0	0	4	3	0	0	1	1	1	0	0	0 00%	0.00%	0 00%
#162	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0 00%
#163	3	0	0	3	3	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#164	288	0	0	288	251	3	0	34	8	5	3	26	9.22%	76.47%	83 87%
#165	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100.00%	100 00%
#166	1	0	0	1	0	0	0	1	0	0	0	1 ,	100.00%	100.00%	100 00%
#167	12	0	0	12	3	0	0	9	0	0	0	9	75 00%	100 00%	100 00%
#168	1	0	0	1	0	0	0	1	0	0	0	1 1	100 00%	100.00%	100 00%
#169	12	0	0	12	1	5	0	6	2	1	1	4	66 67%	66 67%	80.00%
#170	3	0	0	3	2	0	0	1	0	0	0	1	33 33%	100 00%	100 00%
#171	27	0	0	27	7	1	0	19	4	4	0	15	57 69%	78.95%	78 95%
#172	1	0	0	1 1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#173	1	0	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0 00%
#174	3	0	0	3	0	3	0	0	0	0	0	0 :	0.00%	0 00%	0.00%
#175	<u>5</u>	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%

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Company Info	1	1			LSR PR	OCESSING								LOWTHROUG	20
, , , , , , , , , , , , , , , , , , , ,	+	<del> </del>	<del></del>	ļ		ESOG								LOWINKOUC	эn
	M	echanized	Interface I	ised	Manual	Rejects	Valid	ated		Errors		1	-		İ
Name	LENS	EDI ·	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo
#176	80	0	0	80	7	2	0	71	3	2	1	68	88 31%	95 77%	97 14%
#177	3	0	0	3	0	1	0	2	0	0	0	2	100.00%	100 00%	100 00%
#178	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100 00%	100 00%
#179	11	0	0	11	0	2	0	9	2	1	1	7	87.50%	77 78%	87 50%
#180	8	0	0	8	0	0	0	8	4	1	3	4	80.00%	50 00%	80 00%
#181	6	0	0	6	0	3	0	3	0	0	0	3	100.00%	100 00%	100 00%
#182	1	0	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0.00%
#183	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0.00%
#184	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#185	5	0	0	5	0	0	0	5	1	1	0	4	80 00%	80 00%	80 00%
#186	1	0	0	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#187	31	0	0	31	28	3	0	0	0	0	0	0	0 00%	0.00%	0 00%
#188	1	0	0	1	0	0	0	1	1	0	1	0	0.00%	0 00%	0 00%
#189	3	0	0	3	1	0	0	2	0	0	0	2	66 67%	100.00%	100 00%
#190	22	0	0	22	7	1	1	13	5	3	2	8	44.44%	61 54%	72 73%
#191	0	0	1	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#192	7	0	0	7	4	0	1	2	2	0	2	0	0.00%	0 00%	0 00%
#193	4	0	0	4	1	0	0	3	1	1	0	2	50 00%	66 67%	66 67%
#194	10	0	0	10	1	7	0	2	0	0	0	2	66.67%	100 00%	100 00%
#195	3	0	0	3	0	1	0	2	0	0	0	2	100.00%	100 00%	100 00%
#196	2	0	0	2	0	 1	0	1	0	0	0	1	100.00%	100 00%	100 00%
#197	2	0	0	2	0	0	0	2	2	2	0	o	0.00%	0 00%	0.00%
#198	7	0	0	7	0	4	0	3	2	0	2	1	100 00%	33 33%	100 00%
#199	5	0	0	5	0	0	1	4	4	1	3	0	0 00%	0.00%	0 00%
#200	4	0	0	4	0	2	0	2	0	0	0	2	100.00%	100 00%	100 00%
#201	7	0	0	7	0	2	0	5	0	0	0	5	100.00%	100 00%	100 00%
#202	9	0	0	9	3	2	0	4		0 -	0	4	57.14%	100 00%	100 00%
#203	32	0	0	32	13	<u></u>	0	18	<u>*</u> 11	7	<u>¥</u>	7	25 93%	38 89%	50 00%
#204	5	0	0	5	2	1	0	2	0	0	0		50 00%	100 00%	100 00%
#205	4	0	0	4	0	·- · ·	0	3	0	0	0	3	100 00%	100 00%	100 00%
#205	0	0	9	9	0	6	0	3		1	0	2			
#200	16	0		16		4	1	7	' 2	0	.0 2	5	66 67%	66 67%	66 67%
#207 #208	1	0	0	1		0	0			0	. =		55.56%	71 43%	100 00%
#208	6		0	- · · · · · · · · · · · · · · · · · · ·	0	<del>U</del>		1 -	. 0		_ 0	1	100 00%	100 00%	100 00%
#209	51	0	0	6 51	3	24	2	22	<sup>1</sup> 7	0	1	3 15	75.00% 75.00%	75 00% 68 18%	100 00%

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Exhibit November PM Data Attachment 2F

Company Info					LSR PR	OCESSING				T			F	LOWTHROUG	3H
					Li	ESOG									
	Ме	echanized	Interface	Used	Manual	Rejects	Valid	ated		Errors			÷		İ
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#211	38	0	0	38	5	19	0	14	4	0	4	10	66.67%	71 43%	100 00%
#212	0	0	3	3	0	2	0	1	0	0	0	1	100.00%	100 00%	100 00%
#213	1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0 00%	0 00%
#214	1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0 00%	0 00%
#215	67	0	0	67	25	9	0	33	1	1	0	32	55 17%	96 97%	96 97%
#216	44	0	0	44	3	14	0	27	11	8	3	16	59 26%	59 26%	66 67%
#217	0	0	27	27	1	0	0	26	17	8	9	9	50.00%	34 62%	52 94%
#218	0	0	18	18	1	5	0	12	2	1	1	10	83 33%	83 33%	90 91%
#219	1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#220	7	0	0	7	0	3	0	4	4	2	2	0	0 00%	0 00%	0.00%
#221	0	26	0	26	1	9	0	16	7	5	2	9	60 00%	56 25%	64 29%
#222	0	0	29	29	0	4	0	25	6	3	3	19	86 36%	76 00%	86 36%
#223	0	0_	44	44	4	1	0	39	27	10	17	12	46.15%	30 77%	54 55%
#224	115	0	0	115	20	21	0	74	37	25	12	37	45.12%	50 00%	59 68%
#225	18	0	0	18	3	5	2	8	6	2	4	2	28.57%	25.00%	50 00%
#226	0	0	70	70	3	4	0	63	29	14	15	34	66 67%	53 97%	70 83%
#227	9	0	0	9	0	0	0	9	6	1	5	3	75.00%	33 33%	75 00%
#228	3	0	0	3	0	0	0	3	0	0	0	3	100.00%	100 00%	100 00%
#229	1	0	0	1	0	0	0	1	1	0	1	0	0 00%	0 00%	0 00%
#230	11	0	0	11	7	1	0	3	1	1	0	2	20.00%	66 67%	66 67%
#231	5	0	0	5	2	0	0	3	2	2	0	1	20.00%	33 33%	33 33%
LENS Subtotal	10,661	0	0	10,661	2,298	1,917	129	6,317	2,023	1,378	645	4,294	53 88%	67 98%	75 71%
EDI Subtotal	0	568	0	568	116	112	5	335	152	96	56	183	46.33%	54 63%	65 59%
TAG Subtotal	0	0	905	905	221	131	2	551	215	115	100	336	50.00%	60.98%	74 50%
TOTAL INTERFACES	10,661	568	905	12,134	2,635	2,160	136	7,203	2,390	1,589	801	4,813	53.26%	66.82%	75.18%

AGGREGATE ORDER TYPES								1							
Company Info					LSR PF	ROCESSING				1				FLOWT	HROUGH
					L	ESOG								T	
	Me	chanized	Interface	Used	Manual	Rejects	Valid	ated		Errors		1			İ
Name	LENS	EDI ·	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flor Through
#1	0	100	0	100	12	22	1	65	16	7	9	49	72 06%	75.38%	87 50%
#2	15	0	0	15	4	2	0	9	3	2	1	6	50 00%	66 67%	75 00%
#3	596	0	0	596	69	96	6	425	94	73	21	331	69 98%	77.88%	81 93%
#4	1,120	0	0	1,120	49	162	5	904	665	593	72	239	27.13%	26.44%	28 73%
#5	2	0	0	2	0	1	0	1	1	1	0	0	0 00%	0 00%	0 00%
#6	0	736	0	736	180	113	5	438	179	144	35	259	44 43%	59 13%	64 27%
#7	0	0	114	114	0	33	0	81	11	11	0	70	86.42%	86.42%	86 42%
#8	838	0	0	838	206	55	6	571	179	138	41	392	53 26%	68 65%	73 96%
#9	0	1	0	1	0	1	0	0	0	0	0	0	0 00%	0.00%	0 00%
#10	14	0	0	14	1	4	2	7	1	1	0	6	75.00%	85 71%	85 71%
#11	0	0	59	59	9	7	0	43	19	13	6	24	52 17%	55 81%	64 86%
#12	17	0	0	17	0	4	0	13	2	2	0	11	84.62%	84 62%	84 62%
#13	0	91	0	91	34	6	2	49	16	8	8	33	44 00%	67.35%	80 49%
#14	0	0	3	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100 00%
#15	0	0	1	1	0	0	0	1	0	0	0	1	100 00%	100 00%	100 00%
#16	0	0	7	7	0	4	0	3	1	0	1	2	100.00%	66 67%	100 00%
#17	0	0	26	26	0	10	0	16	6	6	0	10	62 50%	62 50%	62 50%
#18	35	0	0	35	6	2	0	27	13	7	6	14	51 85%	51.85%	66 67%
#19	0	764	0	764	43	92	5	624	163	129	34	461	72.83%	73 88%	78 14%
#20	1,426	0	0	1,426	171	112	8	1,135	271	209	62	864	69 45%	76.12%	80 52%
#21	2	0	0	2	0	1	0	1	1	1	Ō	0	0 00%	0 00%	0 00%
#22	4	0	0	4	1	1	0	2	0	0	0	2	66.67%	100.00%	100 00%
#23	2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0 00%
#24	0	0	2	2	0	1	0	1	1	1	0	0	0 00%	0 00%	0.00%
#25	0	0	8	8	0	2	0	6	2	2	0	4	66 67%	66 67%	66 67%
#26	29	0	0	29	1	1	0	27	26	15	11	1	5.88%	3 70%	6.25%
#27	0	78	0	78	66	5	0	7	3	2	1	4	5 56%	57.14%	66 67%
#28	1	0	0	1	1	0	0	0	0	0	0	0 1	0 00%	0 00%	0 00%
#29	424	0	0	424	50	57	2	315	144	113	31	171	51 20%	54 29%	60 21%
#30	425	0	0	425	54	74	3	294	106	86	20	188	57 32%	63 95%	68 61%
#31	0	398	0	398	281	94	1	22	13	9	4	9	3 01%	40 91%	50 00%
#32	3,823	0	0	3,823	355	296	34	3,138	609	508	101	2,529	74 56%	80 59%	83 27%
#33	0	2	0	2	1	1	0	0	0	0	0	0	0 00%	0 00%	0 00%
#34		0	7	7	0	3	0	4	4	4	0	0	0 00%	0 00%	0 00%
#35	58	0	0	58	24	11	0	23	13	10	3	10	22 73%	43 48%	50 00%

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AGGREGATE ORDER TYPES														T	<u> </u>
Company Info					LSR PR	OCESSING								FLOWT	HROUGH
					L	ESOG									
	Me	echanized	Interface I	Jsed	Manual	Rejects	Valid	ated		Errors				1	j
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#36	2,462	0	0	2,462	500	745	18	1,199	457	365	92	742	· 46.17%	61.88%	67 03%
#37	23	0	0	23	0	4	0	19	1	1	0	18	94.74%	94 74%	94 74%
#38	0	181	0	181	151	12	2	16	7	6	1	9	5 42%	56 25%	60 00%
#39	0	14	0	14	0	4	1	9	8	7	1	1	12 50%	11 11%	12 50%
#40	0	0	287	287	0	18	0	269	51	51	0	218	81 04%	81 04%	81 04%
#41	404	0	0	404	12	25	6	361	144	97	47	217	66 56%	60.11%	69 11%
#42	4	0	0	4	1	0	1	2	0	0	0	2	66.67%	100.00%	100 00%
#43	5	0	0	5	2	2	0	1	0	0	0	1	33 33%	100 00%	100 00%
#44	0	745	0	745	273	213	8	251	106	70	36	145	29 71%	57 77%	67 44%
#45	759	0	0	759	102	91	15	551	143	103	40	408	66.56%	74 05%	79 84%
#46	6	0	0	6	1	0	3	2	0	0	0	2	66 67%	100.00%	100 00%
#47	7	0	0	7	0	3	1	3	0	0	0	3	100 00%	100 00%	100 00%
#48	6	0	0	6	1	5	0	0	0	0	0	0	0 00%	0 00%	0 00%
#49	0	1,644	0	1,644	231	240	22	1,151	360	290	70	791	60 29%	68 72%	73 17%
#50	53	0	0	53	12	12	1	28	6	4	2	22	57.89%	78 57%	84 62%
#51	17	0	0	17	6	6	1	4	0	0	0	4	40 00%	100 00%	100 00%
#52	29	0	0	29	10	12	1	6	2	111	1	4	26 67%	66 67%	80 00%
#53	0	0	159	159	28	45	3	83	43	33	10	40	39 60%	48.19%	54 79%
#54	6	0	0	6	1	1	0	4	2	1	1	2	50.00%	50 00%	66 67%
#55	0	0	48	48	27	5	0	16	10	6	4	6	15 38%	37 50%	50 00%
#56	0	0	91	91	16	14	1	60	23	12	11	37	56 92%	61 67%	75 51%
#57	0	0	112	112	28	45	0	39	18	11	7	21	35 00%	53.85%	65 63%
#58	0	0	151	151	52	20	6	73	43	27	16	30	27.52%	41 10%	52 63%
#59	13	0	0	13	5	6	1	1	1	0	1	0	0.00%	0 00%	0 00%
#60	0	0	1	1	0	0	0	1	1	0	1	0	0 00%	0.00%	0 00%
#61	52	0	0	52	0	3	13	36	25	21	4	11	34 38%	30 56%	34 38%
#62	0	1	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0 00%
#63	48	0	0	48	4	7	0	37	9	5	4	28	75 68%	75 68%	84 85%
#64	0	0	15	15	5	8	0	2	1	0	1	1 1	16 67%	50 00%	100 00%
#65	29	0	0	29	16	9	0	4	2	1	1	2	10.53%	50 00%	66 67%
#66	0	0	149	149	34	9	8	98	50	39	11	48	39 67%	48 98%	55 17%
#67	0	0	3	3	3	0	0	0	0	ő	0	0	0 00%	0 00%	0 00%
#68	53	0	0	53	14	15	1 1	23	4	2	2	19	54 29%	82 61%	90 48%
#69	55	0	0	55	15	2	2	36	18	7	11	18	45 00%	50.00%	72 00%
#70	0	462	0	462	219	51	8	184	83	73	10	101	25 70%	54 89%	58 05%

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Company Info					LSR PR	OCESSING								FLOWT	HROUGH
					L	ESOG								l	
	M	chanized	Interface (	Jsed	Manual	Rejects	Valid	ated		Errors		1		1	
Name	LENS	EDI:	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#71	2	0	0	2	00	2	0	0	0	0	0	0	0.00%	0 00%	0 00%
#72	0	47	0	47	14	16	0	17	12	4	8	5	21 74%	29 41%	55 56%
#73	0	37,706	0	37,706	2,253	5,659	63	29,731	5,481	4,069	1,412	24,250	79 32%	81 56%	85 63%
#74	64	0	0	64	4	9	0	51	3	2	1	48	88 89%	94 12%	96 00%
#75	2	0	0	2	1	0	0	1	0	0	0	1	50 00%	100.00%	100 00%
#76	0	0	26	26	12	7	0	7	5	4	1	2	11.11%	28.57%	33.33%
#77	1	0	0	1	0	1	0	0	0	0	0	0	0 00%	0 00%	0 00%
#78	0	0	3	3	0	0	0	3	2	2	0	1	33 33%	33.33%	33 33%
#79	33	0	0	33	1	6	0	26	10	4	6	16	76 19%	61 54%	80 00%
#80	0	488	0	488	164	70	8	246	116	64	52	130	36 31%	52 85%	67 01%
#81	0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0 00%	0.00%
#82	4	0	0	4	1	2	0	1	0	0	0	1	50 00%	100 00%	100 00%
#83	0	0	546	546	62	49	9	426	114	83	31	312	68 27%	73 24%	78 99%
#84	14	0	0	14	5	4	1	4	0	0	0	4	44.44%	100 00%	100 00%
#85	23	0	0	23	1	6	0	16	7	6	1	9	56 25%	56 25%	60 00%
#86	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100 00%	100 00%
#87	14	0	0	14	4	0	2	8	3	2	1	5	45.45%	62 50%	71 43%
#88	0	0	2	2	0	1	0	1	1	1	0	0	0.00%	0 00%	0 00%
#89	182	0	0	182	24	18	6	134	65	51	14	69	47.92%	51 49%	57 50%
#90	14	0	0	14	6	0	6	2	0	0	0	2	25.00%	100 00%	100 00%
#91	1	0	0	1	1	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#92	6	0	0	6	3	2	0	1	0	0	0	1 1	25.00%	100.00%	100 00%
#93	0	13	0	13	1	0	1	11	6	4	2	5	50.00%	45 45%	55 56%
#94	111	0	0	111	24	20	1	66	21	19	2	45	51 14%	68 18%	70 31%
#95	2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0 00%	0 00%
#96	87	0	0	87	3	11	0	73	21	18	3	52	71 23%	71.23%	74 29%
#97	58	0	0	58	7	4	1	46	15	8	7	31	67 39%	67.39%	79 49%
#98	0	23	0	23	1	4	0	18	6	5	1	12	66.67%	66 67%	70 59%
#99	31	0	0	31	12	1	0	18	8	6	2	10	35 71%	55 56%	62 50%
#100	53	0	0	53	14	3	0	36	8	7	1	28	57 14%	77 78%	80 00%
#101	4,283	0	0	4,283	126	106	8	4,043	154	124	30	3,889	93.96%	96 19%	96 91%
#102	0	26	0	26	6	1	0	19	3	1	2	16	69 57%	84 21%	94 12%
#103	4	0	0	4		; 	0	4	2	2	0	2	50.00%	50 00%	50 00%
#103	108	0	0	108	10	6	14	78	54	9	45	24	55 81%	30 77%	72 73%
#104	1.676	0	0	1,676	51	119	9	1,497	1,005	879	126	492	34 60%	32 87%	35 89%

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Company Info					LSR PF	ROCESSING								FLOWT	HROUGH
					L	ESOG								ľ	T T
	M	echanized	Interface l	sed	Manual	Rejects	Valid	ated		Errors	* * * * * * * * * * * * * * * * * * * *			,	İ
Name	LENS	EDI ·	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flo Through
#106	6	0	0	6	11	0	0	5	2	2	0	3	50 00%	60 00%	60 00%
#107	1	0	0	1	0	0	0	1	1	1	0	0	0 00%	0 00%	0 00%
#108	_	0	3	3	3	0	0	0	00	0	0	0	0 00%	0 00%	0 00%
#109	57	0	0	57	8	5	0	44	6	5	1	38	74 51%	86.36%	88 37%
#110	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100 00%	100 00%
#111	18	0	0	18	2	3	2	11	4	1	3	7	70.00%	63 64%	87 50%
#112	9	0	0	9	0	0	0	9	2	2	0	7	77 78%	77.78%	77 78%
#113	15	0	0	15	0	9	0	6	2	2	0	4	66.67%	66 67%	66 67%
#114	0	0	84	84	5	23	0	56	21	14	7	35	64 81%	62.50%	71 43%
#115	15	0	0	15	14	0	0	1	0	0	0	1	6 67%	100 00%	100 00%
#116	237	0	0	237	22	30	1	184	55	42	13	129	66.84%	70.11%	75 44%
#117	0	0	14,013	14,013	1,201	2,757	70	9,985	3,232	2,186	1,046	6,753	66 60%	67.63%	75 55%
#118	1,431	0	0	1,431	259	130	8	1,034	167	120	47	867	69 58%	83 85%	87 84%
#119	0	0	1	1	0	1	0	0	0	O	0	0	0.00%	0 00%	0.00%
#120	0	12	0	12	0	6	0	6	3	3	0	3	50 00%	50.00%	50 00%
#121	0	0	23	23	0	7	0	16	3	3	0	13	81.25%	81.25%	81 25%
#122	2	0	0	2	2	0	0	0	0	0	0	0	0 00%	0 00%	0 00%
#123	0	0	76	76	37	18	0	21	12	8	4	9	16 67%	42.86%	52 94%
#124	2	0	0	2	0	1	0	1	0	0	0	1	100.00%	100 00%	100 00%
#125	231	0	0	231	26	15	1	189	41	34	7	148	71.15%	78.31%	81.32%
#126	0	0	7	7	3	0	0	4	3	0	3	1	25.00%	25 00%	100 00%
#127	1,300	0	0	1,300	128	138	13	1,021	234	187	47	787	71 42%	77 08%	80.80%
#128	0	0	67	67	2	16	0	49	19	13	6	30	66 67%	61.22%	69 77%
#129	0	0	337	337	47	44	2	244	62	47	15	182	65.94%	74 59%	79 48%
#130	643	0	0	643	103	37	11	492	111	87	24	381	66 73%	77.44%	81.41%
#131	80	0	0	80	22	7	0	51	13	9	4	38	55.07%	74.51%	80 85%
#132	1,825	0	0	1,825	1.674	20	1	130	10	3	7	120	6 68%	92.31%	97 56%
#133	1,359	0	0	1,359	1,235	36	6	82	33	19	14	49	3 76%	59 76%	72 06%
#134	30	0	0	30	4	13	0	13	9	2	7	4	40.00%	30.77%	66 67%
#135	33	0	0	33	24	0	1	8	4	1	3	4	13 79%	50.77%	80 00%
#136	0	0	1,305	1,305	361	9	81	854	366	316	- <u>5</u>	488	41 89%	57 14%	60 70%
#137	0	119	0	119	4	48	0	67	28	16	12	39	66 10%	58 21%	70 91%
#138	58	0	0	58	14	8	0	36	9	6	3	27	57.45%	75 00%	81 82%
#139	0	$-\frac{0}{0}$	17	17	0	5	0	12	4	4	0	8	66.67%	66 67%	
#139	13	0	0	13	0	0	- 1	12	2	1	4	10	90.67%	83 33%	66 67% 90 91%

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GREGATE ORDER TYPES															***************************************
Company Info					LSR PR	OCESSING					<del>: ''</del>			FLOWT	HROUGH
					Ł	ESOG								,	
	M	echanized	Interface l	Jsed	Manuat	Rejects	Valid	ated		Errors		<u> </u>			
Name	LENS	EDI	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Failout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow
#141	20	0	0	20	0	9	5	6	1	1	0	5	83 33%	83 33%	83 33%
#142	17	0	0	17	7	4	0	6	0	0	0	6	46 15%	100 00%	100.00%
#143	0	0	7	7	4	0	1	2	2	0	2	0	0.00%	0 00%	0 00%
#144	1	0	0	1 1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
#145	0	171	0	171	18	26	0	127	36	29	7	91	65 94%	71 65%	75 83%
#146	0	0	1	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100 00%
#147	75	0	0	75	17	7	1	50	12	11	1	38	57.58%	76 00%	77 55%
#148	0	0	50	50	2	9	1	38	4	2	2	34	89.47%	89.47%	94 44%
#149	91	0	0	91	16	4	0	71	11	7	4	60	72.29%	84 51%	89 55%
#150	1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100 00%	100 00%
#151	0	0	2	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0 00%
#152	83	0	0	83	10	5	1	67	15	8	7	52	74 29%	77 61%	86 67%
#153	37	0	0	37	0	4	3	30	17	7	10	13	65.00%	43 33%	65 00%
#154	0	0	11	11	0	2	0	9	0	0	0	9	100 00%	100 00%	100 00%
#155	8	0	0	8	0	2	0	6	1	1	0	5	83 33%	83 33%	83 33%
#156	0	29	0	29	22	2	1 ,	4	3	2	1	1	4.00%	25.00%	33 33%
#157	0	0	51	51	0	9	0	42	8	8	0	34	80 95%	80 95%	80 95%
#158	32	0	0	32	2	3	1	26	4	3	1	22	81 48%	84 62%	88 00%
#159	2	0	0	2	0	0	0	2	0	0	0	2	100 00%	100 00%	100 00%
#160	0	61	0	61	8	17	0	36	15	9	6	21	55.26%	58 33%	70 00%
#161	1	0	0	1	0	0	0	1	0	0	0	1 1	100 00%	100 00%	100 00%
#162	0	31	0	31	3	7	0	21	4	2	2	17	77 27%	80.95%	89 47%
#163	5	0	0	5	0	0	0	5	2	1	1	3	75 00%	60 00%	75 00%
#164	0	0	193	193	24	19	0	150	60	52	8	90	54 22%	60 00%	63 38%
#165	215	0	0	215	25	15	2	173	49	35	14	124	67 39%	71 68%	77 99%
#166	107	0	0	107	21	4	1	81	19	15	4	62	63 27%	76 54%	80 52%
#167	0	0	27	27	7	7	1	12	5	0	5	7	50 00%	58.33%	100 00%
#168	14	0	0	14	0	0	1	13	1	1	0	12	92 31%	92 31%	92 31%
#169	6	0	0	6	2	1	0	3	1	0	1	2	50 00%	66.67%	100 00%
#170	42	0	0	42	10	4	0	28	6	4	2	22	61.11%	78.57%	84 62%
#171	0	0	89	89	17	4	0	68	22	19	3	46	56.10%	67 65%	70 77%
#172	106	0	0	106	<u></u> 5	. 8	0	93	12	8	4	81	86 17%	87.10%	91 01%
#173	0	0	10	10	9	0	0 -	1	0	0	0	1	10 00%	100.00%	100 00%
#174	11	0	0	11	1	0	0	10	4	4	0	6	54 55%	60 00%	60 00%
#175	161			161	20	13	1	127	26	25	1	101	69 18%	79 53%	80 16%

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GREGATE ORDER TYPES		<u> </u>	ļ												
Company Info						OCESSING								FLOWT	HROUGH
					LI	ESOG						1		· '	
	Me	echanized	Interface (	Jsed	Manuai	Rejects	Valid	ated		Errors				l	
Name	LENS	EDI ·	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flor Through
#176	69	0	0	69	17	5	0	47	7	7	0	40	62.50%	85.11%	85 11%
#177	36	0	0	36	3	1	0	32	8	6	2	24	72.73%	75.00%	80 00%
#178	0	54	0	54	0	14	0	40	13	13	0	27	67 50%	67.50%	67 50%
#179	0	0	7	7	0	11	0	6	2	2	0	4	66.67%	66 67%	66 67%
#180	0	0	26	26	9	6	0	11	1	1	0	10	50 00%	90 91%	90 91%
#181	13	0	0	13	0	1	0	12	5	2	3	7	77 78%	58.33%	77 78%
#182	0	69	0	69	5	12	0	52	15	4	11	37	80.43%	71 15%	90 24%
#183	1	0	0	1	0	0	0	1	1	0	1	0	0 00%	0 00%	0 00%
#184	29	0	0	29	5	2	0	22	7	3	4	15	65 22%	68.18%	83 33%
#185	0	0	19	19	0	5	0	14	2	2	0	12	85 71%	85 71%	85 71%
#186	0	10	0	10	5	1	0	4	4	2	2	0	0 00%	0.00%	0 00%
#187	149	0	0	149	22	17	1	109	15	12	3	94	73 44%	86 24%	88 68%
#188	0	0	43	43	9	10	1	23	6	4	2	17	56.67%	73 91%	80 95%
#189	0	0	702	702	111	70	4	517	137	104	33	380	63.87%	73 50%	78 51%
#190	550	0	0	550	81	24	12	433	69	51	18	364	73 39%	84 06%	87 71%
#191	0	0	197	197	15	6	22	154	147	115	32	7	5.11%	4.55%	5 74%
#192	353	0	0	353	41	28	22	262	174	127	47	88	34.38%	33 59%	40.93%
#193	5	0	0	5	0	1	1	3	1	0	1	2	100.00%	66 67%	100 00%
#194	3,046	0	0	3,046	352	423	22	2,249	538	392	146	1,711	69.69%	76 08%	81 36%
#195	1,669	0	0	1,669	145	126	14	1,384	398	286	112	986	69.58%	71 24%	77 52%
#196	0	0	166	166	30	42	6	88	32	15	17	56	55 45%	63 64%	78 87%
#197	134	0	0	134	22	49	5	58	27	14	13	31	46 27%	53 45%	68 89%
#198	0	269	0	269	1	25	4	239	69	49	20	170	77.27%	71 13%	77 63%
#199	463	0	0	463	105	33	9	316	55	37	18	261	64 76%	82 59%	87 58%
#200	0	0	950	950	124	134	14	678	279	236	43	399	52.57%	58 85%	62 83%
#201	0	0	975	975	163	117	8	687	251	208	43	436	54.03%	63.46%	67 70%
#202	813	0	0	813	107	88	4	614	81	50	31	533	77 25%	86 81%	91 42%
#203	0	56	0	56	46	4	0	6	0	0	0	6	11 54%	100.00%	100 00%
#204	0	0	4	4	0	0	2	2	1	1	0	1	50.00%	50 00%	50 00%
#205	0	0	4	4	1	0	0	3	3	3	0	0	0 00%	0 00%	0 00%
#206	0	0	2,111	2,111	382	291	41	1,397	520	402	118	877	52 80%	62 78%	68 57%
#207	160	0	0	160	12	.9	1	138	21	14	7	117	81 82%	84 78%	89 31%
#208	0	0	7	7	0	5	0	2	1	0	1	1	100 00%	50 00%	100 00%
#209	0	0	1,069	1,069	163	157	8	741	232	196	36	509	58 64%	68.69%	72 20%
#210	2	0	0	2	0	1	0	1	1	0	1	0	0.00%	0 00%	0 00%

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Exhibit November PM Data Attachment 2F

AGGREGATE ORDER TYPES		ļ		ļ <u>[</u>					<u> </u>					·	
Company Info						OCESSING						1		FLOWT	HROUGH
					L	ESOG								,	
	Me	echanized	Interface (	Jsed	Manual	Rejects	Valid	ated		Errors			į	İ	
Name	LENS	EDI:	TAG	Total Mech LSR's	Total Manual Fallout	Auto Clarification	Pending Supps (Z Status)	L\$R's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	issued SO's	Percent Achieved Flowthrough	Base Calculation	Percent Flow Through
#211	0	0	2,109	2,109	296	281	8	1,524	468	365	103	1,056	61 50%	69 29%	74 31%
#212	0	0	2	2	1	0	0	1	0	0	0	1	50.00%	100 00%	100 00%
#213	0	250	0	250	15	132	0	103	49	39	10	54	50 00%	52 43%	58 06%
#214	58	0	0	58	15	8	1	34	12	6	6	22	51.16%	64 71%	78 57%
#215	0	0	13	13	0	3	0	10	1	0	1	9	100 00%	90 00%	100 00%
#216	29	0	0	29	1	0	1	27	9	1	8	18	90.00%	66 67%	94 74%
#217	0	0	13	13	1	0	0	12	1	1	0	11	84 62%	91 67%	91 67%
#218	2,372	0	0	2,372	207	233	53	1,879	410	254	156	1,469	76 11%	78 18%	85 26%
#219	0	0	22	22	2	4	0	16	4	2	2	12	75 00%	75.00%	85 71%
#220	12	0	0	12	0	1	0	11	1	1	0	10	90 91%	90 91%	90 91%
#221	868	0	0	868	100	150	4	614	70	49	21	544	78.50%	88 60%	91 74%
#222	0	40	0	40	4	13	0	23	4	4	0	19	70 37%	82 61%	82 61%
#223	17	0	0	17	6	0	0	11	0	0	0	11	64 71%	100.00%	100 00%
#224	0	9	0	9	5	0	0	4	3	3	0	1	11 11%	25 00%	25 00%
#225	0	0	16	16	0	3	0	13	2	2	0	11	84 62%	84 62%	84 62%
#226	7	0	0	7	0	1	0	6	2	11	1	4	80 00%	66 67%	80 00%
#227	0	2,540	0	2,540	159	729	4	1,648	643	300	343	1,005	68 65%	60.98%	77 01%
#228	1,280	0	0	1,280	295	100	18	867	293	193	100	574	54.05%	66 21%	74 84%
#229	0	146	0	146	25	31	0	90	23	13	10	67	63 81%	74 44%	83 75%
#230	86	0	0	86	24	6	1	55	11	3	8	44	61 97%	80 00%	93 62%
LENS Subtotal	40260	0	0	40260	7230	3989	408	28633	7420	5668	1752	21213	62 19%	74 09%	78 91%
EDI Subtotal	0	47,386	0	47,386	4,251	7,671	136	35,328	7,490	5,380	2,110	27,838	74 30%	78 80%	83 80%
TAG Subtotal	0	0	26,651	26,651	3,307	4,349	297	18,698	6,320	4,638	1,682	12,378	60 91%	66 20%	72 74%
TOTAL INTERFACES	40,260	47,386	26,651	114,297	14,788	16,009	841	82,659	21,230	15,686	5,544	61,429	66.84%	74.32%	79 66%

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AGGREGATE ORDER TYPES	
Company Info	
	FATAL
Name	REJECTS
#1	12
#2	8
#3	16
	2
#5	3
#6	6
#7	6 5
#8	1
#9	56
#10	15
#11	4
#12	15
#13	27
#14	2
#15	13
#16	99
#17	3
#18	4
#19	6
#20	1 4
#21	4
#22	2
#23	341
#24	1
#25	76
#26	
#27	2
#28	2 3 1
#29	
#30	5

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AGGREGATE ORDER TYPES	
Company Info	
	<u> </u>
	FATA:
Name	FATAL REJECTS
<del></del>	<del></del>
#31	14
#32	35
#33	35
#34	29
#35	8
#36	5
#37	10
#38	2
#39	1
#40	3
#41	71
#42	8
#43	10
#44	4
#45	3
#46	7
#47	9
#48	1
#49	58
#50	19
#51	12
#52	2
#53	26
#54	10
#55	25
#56	7
#57	2
#58	28
#59	226
#59 #60	220
#00	

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AGGREGATE ORDER TYPES	
Company Info	
	FATAL
Name	REJECTS
#61	6
#62	387
#63	697
#64	3
#65	2
#66	1
#67	167
#68	19
#69	1
#70	29
#71	4
#72	15
#73	4
#74	5
#75	22
#76	113
#77	1
#78	1
#79	2
#80	1942
#81	1
#82	3
#83	4
#84	7
#85	24
#86	12
#87	84
#88	1
#89	84
#90	4

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Company Info	
·····	
	FATAL
Name	REJECTS
#91	4
#92	87
#93	2
#94	80
#95	1838
#96	5
#97	62
#98	12
#99	68
#100	9
#101	5
#102	1
#103	4
#104	1
#105	32
#106	16
#107	1
#108	1353
#109	1
#110	12
#111	6
#112	2
#113	7
#114	2
#115	9
#116	13
#117	6
#118	5
#119	4
#120	1

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AGGREGATE ORDER TYPES	
Company Info	
Name	FATAL REJECTS
#121	9
#122	232
#123	64
#124	6
#125	4
#126	21
#127	2
#128	4
#129	14
#130	28
#131	1
#132	25
#133	26
#134	2
#135	2
#136	3
#137	3
#138	
#139	19
#140	182
#141	<u>4</u>
#142	
#143	1
#144	
#145 #146	28 3
#147	1
#147 #148	' 7
#146 #149	
#149 #150	2
#150	5

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AGGREGATE ORDER TYPES	
Company Info	
	FATAL
Name	FATAL REJECTS
#151	4
	'
#152	2
#153	3
#154	1
#155	9
#156	28
#157	10
#158	8
#159	30
#160	11
#161	1
#162	6
#163	5
#164	13
#165	23
#166	3
#167	2
#168	4
#169	21
#170	4
#171	11
#172	48
#173	3
#174	1
#175	3
#176	5
#177	2
#178	1
#179	1
#180	13
#180	13

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AGGREGATE ORDER TYPES	
Company Info	
<u>.</u> .	FATAL REJECTS
Name	
#181	2
#182	1
#183	8
#184	1
#185	3 8
#186	
#187	21
#188	3
#189	5
#190	3
#191	13
#192	5 1
#193	1
#194	7
#195	4
#196	1
#197	9
#198	2
#199	1
#200	8
#201	2
#202	6
#203	4
#204	9
#205	3
#206	2
#207	37
#208	7
#209	8
#210	32

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AGGREGATE ORDER TYPES	
Company Info	
	FATAL
Name	REJECTS
#211	53
#212	29
#213	11
#214	13
#215	28
#216	8
#217	1
#218	1
#219	37
#220	23
#221	3
#222	2
#223	1
#224	28
#225	8
#226	23
#227	2
#228	1
#229	23
#230	25
#231	21
#232	5
#233	48
#234	1
#235	24
#236	1
#237	28
#238	2
#239	4
#240	43

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AGGREGATE ORDER TYPES	1
Company Info	
Name	FATAL REJECTS
#241	18
#242	4
#243	2
#244	11
#245	6
#246	19
#247	35
#248	1
#249	12
#250	30
#251	8
#252	88
#253	7
Total	10,662

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AGGREGATE										
ERROR DETA	AILS (Auto C	larifications (	A) & Errors (E		CAUSATION	1			1	
						CLEC Cause	d		BST Caused	
Error Type (by error code)	Count	%	Σ % ,	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
1000	17,465	14.29%	14.29%	IF CHGING CLASS OF SERVICE ALL PERTINENT USOCS MUST BE POPULATED IN AND OUT	16,928	96 93%	20 11%	537	3 07%	1 413%
7020	958	0.78%	15.08%	NUM= TELNO= TN NOT FOUND IN CRIS	956	99 79%	1.14%	2	0 21%	0 005%
7055	2,216	1.81%	16.89%	NUM= TELNO= ACCOUNT IS FINAL	2,212	99.82%	2 63%	4	0 18%	0.011%
7095	3	0.00%	16.89%	INCORRECT RATE ZONE DATA RECEIVED FROM RSAG	0	0.00%	0.00%	3	100 00%	0 008%
7110	4,464	3.65%	20.55%	COFFI NOT AVAILABLE	1,452	32 53%	1.72%	3012	67 47%	7 926%
7115	12	0.01%	20.56%	DSAP TELEPHONE NUMBER NOT ACTIVE/FOUND IN SITE	3	25.00%	0 00%	9	75 00%	0 024%
7235	533	0.44%	20.99%	10 DIGIT TN REQUIRED WITH USOC/FID=ZCRN	368	69.04%	0 44%	165	30 96%	0 434%
7245	731	0.60%	21.59%	NUM= ZCRT FID, DATA, OR DELIMITER IS MISSING	500	68.40%	0.59%	231	31 60%	0 608%
7250	366	0.30%	21.89%	LSR HOUSENUMBER INCORRECT	366	100 00%	0.43%	0	0 00%	0 000%
7260	2	0.00%	21.89%	LISTING TYPE INVALID	2	100 00%	0.00%	0	0 00%	0 000%
7267	4	0.00%	21.89%	UNE - LOCBAN MISSING FOR LINP ORDER	4	100.00%	0 00%	0	0 00%	0 000%
7295	26	0.02%	21.92%	LINE CLASS OF SERVICE MISSING. NUM AND TN REQUIRED	18	69 23%	0.02%	8	30.77%	0 021%
7300	6	0.00%	21.92%	UNE - CANNOT GENERATE CLASS OF SERVICE USOC	6	100.00%	0.01%	0	0 00%	0 000%
7315	229	0.19%	22.11%	CANNOT GENERATE BILLING NAME AND ADDRESS FIDS	196	85.59%	0.23%	33	14.41%	0 087%
7375	47	0.04%	22.15%	UNE - BOCABS SCREEN ERROR BOE001 ACCOUNT NUMBER NOT FOUND	43	91.49%	0.05%	4	8.51%	0.011%
7380	140	0.11%	22.26%	UNE - ACTL INVALID	140	100.00%	0 17%	0	0.00%	0.000%
7400	7,387	6.05%	28.31%	CLEC DOES NOT OWN THIS ACCOUNT.	7,387	100 00% .	8.77%	0	0.00%	0 000%
7435	1	0.00%	28.31%	WKG SVC - INPUT ADL, CONVSN ORD OR NOTE ABAND STA	1	100.00%	0 00%	- 0	0 00%	0 000%
7445	69	0.06%	28.36%	UNE - CALL FORWARD TN REQUIRED	69	100.00%	0 08%	0	0 00%	0.000%
7465	1,285	1.05%	29.42%	CANNOT CANCEL ORDER	1,008	78.44%	1.20%	277	21 56%	0 729%
7495	19	0.02%	29.43%	UNE - DIR LOCATOR PROBLEM	5	26.32%	0.01%	14	73 68%	0 037%
7555	192	0.16%	29.59%	FID MISSING IN FEATURE DETAIL	168	87 50%	0.20%	24	12 50%	0 063%
7570	1	0 00%	29.59%	SEQ1X NOT ALLOWED WITH ZNB	0	0.00%	0 00%	1	100 00%	0 003%
7630	90	0.07%	29.66%	MEMORY CALL SERVICE NOT AVAILABLE IN SWITCH	50	55 56%	0.06%	40	44 44%	0 105%
7640	4	0.00%	29.67%	DUPLICATE CUSTOMERS EXCEED NINE ON CSR	0	0.00%	0 00%	4	100 00%	0 011%
7645	3,140	2.57%	32.24%	MATCH IN CSR SA AND LSR HOUSENUM NOT FOUND	1,522	48 47%	1.81%	1618	51 53%	4 258%
7660	7	0.01%	32.24%	USOC FUJ1X NOT FOR RESALE	7	100 00%	0 01%	0	0.00%	0 000%
7690	14	0.01%	32.25%	UNE - ACTL AND ENDUSER LSO MUST BE THE SAME FOR LOOP/LINP SERVICE	14	100.00%	0 02%	0	0 00%	0 000%
7710	294	0.24%	32.49%	CANNOT CANCEL OR CHANGE DUE DATE ON NON-EXISTENT ORDER	173	58.84%	0 21%	121	41 16%	0 318%
7715	10	0.01%	32.50%	SOCS TIMEOUT/NOT AVAILABLE	6	60 00%	0.01%	4	40 00%	0 011%
7718	2,145	1.76%	34.26%	UNABLE TO RETRIEVE PSO TO PROCESS SUP	822	38.32%	0.98%	1323	61 68%	3 481%
7725	70	0.06%	34.31%	WAITING PERIOD EQUALS 5 MINUTES	30	42.86%	0.04%	40	57 14%	0 105%
7735	56	0 05%	34.36%	INVALID/MISSING LISTING NAME OR TYPE	56	100 00%	0 07%	0	0 00%	0 000%
7740	17	0.01%	34.37%	LOCAL CALLING PLUS INDICATOR NOT FOUND	12	70.59%	0 01%	5	29 41%	0 013%
7755	11	0.01%	34.38%	UNE - NPANXX NOT FOUND IN CLLI TABLE	7	63.64%	0 01%	4	36 36%	0 011%
7805	89	0 07%	34 46%	SITE COULD NOT BE DETERMINED	44	49.44%	0 05%	45	50 56%	0 118%

AGGREGATE	ORDER TY	PES	1		1					<del></del>	
ERROR DETA	AILS (Auto C	larifications	(A) & Errors (E	))	CAUSATION	Į.	i		T	1	
						CLEC Cause	d	BST Caused			
Error Type (by error code)	Count	%	Σ % ,	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused	
7815	81	0.07%	34.52%	FID=RCU INVALID OR MISSING DATA	60	74.07%	0 07%	21	25.93%	0 055%	
7860	133	0.11%	34.63%	RSAG - NO EXACT MATCH ON STREET NAME	133	100.00%	0.16%	0	0 00%	0 000%	
7890	14	0.01%	34.64%	RSAG - NO EXACT MATCH ON SUPPLEMENTAL ADDRESS	14	100 00%	0.02%	0	0.00%	0 000%	
7900	9	0.01%	34.65%	RSAG - NO MATCH ON STREET NAME	8	88.89%	0.01%	1	11 11%	0 003%	
7905	3,546	2.90%	37.55%	RSAG - INCORRECT COMMUNITY, INCORRECT ZIP CODE OR INVALID ADDRESS FORMAT	3,545	99.97%	4 21%	1	0 03%	0 003%	
7910	2,238	1.83%	39.38%	RSAG - NO MATCH ON EXACT STREET NAME	2,095	93.61%	2.49%	143	6 39%	0 376%	
7935	22	0.02%	39.40%	RSAG-SIMILAR STREET FOUND IN DIFFERENT COMMUNITY AND/OR ZIP	22	100.00%	0.03%	0	0.00%	0 000%	
7945	47	0.04%	39.44%	RSAG SYSTEM ERROR	27	57.45%	0.03%	20	42 55%	0 053%	
8150	56	0.05%	39.48%	ORDER HAS BEEN REQUEUED FOR THE MAXIMUM NUMBER OF OCCURRENCES	15	26.79%	0 02%	41	73 21%	0 108%	
8167	50	0.04%	39.53%	INVALID USOC CHARACTER. FORMAT SAE 013 11 CREXI	50	100.00%	0.06%	0	0.00%	0 000%	
8170	417	0.34%	39.87%	USOC MAY ONLY APPEAR ONCE, FORMAT SAE 110 11 CREX1 /TN	412	98.80%	0.49%	5	1 20%	0.013%	
8173	193	0.16%	40.03%	INVALID CLASS OF SERVICE. FORMAT IDNT 131 UEPRL=	193	100.00%	0 23%	0	0 00%	0 000%	
8180	166	0.14%	40.16%	LNUM=00001 TC TO PRIMARY NUMBER MUST BE DIFFERENT FROM NUMBER BEING REFE	166	100.00%	0 20%	0	0 00%	0 000%	
8183	15	0.01%	40.17%	AREA CALLING PLAN USOC MISMATCH. FORMAT 320 LINE UPP:0000000 / LINE ASSIGN:00	15	100.00%	0.02%	0	0 00%	0 000%	
8185	58	0.05%	40.22%	ESC/ESCWT NOT VALID COMBINATION. FORMAT SAE 424 I1 ESCWT	58	100.00%	0.07%	Ô	0 00%	0 000%	
8187	1,672	1.37%	41.59%	USOC MAY NOT APPEAR ON REQUEST. FORMAT SAE 431 T1 EMP1S /TN	1,671	99.94%	1 98%	1	0.06%	0 003%	
8189	589	0.48%	42.07%	USOC IS NOT VALID ON BST FILE. FORMAT SAE 433 11 CREX6	589	100 00%	0.70%	0	0.00%	0 000%	
8190	1,929	1.58%	43.65%	INVALID USOC FOR BASIC CLASS OF SERVICE. FORMAT SAE 434 11 S98CP /TN	1,879	97.41%	2 23%	50	2 59%	0 132%	
8193	15	0.01%	43.66%	USOC NOT VALID WITH CALLER ID. FORMAT SAE 473 I1 NXMCR /TN	15	100 00%	0.02%	Ô	0 00%	0 000%	
8195	488	0.40%	44.06%	CALL FORWARDING USOC MUST NOT APPEAR. FORMAT SAE 540 11 GCJ /TN	488	100 00%	0 58%	0	0 00%	0 000%	
8197	634	0.52%	44.58%	CALL FORWARDING USOC MUST APPEAR. FORMAT SAE 541	634	100.00%	0.75%	0	0 00%	0 000%	
8199	83	0.07%	44.65%	GCJRC/GCJ COMBINATION INVALID FORMAT SAE 560 11 GCJRC /TN	83	100.00%	0 10%	0	0 00%	0 000%	
8204	137	0.11%	44.76%	BCR/NSS/NX8 INVALID USOC COMBINATION. FORMAT SAE 575 R1 NSS /TN	137	100.00%	0.16%	0	0 00%	0 000%	
8207	61	0.05%	44.81%	BRD/NSQ/NX9 INVALID USOC COMBINATION. FORMAT SAE 576 11 NX9 /TN	61	100.00%	0 07%	0	0.00%	0 000%	
8209	618	0.51%	45.32%	USOC COMBINATION IS INVALID. FORMAT SAE 587 11 ESXDC /TN	618	100 00%	0.73%	0	0 00%	0 000%	
8240	150	0.12%	45.44%	INVALID LINE CLASS OF SVC FOR REQUESTED SERVICE	150	100 00%	0.18%	0	0.00%	0 000%	
8250	41	0.03%	45.47%	USOC= NOT APPLICABLE TO PORT LOOP SERVICE	41	100.00%	0 05%	0	0.00%	0.000%	
8395	1	0.00%	45.47%	LSF INVALID FOR REQTYP/ACT TYPE COMBINATION	1	100 00%	0 00%	0	0 00%	0.000%	
8415	11	0.01%	45.48%	LSF LP ALREADY EXISTS ON ACCOUNT	11	100.00%	0.01%	0	0 00%	0 000%	
8430	4	0.00%	45.49%	LSF DOES NOT EXIST ON ACCOUNT	4	100.00%	0 00%	0	0 00%	0 000%	
8820	10,778	8.82%	54.31%	SOCS ERROR: LUD BILL 004 ACT CODE NOT FOR THIS ORD TYPE	3,125	28.99%	3 71%	7653	71.01%	20 138%	
8825	18,527	15.16%	69.47%	ORDER ERR:	4,237	22 87%	5 03%	14290	77 13%	37 603%	
8830	543	0.44%	69.91%	CLEC ALREADY OWNS THIS ACCOUNT	543	100 00%	0.64%	0	0 00%	0 000%	
8850	56	0.05%	69.96%	CFA NOT FOUND, PLEASE VERIFY CFA	56	100.00%	0 07%	0	0 00%	0 000%	
8855	1	0.00%	69.96%	NO ACTL IN LSR	1	100 00%	0 00%	0	0 00%	0 000%	
8925	1	0.00%	69.96%	CFN HAS INVALID FORMAT ON COFFI SCREEN	0	0 00%	0 00%	1	100.00%	0 003%	

#### **ORDERING**

#### REPORT: FLOWTHROUGH ERROR ANALYSIS REPORT PERIOD: 11/01/2001 - 11/30/2001

S (Auto Ci	arifications (	A) & Errors (E							
		, (-	))	CAUSATION	L	<u> </u>			
					CLEC Cause	i		BST Caused	
Count	%	Σ % ,	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
1,400	1 15%	71.10%	CALL FORWARDING NUMBER MISSING OR INVALID	1,400	100 00%	1.66%	0	0 00%	0 000%
64	0.05%	71.16%	LINECLSSVC AND TOS DO NOT MATCH	64	100.00%	0.08%	0	0.00%	0 000%
908	0.74%	71.90%	FID RCU WITH TWC FOUND ON SAME LINE AS 3-WAY CALLING USOC	907	99.89%	1.08%	1	0 11%	0 003%
1	0.00%	71.90%	SEMICOLON DISALLOWED WITH (+) SIGN IN PERSONAL NAME LISTINGS	1	100.00%	0 00%	0	0 00%	0 000%
		71 93%	LSO/LOCBAN (NPANXX) MISSING OR INVALID	31	100 00%	0.04%	0	0 00%	0 000%
		71.93%	TELNO= PIC REQUIRED PER UNIQUE TELEPHONE NUMBER ON A, V, P9 LINE ACTIVITY TY	1	100 00%	0 00%	0	0 00%	0 000%
		71.93%	TELNO= LPIC REQUIRED PER UNIQUE TELNO ON A, V, P9 LINE ACTIVITY TYPES	1	100 00%	0 00%	0	0 00%	0 000%
		71.98%	UNE - PORTED OUT NUMBER	63	100 00%	0 07%	0	0 00%	0 000%
				10	100.00%	0 01%	0	0 00%	0 000%
			CORRECT ECCKT IS REQUIRED FOR LNA , LNUM	383	100.00%	0.45%	0	0 00%	0 000%
			NC CODE IS A REQUIRED FIELD FOR LOOP REQUESTS	1	100.00%	0.00%	0	0 00%	0 000%
			DLNUM=0001 LTN=HTN ACCOUNT NOT OWNED BY CLEC	4	100 00%	0 00%	0	0 00%	0 000%
			DLNUM=0001 LTN= ACCOUNT ACTIVITY OF N CAN ONLY HAVE AN LACT OF N	12	100 00%	0 01%	0	0 00%	0 000%
				130	100.00%	0.15%	0	0 00%	0 000%
			DLNUM=0002 LTN= ALI MUST BE UNIQUE	455	97.85%	0.54%	10	2 15%	0 026%
			LNUM=00001 =TC FR REFERENCE OF CALLS UNAVAILABLE FOR THIS NUMBER	3	100.00%	0.00%	0	0 00%	0 000%
				32	100.00%	0.04%	0	0 00%	0 (= 1%
				18	81 82%	0.02%	4	18 18%	0.011%
		73.13%	ACT= ALLOWED ONLY ON SAME LOCNUM SERVICE ADDRESS	348	99.71%	0 41%	1	0 29%	0 003%
			IS NOT FOUND ON CSR TO DISCONNECT	73	100 00%	0.09%	0	0 00%	0 000%
				80	97.56%	0.10%	2	2 44%	0 005%
				158	100.00%	0 19%	0	0 00%	0 000%
				2,381	99.46%	2.83%	13	0 54%	0 034%
				36	100.00%	0 04%	0	0 00%	0 000%
				4	100.00%	0.00%	0	0 00%	0 000%
				361	99 72%	0.43%	1	0 28%	0 003%
				2	100.00%	0.00%	0	0 00%	0 000%
				148	100.00%	0 18%	0	0 00%	0 000%
				2,163	99 82%	2.57%	4	0 18%	0 011%
					100 00%	0.06%	0	0 00%	0.000%
					99.67%	1 81%	5	0 33%	0 013%
				1	100 00%	0 02%	0	0 00%	0.000%
					· -	1	0	0 00%	0 000%
					i	0.00%	0	0 00%	0 000%
					-		0	0 00%	0 000%
					+ -	t	0		0 000%
	64 908	64         0.05%           908         0.74%           1         0.00%           31         0.03%           1         0.00%           1         0.00%           63         0.05%           10         0.01%           383         0.31%           1         0.00%           4         0.00%           12         0.01%           130         0.11%           465         0.38%           3         0.00%           32         0.03%           22         0.02%           349         0.29%           73         0.06%           82         0.07%           158         0.13%           2,394         1.96%           36         0.03%           4         0.00%           362         0.30%           2         0.00%           148         0.12%           2,167         1.77%           48         0.04%           1,527         1.25%           17         0.01%           2         0.00%           5         0.00%	64         0.05%         71.16%           908         0.74%         71.90%           1         0.00%         71.90%           31         0.03%         71.93%           1         0.00%         71.93%           1         0.00%         71.93%           63         0.05%         71.98%           10         0.01%         71.99%           383         0.31%         72.30%           4         0.00%         72.31%           12         0.01%         72.31%           130         0.11%         72.42%           465         0.38%         72.80%           3         0.00%         72.80%           32         0.03%         72.80%           32         0.03%         72.80%           349         0.29%         73.13%           73         0.06%         73.19%           82         0.07%         73.26%           158         0.13%         75.35%           36         0.03%         75.38%           4         0.00%         75.38%           4         0.00%         75.38%           4         0.00%         75.3	64   0.05%   71.16%	64	0.05%   71.16%   LINECLSSVC AND TOS DO NOT MATCH   64   100.00%   908   0.74%   71.90%   Filip RCU WITH TWC FOUND ON SAME LINE AS 3-WAY CALLING USOC   907   99.89%   907   99.89%   10.00%   71.90%   SEMICOLON DISALLOWED WITH (+) SIGN N PERSONAL NAME LISTINGS   1   100.00%   11.00%   71.93%   LSOLOCBAN (NPANXX) MISSING OR INVALID   31   100.00%   10.00%   71.93%   TELNO= PIC REQUIRED PER UNIQUE TELEPHONE NUMBER ON A. V. P9 LINE ACTIVITY TO 1   100.00%   10.00%   71.93%   TELNO= PIC REQUIRED PER UNIQUE TELLEPHONE NUMBER ON A. V. P9 LINE ACTIVITY TO 1   100.00%   10.00%   71.93%   TELNO= PIC REQUIRED PER UNIQUE TELLEPHONE NUMBER ON A. V. P9 LINE ACTIVITY TYPES   1   100.00%   10.00%   71.99%   UNE - PORTED OUT NUMBER   63   100.00%   10.00%   77.30%   UNE - PORTED OUT NUMBER ACTIVITY   10   100.00%   10.00%   72.30%   CORRECT ECCKT IS REQUIRED FOR IAA , INUM   383   100.00%   72.30%   CORRECT ECCKT IS REQUIRED FOR IAA , INUM   383   100.00%   72.30%   CORRECT ECCKT IS REQUIRED FIELD FOR LOOP REQUESTS   1   100.00%   10.00%   72.31%   DLNUM-0001 LTN=HTIN ACCOUNT NOT OWNED BY CLEC   4   100.00%   10.00%   72.31%   DLNUM-0001 LTN=HTIN ACCOUNT NOT OWNED BY CLEC   4   100.00%   10.00%	0.05%   71.16%   LINECLSSVC AND TOS DO NOT MATCH   64   100.00%   0.08%   908   0.74%   71.80%   Fine RCU WITH TWE FOUND ON SAME LINE AS 3-WAY CALLING USOC   907   99.88%   1.08%	0.06%   71.10%   LINECLSSVC AND TOS DO NOT MATCH   64   100.00%   0.08%   0   0.74%   71.90%   FID ROLU WITH TWC FOUND ON SAME LINE AS 3-WAY CALLING USOC   907   99.89%   1.00%   0   0.00%   71.90%   SEMICOLO DISALLOWER UNITH (+) SIGN IN PERSONAL NAME LISTINGS   1   100.00%   0.00%	10.05%   71.16%   LINECLSSVC AND TOS DO NOT MATCH

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AGGREGATE	ORDER TY	PES				Ţ				<del></del>
ERROR DET	AILS (Auto C	larifications (	A) & Errors (E		CAUSATION	I.			·····	<del></del>
						CLEC Cause	d		BST Caused	1
Error Type (by error code)	Count	%	Σ % ,	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused
9529	1,763	1.44%	80.35%	CANNOT RESTORE A LINE WHICH IS NOT SUSPENDED/DENIED	1,763	100.00%	2.09%	0	0 00%	0 000%
9530	1	0.00%	80.35%	APPOINTMENT TIME CANNOT BE PRIOR TO 800A OR LATER THAN 500P	1	100.00%	0.00%	0	0 00%	0 000%
9543	58	0.05%	80.40%	LOCNUM= HNUM= HT= HT CANNOT BE IN MORE THAN ONE HID	58	100 00%	0.07%	0	0 00%	0 000%
9545	2	0.00%	80.40%	LOCNUM= HNUM=00001 HA OF D NOT ALLOWED	- 2	100.00%	0.00%	0	0 00%	0 000%
9602	4,335	3.55%	83.95%	USOC=NSS ALREADY EXISTS ON CUSTOMER RECORD	4,309	99.40%	5.12%	26	0 60%	0 068%
9604	14	0.01%	83.96%	TN ON SUP DOES NOT MATCH ORIGINAL TN	12	85.71%	0.01%	2	14.29%	0 005%
9605	161	0.13%	84.09%	USOC NOT FOR RESALE FORMAT SAE 959 T1 PGRAX /ZPGR 1 /RMKR (A)	161	100.00%	0.19%	0	0.00%	0 000%
9606	21	0.02%	84.11%	TNS CANNOT BE REASSIGNED FOR 90 DAYS	21	100 00%	0.02%	0	0.00%	0 000%
9613	19	0.02%	84.12%	EXISTING ACCOUNT TYPE NOT AUTHORIZED FOR MIGRATION YET	19	100.00%	0 02%	- 0	0 00%	0 000%
9616	18	0.01%	84 14%	YPH INVALID	18	100.00%	0.02%	0	0.00%	0 000%
9623	15	0.01%	84.15%	TOUCHTONE IS INVALID WITH AREA PLUS SERVICE	15	100.00%	0.02%	0	0 00%	0 000%
9626	410	0.34%		CLASS OF SERVICE LNPRL NOT ELIGIBLE FOR CONVERSION TO PORT/LOOP	410	100.00%	0 49%	0	0.00%	0.000%
9627	1.826	1.49%	85.98%	ALL CUSTOMER RECORDS ARE FINAL FOR THIS NUMBER	1,826	100.00%	2.17%	0	0 00%	0 000%
9628	300	0.25%	86.23%	REQUEST DOES NOT QUALIFY FOR STAR 98 SERVICE	300	100.00%	0 36%	0	0.00%	0 000%
9629	48	0.04%	86.27%	CALL FORWARDING FID (CFND) AND CFND TN REQUIRED BEHIND USOC S98AF	48	100.00%	0.06%	0	0 00%	0 000%
9639	146	0.12%	86.39%	CATEGORY L USOC MUST APPEAR FOR SAME TN	146	100.00%	0 17%	0	0 00%	0 000%
9641	1.802	1.47%	87.86%	REQUESTED ACTIVITY ALREADY PENDING DM4V32	1,802	100.00%	2.14%	. 0	0 00%	0 000%
9647	105	0.09%	87.95%	BAN DOES NOT EXIST FOR COMPANY CODE	105	100.00%	0.12%	0	0.00%	0 000%
9654	412	0.34%		DIRECTORY DELIVERY ADDRESS IS REQUIRED FOR INDEFINITE OR UNNUMBERED ENDUS	409	99.27%	0.49%	3	0 73%	0 008%
9656	4	0.00%		SLTN NOT FOUND ON CRIS ACCOUNT FOR LNA N, LNUM	4	100.00%	0.00%	0	0 00%	0 000%
9657	2	0.00%	88.29%	ECCKT/UNE1 MISMATCH	2	100.00%	0 00%	0	0 00%	0 000%
9661	7	0.01%	88.29%	LINE SHARE AND ADSL REQUIRED BST VOICE SERVICE	2	28.57%	0.00%	5	71 43%	0 013%
9670	21	0.02%	88.31%	TOUCHTONE USOC REQUIRED INWARD OR RECAPPED - FORMAT SAE 004	21	100.00%	0.02%	- 0	0.00%	0 000%
9671	88	0.07%	88.38%	TOUCHTNE USOC REQUIRED - FORMAT SAE 245	88	100.00%	0 10%	0	0.00%	0.000%
9673	4	0.00%	88.39%	RINGMASTER USOC REQUIRED - FORMAT SAE 387	4	100.00%	0.00%	0	0 00%	0 000%
9674	17	0.01%		INVALID TN/PN DATA - FORMAT SAE 389 I1 DRS /TN /PN /RNP B	17	100.00%	0.02%	. 0	0.00%	0.000%
9675	27	0.02%	88.42%	BBC USOC MUST NOT APPEAR - FORMAT SAE 679 I1 BBC /TN	27	100 00%	0.03%	0	0 00%	0 000%
9679	2	0.00%		FIRST CHARACTER OF LINE NUMBER IS NOT VALID FOR BST IN COFFI	2	100.00%	0 00%	0	0 00%	0.000%
9680	52	0.04%		INVALID REQTYP OR TOS FOR LIFELINE	52	100.00%	0.06%	0	0.00%	0 000%
9681	30	0.02%	88.49%	LINKUP DISCOUNT CANNOT BE ADDED TO EXISTING SERVICE	30	100 00%	0.04%	0	0 00%	0 000%
9682	19	0.02%	88.51%	LINKUP DISCOUNT IS ONLY AVAILABLE ON LIFELINE ACCOUNTS	19	100.00%	0.02%	0	0.00%	0 000%
9685	9,498	7.77%		DUE DATE COULD NOT BE CALCULATED	1,353	14.25%	1,61%	8145	85 75%	21 433%
9686	2	0.00%		RESID NOT VALID IN LFACS	2	100.00%	0.00%	0	0.00%	0 000%
9687	6	0.00%	96.29%	ACT=N/LNA=N IS INVALID WHEN THE REQUESTING CLEC ALREADY HAS A LINESHARE ON		100.00%	0.00%	0	0.00%	0 000%
9700	6	0.00%	96.29%	REQUESTED CIRCUIT NUMBER/ECCKT NOT FOUND	<del>.</del> 6	100.00%	0.01%	0	0 00%	0 000%
9715	34	0.03%	96.32%	TOS IS INVALID FOR REQUESTED SERVICE	33	97.06%	0.01%	1	2 94%	0 003%

ERROR DETA	AILS (Auto C	arifications (	A) & Errors (E		CAUSATION			7 7 1			
						CLEC Cause	d	BST Caused			
Error Type (by error code)	Count	%	Σ %	Error Description	Count	% of Agg	% of CLEC	Count	% of Agg	% of BST Caused	
9772	2	0.00%	96.32%	UNE - ECCKT PROHIBITED WITH LINE ACTIVITY OF A	1	50.00%	0 00%	1	50 00%	0 003%	
9800	21	0.02%	96.34%	MAIN LISTING REQUIRED FOR NEW ACCOUNT	10	47 62%	0.01%	11	52 38%	0 029%	
9860	1,118	0.91%	97.25%	UNABLE TO HANDLE REQUEST; ENDUSER ACCOUNT FROZEN	1,114	99.64%	1.32%	4	0 36%	0 011%	
9861	759	0.62%	97.87%	ADSL NOT ALLOWED WITH THIS SERVICE	757	99.74%	0 90%	2	0.26%	0 005%	
9863	16	0.01%	97.89%	CLEC SHOULD HAVE THE ENDUSER CONTACT THEIR NSP/ISPFOR CHANGES TO ADSL SE	16	100.00%	0 02%	0	0 00%	0 000%	
9866	113	0.09%	97.98%	MULTILINE USOC DOES NOT APPLY	110	97.35%	0.13%	3	2 65%	0 008%	
9867	170	0.14%	98.12%	MULTILINE USOC DOES NOT APPLY	168	98.82%	0.20%	2	1 18%	0 005%	
9869	15	0.01%	98.13%	SINGLE LINE USOC DOES NOT APPLY	15	100.00%	0.02%	0	0 00%	0 000%	
9871	2,046	1.67%	99.81%	ADDRESS/TN INVALID, DUE DATE COULD NOT BE CALCULATED	2,045	99.95%	2.43%	1	0 05%	0 003%	
9881	26	0.02%	99.83%	CANNOT DETERMINE ADDRESS; TN WORKING AT MORE THAN ONE ADDRESS	26	100.00%	0 03%	0	0.00%	0 000%	
9897	212	0.17%	100.00%	TN FOR NON WORKING ADDRESS; DUE DATE COULD NOT BE CALCULATED	212	100.00%	0.25%	Ω	0 00%	0 000%	
Total	122,196	100.00%			84194		100 00%	38,002		100 000%	

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AGGREGATE	ORDER TYP	ES		
ERROR DETA	ULS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
1007	3	0.02%	0.02%	DUPLICATE CC, PON, VER
1012	3	0.02%	0.04%	CANNOT SUPP A PREVIOUSLY CANCELED LSR/PON
1015	4288	26.28%	26.31%	PON DUPLICATE ON INITIAL LSR
1023	22	0.13%	26.45%	NO ORIGINAL LSR FOUND FOR THIS SUP
1025	17	0.10%	26.55%	VER MUST BE GREATER THAN PREVIOUS VERSION
1027	7	0.04%	26.60%	PREVIOUS LSR AGED OFF - (K) STATUS
1030	846	5.18%	31.78%	VER MUST BE GREATER THAN PREVIOUS VERSION
1035	8	0.05%	31.83%	VER MUST BE TWO NUMERICS - 01 OR GREATER FOR 860
1040	80_	0.49%	32.32%	VER MUST BE SPACES OR ZEROES FOR 850
1050	12	0.07%	32.39%	D/SENT - D/SENT CENTURY MUST BE CURRENT OR FUTURE DATE
1055	6	0.04%	32.43%	AN REQUIRED FOR THIS REQTYP/ACT TYPE COMBINATION WHEN ATN IS NOT POPULATED
1060	3	0.02%	32.45%	AN PROHIBITED WHEN ATN IS POPULATED UNLESS REQTYP IS B
1065	16	0.10%	32.55%	AN MUST BE 10 OR 13 ALPHANUMERICS
1070	3	0.02%	32.57%	DDD/DDD-CC MUST BE CURRENT OR FUTURE DATE
1075	9	0.06%	32.62%	ATN REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION WHEN AN IS NOT POPULATED
1080	3	0.02%	32.64%	DDD/DDD-CC MUST BE A VALID DATE
1085	1	0.01%	32.64%	DDDO-CC/DDDO MUST BE CURRENT OR FUTURE DATE
1090	3	0.02%	32.66%	ATN OR AN REQUIRED WHEN EATN IS POPULATED
1100	1	0.01%	32.67%	SERVICE CENTER MUST BE LCSC
1110	771	4.72%	37.39%	INVALID REQTYP - ACCOUNT ACTIVITY TYPE COMBINATION
1125	53	0.32%	37.72%	DDD MUST BE GREATER THAN OR EQUAL TO D/TSENT
1131	275	1.69%	39.40%	DDD IS LESS THAN CALC DATE ON PRIOR VERSION LSR OR SERVICE ORDER DUE DATE
1135	5	0.03%	39.43%	APPTIME-DDD MUST BE HHMM-HHMM (MILITARY TIME) COVERING A SPAN OF TIME OF ONE HOUR OR GREATER
1140	12	0.07%	39.51%	DDDO REQUIRED WHEN ACT IS T AND REQTYP IS A, E, M, OR N
1145	12	0.07%	39.58%	INTERVAL BETWEEN DDD AND DDDO MUST BE 30 CALENDAR DAYS OR LESS
1150	1	0.01%	39.59%	SUP PROHIBITED WHEN 1ST CHARACTER OF REQTYP FIELD CHANGES
1154	1	0.01%	39.59%	LSR/PON IS COMPLETED
1155	1	0.01%	39.60%	DFDT MUST BE POPULATED WITH A SINGLE (HHMM) TIME WHEN CHC IS Y
1157	19	0.12%	39.72%	DFDT PROHIBITED FOR THIS REQTYP/LNA COMBINATION
1166	16	0.10%	39.81%	CHC IS PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
1175	1	0.01%	39.82%	REQTYP REQUIRED (STOP EDIT)
1180	10	0.06%	39.88%	INVALID REQTYP/ACT TYPE COMBINATION (STOP EDIT)

AGGREGATE	ORDER TY	PES	<u> </u>	
ERROR DET	AILS (Fatal E	rrors)	<u> </u>	
Error Type (by error code)	Count	%	Σ%	Error Description
1185	1	0.01%	39.89%	REQTYP VALID ENTRIES MUST BE AB, BB, CB, EB, FB, JB, MB OR NB (STOP EDIT)
1195	1	0.01%	39.89%	ACTIVITY TYPE VALID ENTRY MUST BE N, C, D, T, R, V, S, B, W, L, Y, P OR Q (STOP EDIT)
1200	82	0.50%	40.40%	SUP REQUIRED WHEN VER IS GREATER THAN 00
1215	128	0.78%	41.18%	ACTL MUST BE 11 ALPHANUMERIC CHARACTERS
1230	2879	17.64%	58.82%	LSO MUST BE 6 NUMERICS
1270	3	0.02%	58.84%	SECNCI MUST BE A MINIMUM OF 5 ALPHANUMERIC CHARACTERS
1285	7	0.04%	58.89%	ACTL REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1290	2	0.01%	58.90%	ACTL MUST BE 11 ALPHANUMERICS
1325	2	0.01%	58.91%	LST MUST BE 11 ALPHANUMERICS
1335	4	0.02%	58.93%	LSO REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1345	6	0.04%	58.97%	TOS REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION (STOP EDIT)
1370	3	0.02%	58.99%	TOS THIRD CHARACTER MUST NOT BE F IF REQTYP IS FB
1390	14	0.09%	59.08%	TOS SECOND CHARACTER MUST BE - (HYPHEN) IF REQTYP IS JB
1392	3	0.02%	59.09%	TOS SECOND CHARACTER OF J IS PROHIBITED ON REQTYP OF A,B,C,F OR J (STOP EDIT)
1430	8	0.05%	59.14%	CIC REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1435	1	0.01%	59.15%	CIC MUST BE 4 NUMERICS
1445	1	0.01%	59.16%	INITIATOR TELEPHONE NUMBER REQUIRED
1453	32	0.20%	59.35%	BAN1 REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1455	79	0.48%	59.84%	BAN1 VALID ENTRY MUST BE VALID BILLING ACCOUNT NUMBER OR E WITH TRAILING BLANKS
1490	3	0.02%	59.85%	DRC MUST BE 3 ALPHANUMERICS
1505	4	0.02%	59.88%	INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1510	4	0.02%	59.90%	TEL NO-INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1515	35	0.21%	60.12%	TEL NO-INIT FORMAT MUST BE 10 NUMERICS OR UP TO 15 ALPHANUMERICS
1520	224	1.37%	61.49%	FAX NO-INIT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1525	1	0.01%	61.50%	FAX NO-INIT MUST BE 10 NUMERICS
1530	26	0.16%	61.66%	IMPCON REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
1575	3	0.02%	61.67%	TEL NO DSGCON FORMAT MUST BE 10 NUMERICS IN THE FIRST TEN POSITIONS
1580	1	0.01%	61.68%	FAX NO-DSGCON MUST BE 10 NUMERICS
1600	1	0.01%	61.69%	ZIP CODE-DSGCON REQUIRED WHEN DSGCON IS POPULATED
1605	105	0.64%	62.33%	REMARKS VIRGULES (/) AND ASTERISKS NOT ALLOWED IN THIS FIELD
1610	3	0.02%	62.35%	PBT REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION

GGREGATE	ORDER TYP	PES		
ERROR DETA	AILS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
1620	4	0.02%	62.37%	BCS REQUIRED WITH REQTYP/ACT TYPE/TOS COMBINATION
1630	99	0.61%	62.98%	CANNOT SUP A PREVIOUSLY CANCELED LSR/PON
1635	54	0.33%	63.31%	LSR ORIGINATING SOURCE NOT SAME AS PRIOR VERSION
1640	284	1.74%	65.05%	NO ORIGINAL LSR FOUND FOR THIS SUP
1645	1642	10.06%	75.11%	LSR/PON AGED OFF
1650	537	3.29%	78.40%	LSR/PON COMPLETED
1655	7	0.04%	78.45%	LSR ORIGINATING FORMAT (TCIF) NOT SAME AS ORIGINATING FORMAT
1660	37	0.23%	78.67%	SUP NOT ALLOWED ON THIS ACCOUNT ACTIVITY TYPE
1662	20	0.12%	78.80%	SUP NOT ALLOWED ON RESTORAL WHEN THE REASON WAS DENIED
1664	62	0.38%	79.18%	SUP 03 NOT ALLOWED ON THIS ACCOUNT ACTIVITY TYPE
2005	1	0.01%	79.18%	EU-STREET-1 REQUIRED
2015	6	0.04%	79.22%	EU-STATE REQUIRED
2040	2	0.01%	79.23%	LOCNUM=000 SANO PROHIBITED WHEN SASN IS NOT POPULATED AT THIS LOCATION
2050	2	0.01%	79.24%	LOCNUM=000 SASD PROHIBITED WHEN SASN IS NOT POPULATED AT THIS LOCATION
2055	9	0.06%	79.30%	LOCNUM=000 SASD VALID ENTRY IS E, W, N, S, NE, NW, SE, OR SW AT THIS LOCATION
2060	11	0.07%	79.37%	LOCNUM=000 SASN REQUIRED WITH THIS REQTYP/ACT TYP COMBINATION AT THIS LOCATION
2065	7	0.04%	79.41%	LOCBAN REQUIRED
2070	2	0.01%	79.42%	LOCNUM=000 SATH PROHIBITED WHEN SASN IS NOT POPULATED AT THIS LOCATION
2080	12	0.07%	79.50%	LOCNUM=000 SADLO REQUIRED WHEN SANO IS NOT POPULATED AT THIS LOCATION
2085	36	0.22%	79.72%	LOCNUM=000 FLOOR-EU MUST NOT BE POPULATED WITH FLR IN ANY POSITION AT THIS LOCATION
2090	15	0.09%	79.81%	LOCNUM=000 ROOM-EU MUST NOT BE POPULATED WITH RM OR ROOM IN ANY POSITION AT THIS LOCATION
2095	10	0.06%	79.87%	LOCNUM=000 BLDG-EU MUST NOT BE POPULATED WITH BLDG IN ANY POSITION AT THIS LOCATION
2100	1	0.01%	79.87%	LOCNUM=000 CITY-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2105	1	0.01%	79.88%	LOCNUM=000 STATE-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2109	35	0.21%	80.10%	LOCNUM=000 ZIP CODE=EU REQUIRED WHEN SASN IS POPULATED AT THIS LOCATION
2110	445	2.73%	82.82%	LOCNUM=000 ZIP CODE-EU REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION AT THIS LOCATION
2115	4	0.02%	82.85%	FBCON-TELNO MUST BE MINIMUM OF 10 NUMERICS
2120	388	2.38%	85.22%	EATN, EAN, ATN OR AN ARE PROHIBITED ON THIS REQTYP/ACT CODE
2130	11	0.07%	85.29%	LOCNUM=000 TEL NO-LCON MUST BE 10 NUMERICS AT THIS LOCATION
2155	1	0.01%	85.30%	ATN MUST BE 10 NUMERICS
2185	6	0.04%	85.34%	EAN MUST BE 10 NUMERICS OR 13 ALPHANUMERICS

AGGREGATE	ORDER TYP	ES		
ERROR DETA	ALS (Fatal Er	rors)		
Error Type (by error code)	Count	%	Σ%	Error Description
2200	2	0.01%	85.35%	EATN MUST BE 10 NUMERICS
2340	1	0.01%	85.35%	ERL WITH THE DATA OF Y PROHIBITED WHEN LEATN IS POPULATED
2350	8	0.05%	85.40%	ERL REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
2355	3	0.02%	85.42%	ERL PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
3010	49	0.30%	85.72%	REFNUM=0001-TELNO= LINE ACTIVITY MUST BE Y OR L WHEN ACCOUNT ACTIVITY = SS OR RS
3035	8	0.05%	85.77%	REFNUM=0001-TELNO= OTN MUST BE 10 NUMERICS
3045	3	0.02%	85.79%	REFNUM=0001 ECCKT MUST BE CLT, CLF OR CLS FORMAT
3047	39	0.24%	86.03%	LNUM=00001 CFA LOC A OR LOC Z CLLI DOES NOT MATCH ACTL
3050	26	0.16%	86.19%	LOCNUM=000 LNUM=00001 CFA FORMAT IS INVALID
3055	1	0.01%	86.19%	REFNUM=0001-TELNO= FPI MUST BE VALID VALUE FOR REQTYP AND ACTIVITY
3085	1	0.01%	86.20%	REFNUM=0001-TELNO= TC OPT VALID ENTRIES ARE:00, 03, 05, 08, 21, 23, 25, 26, 31, 51, 81
3090	52	0.32%	86.52%	REFNUM=0001-TELNO= TC OPT PROHIBITED ON THIS ACT TYPE AND REQTYP
3100	3	0.02%	86.54%	LOCNUM=000 LNUM=00001 TELNO= CHAN/PAIR REQUIRED WHEN CABLE ID IS POPULATED
3110	41	0.25%	86.79%	LOCNUM=001 LNUM=00001 TELNO= CKR FORMAT INVALID
3115	13	0.08%	86.87%	LOCNUM=000 LNUM=00002 TELNO= ECCKT IS PROHIBITED WITH REQTYP/ACT/LNA COMBINATION
3125	3	0.02%	86.89%	LOCNUM=000 LNUM=00001 TELNO= ECCKT FORMAT INVALID
3135	38	0.23%	87.12%	REFNUM=0001-TELNO TC PER-CC/TC PER-DATE REQUIRED WHEN TCTO-PRIMARY FIELD IS POPULATED
3140	9	0.06%	87.17%	LOCNUM=000 LNUM=00001 TELNO= ECCKT REQUIRED WHEN EAN OR LEAN IS POPULATED
3160	2	0.01%	87.19%	LOCNUM=000 LNUM=00001 TELNO= FA VALID ENTRY MUST BE N, C OR D
3165	6	0.04%	87.22%	REFNUM=0001-TELNO=TBE PROHIBITED ON THIS ACTIVITY FOR THIS REQTYPE
3170	10	0.06%	87.28%	REFNUM=0001-TELNO≈ CFA INVALID FORMAT
3180	7	0.04%	87.33%	REFNUM=0001-TELNO≈ FA VALID ENTRIES ARE A, C OR D
3190	36	0.22%	87.55%	LOCNUM=000 LNUM=00001 TELNO= FEATURE MUST BE 3, 5 OR 6 ALPHANUMERICS
3205	13	0.08%	87.63%	LOCNUM=000 LNUM=00001 TELNO= FEATURE DETAIL REQUIRED WHEN FA IS C
3220	1	0.01%	87.63%	LOCNUM=000 LNUM=00001 TELNO= IWJK MUST BE 5 ALPHANUMERICS
3245	8	0.05%	87.68%	LOCNUM=000 LNUM=00001 TELNO= IWJQ REQUIRED WHEN JR IS Y
3380	11	0.07%	87.75%	LOCNUM=000 LNUM=00001 TELNO= LNA MUST BE N IF ACT IS N
3385	4	0.02%	87.77%	LOCNUM=000 LNUM=00001 TELNO= LNA MUST BE D, G, N, P, V, W OR X IF ACT IS V, P OR Q
3390	2	0.01%	87.79%	LOCNUM=000 LNUM=00002 TELNO= LNA REQUIRED WHEN ACT TYP IS N, C, T, R, V, S, P OR Q
3400	20	0.12%	87.91%	LOCNUM=000 LNUM=00001 TELNO= LNA MUST BE N OR C IF ACT IS T
3405	1	0.01%	87.92%	LOCNUM=000 LNUM=00001 TELNO= LNA MUST BE R IF ACT IS R

AGGREGATE	ORDER TYP	ES		
ERROR DETA	AILS (Fatal Er	rors)		
Error Type (by error code)	Count	%	Σ%	Error Description
3410	79	0.48%	88.40%	LNUM=00001 TELNO= LNA MUST BE X OR G IF OTN IS POPULATED
3415	23	0.14%	88.54%	LOCNUM=000 LNUM=00002 TELNO= LNA MUST BE N, C, D, R, X, V, G, W, P, L OR B
3420	15	0.09%	88.63%	LOCNUM=000 LNUM=1 TELNO= LNA MUST BE N, C, D, P, OR X IF ACT IS C
3422	3	0.02%	88.65%	LNUM=00001 LNA MUST BE N OR D IF REQTYP IS A DIGITAL, DATA DESIGNED (DS1)
3427	3	0.02%	88.67%	LNUM=00001 TELNO= LNA OF G PROHIBITED ON REQTYP/ACT TYP COMBINATION
3430	4	0.02%	88.69%	FOR REQTYP E,F OR M, IF ACT IS P, Q OR V AT LEAST ONE LNA MUST BE G, P, V, W OR X
3433	12	0.07%	88.77%	LOCNUM=000 LNUM=00001 TELNO= LNA PROHIBITED ON THIS REQTYP/ACT TYP/SECNCI COMBINATION
3439	22	0.13%	88.90%	LNUM=00001 TN= LNA MUST BE D ON ACT OF D WHEN REQTYP IS A WITH SECNCI POPULATED
3445	3	0.02%	88.92%	LOCNUM=000 LNUM=00001 TELNO= LNECLSSVC MUST BE 3 OR 5 ALPHANUMERICS
3460	3	0.02%	88.94%	LOCNUM=000 LNUM= TELNO= LNUM REQUIRED WITH THIS REQTYP/LNA TYPE COMBINATION (STOP EDIT)
3470	17	0.10%	89.04%	LOCNUM=000 LNUM=00001 TELNO=LNUM MUST BE UNIQUE WITHIN EACH LOCNUM EXCEPT FOR REQTYP E-IS
3485	9	0.06%	89.10%	LOCNUM=001 LNUM=00001 LOCNUM DOES NOT MATCH AN END USER LOCNUM FOR THIS LSR
3545	1	0.01%	89.10%	LNUM=00001 TELNO= OTN REQUIRED WITH THIS REQTYP/LNA COMBINATION
3630	3	0.02%	89.12%	LNUM=00001 TELNO= SHELF REQUIRED ON REQTYP F IF LNA IS C, G, N OR V
3705	14	0.09%	89.21%	LNUM=00001 TNS MUST BE A MINIMUM OF 10 OR A MAXIMUM OF 15 ALPHANUMBERIC INCLUDING HYPHEN
3735	23	0.14%	89.35%	LNUM=00001 TELNO= PIC REQUIRED ON LNA G, N, P OR V
3740	10	0.06%	89.41%	LNUM=00001 TELNO= PIC VALID ENTRY IS NONE UNDO NO OR VALID PIC WHEN LNA IS C, P OR X
3745	14	0.09%	89.50%	LNUM=00001 TELNO= PIC VALID ENTRIES ARE NONE, UNDC OR A VALID PIC CODE WHEN LNA IS G, N OR
3755	15	0.09%	89.59%	LNUM=00001 TELNO= LPIC REQUIRED ON LNA G, N, P OR V
3760	11	0.07%	89.66%	LNUM=00001 TELNO= LPIC VALID ENTRIES ARE NONE, UNDC, NC OR VALID LPIC CODE WHEN LNA IS C P
3765	18	0.11%	89.77%	LNUM=00001 TELNO= LPIC VALID ENTRIES ARE NONE, UNDC OR A VALID LPIC CODE WHEN LNA IS G, N
3790	42	0.26%	90.02%	LNUM=00001 - TELNO= PTKCON REQUIRED WHEN THE LNA IS G, N OR V
4000	32	0.20%	90.22%	DL DATA ELEMENTS REQUIRED
4015	2	0.01%	90.23%	REFNUM=0001-TELNO= LIST MUST BE VALID ENTRY
4020	7	0.04%	90.27%	DLNUM=0001 LTN= DLNUM MUST BE UNIQUE
4030	4	0.02%	90.30%	DLNUM=0001 LTN= LACT REQUIRED
4035	11	0.07%	90.37%	DLNUM=0001 LTN=ALI CODE PROHIBITED WHEN THE RTY 2ND AND 3RD CHARACTERS ARE ML
4040	1	0.01%	90.37%	REFNUM=0001-TELNO= LISTED ADDRESS REQUIRED WITH THIS REQTYP AND ACTIVITY TYPE
4045	290	1.78%	92.15%	REFNUM=0001-TELNO=0 LISTED ADDRESS PROHIBITED WITH THIS RECTYP AND ACTIVITY TYPE
4050	58	0.36%	92.51%	INVALID YPH ENTRY
4055	37	0.23%	92.73%	YPH REQUIRED WHEN FIRST CHARACTER OF TOS IS 1 OR 3

AGGREGATE	ORDER TYP	ES		
ERROR DETA	AILS (Fatal Er	rors)		
Error Type (by error code)	Count	%	Σ%	Error Description
4060	1	0.01%	92.74%	DLNUM=0001 LTN= VALID RTY REQUIRED
4061	57	0.35%	93.09%	DLNUM=0001 LTN= LASN,ADI,OR LALOC REQUIRED FOR REQTYP J, RTY OF LML, AND LACT OF N
4065	287	1.76%	94.85%	DLNUM=&DLNM LTN=&LTN ASSOCIATED LACT COMBINATION I AND O IS MISSING
4075	27	0.17%	95.01%	MAIN LISTING REQUIRED
4090	9	0.06%	95.07%	DLNUM=0001 LTN= VALID LTY REQUIRED
4095	5	0.03%	95.10%	REFNUM=0001-TELNO= DDA-CITY PROHIBITED FOR THIS REQTYP AND ACTIVITY TYPE
4110	12	0.07%	95.17%	DLNUM=0001 LTN=4 VALID STYC CI, SH, SI, OR SL REQUIRED
4120	4	0.02%	95.20%	DLNUM=0001 LTN= TOA B, R, RP OR BP REQUIRED
4160	16	0.10%	95.29%	DLNUM=0001 LTN= DOI REQUIRED VALUE MUST BE 0 - 6
4170	1	0.01%	95.30%	DLNUM=0003 LTN= DOI MUST BE 1
4180	16	0.10%	95.40%	DLNUM=0001 LTN= DOI VALUE MUST BE ZERO
4190	14	0.09%	95.48%	DLNUM=0002 LTN= DOI VALUE INVALID FOR STYLE CODE
4200	2	0.01%	95.50%	DLNUM=0001 LTN MUST BE 10 NUMERICS
4220	1	0.01%	95.50%	DLNUM=0001 LTN= LNLN REQUIRED
4265	4	0.02%	95.53%	DLNUM=0001 LTN=4075632496 TITLE OF LINEAGE INVALID
4280	23	0.14%	95.67%	DLNUM=0001 LTN= TITLE1 DATA INVALID
4290	1	0.01%	95.67%	DLNUM=0002 LTN= TITLE2 DATA INVALID
4310	2	0.01%	95.69%	DLNUM=0001 LTN= LANO PROHIBITED WITHOUT LASN
4320	3	0.02%	95.70%	DLNUM=0001 LTN=9043740664 LASF PROHIBITED WITHOUT LANO
4365	7	0.04%	95.75%	DLNUM=0001 LTN= LASS ENTRY INVALID .
4385	58	0.36%	96.10%	DLNUM=0001 LTN= INVALID LAST ENTRY
4395	4	0.02%	96.13%	DLNUM=0001 LTN= LAST PROHIBITED WITH ADI
4405	1	0.01%	96.13%	DLNUM=0002 LTN=8502361678 LTEXT REQUIRED
4430	1	0.01%	96.14%	DLNUM=0003 LTN= INVALID LTXTY DATA
4455	1	0.01%	96.15%	DLNUM=0003 LTN= LTXNUM VALUE MUST BE 4 NUMERICS
4470	5	0.03%	96.18%	DLNUM=0001 LTN= LTXNUM MUST BE CONSECUTIVE AND UNIQUE WITHIN THE DLNUM
4475	12	0.07%	96,25%	DLNUM=0002 LTN= INVALID YPH ENTRY
4478	24	0.15%	96.40%	DLNUM=0001 LTN= YPH ENTRY MUST BE 999001 WHEN LTY IS 2 OR 3
4480	1	0.01%	96.40%	DLNUM=0001 LTN= YPH PROHIBITED WITH LACT Z
4485	16	0.10%	96.50%	DLNUM=0001 LTN= YPH REQUIRED WHEN THE TOS IS 1 OR 3 AND RTY IS ML, AM OR CM
4490	33	0.20%	96.70%	DLNUM=0001 LTN= YPH PROHIBITED WITH THIS RTY

AGGREGATE	ORDER TYP	PES		
ERROR DETA	AILS (Fatal E	rrors)		
Error Type (by error code)	Count	%	Σ%	Error Description
4505	43	0.26%	96.97%	)LNUM=0001 LTN= SIC REQUIRED WHEN ACT IS N, V, OR P
4510	41	0.25%	97.22%	DLNUM=0001 LTN=ONLY ONE SIC ALLOWED PER ACCOUNT
4530	4	0.02%	97.24%	)LNUM=0003 LTN= ADI PROHIBITED WHEN LASN OR LALOC IS POPULATED
4600	27	0.17%	97.41%	)LNUM=0001 LTN= AMPERSAND REQUIRED WITH DLNM
4630	1	0.01%	97.41%	)LNUM=0001 LTN= SEQTEXT PROHIBITED WITHOUT THE SO FIELD OF A
4650	2	0.01%	97.43%	JLNUM=0002 LTN= SEQTN PROHIBITED WHEN THE SEQTEXT OR SEQADDR US NOT POPULATED
4670	2	0.01%	97.44%	DLNUM=0003 LTN= LVL REQUIRED WHEN STYC IS CI (STOP EDIT)
4685	5	0.03%	97.47%	JLNUM=0002 LVL ENTRIES MUST BE SEQUENTIAL AND THE THE SAME LVL VALUE CANNOT APPEAR MORE THAN TWICE
4690	1	0.01%	97.48%	XLNUM=0002 LTN= HS PROHIBITED WHEN THE STYC IS NOT CI, SH OR SI
4740	3	0.02%	97.49%	)LNUM=0001 LTN= INS1 REQUIRED WHEN INTEXT OR INADDR IS POPULATED
4810	2	0.01%	97.51%	)LNUM=0001 LTN= INS1 REQUIRED WHEN INTEXT IS POPULATED
4825	2	0.01%	97.52%	JLNUM=0001 LTN= INS1 REQUIRED WHEN INADDR IS POPULATED
4835	2	0.01%	97.53%	DACT ENTRY MUST BE N
4837	8	0.05%	97.58%	)ACT REQUIRED
4870	1	0.01%	97.59%	)DASN IS REQUIRED
4895	1	0.01%	97.59%	)DALOC REQUIRED
4900	1	0.01%	97.60%	DAST REQUIRED
4905	11	0.01%	97.60%	)DAZC REQUIRED
4920	4	0.02%	97.63%	JIRQTY A PROHIBITED WITHOUT DIRTYP
4925	4	0.02%	97.65%	NRQTY NC PROHIBITED WITHOUT DIRTYP
5000	2	0.01%	97.67%	HUNTING PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
5005	7	0.04%	97.71%	OCNUM=000 THE FOLLOWING FIELDS ARE REQUIRED; HNUM, HA, AND HID
5015	. 7	0.04%	97.75%	ITQTY MUST EQUAL TOTAL NUMBER OF HNUM ON THIS REQUEST
5025	2	0.01%	97.76%	OCNUM=000 HNUM= HA=G HA MUST BE N, E, C, OR D
5030	1	0.01%	97.77%	OCNUM=000 HNUM=00001 HA OF E PROHIBITED ON ACT TYPE N, T, P OR Q
5050	2	0.01%	97.78%	OCNUM=001 DOES NOT MATCH AN END USER LOCNUM ON THIS LSR
5065	1	0.01%	97.79%	OCNUM=000 HNUM=00001 HID ENTRY FOR HNTYP 1 2 3 OR 4 MUST BE N OR UP TO 3 ALPHAS OR 4 NUMERICS
5070	7	0.04%	97.83%	OCNUM=000 HNUM=00001 HID MUST BE N WHEN HA IS N AND HNTYP IS 1, 2, 3 OR 4
5090	11	0.01%	97.84%	OCNUM=000 HNUM=00001 TLI REQUIRED IF HNTYPE IS 5 OR 6
5095	2	0.01%	97.85%	OCNUM=000 HNUM=00001 TLI PROHIBITED WHEN HNTYP IS 1, 2, 3 OR 4 AND NOTYP IS T
5098	2	0.01%	97.86%	LOCNUM=000 HNUM=00001 HNTYP REQUIRED FOR THIS ACT TYPE/HA COMBINATION

AGGREGATE	ORDER TYP	E\$		
ERROR DETA	JLS (Fatal Er	rors)		
Error Type (by error code)	Count	%	Σ%	Error Description
5105	1	0.01%	97.87%	LOCNUM=000 HNUM=00001 HLA=C HLA VALID ENTRIES ARE N, E OR D
5110	1	0.01%	97.87%	LOCNUM=001 HNUM=00001 HLA=N HLA OF N PROHIBITED WHEN HUNT GROUP ACTIVITY IS E
5115	8	0.05%	97.92%	LOCNUM=000 HNUM=00001 HLA=E HLA OF E PROHIBITED WHEN HUNT GROUP ACTIVITY IS N
5135	23	0.14%	98.06%	LOCNUM=000 HNUM=00001 HTSEQ=0005 SAME HT NOT ALLOWED IN MORE THAN ONE HTSEQ WHEN HLA IS N OR E
5175	3	0.02%	98.08%	HNUM=00001 HT=T0001-T0002 HT MUST BE 10 NUMERICS OR 14 NUMERICS WITH A HYPHEN IF HNTYP 1-4
5185	17	0.10%	98.19%	LOCNUM=000 HNUM=00001 HT= FOR HNTYP 5 OR 6, HT MUST BE 5 OR 10 ALPHANUMERIC
6005	1	0.01%	98.19%	NC CODE INVALID
6045	30	0.18%	98.38%	INVALID NC/NCI/SECNCI COMBINATION (STOP EDIT)
6050	5	0.03%	98.41%	REQTYP/LOOP TYPE COMBINATION INVALID
6055	11	0.07%	98.47%	LQTY IS REQUIRED FOR REQTYP/ACT COMBINATION
7000	4	0.02%	98.50%	EAN OR EATN OR LEATN ON LINES OR LEAN ON LINES IS REQUIRED WHEN ACT IS P, Q OR V
7005	2	0.01%	98.51%	EAN, EATN, LEATN, AND LEAN ARE MUTUALLY EXCLUSIVE
8005	12	0.07%	98.58%	DNUM=00001 TC OPT PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
8040	8	0.05%	98.63%	LOCNUM= DISCNBR=&DISCNM DNUM=&DNUM TC TO PRIMARY CANNOT BE THE SAME AS THE NUMBER BEING REFFER
8110	4	0.02%	98.66%	LOCNUM= DNUM=00001 TC PER DATE IS INVALID, MUST BE LATER THAN THE LSR RECEIPT DATE
8115	13	0.08%	98.74%	LNUM=00001 TC OPT PROHIBITED WITH THIS REQTYP/ACT TYPE COMBINATION
8120	4	0.02%	98.76%	LNUM=00002 TC OPT VALID ENTRY IS ST, NO, CA OR TC
8140	78	0.48%	99.24%	LNUM=00001 TC OPT PROHIBITED IF TC FR IS NOT POPULATED ON REQTYP E, F OR M FOR LNA C, G, N OR V
8155	2	0.01%	99.25%	LNUM=00001 TC OPT PROHIBITED IF LNUM DISC NBR IS NOT POPULATED ON REQTYP A
8180	22	0.13%	99.39%	LNUM=00001 TC TO PRIMARY NUMBER MUST BE DIFFERENT FROM NUMBER BEING REFERRED
8210	3	0.02%	99.41%	LNUM=00002 TC PER PROHIBITED WHEN LNUM TC OPT IS NOT ST OR TC
8215	16	0.10%	99.50%	LNUM=00001 TC PER DATE INVALID. IT MUST BE LATER THAN THE LSR RECEIPT DATE
8255	78	0.48%	99.98%	INVALID ACTIVITY TYPE
9870	3	0.02%	100.00%	ATN OR EATN REQUIRED WITH THIS REQTYP/ACT TYPE COMBINATION
Total:	16318	100.00%		

AGGREGA	ATE ORDER TYPES	
<b>ERROR DI</b>	ETAILS - 8825	
Error Type (by error code)	Error Description	
8825	ORDER ERR: SA LIST 023 LIN STREET NAME FOR SA NOT VALID FOR NPA NXX!	
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA	
8825	ORDER ERR: CS IDNT 011 LIN USOC FOLLOWING CS IS INCORRECT! OCS 1FR	
8825	ORDER ERR: LN LIST 010 LIN RECAPPED LN, NLST OR NP MAY NOT APPEAR! ILN (LNR) CROS	
8825	ORDER ERR: DSA IDNT 010 LI DSA PRESENT - NEED CATEGORY L USOC OR SMV USOC!	
8825	ORDER ERR: TN SAE 038 LINE TN OR TLI IS REQUIRED FOR INWARD CATEGORY D USOCS!	i
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST_CHARACTER! I1_UEAC2_/C	
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! 11 UEAC2 /C	
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! 11 UEAC2 /C	
8825	ORDER ERR: ZLLU SAE 009 LI ZLLU MUST APPEAR!	
8825	ORDER ERR: TYA BILL 008 LI TYA REQUIRED WITH SIC CODE OF 98XX	
8825	ORDER ERR: LCON SAE 007 LI LCON FORMAT INCORRECT! IG2 CKL	
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! 11 1FR /TN	
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA	
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! I1 DRS /TN	
8825	ORDER ERR: DSA IDNT 009 LI DSA MUST APPEAR IN IDNT!	
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11 DRS /TN	
8825	ORDER ERR: ZLLU SAE 009 Li ZLLU MUST APPEAR!	
8825	ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1 1FB /TN	
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! I1 14R /TN	
8825	ORDER ERR: CFND SAE 016 LI SEE SOER DOCUMENTATION! T1	
8825	ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1 1FB	
8825	ORDER ERR: PIC SAE 012 LIN PIC MUST APPEAR ON I AND T ACTION CODED CATEGORY D USOC!	
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!	
8825	ORDER ERR: FORMAT SAE 389 I1 DRS /TN	ł
8825	ORDER ERR: ZLLU SAE 009 LI ZLLU MUST APPEAR!	
8825	ORDER ERR: NLST LIST 013 L SEE SOER DOCUMENTATION! INLST(NON-LIST) INTERPRINT EQUI	
8825	ORDER ERR: LN LIST 010 LIN SEE SOER DOCUMENTATION! ILN	I
8825	ORDER ERR: RCU SAE 009 LIN RCU CODESET INVALID! I1 14R /	
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!	]
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!	]
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!	

AGGREGA	TE ORDER TYPES
ERROR DE	TAILS - 8825
Error Type (by error code)	Error Description
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: SS BILL 007 LIN SS DATA FORMAT INCORRECT! ISS
8825	ORDER ERR: SIC LIST 012 LI SIC CODE NOT ON BRIS SIC TABLE! ISIC 3047
8825	ORDER ERR: RESH BILL 023 L USOC BSX++ MAY NOT APPEAR!
8825	ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB)
8825	ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB)
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! I1
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA
8825	ORDER ERR: FORMAT 374 LINE EUCLC: 0001 RELAY: 0000=
8825	ORDER ERR: ADL SAE 010 LIN ADL MUST APPEAR! 11
8825	ORDER ERR: LOC LIST 019 LI INVALID LAST CHARACTER FOR LEVELS 1-3! ILOC LOT 4 DES (
8825	ORDER ERR: SA LIST 023 LIN STREET NAME FOR SA NOT VALID FOR NPA NXX!
8825	ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB)
8825	ORDER ERR: NP LIST 010 LIN SEE SOER DOCUMENTATION! INP (NON-PUB)
8825	ORDER ERR: PR SAE 010 LINE ZERO MUST NOT APPEAR AS FIRST CHARACTER! I1 UEAC2 /C
8825	ORDER ERR: LCON SAE 007 LI LCON FORMAT INCORRECT! CKL
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: ROUT LIST 007 L ROUT INVALID ON THIS ORDER!
8825	ORDER ERR: TYA BILL 008 LI TYA REQUIRED WITH SIC CODE OF 98XX
	ORDER ERR: PKG SAE 010 LIN PKG NOT VALID ON THIS USOC! T1
8825	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! I1
8825	ORDER ERR: TCP TFC 007 LIN INVALID TCP DATE! TCP 06-13-00
8825	ORDER ERR: PDN IDNT 008 LI PDN MISSING OR DATA INCORRECT!
8825	ORDER ERR: DSA IDNT 009 LI DSA MUST APPEAR IN IDNT!
	ORDER ERR: RNP SAE 006 LIN SEE SOER DOCUMENTATION! 11
8825	ORDER ERR: ADL SAE 010 LIN ADL MUST APPEAR! 11 1FR /TN
8825	ORDER ERR: PCA SAE 013 LIN SEE SOER DOCUMENTATION! T1
8825	ORDER ERR: LA LIST 013 LIN SEE SOER DOCUMENTATION! ILA

IGGREGATI	ORDER TYPES
ERROR DET	AILS - 1000
Error Type (by error code)	Error Description
1000	CLEARED ERR BY ISSUING ORDER MANUALLY
1000	CLEARED SYSTEM ERRORS OSCOL AND UEAMC
1000	CLEARED UP SYSTEM ERRORS
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER#
1000	CORRECTED SYSTEM GENERATED ERRORS FOR ORDER#
1000	CLEANED UP SYSTEM ERRORS
1000	CANCEL PER CLEC.
1000	PUT IN E STATUS TO DROP OFF-ORD CANCELLED BY CLEC
1000	CLEARED ALL SYSTEM ERRORS IN DUE DATE CHANGE BY SYSTEM TO 070700
1000	ORDERDD 06-27-00 WORKED TO CHG LISTING
1000	PLACED IN E-STAT SUP 1 ON VER 1 THANKS
1000	ERR PLACED IN E-STAT SUP 1
1000	ERR CLEARED-ORDER ISS TO PROVIDE 1 LOOP
1000	CORRECT SYSTEM ERRORS
1000	CAN PER CLEC
1000	ERROR TO DROP, PON CANCELLED PER SUP 01
1000	EU NAME IS INCOMPLETE, PLS VERIFY AND RESUBMIT;
1000	CLEAN UP SYSTEM ERROR AND ADD SHELVES TO LOC FLR INFO
1000	CORRECTED SYSTEM ERRORS FOR ORDER#
1000	CORRECTED ERRORS ON ORDER BY REMOVING OCOSL & UEAMC WHICH SHOULD NOT BE ON LY REQUEST
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER, ORDER #
1000	ERROR TO DROP, UNABLE TO FORCE FOC ON C51RKDT0 CPX 06-08-00.
1000	ACCOUNT, SERVICE ORDER, DD 06-30-00
1000	ERROR TO DROP, UNABLE TO FORCE FOC ON
1000	CANCELLED ORDER PER SUP 1 LESOG
1000	CORRECT MAN CODE ON ROUTING ERROR MADE BY SYSTEM
1000	RECVD SUP 1 TO CANCEL
1000	CORRECT SYSTEM ERROS
1000	ERR PLACED IN E-STAT SUP 1 ON VER 1
1000	UPDATE TO CHANGE DUE DATE TO 6-27
1000	ERR PLACED IN E-STAT ORDER COMPLETED
1000	CLEARED ERR FOR ORDER # , PON#,

#### **ORDERING**

AGGREGATE	ORDER TYPE\$
ERROR DET	AILS - 1000
Error Type (by error code)	Error Description
1000	CORRECT SYSTEM ERRORS
1000	CORRECT SYSTEM ERRORS
1000	CLEARED ERROR FOR SYSTEM GENERATED ORDER #
1000	CLEARED ERROR
1000	CORRECT SVC ORDER BY REMOVING OCOSL & UEAMC-WHCH SHOULD NOT BE ON LY RQST
1000	CORRECT ERRORS
1000	CORRECTED SYSTEM GENERATED ORDERS, ORDER#
1000	CORRECTED SYSTEM GENERATED ORDER #
1000	SENT S STATUS REFERAL FORM 06-20-00.
1000	ISS ORD C509GNJ6 DD 0703 ERR STAT 2 COR FOC-
1000	DD 2000-07-05
1000	ORDER CANCELLED
1000	CLAIMED IN ERROR
1000	ORDER PLACED IN ERROR BUCKET. RECORD ORD CPX B4 FOC WAS SENT.
1000	DD 06-14-00
1000	DD 07-06-00
1000	ORDER NY32B0F8 DOES NOT HAVE PON ON IT
1000	DD 2000-07-05
1000	CORRECT SYSTEM ERRORS
1000	CLEAR UP SYSTEM ERRORS
1000	ERR TO DROP OFF, ORD
1000	ERR CLEARED-ORDER ISS TO PROVIDE 1 LOOP
1000	CORRECT SYSTEM ERRORS
1000	CORRECT SYSTEM PROBLEMS
	CLEARED UP SYSTEM ERRORS
	CLEARED ERRORS FROM ORDER TO FLOW THRU
	CLEAR SYSTEM ERRORS OCOSL AND DFDT
	CORRECT ON ODR NUMBER
1000	ORDER BY PLACING DFDT INFO IN PROPER PLACE AND REMOVING OCOSL (NOT VALID ON LYORDER)

# REPORT: PERCENT LNP FLOW THROUGH SERVICE REQUESTS (SUMMARY) REPORT PERIOD: 11/01/01 - 11/30/01

Exhibit November PM Data Attachment 2F

	PERCENT ACHIEVED FLOW- THROUGH	PERCENT FLOW THROUGH
CLEC AGGREGATE		
REGION ALL SERVICES	54.86%	91.24%

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Company Info	_	LSR PROCESSING										OWTHROU	GH L	
	Mechan	Mechanized Interface Us			Rejects	Validated	Errors							
Name	EDI	TAG	Total Mech LSR's	Total Manuai Fallout	Auto Clarification	LSR's	Total <b>System</b> Fallout	BST Caused Fallout	CLEC Caused Fallout	<b>issue</b> d SO's	Percent Achieved Flowthrough	Base Calculation	Pecent Fi Throug	
#1	194	0	194	14	19	161	18	15	3	143	83.14%	88.82%	90 51	
#2	160	0	160	79	22	59	17	7	10	42	32.81%	71 19%	85.71	
#3	488	0	488	254	59	175	85	52	33	90	22.73%	51.43%	63 3	
#4	0	9	9	4	2	3	2	2	0	1	14.29%	33.33%	33.3	
#5	1	0	1	0	0	1	1	0	1	0	0.00%	0.00%	0.00	
#6	0	283	283	32	24	227	46	41	5	181	71.26%	79.74%	81.5	
#7	712	0	712	318	128	266	74	27	47	192	35.75%	72.18%	87 6	
#8	0	391	391	143	67	181	64	34	30	117	39.80%	64 64%	77.4	
#9	3,031	0	3,031	495	332	2,204	284	66	218	1920	77.39%	87.11%	96 6	
#10	122	0	122	105	12	5	4	1	3	1	0.93%	20.00%	50.0	
#11	194	0	194	94	37	63	24	16	8	39	26.17%	61.90%	70.9	
#12	0	30	30	17	6	7	4	1	3	3	14.29%	42.86%	75.0	
#13	45	0	45	16	16	13	6	2	4	7	28.00%	53.85%	77.7	
#14	3,600	0	3,600	658	214	2,728	197	44	153	2531	78.29%	92.78%	98.2	
#15	0	44	44	26	5	13	2	0	2	11	29.73%	84.62%	100.0	
#16	1,951	0	1,951	461	153	1,337	211	148	63	1126	64.90%	84.22%	88.3	
#17	104	0	104	78	20	6	1	0	1	5	6.02%	83.33%	100 (	
#18	0	976	976	343	123	510	127	74	53	383	47.88%	75 10%	83.8	
#19	227	0	227	99	33	95	12	7	5	83	43.92%	87 37%	92.2	
#20	0	1,919	1,919	1,794	125	0	0	0	0	0 _	0.00%	0.00%	0.00	
#21	0	80	80	36	10	34	8	3	5	26	40.00%	76 47%	89 6	
#22	3,557	0	3,557	790	342	2,425	698	180	518	1727	64 03%	71.22%	90 5	
#23	0	1,893	1,893	784	263	846	259	127	132	587	39.19%	69.39%	82.2	
#24	41	0	41	6	1	34	13	5	8	21	65.63%	61.76%	80.7	
#25	0	39	39	14	4	21	7	2	5	14	46.67%	66.67%	87.5	
#26	0	4	4	2	0	2	2	0	2	0	0.00%	0.00%	0.00	
#27	614	0	614	207	83	324	35	19	16	289	56.12%	89.20%	93.83	
#28	256	0	256	112	16	128	53	41	12	75	32.89%	58.59%	64 66	
#29	0	1	1	1	0	0	0	0	0	0	0.00%	0.00%	0 00	

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**ORDERING** 

#### REPORT: PERCENT LNP FLOW THROUGH SERVICE REQUESTS (AGGREGATE DETAIL) Exhibit November PM Data REPORT PERIOD: 11/01/01 - 11/30/01

#30	0	2	2	1	1	0	0	0	0	0	0.00%	0.00%	0 00%
#31	0	66	66	20	10	36	15	11	4	21	40.38%	58.33%	65 63%
EDI Subtotal	15297	0	15297	3786	1487	10024	1733	630	1103	8291	65.25%	82.71%	92 94%
TAG Subtotai		5737	5737	3217	640	1880	536	295	241	1344	27.68%	71.49%	82 00%
TOTAL INTERFACES	15297	5737	21034	7003	2127	11904	2269	925	1344	9635	54.86%	80.94%	91.24%

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Attachment 2F

# REPORT: PERCENT LNP FLOWTHROUGH SERVICE REQUESTS (FATAL REJECTS BY CLEC) REPORT PERIOD: 11/01/01 - 11/30/01

Company Info	<u> </u>
Company into	<del>                                     </del>
	· - · - · · ·
Name	FATAL REJECTS
#1	7
#2	20
#3	205
#4	2
#5	12
#6	192
#7	51
#8	78
#9	21
#10	21
#11	6
#12	27
#13	71
#14	4
#15	101
#16	23
<b></b> #17	154
#18	33
#19	297
#20	10
#21	593
#22	62
#23	5
#24	4
#25	0
#26	0
#27	74
#28	29

# REPORT: PERCENT LNP FLOWTHROUGH SERVICE REQUESTS (FATAL REJECTS BY CLEC) REPORT PERIOD: 11/01/01 - 11/30/01

Exhibit November PM Data Attachment 2F

AGGREGATE ORDER TYPES	
Company Info	
	FATAL
Name	FATAL REJECTS
#29	0
#30	2
#31	4
Total	2108

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										Trur	nk Gro	un Per	formar	ice - Ag	areas	ite										
Florida		T	Average blo	ockino n	ercentage	by hour					0.0	<u> </u>		.00 / (	, y y-		·			1		i				
	ŀ		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	2.1
																							20	***		
Dec-00	NF	BellSouth	0.0000	0.0002	0 0002	0.0000	0.0000	0.0001	0 0167	0.0186	0.0108	0 0071	0 0053	0.0143	0 0133		0.0124	0.0094	0 0130	0 0096	0 0019	0 0605	0 0592	0 0074	0 0000	0.0000
		CLEC Difference	-0 0009	0.0002	0 0014 -0 0013	-0.0010	0 0192 -0 0192	0 0060	0.0005	0.0263	0.0750	0 0487	0 0487	0.0470	0 0049	0.0034	0 0086	0 0141	0 0170	0 0102	0 0179	0 0997	0 1077	0 0870	0 0274	0 0085
	SF	BellSouth	0 0025	0.0026	0.0004	0.0006	0.0000	-0 0058 0 0010	0.0281	-0.0076 0 1194	-0 0643 0 0723	-0.0417 0 1712	-0.0434 0.1686	-0.0327 0 1635	0.0084	0.0123 0.0818	0.0038 0.0875	-0 0047 0 0712	-0 0040 0.0682	-0 0006 0 0945	-0 0160 0 0688	-0 0393 0 0629	-0 0485 0 0792	-0 0796 0 0423	-0 0274 0 0019	-0 0085 0 0058
	1	CLEC	0 0004	0.0000	0.0004	0.0006	0.0052	0 0247	0.1208	0 2535	0 1887	0 1567	0.0928	0.0999	0 1097	0.0860	0.0841	0 0893	0.0002	0.2646	0.1315		0 0792	0 1025	0.0019	0 0058
		Difference	0 0021	0.0025	-0.0004	0.0000	-0.0052	-0 0238	-0 0927	-0.1341	-0 1164	0 0146	0 0758	0 0636	0.0065	-0.0042	0 0034	-0 0180	-0 0602	-0 1701	-0 0627		-0 0085	-0 0602	-0 0137	0.0000
		T= 110 11		2																						
Jan-01	NF	BellSouth	0 0000	0.0000	0.0005	0.0000	0 0000	0 0000	0 0027	0 0056	0 0012	0.0007	0 0039	0 0037	0 0037	0 0013	0 0012	0 0104	0 0379	0 0110	0 0061	0 1843	0 3420	0 0163	0 0000	0 0000
	+	CLEC Difference	-0 0027	-0 0001	0.0004	0.0001 -0.0001	0.0000	0.0009 -0 0009	0.0024	0.006	0 0025	0.0178 -0.0171	0 0153 -0 0114	0 0084 -0 0048	0 0042 -0.0005	0.0066 -0 0053	0 0132 -0 0120	0 0315 -0.0211	0 0687 -0 0308	-0.0137	0 0566 -0 0505	0 4227 -0 2383	0 6889 -0 3469	0 2345	0 0272	0.0015
<b></b>	SF	BellSouth	0.0030	0 0000	0.0001	0.0000	0.0000	0.0001	0.0024	0.0030	0.0380	0.0056	0 0193	0.0146	0.0085	0 0253	0 0241	0.0211	0 0727	0.0137	0 0102	0 0301	0 0499	-0 2182 0 0064	-0 0272 0 0039	-0 0015 0 0013
		CLEC	0 0286	0.0010	0.0045	0.0000	0 0261	0 0954	0.0272	0 1394	0 0829	0 0397	0.1624	0 2275	0.0997	0 0409	0 0643	0 1242	0.2107	0 3766	0 1524	0 2638	0 4444	0 3759	0 0033	0 0259
		Difference	-0 0255	-0 0010	-0.0045	0.0000	-0.0261	-0.0954	-0.0219	-0 1266	-0.0449	-0 0341	-0 1431	-0 2130	-0 0913	-0 0156	-0 0402	-0 0554	-0 1380	-0 3378	-0 1422		-0 3945	-0 3695	-0 0202	-0 0246
F-L 04	145	D-IICII	0.00001	0.0004	0.0000	0.0000	0.0000	0.0000	0.0000	0.00461	0.0000	0.0000	0.0000	0.0465	2.0055	0.000	0.040									
Feb-01	NF	BellSouth CLEC	0.0000	0.0001	0.0000	0.0000	0.0000	0.0000	0.0000	0.0040	0.0003	0.0002 0.0946	0.0062	0.0130 0.1021	0.0528	0.0206	0 0154	0 0095	0.0131	0.0078	0 0089	0 0714	0 2478	0 0310	0 0000	0.0010
	+	Difference	-0.0003	-0.0001	-0.0002	-0 0002	-0.0779	-0.0292	0.00001	0.00038	-0.0256	-0.0945	-0 1209	-0.10211	-0.0328	-0.0167	-0 0681	-0.0888	-0 0733	-0 0141	-0 0576	-0 4285	-0 7213	-0 4546	0 0288 -0 0288	0 0018 -0 0008
	SF	BellSouth	0 0001	0 0000	0.0000	0.0000	0.0000	0.0006	0.0089	0 0102	0.0161	0 0214	0.0167	0 0230	0 0131	0.0273	0 0216	0.0000	0 0575	0 0595	0 0034	0 0342	0 0330	0 0250	0 0002	0 0000
		CLEC	0 0006	0.0062	0.0169	0.0032	0 0217	0.0007	0.0195	0 1158	0.1720	0 0820	0 3548	0.4414	0 0604	0 0497	0.1393	0 3564	0 3487	0.4954	0 1330	0 1577	0 3080	0 3467	0 0211	0 0017
		Difference	-0 0005	-0.0062	-0.0169	-0 0032	-0 0217	-0 0001	-0.0106	-0.1055	-0.1559	-0.0606	-0.3381	-0 4184	-0 0473	-0 0224	-0.1177	-0 3186	-0 2911	-0 4359	-0 1296	-0.1235	-0 2750	-0 3217	-0 0210	-0 0008
Mar-01	FL	BellSouth	0 0001	0.0000	0.0004	0.0000	0.0000	0 0001	0.0027	0.0582	0.0131	0.0193	0 0211	0 0294	0.0060	0.0097	0 0122	0 0227	0 0332	0 0260	0 0143	0 0461	0 0735	0.0000	0.0004	0.0047
mai vi	+	CLEC	0 4914	0.0066	0.0053	0.0072	0.0008	0 0070	0.0027	0.1675	0.0418	0.0329	0 0980	0 1293	0.0504	0.0097	0 0502	0 1276	0 2120	0.2847	0.1275	0 1480	0 2645	0 0068	0 0001	0 0047
		Difference	-0.4913	-0.0066	-0.0049	-0.0072	-0.0008	-0.0069	-0.0144	-0.1093	-0 0287	-0.0137	-0.0769	-0 0999	-0.0444	-0 0195	-0 0380	-0 1049	-0.1788	-0.2587	-0.1131		-0 1910	-0 1015	-0 0054	-0 0209
Apr-01	FL	BellSouth	0.0008	0 0001	0.0000	0.0053	0.0000	0.0003	0.0011	0.0082	0.0234	0.0025	0 0326	0.0352	0 0134	0.0286	0 0297	0 0487	0.0449	0.0114	0 0008	0 0034	0 0104	0 0100	0 0002	0 0004
	-	CLEC Difference	-0.0003	0.0028	0.0007 -0.0007	0.0293 -0 0240	0.0002 -0.0002	0.0011 -0.0007	0.0150 -0.0139	0.0501	0.0764 -0.0529	0.0290 -0.0265	0.0283	0.0420 -0.0068	0 0298 -0.0163	0.0284	0.0494 -0 0197	0.0977 -0 0490	0.2310 -0 1861	0.3232	0 0929 -0 0921	-0 0422 -0 0388	0 0870 -0 0767	0 1428 -0 1329	0 0381 -0 0379	0 0047 -0 0043
		Dimorchioc	1 0.0000	0.0021	-0.0001	0 02401	0.0002	0.0007	0.0100	0.0415	-0.0023	-0.0203	0 00+0	-0.0000	-0.0103	0.0002	-00137	-0 0430	-0 1001]	-0.3110	-0 0321	-0 0366	-0 0767	-0 1329	-0 03/9	-0 0043
May-01	FL	BellSouth	0.0001	0.0000	0.0094	0.0000	0.0000	0.0040	0.0029	0.1190	0 0675	0 0055	0.0151	0.0720	0 0076	0 1039	0 0984	0 0566	0 0560	0.0174	0 0047	0 0039	0 0060	0.0023	0 0003	0 0002
		CLEC	0 0031	0.0428	0.0027	0 0109	0.0218	0 0075	0 0183	0 1856	0.1221	0 0255	0.0315	0.0603	0.0154	0 0335	0 0518	0.1592	0 2027	0.3416	0 0852	0 0391	0 0845	0 1109	0 0386	0 0024
		Difference	-0 0030	-0.0428	0.0068	-0.0109	-0 0218	-0 0035	-0.0153	-0 0666	-0.0546	-0 0200	-0.0163	0.0116	-0 0078	0 0705	0 0466	-0 1026	-0 1467	-0 3241	-0 0805	-0 0352	-0 0785	-0 1086	-0 0383	-0 0021
Jun-01	FL	BellSouth	0.0002	0.0000	0.0000	0.0000	0.0001	0.0004	0.0021	0.05061	0.0686	0.0047	0.0128	0.0172	0 0109	0 0104	0 0071	0 0033	0 0057	0 0117	0 0016	0 0025	0 0132	0 0334	0 0145	0 0005
	+	CLEC	0.1139	0.0374	0.0890	0.0669	0.0777	0.0678	0.0278	0.0296	0 0405	0.0946	0 0848	0 0846	0 0413	0 0292	0.0667	0.0916	0.0699	0 0725	0 0627	0 1410	0 3694	0 3193	0 1157	0 0525
		Difference	-0.1137	-0.0374	-0.0890	-0.0669	-0.0777	-0.0674	-0.0257	0.0210	0 0281	-0.0899	-0 0720	-0.0674	-0 0303	-0 0188	-0 0596	-0.0883	-0.0643	-0 0608	-0 0611	-0 1385	-0 3562	-0 2859	-0 1012	-0 0521
	-	D 40 4	0.0000	0.0000	0.0000	0.0000	0.0004	2 0000	0.0044	0.0077	0.0470	0.0450	0.0045													إحص
Jul-01	FL	BellSouth CLEC	0.0000	0.0000	0.0000 0.0001	0.0000	0.0001	0.0000	0.0014	0.0377	0.0173 0.0100	0 0152 0 0166	0.0045	0 0222	0 0038	0 0213 0 0526	0 0088	0 0077	0 0051	0.0119	0 0040	0 0022	0 0025	0 0041	0 0086	0 0026
	+	Difference	-0.0119	-0.0049	-0.0001	-0.0001	-0.0037	-0.0008	0 00009	0.0368	0 0073	-0 0013	-0.0488	-0 0318	-0 0150	-0 0313	-0 0340	-0 0264	-0.0205	-0.0046	-0.0115	-0 0152	-0 0193	0 0203 -0 0163	0 0140 -0 0054	-0 0146 -0 0119
																		, , , , ,		5155 75		0 0 102	0 0 100	0 0 100	0 1.00 1	0 0.110
Aug-01	FL	BellSouth	0 0001	0 0000	0.0000	0.0000	0.0000	0 0000	0.0013	0.0865	0 0373	0 0024	0.0048	0 0072	0 0176	0.0090	0.0137	0 0109	0 0275	0 0144	0.0052	0 0053	0 0085	0 0044	0 0004	0.0011
	1-	CLEC	0 0070	0 0000	0.0000	0.0001 -0.0001	0.1356 -0 1356	-0.0001	0.0001	0.0856	0.0105	0 0044 -0 0020	-0.0233 -0.0184	0.0210	0.0038	0.0100	0 0337	0 0307	0 0327	0 0039	0 0083	0 0222	0 0240	0 0239	0 0056	0 0003
	<u> </u>	Difference	-0 0070	0 0000	0 0000	-0.00011	-0 1330	-0.0001	0.0013	0.06361	0.02001	-0 0020	-0.0184]	-0 0139	0 0138	-0 <b>0</b> 010	-0.0200	-0 0198	-0.0052	0 0106	-0.0031	-0 0169	-0.0155	-0 0195	-0 0053	0 0007
Sep-01	FL	BellSouth	0 0000	0.0002	0 0000	0 0001	0 0006	0.0001	0 0000	0.0001	0 0000	0.0017	0 0032	0 0007	0 0000	0.0001	0.0002	0.0004	0 0004	0.0000	0 0000	0 0007	0 0053	0 0016	0.0002	0 0000
	- II-	CLEC	0 0208	0 0305	0.0482	0 1486	0.0902	0.0680	0.0524	0.0267	0 0114	0.0251	0 0218	0 0126	0 0104	0 0095	0 0136	0.1117	0 0158	0 0261	0 0111	0 0198	0 0418	0 0419	0 0221	0.0173
		Difference	-0.0208	-0.0303	-0 0482	-0.1485	-0 0897	-0 0678	-0.0524	-0.0266	-0 0114	-0 0234	-0 0186	-0.0119	-0 0104	-0.0094	-0 0134	-0 1113	-0 0154	-0 0261	-0 0111	-0 0191	-0 0366	-0 0403	-0 0219	-0.0173
0-4.61	-	D-110	0.0004	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0000	0.0044	0.0000	0.0000	0.0005	0.0045	0.000	0.0075	0.0175	0.0004	0.000	0.6333	0.00	0.05	0.0000	0.000
Oct-01	FL	BellSouth CLEC	0.0001	0.0000	0.0000	0.0268	0.0000	0.0000	0.0000	0.0000	0.0000	0.0011	0.0000	0.0022 0.0547	0 0005	0.0012 0.0123	0 0021	0 0375 0 1002	0 0175	0 0001	0.0001 0.1450	0 0039	0 0045	0 0002 0 2276	0 0000 <sup>1</sup> 0 0506 <sup>1</sup>	0 0000 0 0009
	+-	Difference	-0 0002	-0 0052	-0 0004	-0.0268	-0.2831	-0.0613	-0 0070	-0.0023	-0.0361	-0.0838	-0 0079	-0.0547	-0 0094	-0.0123	-0 0286	-0 0627	-0 0986	-0 0960	-0 1449	-0.2570 <sub>1</sub> -0.2531	0 3677 <sup>1</sup> -0 3633	-0 2276		-0.0009
					- 0004			2.30.0				2.3000	2 30.0	- 3020	5 300 T	5 3,,,1	0 0£.00	0 0021	0 0000	0.0000	עדדו ע	0 2001	-0 00000	V 1417	5 0000	., 0003
Nov-01	FL	BellSouth	0.0000	0 0003	0.0000	0 0000	0 0002	0.0000	0 0000	0 0000	0 0000	0 0014	0 0030	0 0022	0 0006	0 0011	0 0027	0 0068	0 0053	0 0016	0 0022	0.0109	0 0072	0 0053	0 0010	0.0000
		CLEC	0.0089	0.0056	0 0018	0.0467	0.0033	0.0135	0.0015	0 0168	0.0185	0 0050	0 0206	0 0049	0 0010	0 0118	0.0159	0 0131	0 0130	0 0229	0 0603	0 1268	0 2037	0 1577	0 0442	0.0064
		Difference	-0 0089	-0.0053	-0.0018	-0.0467	-0.0031	-0 0135	-0 0015	-0 0168	-0 0185	-0 0036	-0 0176	-0 0027	-0 0004	-0.0107	-0.0132	-0.0063	-0 0077	-0 0213	-0 0582	-0 1158	-0 1965	-0 1524	-0 0431	-0.0004